## MITSUBISH ELECTRIC

Mitsubishi Programmable Controller MELSEC iQR

MELSEC iQ-R Programming Manual
(Instructions, Standard Functions/Function Blocks)

## SAFETY PRECAUTIONS

## (Read these precautions before using this product.)

Before using MELSEC iQ-R series programmable controllers, please read the manuals for the product and the relevant manuals introduced in those manuals carefully, and pay full attention to safety to handle the product correctly.
Make sure that the end users read this manual and then keep the manual in a safe place for future reference.

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(1) Mitsubishi programmable controller ("the PRODUCT") shall be used in conditions;
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ii) where the backup and fail-safe function are systematically or automatically provided outside of the PRODUCT for the case of any problem, fault or failure occurring in the PRODUCT.
(2) The PRODUCT has been designed and manufactured for the purpose of being used in general industries.

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## INTRODUCTION

Thank you for purchasing the Mitsubishi MELSEC iQ-R series programmable controllers.
This manual describes the instructions and standard functions/function blocks required for programming.
Before using this product, please read this manual and the relevant manuals carefully and develop familiarity with the functions and performance of the MELSEC iQ-R series programmable controller to handle the product correctly.
When applying the program examples provided in this manual to an actual system, ensure the applicability and confirm that it will not cause system control problems.
Please make sure that the end users read this manual.

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## RELEVANT MANUALS

| Manual name [manual number] | Description | Available form |
| :---: | :---: | :---: |
| MELSEC iQ-R Programming Manual (Instructions, Standard Functions/ Function Blocks) <br> [SH-081266ENG] (this manual) | Instructions for the CPU module, dedicated instructions for the intelligent function modules, and standard functions/function blocks | e-Manual EPUB PDF |
| MELSEC iQ-R Programming Manual (Program Design) [SH-081265ENG] | Program specifications, such as ladder programs and ST programs, and labels | e-Manual EPUB PDF |
| GX Works3 Operating Manual [SH-081215ENG] | System configuration, parameter settings, and online operations of GX Works3 | e-Manual EPUB PDF |

## Point $\rho$

e-Manual refers to the Mitsubishi FA electronic book manuals that can be browsed using a dedicated tool.
e-Manual has the following features:

- Required information can be cross-searched in multiple manuals.
- Other manuals can be accessed from the links in the manual.
- The hardware specifications of each part can be found from the product figures.
- Pages that users often browse can be bookmarked.


## TERMS

Unless otherwise specified, this manual uses the following terms.

| Term | Description |
| :---: | :---: |
| A/D converter module | A generic term for the MELSEC iQ-R series analog-digital converter module and channel isolated analog-digital converter module |
| CC-Link IE Controller Network module | The abbreviation for the MELSEC iQ-R series CC-Link IE Controller Network-equipped module |
| CC-Link IE Field Network master/ local module | The abbreviation for the CC-Link IE Field Network-equipped master/local module |
| CPU module | The abbreviation for the MELSEC iQ-R series CPU module |
| D/A converter module | A generic term for the MELSEC iQ-R series digital-analog converter module and channel isolated digital-analog converter module |
| Ethernet module | The abbreviation for the MELSEC iQ-R series CC-Link IE built-in Ethernet interface module |
| FBD/LD | The abbreviation for the function block diagram/ladder diagram |
| MELSECNET/10 | The abbreviation for the MELSECNET/10 network system |
| MELSECNET/H | The abbreviation for the MELSECNET/H network system |
| RnCPU | A generic term for the R04CPU, R08CPU, R16CPU, R32CPU, and R120CPU |
| RnPCPU | A generic term for the R08PCPU, R16PCPU, R32PCPU, and R120PCPU |
| ST language | The abbreviation for the structured text language |
| External device | A generic term for personal computers connected with Ethernet for data communication and other Ethernet-equipped modules |
| Analog module | A generic term for the A/D converter module, D/A converter module, and temperature input module |
| Positioning module | The abbreviation for the MELSEC iQ-R series positioning module |
| Intelligent function module | A module that has functions other than input and output, such as an analog module |
| Engineering tool | The product name of the software package for the MELSEC programmable controllers |
| Operand | A generic term for the devices, such as source data (s), destination data (d), number of devices ( n ), and others, used as parts to configure instructions and functions |
| Temperature input module | A generic term for the MELSEC iQ-R series channel isolated thermocouple input module and channel isolated RTD input module |
| Control CPU | A CPU module that controls connected I/O modules and intelligent function modules. In a multiple CPU system, there are multiple CPU modules and each connected module can be controlled by a different CPU module. |
| Programmable controller CPU | A generic term for the R04CPU, R08CPU, R16CPU, R32CPU, and R120CPU |
| Predefined protocol support function | A function of GX Works3. <br> This function sets protocols appropriate to each external device and reads/writes protocol setting data. |
| I/O module | A generic term for the input module, output module, I/O combined module, and interrupt module |
| Network module | A generic term for the following modules: <br> - Ethernet interface module <br> - CC-Link IE Controller Network module <br> - CC-Link IE Field Network module <br> - MELSECNET/H module <br> - MELSECNET/10 module |
| Buffer memory | A memory in an intelligent function module, where data (such as setting values and monitoring values) are stored. When using the CPU module, the memory is indicated for storing data (such as setting values and monitored values) of the Ethernet function and data used for data communication of the multiple CPU function. |
| Process CPU | A generic term for the R08PCPU, R16PCPU, R32PCPU, and R120PCPU |
| Master station | A station that controls the entire network on CC-Link IE Field Network. This station can perform cyclic transmission and transient transmission with all stations. Only one master station can be used in a network. |
| Label | A label that represents a device in a given character string |
| Local station | A station that performs cyclic transmission and transient transmission with the master station and other local stations on CC-Link IE Field Network. |

## Instruction symbols

Unless otherwise specified, this manual uses the following generic symbols for some instructions.

| Classification | Instruction symbol | Generic symbol |
| :---: | :---: | :---: |
| PID control instruction | S(P).PIDINIT, PIDINIT(P) | PIDINIT |
|  | S(P).PIDCONT, PIDCONT(P) | PIDCONT |
|  | S(P).PIDPRMW, PIDPRMW(P) | PIDPRMW |
| Multiple CPU dedicated instruction | D(P).DDRD, M(P).DDRD | DDRD |
|  | D(P).DDWR, M(P).DDWR | DDWR |
| Network common instruction | JP.READ, GP.READ | READ |
|  | JP.SREAD, GP.SREAD | SREAD |
|  | JP.WRITE, GP.WRITE | WRITE |
|  | JP.SWRITE, GP.SWRITE | SWRITE |
|  | JP.SEND, GP.SEND | SEND |
|  | JP.RECV, GP.RECV | RECV |
|  | G.RECVS, Z.RECVS | RECVS |
|  | J(P).REQ, G(P).REQ | REQ |
|  | $J(P) . R I R D, G(P) . R I R D$ | RIRD |
|  | J(P).RIWT, G(P).RIWT | RIWT |
| Ethernet instruction | GP.OPEN, ZP.OPEN | OPEN |
|  | GP.CLOSE, ZP.CLOSE | CLOSE |
|  | GP.BUFRCV, ZP.BUFRCV | BUFRCV |
|  | G.BUFRCVS, Z.BUFRCVS | BUFRCVS |
|  | GP.BUFSND, ZP.BUFSND | BUFSND |
|  | G(P).UINI, Z(P).UINI | UINI |
| CC-Link IE Controller Network instruction | $J(P) \cdot R R U N, G(P) \cdot R R U N, Z(P) \cdot R R U N$ | RRUN |
|  | $J(P)$.RSTOP, G(P).RSTOP, Z(P).RSTOP | RSTOP |
|  | J(P).RTMRD, G(P).RTMRD, Z(P).RTMRD | RTMRD |
|  | J(P).RTMWR, G(P).RTMWR, Z(P).RTMWR | RTMWR |
|  | G(P).UINI, Z(P).UINI | UINI |
| CC-Link IE Field Network instruction | JP.REMFR, ZP.REMFR | REMFR |
|  | JP.REMTO, ZP.REMTO | REMTO |
|  | G(P).UINI, Z(P).UINI | UINI |
| Positioning instruction | G.ABRST1, G.ABRST2, G.ABRST3, G.ABRST4, Z.ABRST1, Z.ABRST2, Z.ABRST3, Z.ABRST4 | ABRSTロ |
|  | GP.PSTRT1, GP.PSTRT2, GP.PSTRT3, GP.PSTRT4, ZP.PSTRT1, ZP.PSTRT2, ZP.PSTRT3, ZP.PSTRT4 | PSTRTロ |
|  | GP.TEACH1, GP.TEACH2, GP.TEACH3, GP.TEACH4, ZP.TEACH1, ZP.TEACH2, ZP.TEACH3, ZP.TEACH4 | TEACHD |
|  | GP.PFWRT, ZP.PFWRT | PFWRT |
|  | GP.PINIT, ZP.PINIT | PINIT |

## MANUAL PAGE ORGANIZATION

In this manual, pages are organized and the symbols are used as shown below.

## How to read Part 3 and Part 4

The following illustration is for explanation purpose only, and should not be referred to as an actual documentation.

（1）Instruction symbol
－An instruction symbol followed by parentheses indicates multiple instructions．For example，＂GRY（P）（＿U）＂indicates four instructions：GRY，GRYP，GRY＿U，and GRYP＿U．

| Instruction symbol | Meaning |
| :--- | :--- |
| Instruction symbol followed by＂（P）＂ | This instruction is executed only on the rising edge（off to on）． |
| Instruction symbol followed by＂（＿U）＂ | This instruction handles 16－bit or 32－bit unsigned binary data． |

－An instruction symbol followed by＂$\square$＂indicates multiple instructions．For example，＂LDDT $\square$＂indicates six instructions： LDDT＝，LDDT＜＞，LDDT＞，LDDT＜＝，LDDT＜，and LDDT＞＝．
2 Description formats of ladder diagram structured text language，and FBD／LD
An instruction symbol should be described in the enclosed area of each ladder or FBD／LD program．
Execution condition is input to EN of each structured text or FBD／LD program．And，execution result should be described for ENO．
3 Execution condition（ $\leqslant$ Page 45 Execution Condition）
（4）Description of operands，setting ranges，data types，and label data types
－For the data type，refer to the following．
$\lessgtr$ Page 29 Data Specification Method
（5）Devices that can be used as operands The following table summarizes the use segments．

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ1ロ＊4 | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ＊4， U3ED（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| Applicable device ${ }^{* 1}$ | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}{ }^{* 2} \\ & \mathrm{FY}^{* 2} \end{aligned}$ | Jロ\X <br> Jロ｜Y <br> Jロ\B <br> JपISB | $\begin{aligned} & T^{* 3}, S T^{* 3}, C^{* 3}, D \\ & \text { W, SD, SW, FD } \\ & \text { R, ZR, RD } \end{aligned}$ | UZIGロ <br> U3EDIGロ <br> U3EDTHGロ <br> JロIW <br> JUSW | Z | $\begin{aligned} & \mathrm{LT}^{* 3} \\ & \mathrm{LST}^{* 3} \\ & \mathrm{LC}^{* 3} \end{aligned}$ | LZ | ＠ロ ＠ロ．ロ | K，H | E | \＄ | $\begin{aligned} & \text { P, I, J, } \\ & \text { U, DX, } \\ & \text { DY, N, } \\ & \text { V } \end{aligned}$ |

＊1 For details on each device，refer to the following．
［］MELSEC iQ－R CPU Module User＇s Manual（Application）
＊2 FX and FY can be used for bit data only，and FD for word data only．
＊3 When T，ST，C，LT，LST，or LC is used for instructions other than those listed below，it can only be used as word data．It cannot be used as bit data．
［Instructions that can be used as bit data］
LD，LDI，AND，ANI，OR，ORI，LDP，LDF，ANDP，ANDF，ORP，ORF，LDPI，LDFI，ANDPI，ANDFI，ORPI，ORFI，OUT，RST，BKRST， MOVB（P），CMLB（P）
＊4 This device can be used with a network module with a network number specified．
＊5 In the＂Others＂column，a device（s）that can be set for each instruction is shown．
6 Control data．Some instructions require control data that determine the operations of the instructions．When control data need to be set by a user，set values according the setting range．
7 Processing details of the instruction．Unless otherwise specified，the following programs are regarded as interrupt programs．
－Interrupt program using the interrupt pointer（I）
－Fixed scan execution type program
－Event execution type program that is triggered by the interrupt pointer（I）

## 8 Precautions

（9）Error code and error details if the instruction has any possible operation error
－A device in which an error code is stored is provided in the error code column．When an error code is stored in SDO，an error flag（SMO）turns on．（The error status can be checked with the module label of the CPU module．）
－For the errors not provided here，refer to the following．
$\square \square$ MELSEC iQ－R CPU Module User＇s Manual（Application）

## How to read Part 5 and Part 6

The following illustration is for explanation purpose only, and should not be referred to as an actual documentation.


## (1) Function symbol

A function symbol followed by parentheses indicates multiple functions or function blocks. For example,
"BOOL_TO_DINT(_E)" includes two functions: "BOOL_TO_DINT" and "BOOL_TO_DINT_E".

| Function symbol | Meaning |
| :--- | :--- |
| Function symbol followed by "(_E)" | This standard function or standard function block can write program with EN/ENO. |

(2) Description formats of ladder diagram structured text language, and FBD/LD

In the enclosed area, either of the following symbol should be described.

- Standard function: Function symbol
- Standard function block: Instance name and function block symbol

Execution condition is input to EN of each standard function or function block. And, execution result is output from ENO of each standard function or function block.

The return value of functions are not displayed in FBD/LD programs.
For instances, refer to the following.
[] MELSEC iQ-R Programming Manual (Program Design)
(3) Description of operands, types, data types, and label data types

- For the data type, refer to the following.
$\backsim$ Page 29 Data Specification Method
(4) Processing details of the standard function or standard function block
(5) Error code and error details if the standard function or standard function block has any possible operation error A device in which an error code is stored is provided in the error code column. When an error code is stored in SDO, an error flag (SMO) turns on. (The error status can be checked with the module label of the CPU module.)
For the errors not provided here, refer to the following.
[] MELSEC iQ-R CPU Module User's Manual (Application)


## PART 1 OVERVIEW

Part 1 consists of the following chapter.

1 OVERVIEW

### 1.1 Instruction Configuration

Many instructions available for programmable controllers are each divided into the instruction part and device part.
The instruction part and device part are used as follows.

- Instruction part: Indicates the function of the relevant instruction.
- Device part: Indicates the data used for the instruction.

The device part is further classified to source data, destination data, and numerical data.

## Source (s)

Source is the data used in the operation.
Depending on the label or device specified in each instruction, the source becomes as follows.

| Type | Description |
| :--- | :--- |
| Constant | The constant specifies a numerical value used in the operation. <br> It is set during program creation and cannot be changed during program execution. <br> When using constants in variable data, perform index modification. ${ }^{*}$ |
| Bit device | The user specifies the device where the data to be used in the operation is stored. <br> Necessary data must be thus stored in the specified device before operation execution. <br> By changing the data to be stored in the specified device during program execution, the data to be used by the <br> instruction can be changed. |

*1 For the index modification, refer to the following.
[] MELSEC iQ-R CPU Module User's Manual (Application)

## Destination (d)

Data after operation is stored in the destination area.
However, some instructions require the data to be used in the operation to be stored before the operation.

## Ex.

Binary 16-bit data addition instruction


The data required for operation is stored before the operation.
Only the operation result is stored.
A label or device to store data must be set for the destination.

## Numerical value ( n )

For the numerical values of the numbers of devices, transfers, data, and character strings, specify those used by an instruction which uses multiple devices or an instruction which specifies the numbers of repetitions, data to be processed, and character strings.

Ex.
Block transfer instruction


The number of transfers executed by the BMOV instruction is specified.
A numerical value from 0 to 65535 or 0 to 4294967295 can be set for the size such as the number of devices, transfers, or characters. *1
Note, however, that when the size specification such as the number of devices, transfers, or characters is 0 , the relevant instruction results in non-processing.
*1 The setting range varies depending on the instruction. For details, refer to the description of each instruction.

Be careful when a large numerical value is used such as for the number of transfers. It delays the scan time.

### 1.2 Data Specification Method

The following table lists the types of data that can be used for instructions in CPU modules.


## Device data

| Data type | Description | Specifiable device/constant ${ }^{* 1}$ |
| :---: | :---: | :---: |
| Bit | Bit data can be handled. <br> $\uparrow$ Page 33 Bit data | - Bit device <br> - Bit specification of word device |
| Word | Word data can be handled. <br> $\mapsto$ Page 34 16-bit data (word data) | - Word device <br> - Digit-specified bit device (K1 to K4) ${ }^{*}{ }^{2}$ <br> - Decimal constant <br> - Hexadecimal constant |
| 16-bit signed binary | 16-bit data can be handled. <br> The value range varies depending on whether the value is signed or unsigned. <br> $\longmapsto$ Page 34 16-bit data (word data) |  |
| 16-bit unsigned binary |  |  |
| Double word | Double-word data can be handled. <br> $\longmapsto$ Page 37 32-bit data (double word data) | - Word device <br> - Double-word device <br> - Digit-specified bit device (K1 to K8) ${ }^{*}{ }^{2}$ <br> - Decimal constant <br> - Hexadecimal constant |
| 32-bit signed binary | Two consecutive sets of 32 -bit data or 16-bit data can be handled. <br> The value range varies depending on whether the value is signed or unsigned. $\longmapsto$ Page 37 32-bit data (double word data) |  |
| 32-bit unsigned binary |  |  |
| 64-bit signed binary | Two consecutive sets of 64-bit data or 32-bit data can be handled. <br> The value range varies depending on whether the value is signed or unsigned. | - Word device <br> - Double-word device <br> - Decimal constant <br> - Hexadecimal constant |
| 64 -bit unsigned binary |  |  |
| BCD 4-digit | BCD 4-digit data can be handled. <br> 16 -bit data is divided by 4 digits and each digit is specified in 0 to 9 . | - Word device <br> - Digit-specified bit device (K1 to K4) ${ }^{*}{ }^{2}$ <br> - Decimal constant <br> - Hexadecimal constant |
| BCD 8-digit | BCD 8-digit data can be handled. <br> 32-bit data is divided by 8 digits and each digit is specified in 0 to 9 . | - Word device <br> - Double-word device <br> - Digit-specified bit device (K1 to K8) ${ }^{*}{ }^{2}$ <br> - Decimal constant <br> - Hexadecimal constant |
| BCD 16-digit | BCD 16-digit data can be handled. <br> 64-bit data is divided by 16 digits and each digit is specified in 0 to 9 . | - Word device <br> - Double-word device <br> - Decimal constant <br> - Hexadecimal constant |
| Single-precision real number | Single-precision real number data (single-precision floating-point data) can be handled. <br> $\longmapsto$ Page 40 Configuration of single-precision real number data | - Word device <br> - Double-word device <br> - Real constant |
| Double-precision real number | Double-precision real number data (double-precision floating-point data) can be handled. Page 41 Configuration of double-precision real number data | - Word device <br> - Double-word device <br> - Real constant |
| Character string | ASCII code and Shift JIS code character string data can be handled. <br> Page 43 String data | - Word device <br> - Character string constant |
| Unicode character string | Unicode character string data can be handled. <br> $\omega$ Page 43 String data | - Word device <br> - Character string constant |
| Device name | A device can be specified directly. | - Device name corresponding to applicable device |

*1 A constant can be used in the data specified for the source ( s ) or numerical data ( n ) by an instruction.
*2 For the specification method, refer to the detail page of each data type.

| Label data |  |
| :---: | :---: |
| ■Primitive data type |  |
| Data type (label) | Specifiable label |
| Bit (BOOL) | - Bit type label <br> - Bit-specified word [unsigned]/bit string [16 bits] type label <br> - Bit-specified word [signed] type label <br> - Timer/retentive timer/long timer/long retentive timer type label contact/coil <br> - Counter/ long counter type label contact/coil |
| Word [unsigned]/bit string [16 bits] (WORD) | - Word [unsigned]/bit string [16 bits] type label <br> - Digit-specified bit type label (K1 to K4) <br> - Current value of timer/retentive timer type label <br> - Current value of counter type label |
| Double word [unsigned]/bit string [32 bits] (DWORD) | - Double word [unsigned]/bit string [32 bits] type label <br> - Digit-specified bit type label (K1 to K8) <br> - Current value of long timer/long retentive timer type label <br> - Current value of long counter type label |
| Word [signed] (INT) | - Word [signed] type label <br> - Digit-specified bit type label (K1 to K4) <br> - Current value of timer/retentive timer type label <br> - Current value of counter type label |
| Double word [signed] (DINT) | - Double word [signed] type label <br> - Digit-specified bit type label (K1 to K8) <br> - Current value of long timer/long retentive timer type label <br> - Current value of long counter type label |
| Single-precision real number (REAL) | - Single-precision real data type label |
| Double-precision real number (LREAL) | - Double-precision real data type label |
| Time <br> (TIME) | - Time type label |
| Character string (STRING) | - Character string type label |
| Character string [Unicode] (WSTRING) | - Character string [Unicode] type label |
| Pointer (POINTER) | - Pointer type label |

## Point ${ }^{\circ}$

For details on individual labels, refer to the following.
[] MELSEC iQ-R Programming Manual (Program Design)

## Generic data type

The generic data type is the data type of the labels which summarize several primitive data types.
Generic data types are used when multiple data types are allowed for arguments and return values of functions or function blocks.

Labels defined in generic data types can be used in any sub-level data type.

| Data type (label) |  |  |  |  |  |  |  | Specifiable label <br> Bit <br> Bit <br> Word [unsigned]/bit string [16 <br> bits] |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| ANY*1 | ANY_SIMPL | ANY_ELEMENTA RY | ANY_BIT |  |  |  | ANY_BOOL |  |
|  |  |  |  |  |  |  | ANY_BITADDR*1 |  |
|  |  |  |  |  |  |  | ANY16_U |  |
|  |  |  |  |  |  |  | ANY32_U | Double word [unsigned]/bit string [32 bits] |
|  |  |  | ANY_WORDADD R | $\begin{aligned} & \text { ANY_NU } \\ & \text { M } \end{aligned}$ | $\begin{aligned} & \text { ANY_IN } \\ & \text { T } \end{aligned}$ | ANY1 <br> 6 | ANY16_S | Word [signed] |
|  |  |  |  |  |  |  | ANY16_U | Word [unsigned]/bit string [16 bits] |
|  |  |  |  |  |  | ANY3$2$ | ANY32_S | Double word [signed], time |
|  |  |  |  |  |  |  | ANY32_U | Double word [unsigned]/bit string [32 bits] |
|  |  |  |  |  | ANY_REAL |  | ANYREAL_32 | Single-precision real number |
|  |  |  |  |  |  |  | ANYREAL_64 | Double-precision real number |
|  |  |  |  | ANY_STRING |  |  | ANYSTRING_SINGL E | String |
|  |  |  |  |  |  |  | ANYSTRING_DOUB LE | Character string [Unicode] |
|  |  |  |  | ANY16_OR_STRING_SINGLE |  |  | ANY16_S | Word [signed] |
|  |  |  |  |  |  |  | ANY16_U | Word [unsigned]/bit string [16 bits] |
|  |  |  |  |  |  |  | ANYSTRING_SINGL E | String |
|  |  |  |  | ANY_DT |  |  |  | Word [signed], word [unsigned]/bit string [16 bits] |
|  |  |  |  | ANY_TM |  |  |  | Word [signed], word [unsigned]/bit string [16 bits] |
|  | ANY_STRUCT*1 |  |  |  |  |  |  | Structures |
|  | STRUCT |  |  |  |  |  |  | Structures |

*1 Can also be used as an array.

## Generic data type (array)

For the following generic data type, define the number of array elements.

| Data type (label) |  |  | Specifiable label |
| :---: | :---: | :---: | :---: |
| ANYBIT_ARRAY |  |  | Bit |
| ANYWORD_ARRAY | ANY16_ARRAY | ANY16_S_ARRAY | Word [signed] |
|  |  | ANY16_U_ARRAY | Word [unsigned]/bit string [16 bits] |
|  | ANY32_ARRAY | ANY32_S_ARRAY | Double word [signed], time |
|  |  | ANY32_U_ARRAY | Double word [unsigned]/bit string [32 bits] |
|  | ANY_REAL_ARRAY | ANY_REAL_32_ARRAY | Single-precision real number |
|  |  | ANY_REAL_64_ARRAY | Double-precision real number |
|  | ANY_STRING_ARRAY | ANY_STRING_SINGLE_ARRAY | Character string |
|  |  | ANY_STRING_DOUBLE_ARRAY | Character string [Unicode] |
| STRUCT_ARRAY |  |  | Structures |

## Bit data

## Data size and data range

Bit data is handled in increments of bits such as contacts and coils．

| Data name | Data size | Value range |
| :--- | :--- | :--- |
| Bit data | 1 bit | 0,1 |

## Handling bit data with bit devices and labels

Bit data of one point per point can be handled．

## Handling bit data with bit word devices

By specifying a bit number for a word device，bit data of the specified bit number can be handled．
The notation for bit number specification is as follows．

Word device number
Bit number
A bit number can be specified in hexadecimal in the range from 0 to $F$ ．
For example，bit 5 （b5）of D0 is specified as D0．5，and bit 10 （b10）of D0 is specified as D0．A．
The following word devices support bit specification．

| Item | Device |
| :---: | :---: |
| Word devices which support bit specification | －Data register（D） <br> －Link register（W，Jロ\W） <br> －Link special register（SW，J口ISW） <br> －Function register（FD） <br> －Special register（SD） <br> －Module access device（UロIG） <br> －CPU buffer memory access device（U3ED\G，U3ED\HG） <br> －File register（R，ZR） <br> －Module refresh register（RD） |

## Handling bit data with word type labels

By specifying a bit number for a word type label，bit data of the specified bit number can be handled．
The notation for bit number specification is as follows．
Label name
Bit number

The following data types of labels support bit specification．

| Item | Data type |
| :--- | :--- |
| Data types of labels which support bit specification． | －Word［signed］（INT type） |
|  | －Word［unsigned］／bit string［16 bits］（WORD type） |
|  | －Current value（N）of timer（TIMER type）${ }^{* 1}$ |
|  | －Current value（N）of retentive timer（RETENTIVETIMER type）${ }^{* 1}$ |
|  | －Current value（N）of counter（COUNTER type）$)^{* 1}$ |

[^0]
## 16-bit data (word data)

## Data size and data range

16-bit data includes signed and unsigned 16-bit data.
In signed 16-bit data, a negative number is represented in two's complement.

| Data name | Data size |  |  |
| :--- | :--- | :--- | :--- |
|  |  | Value range |  |
| Signed 16-bit data | 16 bits (1 word) | -32768 to 32767 | Hexadecimal notation |
| Unsigned 16-bit data |  | 0 to 65535 | 0000 H to FFFFH |

Handling 16-bit data with bit devices
A bit device can be handled as 16-bit data by performing digit specification.

| Item |  | Notation | Example |
| :---: | :---: | :---: | :---: |
| Bit device | Other than link direct device |  | $\begin{aligned} & \text { K4X10 } \\ & \text { K2M113 } \end{aligned}$ |
|  | Link direct device |  | J1KK3B10 J10\K2Y10 |

## Handling 16-bit data with bit type array labels

A bit type array label can be handled as 16 -bit data by performing digit specification.
The following table shows the notation for handling a bit type array label as 16-bit data by digit specification.

| Item | Notation | Example |  |
| :--- | :--- | :--- | :--- |
| Bit type array label | $\mathrm{K} \square \boxed{\text { Label name }}$ | K1L_BOOL |  |
|  |  | Number of digits: Specify the number within the range of 1 to 4. |  |

## Digit specification range

The following table lists the range of 16 -bit data for each digit specification.

| Digit specification | Decimal notation | Hexadecimal notation |
| :--- | :--- | :--- |
| K1 | 0 to 15 | 0 H to FH |
| K2 | 0 to 255 | 00 H to FFH |
| K3 | 0 to 4095 | 000 H to FFFH |
| K4 | Signed 16-bit data: -32768 to 32767 <br> Unsigned 16-bit data: 0 to 65535 | 0000 H to FFFFH |

Ex.
When digit specification is made for XO , the applicable number of points is as follows.

- K1X0 $\rightarrow 4$ points from X0 to X3
- K2X0 $\rightarrow 8$ points from X0 to $\mathrm{X7}$
- K3X0 $\rightarrow 12$ points from X0 to XB
- K4X0 $\rightarrow 16$ points from X0 to XF



## Specifying a bit device with digit specification in the source (s)

When a bit device with digit specification is specified in the source of an instruction, 0 is stored in the bits, which follow the bit for which digit specification is made in the source, in the word device of the destination.


## Specifying a bit device with digit specification in the destination (d)

When a digit specification is made in the destination of an instruction, the number of points by the digit specification is applicable in the destination.
The bit devices after the number of points specified by digits remain unchanged.


## Handling 16-bit data with word devices/labels

## ■Word device

One point of word device can handle 16-bit data.

## ■Word type label

One point of word type label can handle 16-bit data.

## 32-bit data (double word data)

## Data size and data range

32-bit data includes signed and unsigned 32-bit data.
In signed 32-bit data, a negative number is represented in two's complement.

| Data name | Data size | Value range |  |
| :---: | :---: | :---: | :---: |
|  |  | Decimal notation | Hexadecimal notation |
| Signed 32-bit data | 32 bits (2 word) | -2147483648 to 2147483647 | 00000000H to FFFFFFFFFH |
| Unsigned 32-bit data |  | 0 to 4294967295 |  |

## Handling 32-bit data with bit devices

A bit device can be handled as 32-bit data by performing digit specification.

| Item |  | Notation | Example |
| :---: | :---: | :---: | :---: |
| Bit device | Other than link direct device |  | $\begin{aligned} & \text { K8X80 } \\ & \text { K6B018 } \end{aligned}$ |
|  | Link direct device |  | J1KK7B30 J10KK5X128 |

## Handling 32-bit data with bit type array labels

A bit type array label can be handled as 32-bit data by performing digit specification.
The following table shows the notation for handling a bit type array label as 32-bit data by digit specification.

| Item | Notation | Example |
| :---: | :---: | :---: |
| Bit type array label | Label name <br> Number of digits: Specify the number within the range of 1 to 8 . | K8L_BOOL |

## Digit specification range

The following table lists the range of 32-bit data for each digit specification.

| Digit specification | Decimal notation | Hexadecimal notation |
| :--- | :--- | :--- |
| K1 | 0 to 15 | 0 H to FH |
| K2 | 0 to 255 | 00 H to FFH |
| K3 | 0 to 4095 | 000 H to FFFH |
| K4 | 0 to 65535 | 0000 H to FFFFH |
| K5 | 0 to 1048575 | 00000 H to FFFFFH |
| K6 | 0 to 16777215 | 000000 H to FFFFFFH |
| K7 | 0 to 268435455 | 0000000 H to FFFFFFFH |
| K8 | Signed 32-bit data: -2147483648 to 2147483647 <br> Unsigned 32-bit data: 0 to 429496295 | 00000000 H to FFFFFFFFH |

Ex.
When digit specification is made for X 0 , the applicable number of points is as follows.

- $\mathrm{K} 1 \mathrm{X0} \rightarrow 4$ points from X 0 to X 3
- K2X0 $\rightarrow 8$ points from X0 to X7
- K3X0 $\rightarrow 12$ points from X0 to XB
- K4X0 $\rightarrow 16$ points from X0 to XF
- K5X0 $\rightarrow 20$ points from X0 to X13
- K6X0 $\rightarrow 24$ points from X0 to X17
- K7X0 $\rightarrow 28$ points from X0 to X1B
- K8X0 $\rightarrow 32$ points from X0 to X1F



## Specifying a bit device with digit specification in the source (s)

When a bit device with digit specification is specified in the source of an instruction, 0 is stored in the bits, which follow the bit for which digit specification is made in the source, in the word device of the destination.


## Specifying a bit device with digit specification in the destination (d)

When a digit specification is made in the destination of an instruction, the number of points by the digit specification is applicable in the destination.
The bit devices after the number of points specified by digits remain unchanged.


## Handling 32-bit data with word devices/labels

## ■Word device

Two points of word device can handle 32-bit data.
Note, however, that one point of the following devices can handle 32-bit data.

- Long timer (LT)
- Long retentive timer (LST)
- Long counter (LC)
- Long index register (LZ)


## ■Double word type label

One point of double word device can handle 32-bit data.

## Real number data (floating-point data)

## Data size and data range

Real number data includes single-precision 32-bit real number data and double-precision 64-bit real number data.
Real number data can be stored only in devices other than bit devices or in single-precision or double-precision real data type labels.

| Data name |  | Data size | Value range |
| :---: | :---: | :---: | :---: |
| Single-precision real number data (single-precision floating-point data) | Positive number | 32 bits (2 word) | $2^{-126} \leq$ real number<2 ${ }^{126}$ |
|  | Zero |  | 0 |
|  | Negative number |  | $-2^{128}<$ real number $\leq-2^{-126}$ |
| Double-precision real number data (double-precision floating-point data) | Positive number | 64 bits (4 word) | $2^{-1022} \leq$ real number<2 ${ }^{1024}$ |
|  | Zero |  | 0 |
|  | Negative number |  | $-2^{1024}<$ real number $\leq-2^{-1022}$ |

## Configuration of single-precision real number data

Single-precision real number data consists of a sign, mantissa, and exponent, and is expressed as shown below.


The following figure shows the bit configuration of the internal expression of single-precision real number data and the meaning of each part.


## Sign (1 bit)

This bit represents the positive or negative sign of a numerical value. "0" indicates a positive number or 0 . "1" Indicates a negative number

## -Mantissa (23 bits)

A mantissa means $\mathrm{XXXXX} \cdots$ of $1 . X X X X X \cdots \times 2^{\mathrm{N}}$ representing a single-precision real number in binary

## Exponent (8 bits)

An exponent means N of $1 . \mathrm{XXXXX} \cdots \times 2^{\mathrm{N}}$ representing a single-precision real number in binary. The following table shows the relationships between the exponent value and N of a single-precision real number.

| Exponent (b24 to b30) | FFH | FEH | FDH | ... | 81H | 80H | 7FH | 7EH | ... | 02H | 01H | 00H |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| N | Not used | 127 | 126 | ... | 2 | 1 | 0 | -1 | ... | -125 | -126 | Not used |

## Configuration of double-precision real number data

Double-precision real number data consists of a sign, mantissa, and exponent, and is expressed as shown below.
$\square$
$\qquad$ Exponent

The following figure shows the bit configuration of the internal expression of double-precision real number data and the meaning of each part.


## Sign (1 bit)

This bit represents the positive or negative sign of a numerical value. " 0 " indicates a positive number or $0 . " 1$ " Indicates a negative number.

## Mantissa (52 bits)

A mantissa means $X X X X X \cdots$ of $1 . X X X X X \cdots \times 2^{N}$ representing a single-precision real number in binary.

## Exponent (11 bits)

An exponent means N of $1 . \mathrm{XXXXX} \cdots \times 2^{\mathrm{N}}$ representing a single-precision real number in binary. The following table shows the relationships between the exponent value and N of a single-precision real number.

| Exponent (b52 to b62) | 7FFH | 7FEH | 7FDH | $\ldots$ | 401H | 400H | 3FFH | 3FEH | $\ldots$ | $\mathbf{0 2 H}$ | $\mathbf{0 1 H}$ | 00H |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| N | Not used | 1023 | 1022 | $\ldots$ | 2 | 1 | 0 | -1 | $\cdots$ | -1021 | -1022 | Not used |

## Precautions

When setting an input value of single-precision real number from the engineering tool
The number of significant digits is about 7 because the engineering tool processes single precision real number data in 32-bit single precision.
When the input value of single-precision real number data exceeds 7 digits, the 8th digit is rounded off.
Therefore, if the rounded-off value goes out of the range from -2147483648 to 2147483647 , it will not be an intended value.

## Ex.

When " 2147483647 " is set as an input value, it is handled as " 2147484000 " because 8 th digit " 6 " is rounded off

## Ex.

When "E1.1754943562" is set as an input value, it is handled as "E1.175494" because 8th digit " 3 " is rounded off.
When setting an input value of double-precision real number from the engineering tool
The number of significant digits is about 15 because the engineering tool processes double precision real number data in 64bit double precision.
When the input value of double-precision real number data exceeds 15 digits, the 16 th digit is rounded off.
Therefore, if the rounded-off value goes out of the range from -2147483648 to 2147483647 , it will not be an intended value.

## Ex.

When " 2147483646.12345678 " is set as an input value, it is handled as " 2147483646.12346 " because 16 th digit " 6 " is rounded off.

## Ex.

When "E1.7976931348623157+307" is set as an input value, it is handled as "E1.79769313486232+307" because 16th digit " 5 " is rounded off.

The monitor function of the engineering tool can monitor real number data of CPU modules.
To represent " 0 " in real number data, set all numbers in each of the following range to 0 .

- Single-precision real number data: b0 to b31
- Double-precision real number data: b0 to b63

The setting range of real number data is as follows. *1

- Single-precision real number data: $-2^{128}<$ [single-precision real number data] $\leq-2^{-126}, 0,2^{-126} \leq$ ssingleprecision real number data]<2 ${ }^{128}$
- Double-precision real number data: $-2^{1024}<\left[\right.$ double-precision real number data] $\leq-2^{-1022}, 0,2^{-1022} \leq$ [doubleprecision real number data]<2 ${ }^{1024}$
Do not specify "-0" (only the most significant bit is 1 ) in real number data. Performing a real number operation using -0 results in an operation error.
*1 For the operations to be performed when an overflow or underflow occurs or when a special value is input, refer to the following. [] MELSEC iQ-R CPU Module User's Manual (Application)


## String data

## Format of character string data

The following table lists the types of character string data, each of which ends with a NULL code to be handled as a character string.

| Type | Character code | Last character |
| :--- | :--- | :--- |
| Character string | ASCII code, Shift JIS code | NULL(00H) |
| Unicode character string | Unicode (UTF-16 (little endian)) | NULL(0000H) |

Character string data is stored in devices or an array in ascending order of device numbers or array element numbers.

```
Device number
or array element Lower \(\longrightarrow\) Upper
number
```

| Character <br> code string$\longrightarrow$ Null code |  |
| :---: | :---: |
| ABC $\cdots \mathrm{XYZ}$ |  |
| "ABC $\cdots$ XYZ" |  |

## Notation of character string

The following shows the notation of character strings in ladder programs.

| Data type |  | Notation | Example |
| :--- | :--- | :--- | :--- |
| String | STRING | Enclose a character string (ASCII, Shift JIS) and Unicode string in double <br> quotation marks ("). | "ABC" |
| Character string <br> [Unicode] | WSTRING |  |  |

The following shows the notation of character strings in ST programs.

| Data type | Notation | Example |  |
| :--- | :--- | :--- | :--- |
| String | STRING | Enclose a character string (ASCII, Shift JIS) in single quotation marks ("). | Stest:='ABC'; |
| Character string <br> [Unicode] | WSTRING | Enclose a Unicode string in double quotation marks ("). | Stest:="ABC"; |

The following shows the notation of character strings in FBD/LD programs.

| Data type |  | Notation | Example |
| :--- | :--- | :--- | :--- |
| String | STRING | Enclose a character string (ASCII, Shift JIS) in single quotation marks ("). |  |
| Character string <br> [Unicode] | WSTRING | Cannot be used. |  |

## Data range

The following table summarizes the ranges of character string data.

| Type | Maximum number of character strings | Maximum number of character strings that can be <br> handled in the program |
| :--- | :--- | :--- |
| Character string | 255 single-byte characters (excluding the last NULL <br> character) | 16383 characters (excluding the last NULL character) <br> Unicode character string ${ }^{* 1}$ |

*1 For the Unicode character string, characters up to the basic multilingual plane can be used.

## Number of words required for storing data

Character string data can be stored in word devices.
The following table lists the numbers of words required for storing character string data.

| Number of character string <br> bytes | Number of words required for storing character strings | Number of words required for storing Unicode character <br> strings |
| :--- | :--- | :--- |
| 0 byte | 1 [word] | 1 [word] |
| Odd number of bytes | (Number of character string bytes +1$) \div 2$ [words] | - (because one character is an even number of bytes) |
| Even number of bytes | (Number of character string bytes $\div 2)+1$ [words] | Number of characters +1 [words] |

## Character string data storage location

An image of the character string data storage location is shown below.

## Character strings

In each character string storage image, "NULL" indicates a NULL code (00H).


Unicode character strings
In each Unicode character string storage image, "NULL" indicates a NULL code (0000H).

| Character string to be stored | Image of storing character string data from D0 |  | Image of storing character string data from word type label array arrayA[0] |  |
| :---: | :---: | :---: | :---: | :---: |
| Null character string (" ") | D0 | NULL | arrayA[0] | NULL |
| ABCD | D0 D1 D2 D3 D4 | A B C D <br> NULL | $\operatorname{array} \mathrm{A}[0]$ <br> arrayA[1] <br> arrayA[2] <br> arrayA[3] <br> $\operatorname{arrayA}[4]$ | A <br> B <br> C <br> D <br> NULL |

### 1.3 Execution Condition

## Types of execution conditions

The following table lists the execution conditions of instructions.

| Execution condition |  | Description ${ }^{* 1}$ |
| :---: | :---: | :---: |
| On |  | An instruction is executed during on. It is executed only while the precondition of the instruction is on. When the precondition is off, the instruction is not executed. |
| Rising edge |  | An instruction is executed one time when turned on. It is executed only once on the rising edge (off to on) of the precondition of the instruction and is no longer executed later even when the condition turns on. |
| Off |  | An instruction is executed during off. It is executed only while the precondition of the instruction is off. When the precondition is on, the instruction is not executed. |
| Falling edge |  | An instruction is executed one time when turned off. It is executed only once on the falling edge (on to off) of the precondition of the instruction and is no longer executed later even when the condition turns off. |
| Every scan | - | An instruction is always executed regardless of whether the precondition of the instruction is on or off. When the precondition is off, the instruction performs off processing. |

*1 When the program is described in structured text language (ST) or function block diagram/ladder diagram (FBD/LD), EN will be the precondition of the instruction.

## Execution condition of each instruction

The execution condition varies depending on the instruction. For execution condition, refer to the details of each instruction in this manual.

When the program is described in structured text language (ST) or function block diagram/ladder diagram (FBD/LD), EN will be the execution condition. The instruction is executed only when EN is TRUE. The status of ENO will be the same as that of EN.
Note that the execution condition of standard functions and function blocks differs depending on the existence of EN. If there is no EN , the standard function or function block is executed at every scan. For the execution condition of the standard function or function block with EN, refer to the details of each standard function or function block in this manual.

## 1．4 High－speed Instruction Processing

## Subset processing

Subset processing can reduce the number of steps or speed up the instruction processing when the device and label specified by each operand of an instruction satisfy the specified conditions．
Instruction symbols and the number of operands do not change whether subset processing is applicable or not．

## Instructions that support subset processing

For the availability of subset processing for each instruction，refer to the following．
$\longmapsto$ Page 1708 Number of Basic Steps and Availability of Subset Processing

## Operand condition

The conditions that the operands need to satisfy to enable subset processing are shown．

## When a device is specified in an operand

The following table lists the conditions that an operand which specifies a device needs to satisfy．

| Data type of operand | Condition ${ }^{* 1}$ |
| :---: | :---: |
| Bit data | One of the following is satisfied． <br> －User device <br> －Host CPU specification of CPU buffer memory access device（excluding index modification to＂U3En＂）${ }^{* 2}$ <br> －Other CPU modules specification of fixed scan communication area of CPU buffer memory access device ${ }^{* 3}$ <br> －File register <br> －Local device <br> －Refresh data register |
| Signed 16－bit data Unsigned 16－bit data Signed 32－bit data Unsigned 32－bit data | One of the following is satisfied． <br> －User device <br> －Host CPU specification of CPU buffer memory access device（excluding index modification to＂U3En＂）${ }^{*}$ 2 <br> －Other CPU modules specification of fixed scan communication area of CPU buffer memory access device ${ }^{* 3}$ <br> －Index register <br> －File register <br> －Local device <br> －Refresh data register <br> －Constant（decimal，hexadecimal） |
| Single－precision real number | One of the following is satisfied． <br> －User device <br> －Host CPU specification of CPU buffer memory access device（excluding index modification to＂U3En＂）${ }^{* 2}$ <br> －Other CPU modules specification of fixed scan communication area of CPU buffer memory access device ${ }^{* 3}$ <br> －Index register <br> －File register <br> －Local device <br> －Refresh data register <br> －Constant（single－precision real number） |

＊1 Including the cases where bit numbers，digits，indirect addresses，or index－modified devices are specified
＊2 True when U3En\Gロ，U3En\GロZn，U3En\HGロ，or U3En\HGロZn is used in the CPU buffer memory access device of the host CPU module．
＊3 True when U3En\HGD or U3En\HGロZn is used in the CPU buffer memory access device of another CPU module．

## When the label assigned to a device is specified in an operand

The same conditions as those applicable when a device is specified in an operand apply．
When the label assigned to each label area is specified in an operand
When the label assigned to a label area or latch label area is specified in an operand，any instruction which supports subset processing performs subset processing regardless of the data type of the operand．（Including the cases where bit numbers or digits are specified．）

### 1.5 Precautions on Programming

## Errors common to instructions

The following table lists the conditions under which an error occurs when the instruction is executed.

| Error content ${ }^{* 1}$ | Error code |
| :--- | :--- |
| An I/O number which is out of range (other than 000 H to FFFH and 3EOH to 3E3H) is specified. | 2800 H |
| An I/O number which corresponds to no module is specified. | 2801 H |
| An I/O number of the module that cannot be specified by using the instruction is specified. | 2803 H |
| A network number which is out of range (1 to 239) is specified. | 2804 H |
| A network number which does not exist is specified. | 2805 H |
| • The device or label specified by the instruction exceeds the available range. <br> - The file register is accessed while the file register is not set in the file setting of a CPU parameter or the file register to be used in <br> the program is not set. | 2820 H |
| - The range of the buffer memory of the module specified by the instruction is exceeded. <br> - The module specified by the instruction does not have buffer memory. | 2823 H |

*1 For a contact instruction, an error is not detected but the operation result becomes no continuity.

## Checking the ranges of instruction runtime devices and labels

## Checking the ranges of devices and labels

When a device or label is specified in an instruction, no range check is performed, so a program needs to be created so that the operation result falls within the range of the relevant device or label.
If a range exceeding that of the relevant device or label is specified, no error is detected but data is written to other device or label areas.
However, an error (error code: 2820) occurs if data is written to outside the areas.
The same applies if the label assigned to a device is specified in an instruction in the program.

## Ex.

When a global device is specified

Device assignment image in
the device/label memory

(1) The transfer destination is in the range corresponding to D1023 to D1032. Because the range from D1024 to D1032 does not exist, the data in other devices is destroyed.

## Checking the range of file register

When a file register is specified in an instruction, a range check is performed, so a program needs to be created so that the operation result falls within the range of the relevant file register.
If a range exceeding that of the file register (ZR) is specified, an error (error code: 2820) occurs.
If a range exceeding that of the file register of the block number used by the file register ( R ) is specified, an error (error code: 2820) occurs.

Ex.
When a file register (ZR) is specified


Device assignment image in the device/label memory

(1) The transfer destination is in the range of file register MAIN1. Data is written to ZR0 and ZR1.
(2) The transfer destination is out of the range of file register MAIN1. An error occurs because the area range of file register MAIN1 is exceeded.

## Ex.

When a file register (R) is specified


Device assignment image in
the device/label memory

(1) The transfer destination is in the range of the R device of block number 0 . Data is written to R 0 and R 1 .
(2) The transfer destination is out of the range of the $R$ device of block number 0 . An error occurs because the area range of the $R$ device of block number 0 .

Operation when a long timer or long retentive timer device is used

When the data to be handled exceeds the width ( 32 bits) of the current value, the long timer or long retentive timer operates by using not only the area of the current value but also the areas of the previous value, contact, and coil.


When the BMOV instruction is used to batch-transfer current values, current values alone cannot be batch-transferred. Batchtransfer the current values, contacts, and coils altogether and, after the batch transfer is finished, use only the current values. When the DMOV instruction is used to batch-transfer current values, repeat the transfer of the current values alone using the FOR to NEXT instruction.

Ex.
To batch-transfer the current values of the timer device


Ex.
To batch-transfer the current values of the long timer device

[When the DMOV instruction is used]


# Operations arising when the OUT, SET/RST, and PLS/PLF instructions of the same device are used 

If two or more OUT, SET/RST, and PLS/PLF instructions are executed using the same device during one scan, they operate as described in this section.

## For OUT instructions of the same device

More than one OUT instruction of the same device must not be issued during one scan.
Otherwise, the specified device turns on or off, depending on the operation result up to each OUT instruction while it is in execution.
In this case, the device may turn on/off during one scan because the on/off state of the specified device is determined during execution of each OUT instruction.

The following figure shows the behavior arising when a circuit turning on/off the same internal relay (MO) is created with input X0 and X1.

(1) Since $X 0$ is on, M0 turns on
(2) Since X1 is off, M0 turns off.
(3) Since X 1 is off, M0 remains off.
(4) Since X 1 is on, M0 turns on

If output $(\mathrm{Y})$ is specified using an OUT instruction, the on/off state of the last OUT instruction executed during the one scan will be output.

## If SET/RST instructions of the same device are used

## ■For SET instructions

The SET instruction turns on the specified device if the execution command is on, and causes no operation if it is off.
Thus, if two or more SET instructions of the same device are executed during one scan, the specified device turns on even if one execution command is on.

## For RST instructions

The RST instruction turns on the specified device if the execution command is off, and causes no operation if it is off.
Thus, if two or more RST instructions of the same device are executed during one scan, the specified device turns on even if one execution command is off.

## ■lf the SET and RST instructions of the same device exist in one scan

If the SET and RST instructions of the same device exist in one scan, the SET instruction turns on the specified device if the execution command is on, and turns off the specified device if it is on.
If both the SET and RST instructions are off, the on/off state of the specified device will be unchanged.

(1) Since XO is on, M0 turns on.
(2) Since X1 is off, M0 remains on. (The RST instruction results in non-processing.)
(3) Since X0 is off, MO remains on. (The SET instruction results in non-processing.)
(4) Since X 1 is on, M0 turns off.

If output $(\mathrm{Y})$ is specified using a SET/RST instruction, the on/off state of the last SET/RST instruction executed during the one scan will be output.

## If PLS instructions of the same device are used

The PLS instruction turns on the specified device when the execution command specifies an off-to-on change. The specified device is turned off unless the execution command specifies an off-to-on change (i.e. off to off, on to on, on to off).
Thus, if two or more PLS instructions of the same device are issued during one scan, the specified device is turned on when the execution command of each PLS instruction specifies an off-to-on change. The specified device is turned off unless the execution command specifies an off-to-on change.
Thus, if two or more PLS instructions are issued during one scan, the device turned on by a PLS instruction may not turn on for one scan.


- If X0 and X1 differs in the on/off timing (i.e. the specified device does not turn on for one scan)

(1) Since XO turns on, M0 turns on.
(2) Since X 1 is other than turning on, M0 turns off.
(3) Since XO is other than turning on, M 0 remains off.
(4) Since X1 turns on, M0 turns on.
- If the off-to-on changes of $X 0$ and X 1 are at the same timing

(1) Since $X 0$ turns on, M0 turns on.
(2) Since X 1 turns on, M0 remains off.
(3) Since $\mathrm{X0}$ is other than turning on, M0 turns off
(4) Since X 1 is other than turning on, M0 remains off.

If output $(\mathrm{Y})$ is specified using a PLS instruction, the on/off state of the last PLS instruction executed during the one scan will be output.

## If PLF instructions of the same device are used

The PLF instruction turns on the specified device when the execution command specifies an off-to-on change. The specified device is turned off unless the execution command specifies an on-to-off change (i.e. off to off, off to on, on to on).
Thus, if two or more PLS instructions of the same device are issued during one scan, the specified device is turned on when the execution command of each PLS instruction specifies an on-to-off change. The specified device is turned off unless the execution command specifies an on-to-off change.
Thus, if two or more PLF instructions are issued during one scan, the device turned on by a PLF instruction may not turn on for one scan.


- If X0 and X1 differs in the on/off timing (i.e. the specified device does not turn on for one scan)

(1) Since X0 turns off, M0 turns on.
(2) Since $X 1$ is other than turning off, M0 turns off.
(3) Since $X 0$ is other than turning off, MO remains off.
(4) Since X 1 is other than turning off, M0 remains off.
- If the on-to-off changes of $X 0$ and $X 1$ are at the same timing

(1) Since $X 0$ turns off, M0 turns on.
(2) Since X1 turns off, M0 remains on.
(3) Since $X 0$ is other than turning off, MO turns off.
(4) Since X 1 is other than turning off, M0 remains off.

If output ( Y ) is specified using a PLF instruction, the on/off state of the last PLF instruction executed during the one scan will be output.

## Restrictions on using file registers

When a file register is specified for the refresh device, note the following restrictions.

## When a file register having the same name as a program is specified

If the use of a file register having the same name as a program is specified in the parameter, refresh cannot be performed correctly. When a file register having the same name of a program is used, data is refreshed by the file register having the same name of the program that has been set at the final number in the program settings.
To read or write refresh data, use the QDRSET instruction to switch to the corresponding file register and specify it.

## If the file name or drive number is changed by the QDRSET instruction

If the file register file name or drive number is changed by the QDRSET instruction, the setting file is linked immediately before refresh.
To read or write refresh data, specify it in the setting file immediately before refresh.

## When the block number is changed by the RSET instruction

When the block number is changed by the RSET instruction, note the following.

- Data is refreshed by the file register ( R ) of the new block number.
- Data is refreshed by the file register $(R)$ of the block number immediately before refresh.

To read or write refresh data, specify the block number immediately before refresh.

## PART 2 LISTS OF <br> INSTRUCTIONS AND FUNCTIONS

Part 2 consists of the following chapters.

2 CPU MODULE INSTRUCTIONS

3 MODULE DEDICATED INSTRUCTIONS

4 STANDARD FUNCTIONS/FUNCTION BLOCKS

CPU MODULE INSTRUCTIONS

The following table summarizes how to read the instruction lists.

| Item | Description |
| :--- | :--- |
| Instruction symbol | An instruction name |
| Processing details | An overview of the instruction |
| Availability | Instruction availability of each module <br> (1): Programmable controller CPU, (2): Process CPU <br>  <br> ○: Available, $\triangle:$ Available with restrictions, $\times:$ Not available |
| Reference | Section where detailed information is described |

### 2.1 Sequence Instructions

## Contact instructions

## חOperation start, series connection, parallel connection

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| LD | Outputs the on/off information of the specified device as the operation result. (Normally open contact operation start instruction) | $\bigcirc$ | $\bigcirc$ | Page 162 LD, LDI, AND, ANI, OR, ORI |
| LDI | Outputs the on/off information of the specified device as the operation result. (Normally closed contact operation start instruction) | $\bigcirc$ | $\bigcirc$ |  |
| AND | Performs an AND operation between the on/off information of the specified device and the previous operation result, and output the operation result. (Normally open contact series connection instruction) | $\bigcirc$ | $\bigcirc$ |  |
| ANI | Performs an AND operation between the on/off information of the specified device and the previous operation result, and output the operation result. (Normally open contact series connection instruction) | $\bigcirc$ | $\bigcirc$ |  |
| OR | Performs an OR operation between the on/off information of the specified device and the previous operation result, and output the operation result. (Single normally open contact parallel connection instruction) | $\bigcirc$ | $\bigcirc$ |  |
| ORI | Performs an OR operation between the on/off information of the specified device and the previous operation result, and output the operation result. (Single normally closed contact parallel connection instruction) | $\bigcirc$ | $\bigcirc$ |  |

-Pulse operation start, pulse series connection, pulse parallel connection

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| LDP | Turns on only at the rising edge (off to on) of the specified bit device. (Rising edge pulse operation start instruction) | $\bigcirc$ | $\bigcirc$ | Page 164 LDP, LDF, ANDP, ANDF, ORP, ORF |
| LDF | Turns on only at the falling edge (on to off) of the specified bit device. (Falling edge pulse operation start instruction) | $\bigcirc$ | $\bigcirc$ |  |
| ANDP | Performs an AND operation with the previous operation result. (Rising edge pulse series connection instruction) | $\bigcirc$ | $\bigcirc$ |  |
| ANDF | Performs an AND operation with the previous operation result. (Falling edge pulse series connection instruction) | $\bigcirc$ | $\bigcirc$ |  |
| ORP | Performs an OR operation with the previous operation result. (Rising edge pulse parallel connection instruction) | $\bigcirc$ | $\bigcirc$ |  |
| ORF | Performs an OR operation with the previous operation result. (Falling edge pulse parallel connection instruction) | $\bigcirc$ | $\bigcirc$ |  |

$\square$ Pulse NOT operation start, pulse NOT series connection, pulse NOT parallel connection

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| LDPI | Turns on when the specified device is off, on, or at the falling edge (on to off). (Rising edge pulse NOT operation start instruction) | $\bigcirc$ | $\bigcirc$ | Page 167 <br> LDPI, LDFI, <br> ANDPI, ANDFI, ORPI, ORFI |
| LDFI | Turns on when the specified device is at the rising edge (off to on), off, or on. (Falling edge pulse NOT operation start instruction) | $\bigcirc$ | $\bigcirc$ |  |
| ANDPI | Performs an AND operation with the previous operation result. (Rising edge pulse NOT series connection instruction) | $\bigcirc$ | $\bigcirc$ |  |
| ANDFI | Performs an AND operation with the previous operation result. (Falling edge pulse NOT series connection instruction) | $\bigcirc$ | $\bigcirc$ |  |
| ORPI | Performs an OR operation with the previous operation result. (Rising edge pulse NOT parallel connection instruction) | $\bigcirc$ | $\bigcirc$ |  |
| ORFI | Performs an OR operation with the previous operation result. (Falling edge pulse NOT parallel connection instruction) | $\bigcirc$ | $\bigcirc$ |  |

## Association instructions

## ■Ladder block series/parallel connection

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| ANB | Performs AND operations between logical blocks (series connection between logical blocks) | $\bigcirc$ | $\bigcirc$ | Page 170 ANB, |
| ORB | Performs OR operations between logical blocks (series connection between logical blocks) | $\bigcirc$ | $\bigcirc$ | ORB |

## Storing/reading/clearing the operation result

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| MPS | Stores the operation result (on/off) immediately before the MPS instruction. | $\bigcirc$ | $\bigcirc$ | Page 171 MPS, |
| MRD | Reads the operation result stored by using the MPS instruction. | $\bigcirc$ | $\bigcirc$ |  |
| MPP | Clears the operation result stored by using the MPS instruction. | MRP | MPP |  |

Inverting the operation result

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| INV | Inverts the operation result up to just before the INV instruction. | $\bigcirc$ | $\bigcirc$ | Page 173 INV |

Converting the operation result into a pulse

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| MEP | Turns on at the rising edge (off to on) of the operation result up to the MEP instruction. | $O$ | $O$ | Page 174 MEP, |
| MEF | Turns on at the falling edge (on to off) of the operation result up to the MEF instruction. | $O$ | $O$ | MEF |

Converting the edge relay operation result into a pulse

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| EGP | Stores the operation result up to the EGP instruction in the edge relay (V). The instruction turns on <br> at the rising edge (off to on) of the operation result. | $\bigcirc$P | Page 175 EGP, <br> EGF |  |
| EGF | Stores the operation result up to the EGF instruction in the edge relay (V). The instruction turns on <br> at the falling edge (on to off) of the operation result. | $\bigcirc$ | $\bigcirc$ |  |

## Output instructions

Out (excluding the timer, counter, and annunciator)

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| OUT | Outputs the operation result to the specified device. | O | ○ | Page 177 OUT |

## Timer, long timer

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| OUT T | Starts time measurement when the operation result up to the OUT instruction is on. When time is up, the normally open contact turns on (continuity state) and the normally closed contact turns off (non-continuity state). <br> - OUT T: Low-speed timer instruction <br> - OUTH T: High-speed timer instruction <br> - OUT ST: Low-speed retentive timer instruction <br> - OUTH ST: High-speed retentive timer instruction <br> - OUT LT: Low-speed long timer instruction <br> - OUT LST: Low-speed long retentive timer instruction | $\bigcirc$ | $\bigcirc$ | Page 179 OUT T, OUTH T, OUT ST, OUTH ST |
| OUTH T |  | $\bigcirc$ | $\bigcirc$ |  |
| OUT ST |  | $\bigcirc$ | $\bigcirc$ |  |
| OUTH ST |  | $\bigcirc$ | $\bigcirc$ |  |
| OUT LT |  | $\bigcirc$ | $\bigcirc$ | Page 182 OUT LT, OUT LST |
| OUT LST |  | $\bigcirc$ | $\bigcirc$ |  |

Counter, long counter

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| OUT C | This instruction increments the current counter value (count value) by one when the operation <br> result up to the OUT instruction turns on. When the count value reaches the set value, the normally <br> open contact of the counter turns on (continuity state) and the normally closed contact turns off <br> (non-continuity state). <br> - OUT C: Counter <br> - OUT LC: Long counter | $\bigcirc$ | Page 185 OUT |  |
|  | C | C | Page 187 OUT <br> OUT |  |

Annunciator

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| OUT F | Outputs the operation result up to the OUT F instruction to the specified annunciator. | O | ○ | Page 189 OUT <br> F |

Setting devices (excluding annunciator)

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | $(\mathbf{1 )}$ | (2) |  |
| SET | Turns on the specified bit. | $\bigcirc$ | $\bigcirc$ | Page 190 SET |

Resetting devices (excluding annunciator)

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| RST | Turns off the specified device. For the timer and counter, the instruction clears the current value to <br> 0 and turns off the contact or coil. | $\bigcirc$ | $\bigcirc$ | Page 192 RST |

Setting/resetting annunciator

| Instruction symbol | Processing details | Availability |  | Reference |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| SET F | Turns on the specified annunciator. | $\bigcirc$ | O | Page 194 SET |
| RST F | Turns off the specified annunciator. | F | Page 196 RST <br> F |  |

■Rising/falling edge output

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| PLS | Turns on the specified device for one scan on the rising edge (off to on) of the execution command. | $\bigcirc$ | $\bigcirc$ | Page 198 PLS |
| PLF | Turns on the specified device for one scan on the falling edge (on to off) of the execution <br> command. | $\bigcirc$ | $\bigcirc$ | Page 200 PLF |

## ■Inverting the bit device output

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| FF | Inverts the status of the specified device. | $\bigcirc$ | $\bigcirc$ | Page 202 FF |

Converting the direct access output into a pulse

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| DELTA | Converts the specified direct access output (DY) into pulse output. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 203 \\ & \text { DELTA(P) } \end{aligned}$ |
| DELTAP |  | $\bigcirc$ | $\bigcirc$ |  |

## Shift instructions

## ■Shifting bit devices

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| SFT | Shifts the on/off state of the device area just before the one specified to the specified device area, <br> and turns off the shift source device. | $\bigcirc$ | $\bigcirc$ | Page 205 |
|  |  | SFT(P) |  |  |


| - Setting/resetting a master control |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Instruction symbol | Processing details | Availability |  | Reference |
|  |  | (1) | (2) |  |
| MC | Starts a master control. | $\bigcirc$ | 0 | Page 207 MC, MCR |
| MCR | Ends a master control. | $\bigcirc$ | $\bigcirc$ |  |

## Termination instructions

Ending the main routine program

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| FEND | Used to separate the main routine program from subroutine programs and interrupt programs in a <br> program file. | $\bigcirc$ | $\bigcirc$ | Page 211 <br> FEND |

Ending the sequence program

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| END | Indicates the end of a program. | $\bigcirc$ | $\bigcirc$ | Page 212 END |

## Stop instruction

## EStopping the sequence program

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| STOP | Stops the operation of the CPU module. (The operation of this instruction is the same as setting the <br> switch of the CPU module to the STOP position.) | $\bigcirc$ | $\bigcirc$ | Page 214 <br> STOP |

## No operation instruction

■No operation

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| NOP | Used to insert a space for debugging. | $O$ | O | Page 215 NOP |

### 2.2 Basic Instructions

## Comparison operation instructions

Comparing 16-bit binary data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| LD=, AND=, OR= | Compares the two sets of 16-bit binary data specified. (Devices are used as normally open contacts.) | $\bigcirc$ | $\bigcirc$ | Page 216 LDロ(_U), ANDD(_U), ORD(_U) |
| $\begin{aligned} & \mathrm{LD}=\_\mathrm{U}, \mathrm{AND}=\_\mathrm{U}, \\ & \mathrm{OR}=\_\mathrm{U} \end{aligned}$ |  | $\bigcirc$ | $\bigcirc$ |  |
| LD<>, AND<>, OR<> |  | $\bigcirc$ | $\bigcirc$ |  |
| $\begin{aligned} & \text { LD<>_U, AND<>_U, } \\ & \text { OR<>_U } \end{aligned}$ |  | $\bigcirc$ | $\bigcirc$ |  |
| LD>, AND>, OR> |  | $\bigcirc$ | $\bigcirc$ |  |
| $\begin{aligned} & \text { LD>_U, AND>_U, } \\ & \text { OR>_U } \end{aligned}$ |  | $\bigcirc$ | $\bigcirc$ |  |
| LD<=, $\mathrm{AND}<=$, $\mathrm{OR}<=$ |  | $\bigcirc$ | $\bigcirc$ |  |
| $\begin{aligned} & \mathrm{LD}<=\_\mathrm{U}, \mathrm{AND}<=\text { U, } \\ & \mathrm{OR}<=\_\mathrm{U} \end{aligned}$ |  | $\bigcirc$ | $\bigcirc$ |  |
| LD<, AND<, OR< |  | $\bigcirc$ | $\bigcirc$ |  |
| $\begin{aligned} & \mathrm{LD}<\_\mathrm{U}, \mathrm{AND}<\_\mathrm{U}, \\ & \mathrm{OR}<\_\mathrm{U} \end{aligned}$ |  | $\bigcirc$ | $\bigcirc$ |  |
| LD>=, AND>=, OR>= |  | $\bigcirc$ | $\bigcirc$ |  |
| $\begin{aligned} & \text { LD>=_U, AND>=_U, } \\ & \text { OR>=_U } \end{aligned}$ |  | $\bigcirc$ | $\bigcirc$ |  |

Comparing 32-bit binary data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| LDD=, ANDD=, ORD= | Compares the two sets of 32 -bit binary data specified. (Devices are used as normally open contacts.) | $\bigcirc$ | $\bigcirc$ | Page 218 <br> LDDD(_U), <br> ANDDD(_U), <br> ORDD(_U) |
| $\begin{aligned} & \mathrm{LDD}=\_\mathrm{U}, \mathrm{ANDD}=\_\mathrm{U}, \\ & \mathrm{ORD}=\_\mathrm{U} \end{aligned}$ |  | $\bigcirc$ | $\bigcirc$ |  |
| $\begin{aligned} & \text { LDD<>, ANDD<>, } \\ & \text { ORD<> } \end{aligned}$ |  | $\bigcirc$ | $\bigcirc$ |  |
| $\begin{aligned} & \text { LDD<>_U, } \\ & \text { ANDD<>_U, } \\ & \text { ORD<>_U } \end{aligned}$ |  | $\bigcirc$ | $\bigcirc$ |  |
| LDD>, ANDD>, ORD> |  | $\bigcirc$ | $\bigcirc$ |  |
| $\begin{aligned} & \text { LDD>_U, ANDD>_U, } \\ & \text { ORD>_U } \end{aligned}$ |  | $\bigcirc$ | $\bigcirc$ |  |
| $\begin{aligned} & \text { LDD<=, ANDD<=, } \\ & \text { ORD<= } \end{aligned}$ |  | $\bigcirc$ | $\bigcirc$ |  |
| $\begin{aligned} & \mathrm{LDD}<==\mathrm{U}, \\ & \text { ANDD }<==\mathrm{U}, \\ & \mathrm{ORD}<==\mathrm{U} \end{aligned}$ |  | $\bigcirc$ | $\bigcirc$ |  |
| LDD<, ANDD<, ORD< |  | $\bigcirc$ | $\bigcirc$ |  |
| $\begin{aligned} & \text { LDD<_U, ANDD<_U, } \\ & \text { ORD<_U } \end{aligned}$ |  | $\bigcirc$ | $\bigcirc$ |  |
| $\begin{aligned} & \text { LDD>=, ANDD>=, } \\ & \text { ORD>= } \end{aligned}$ |  | $\bigcirc$ | $\bigcirc$ |  |
| $\begin{aligned} & \text { LDD>=_U, } \\ & \text { ANDD>=_U, } \\ & \text { ORD>=_U } \end{aligned}$ |  | $\bigcirc$ | $\bigcirc$ |  |

Comparing 16-bit binary block data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| $\begin{aligned} & \mathrm{BKCMP}=, \mathrm{BKCMP}<>, \\ & \mathrm{BKCMP>}, \mathrm{BKCMP}<= \\ & \mathrm{BKCMP}<, \mathrm{BKCMP>}= \end{aligned}$ | Compares the two sets of 16-bit binary block data specified. | $\bigcirc$ | $\bigcirc$ | Page 220 BKCMPロ(P)( <br> U) |
| BKCMP=P, <br> BKCMP<>P, <br> BKCMP>P, <br> BKCMP<=P, <br> BKCMP<P, <br> BKCMP>=P |  | $\bigcirc$ | $\bigcirc$ |  |
| $\begin{aligned} & \text { BKCMP=_U, } \\ & \text { BKCMP<>_U, } \\ & \text { BKCMP>_U, } \\ & B K C M P<=\_U, \\ & B K C M P<\_U, \\ & B K C M P>=\_U \end{aligned}$ |  | $\bigcirc$ | $\bigcirc$ |  |
| $\begin{aligned} & \text { BKCMP=P_U, } \\ & \text { BKCMP<>P_U, } \\ & \text { BKCMP>P_U, } \\ & \text { BKCMP<=P_U, } \\ & \text { BKCMP<P_U, } \\ & \text { BKCMP>=_U } \end{aligned}$ |  | $\bigcirc$ | $\bigcirc$ |  |

Comparing 32-bit binary block data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| DBKCMP=, DBKCMP<>, DBKCMP>, DBKCMP<=, DBKCMP<, DBKCMP>= | Compares the two sets of 32-bit binary block data specified. | $\bigcirc$ | $\bigcirc$ | Page 222 DBKCMPD(P)( <br> _U) |
| DBKCMP=P, DBKCMP<>P, DBKCMP>P, DBKCMP<=P, DBKCMP<P, DBKCMP>=P |  | $\bigcirc$ | $\bigcirc$ |  |
| $\begin{aligned} & \text { DBKCMP=_U, } \\ & \text { DBKCMP<>_U, } \\ & \text { DBKCMP>_U, } \\ & \text { DBKCMP<=_U, } \\ & \text { DBKCMP<_U, } \\ & \text { DBKCMP>=_U } \end{aligned}$ |  | $\bigcirc$ | $\bigcirc$ |  |
| $\begin{aligned} & \text { DBKCMP=P_U, } \\ & \text { DBKCMP<>P_U, } \\ & \text { DBKCMP>P_U, } \\ & \text { DBKCMP<=P_U, } \\ & \text { DBKCMP<P_U, } \\ & \text { DBKCMP>=P_U } \end{aligned}$ |  | $\bigcirc$ | $\bigcirc$ |  |

## Arithmetic operation instructions

■Adding/subtracting 16-bit binary data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| + | Adds the two sets of 16-bit binary data specified. (Two operands) | $\bigcirc$ | $\bigcirc$ | Page 225 $+(P)(U)$ [when two operands are set] |
| +P |  | $\bigcirc$ | $\bigcirc$ |  |
| +_U |  | $\bigcirc$ | $\bigcirc$ |  |
| +P_U |  | $\bigcirc$ | $\bigcirc$ |  |
| + | Adds the two sets of 16-bit binary data specified. (Three operands) | $\bigcirc$ | $\bigcirc$ | Page 227 $+(P)\left(\_U\right)$ [when three operands are set] |
| +P |  | $\bigcirc$ | $\bigcirc$ |  |
| +_U |  | $\bigcirc$ | $\bigcirc$ |  |
| +P_U |  | $\bigcirc$ | $\bigcirc$ |  |
| - | Performs subtraction between the two sets of 16-bit binary data specified. (Two operands) | $\bigcirc$ | $\bigcirc$ | Page 229 (P)(_U) [when two operands are set] |
| -P |  | $\bigcirc$ | $\bigcirc$ |  |
| -_U |  | $\bigcirc$ | $\bigcirc$ |  |
| -P_U |  | $\bigcirc$ | $\bigcirc$ |  |
| - | Performs subtraction between the two sets of 16-bit binary data specified. (Three operands) | $\bigcirc$ | $\bigcirc$ | Page 231 (P)(_U) [when three operands are set] |
| -P |  | $\bigcirc$ | $\bigcirc$ |  |
| -_U |  | $\bigcirc$ | $\bigcirc$ |  |
| -P_U |  | $\bigcirc$ | $\bigcirc$ |  |

■Adding/subtracting 32-bit binary data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| D+ | Adds the two sets of 32-bit binary data specified. (Two operands) | $\bigcirc$ | $\bigcirc$ | Page 233 <br> D+(P)(_U) <br> [when two operands are set] |
| D+P |  | $\bigcirc$ | $\bigcirc$ |  |
| D+_U |  | $\bigcirc$ | $\bigcirc$ |  |
| D+P_U |  | $\bigcirc$ | $\bigcirc$ |  |
| D+ | Adds the two sets of 32-bit binary data specified. (Three operands) | $\bigcirc$ | $\bigcirc$ | Page 235 D+(P)(_U) <br> [when three operands are set] |
| D+P |  | $\bigcirc$ | $\bigcirc$ |  |
| D+_U |  | $\bigcirc$ | $\bigcirc$ |  |
| D+P_U |  | $\bigcirc$ | $\bigcirc$ |  |
| D- | Performs subtraction between the two sets of 32-bit binary data specified. (Two operands) | $\bigcirc$ | $\bigcirc$ | Page 237 D(P)(_U) [when two operands are set] |
| D-P |  | $\bigcirc$ | $\bigcirc$ |  |
| D-_U |  | $\bigcirc$ | $\bigcirc$ |  |
| D-P_U |  | $\bigcirc$ | $\bigcirc$ |  |
| D- | Performs subtraction between the two sets of 32-bit binary data specified. (Three operands) | $\bigcirc$ | $\bigcirc$ | Page 239 D(P)(_U) [when three operands are set] |
| D-P |  | $\bigcirc$ | $\bigcirc$ |  |
| D-_U |  | $\bigcirc$ | $\bigcirc$ |  |
| D-P_U |  | $\bigcirc$ | $\bigcirc$ |  |

## ■Multiplying/dividing 16-bit binary data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| * | Multiplies the two sets of 16-bit binary data specified. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 241 \\ & \text { *(P)(_U) } \end{aligned}$ |
| *P |  | $\bigcirc$ | $\bigcirc$ |  |
| *_U |  | $\bigcirc$ | $\bigcirc$ |  |
| *P_U |  | $\bigcirc$ | $\bigcirc$ |  |
| 1 | Performs division between the two sets of 16-bit binary data specified. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 243 \text { / } \\ & \text { (P)(_U) } \end{aligned}$ |
| /P |  | $\bigcirc$ | $\bigcirc$ |  |
| I_U |  | $\bigcirc$ | $\bigcirc$ |  |
| IP_U |  | $\bigcirc$ | $\bigcirc$ |  |

-Multiplying/dividing 32-bit binary data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| D* | Multiplies the two sets of 32-bit binary data specified. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 245 \\ & \mathrm{D}^{*}(\mathrm{P})(\mathrm{U}) \end{aligned}$ |
| D*P |  | $\bigcirc$ | $\bigcirc$ |  |
| D*_U |  | $\bigcirc$ | $\bigcirc$ |  |
| D*P_U |  | $\bigcirc$ | $\bigcirc$ |  |
| D/ | Performs division between the two sets of 32-bit binary data specified. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 247 \text { D/ } \\ & \text { (P)(_U) } \end{aligned}$ |
| D/P |  | $\bigcirc$ | $\bigcirc$ |  |
| D/_U |  | $\bigcirc$ | $\bigcirc$ |  |
| D/P_U |  | $\bigcirc$ | $\bigcirc$ |  |

## IAdding/subtracting BCD 4-digit data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| B+ | Adds the two sets of BCD 4-digit data specified. (Two operands) | $\bigcirc$ | $\bigcirc$ | Page 249 $\mathrm{B}+(\mathrm{P})$ [when two operands are set] |
| $B+P$ |  | $\bigcirc$ | $\bigcirc$ |  |
| B+ | Adds the two sets of BCD 4-digit data specified. (Three operands) | $\bigcirc$ | $\bigcirc$ | Page 250 $\mathrm{B}+(\mathrm{P})$ [when three operands are set] |
| B + P |  | $\bigcirc$ | $\bigcirc$ |  |
| B- | Performs subtraction between the two sets of BCD 4-digit data specified. (Two operands) | $\bigcirc$ | $\bigcirc$ | Page 252 B-(P) <br> [when two operands are set] |
| B-P |  | $\bigcirc$ | $\bigcirc$ |  |
| B- | Performs subtraction between the two sets of BCD 4-digit data specified. (Three operands) | $\bigcirc$ | $\bigcirc$ | Page 253 B-(P) <br> [when three operands are set] |
| B-P |  | $\bigcirc$ | $\bigcirc$ |  |

Adding/subtracting BCD 8-digit data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| DB+ | Adds the two sets of BCD 8-digit data specified. (Two operands) | $\bigcirc$ | $\bigcirc$ | Page 255 DB+(P) [when two operands are set] |
| DB + P |  | $\bigcirc$ | $\bigcirc$ |  |
| DB+ | Adds the two sets of BCD 8-digit data specified. (Three operands) | $\bigcirc$ | $\bigcirc$ | Page 256 DB+(P) [when three operands are set] |
| DB + P |  | $\bigcirc$ | $\bigcirc$ |  |
| DB- | Performs subtraction between the two sets of BCD 8-digit data specified. (Two operands) | $\bigcirc$ | $\bigcirc$ | Page 258 DB( P ) [when two operands are set] |
| DB-P |  | $\bigcirc$ | $\bigcirc$ |  |
| DB- | Performs subtraction between the two sets of BCD 8-digit data specified. (Three operands) | $\bigcirc$ | $\bigcirc$ | Page 259 DB(P) [when three operands are set] |
| DB-P |  | $\bigcirc$ | $\bigcirc$ |  |

Multiplying/dividing BCD 4-digit data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| B* | Multiplies the two sets of BCD 4-digit data specified. | $\bigcirc$ | $\bigcirc$ | Page $261 \mathrm{~B}^{*}(\mathrm{P})$ |
| B*P |  | $\bigcirc$ | $\bigcirc$ |  |
| B/ | Performs division between the two sets of BCD 4-digit data specified. | $\bigcirc$ | $\bigcirc$ | Page $263 \mathrm{~B} /(\mathrm{P})$ |
| B/P |  | $\bigcirc$ | $\bigcirc$ |  |

## ■Multiplying/dividing BCD 8-digit data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| DB* | Multiplies the two sets of BCD 8-digit data specified. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 265 \\ & \mathrm{DB}^{*}(\mathrm{P}) \end{aligned}$ |
| DB*P |  | $\bigcirc$ | $\bigcirc$ |  |
| DB/ | Performs division between the two sets of BCD 8-digit data specified. | $\bigcirc$ | $\bigcirc$ | Page 267 DB/ <br> (P) |
| DB/P |  | $\bigcirc$ | $\bigcirc$ |  |

■Adding/subtracting 16-bit binary block data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| BK+ | Adds the two 16-bit binary data blocks specified. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 269 \\ & B K+(P)\left(\_U\right) \end{aligned}$ |
| BK+P |  | $\bigcirc$ | $\bigcirc$ |  |
| BK+_U |  | $\bigcirc$ | $\bigcirc$ |  |
| BK+P_U |  | $\bigcirc$ | $\bigcirc$ |  |
| BK- | Performs subtraction between the two 16-bit binary data blocks specified. | $\bigcirc$ | $\bigcirc$ | Page 271 BK(P)(_U) |
| BK-P |  | $\bigcirc$ | $\bigcirc$ |  |
| BK-_U |  | $\bigcirc$ | $\bigcirc$ |  |
| BK-P_U |  | $\bigcirc$ | $\bigcirc$ |  |

## Adding/subtracting 32-bit binary block data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| DBK+ | Adds the two 32-bit binary data blocks specified. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 273 \\ & \text { DBK }+(\mathrm{P})\left(\_\mathrm{U}\right) \end{aligned}$ |
| DBK+P |  | $\bigcirc$ | $\bigcirc$ |  |
| DBK+_U |  | $\bigcirc$ | $\bigcirc$ |  |
| DBK+P_U |  | $\bigcirc$ | $\bigcirc$ |  |
| DBK- | Performs subtraction between the two 32-bit binary data blocks specified. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 276 \text { DBK- } \\ & \text { (P)(_U) } \end{aligned}$ |
| DBK-P |  | $\bigcirc$ | $\bigcirc$ |  |
| DBK-_U |  | $\bigcirc$ | $\bigcirc$ |  |
| DBK-P_U |  | $\bigcirc$ | $\bigcirc$ |  |

—lncrementing/decrementing 16-bit binary data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| INC | Increments the specified 16-bit binary data by one. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 279 \\ & \text { INC(P)(_U) } \end{aligned}$ |
| INCP |  | $\bigcirc$ | $\bigcirc$ |  |
| INC_U |  | $\bigcirc$ | $\bigcirc$ |  |
| INCP_U |  | $\bigcirc$ | $\bigcirc$ |  |
| DEC | Decrements the specified 16-bit binary data by one. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 280 \\ & \text { DEC(P)(_U) } \end{aligned}$ |
| DECP |  | $\bigcirc$ | $\bigcirc$ |  |
| DEC_U |  | $\bigcirc$ | $\bigcirc$ |  |
| DECP_U |  | $\bigcirc$ | $\bigcirc$ |  |

■lncrementing/decrementing 32-bit binary data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| DINC | Increments the specified 32-bit binary data by one. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 281 \\ & \text { DINC(P)(_U) } \end{aligned}$ |
| DINCP |  | $\bigcirc$ | $\bigcirc$ |  |
| DINC_U |  | $\bigcirc$ | $\bigcirc$ |  |
| DINCP_U |  | $\bigcirc$ | $\bigcirc$ |  |
| DDEC | Decrements the specified 32-bit binary data by one. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 282 \\ & \text { DDEC(P)(_U) } \end{aligned}$ |
| DDECP |  | $\bigcirc$ | $\bigcirc$ |  |
| DDEC_U |  | $\bigcirc$ | $\bigcirc$ |  |
| DDECP_U |  | $\bigcirc$ | $\bigcirc$ |  |

## Logical operation instructions

-Performing an AND operation on 16-bit/32-bit data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| WAND | Performs an AND operation on the two sets of 16-bit binary data specified. (Two operands) | $\bigcirc$ | $\bigcirc$ | Page 283 |
| WANDP |  | $\bigcirc$ | $\bigcirc$ | WAND (P) <br> [when two operands are set] |
| WAND | Performs an AND operation on the two sets of 16-bit binary data specified. (Three operands) | $\bigcirc$ | $\bigcirc$ | Page 284 |
| WANDP |  | $\bigcirc$ | $\bigcirc$ | WAND (P) [when three operands are set] |
| DAND | Performs an AND operation on the two sets of 32-bit binary data specified. (Two operands) | $\bigcirc$ | $\bigcirc$ | Page 286 |
| DANDP |  | $\bigcirc$ | $\bigcirc$ | DAND(P) <br> [when two operands are set] |
| DAND | Performs an AND operation on the two sets of 32-bit binary data specified. (Three operands) | $\bigcirc$ | $\bigcirc$ | Page 287 |
| DANDP |  | $\bigcirc$ | $\bigcirc$ | DAND(P) <br> [when three operands are set] |

Performing an AND operation on 16-bit block data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| BKAND | Performs an AND operation on the two 16-bit binary data blocks specified. | $\bigcirc$ | $\bigcirc$ | Page 289 <br> BKAND(P) |
| BKANDP |  | $\bigcirc$ | $\bigcirc$ |  |

Performing an OR operation on 16-bit/32-bit data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| WOR | Performs an OR operation on the two sets of 16-bit binary data specified. (Two operands) | $\bigcirc$ | $\bigcirc$ | Page 291 |
| WORP |  | $\bigcirc$ | $\bigcirc$ | WOR(P) [when two operands are set] |
| WOR | Performs an OR operation on the two sets of 16-bit binary data specified. (Three operands) | $\bigcirc$ | $\bigcirc$ | Page 292 |
| WORP |  | $\bigcirc$ | $\bigcirc$ | WOR(P) [when three operands are set] |
| DOR | Performs an OR operation on the two sets of 32-bit binary data specified. (Two operands) | $\bigcirc$ | $\bigcirc$ | Page 294 |
| DORP |  | $\bigcirc$ | $\bigcirc$ | DOR(P) [when two operands are set] |
| DOR | Performs an OR operation on the two sets of 32-bit binary data specified. (Three operands) | $\bigcirc$ | $\bigcirc$ | Page 295 |
| DORP |  | $\bigcirc$ | $\bigcirc$ | DOR(P) [when three operands are set] |

## ■Performing an OR operation on 16-bit block data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| BKOR | Performs an OR operation on the two 16-bit binary data blocks specified. | $\bigcirc$ | $\bigcirc$ | Page 297 <br> BKOR(P) |
| BKORP |  | $\bigcirc$ | $\bigcirc$ |  |

## Performing an XOR operation on 16-bit/32-bit data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| WXOR | Performs an XOR operation on the two sets of 16-bit binary data specified. (Two operands) | $\bigcirc$ | $\bigcirc$ | Page 299 |
| WXORP |  | $\bigcirc$ | $\bigcirc$ | WXOR(P) <br> [when two operands are set] |
| WXOR | Performs an XOR operation on the two sets of 16-bit binary data specified. (Three operands) | $\bigcirc$ | $\bigcirc$ | Page 300 |
| WXORP |  | $\bigcirc$ | $\bigcirc$ | WXOR(P) <br> [when three operands are set] |
| DXOR | Performs an XOR operation on the two sets of 32-bit binary data specified. (Two operands) | $\bigcirc$ | $\bigcirc$ | Page 302 |
| DXORP |  | $\bigcirc$ | $\bigcirc$ | DXOR(P) <br> [when two operands are set] |
| DXOR | Performs an XOR operation on the two sets of 32-bit binary data specified. (Three operands) | $\bigcirc$ | $\bigcirc$ | Page 303 |
| DXORP |  | $\bigcirc$ | $\bigcirc$ | DXOR(P) <br> [when three operands are set] |

Performing an XOR operation on 16-bit block data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| BKXOR | Performs an XOR operation on the two 16-bit binary data blocks specified. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 305 \\ & \text { BKXOR(P) } \end{aligned}$ |
| BKXORP |  | $\bigcirc$ | $\bigcirc$ |  |

Performing an XNOR operation on 16-bit/32-bit data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| WXNR | Performs an XNOR operation on the two sets of 16-bit binary data specified. (Two operands) | $\bigcirc$ | $\bigcirc$ | Page 307 |
| WXNRP |  | $\bigcirc$ | $\bigcirc$ | WXNR(P) <br> [when two operands are set] |
| WXNR | Performs an XNOR operation on the two sets of 16-bit binary data specified. (Three operands) | $\bigcirc$ | $\bigcirc$ | Page 308 |
| WXNRP |  | $\bigcirc$ | $\bigcirc$ | WXNR(P) <br> [when three operands are set] |
| DXNR | Performs an XNOR operation on the two sets of 32-bit binary data specified. (Two operands) | $\bigcirc$ | $\bigcirc$ | Page 310 |
| DXNRP |  | $\bigcirc$ | $\bigcirc$ | DXNR(P) <br> [when two operands are set] |
| DXNR | Performs an XNOR operation on the two sets of 32-bit binary data specified. (Three operands) | $\bigcirc$ | $\bigcirc$ | Page 311 |
| DXNRP |  | $\bigcirc$ | $\bigcirc$ | DXNR(P) <br> [when three operands are set] |

Performing an XNOR operation on 16-bit block data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| BKXNR | Performs an XNOR operation on the two 16-bit binary data blocks specified. | $\bigcirc$ | $\bigcirc$ | Page 313 <br> BKXNR(P) |
| BKXNRP |  | $\bigcirc$ | $\bigcirc$ |  |

## Bit processing instructions

Setting/resetting a bit in the word device

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| BSET | Sets the 'n'th bit in the specified word device to 1 . <br> (d) | $\bigcirc$ | $\bigcirc$ | Page 315 |
| BSETP |  | $\bigcirc$ | $\bigcirc$ | BSET(P) |
| BRST | Resets the 'n'th bit in the specified word device to 0 . <br> (d) | $\bigcirc$ | $\bigcirc$ | Page 317 |
| BRSTP |  | $\bigcirc$ | $\bigcirc$ | BRST(P) |

Performing a bit test

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| TEST | Extracts the 'n'th bit in the specified word device. <br> (s1) | $\bigcirc$ | $\bigcirc$ | Page 318 |
| TESTP |  | $\bigcirc$ | $\bigcirc$ | TEST(P) |
| DTEST | Extracts the 'n'th bit in the specified double-word device. <br> (s1) | $\bigcirc$ | $\bigcirc$ | Page 320 |
| DTESTP |  | $\bigcirc$ | $\bigcirc$ | DTEST(P) |

Batch-resetting bit devices

| Instruction symbol | Processing details |  |  |  |  | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  |  | (1) | (2) |  |
| BKRST | Resets the ( n ) points of bit devices starting from the bit device specified. |  |  |  |  | $\bigcirc$ | $\bigcirc$ | Page 322 BKRST(P) |
| BKRSTP | (d) | $\begin{gathered} \hline \text { ON } \\ \hline \text { OFF } \\ \hline \text { ON } \\ \hline \text { ON } \end{gathered}$ |  | OFF OFF <br> OFF <br> OFF |  | $\bigcirc$ | $\bigcirc$ |  |

## Data conversion instructions

Converting binary data to BCD 4-digit/8-digit data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| BCD | Converts the specified 16-bit binary data to BCD 4-digit data. | $\bigcirc$ | $\bigcirc$ | Page 339 |
| BCDP |  | $\bigcirc$ | $\bigcirc$ | BCD (P) |
| DBCD | Converts the specified 32-bit binary data to BCD 8-digit data. | $\bigcirc$ | $\bigcirc$ | $\begin{align*} & \text { Page } 341 \\ & \text { DBCD(P) } \tag{d} \end{align*}$ |
| DBCDP |  | $\bigcirc$ | $\bigcirc$ |  |

Converting BCD 4-digit/8-digit data to 16-bit/32-bit binary data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| BIN | Converts the specified BCD 4-digit data to 16-bit binary data. | $\bigcirc$ | $\bigcirc$ | Page 343 |
| BINP |  | $\bigcirc$ | $\bigcirc$ | BIN(P) |
| DBIN | Converts the specified BCD 8-digit data to 32-bit binary data. | $\bigcirc$ | $\bigcirc$ | Page 345 DBIN(P) |
| DBINP |  | $\bigcirc$ | $\bigcirc$ |  |

Converting single-precision real number to 16-bit/32-bit signed binary data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| FLT2INT | Converts the specified single-precision real number to 16-bit signed binary data.$\xrightarrow[\text { (s) }+1,(\mathrm{~s})]{\text { Converting to binary }} \text { (d) }$ | $\bigcirc$ | $\bigcirc$ | Page 347 <br> FLT2INT(P) |
| FLT2INTP |  | $\bigcirc$ | $\bigcirc$ |  |
| FLT2DINT | Converts the specified single-precision real number to 32-bit signed binary data.$\frac{(\mathrm{s})+1,(\mathrm{~s})}{\text { Converting to binary }}(\mathrm{d})+1,(\mathrm{~d})$ | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 351 \\ & \text { FLT2DINT(P) } \end{aligned}$ |
| FLT2DINTP |  | $\bigcirc$ | $\bigcirc$ |  |

Converting single-precision real number to 16-bit/32-bit unsigned binary data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| FLT2UINT | Converts the specified single-precision real number to 16-bit unsigned binary data.$\xrightarrow[4]{(\mathrm{s})+1,(\mathrm{~s})} \xrightarrow[\text { Real number (0 to } 65535)]{\text { Converting to binary }}(\mathrm{d})$ | $\bigcirc$ | $\bigcirc$ | Page 349 <br> FLT2UINT(P) |
| FLT2UINTP |  | $\bigcirc$ | $\bigcirc$ |  |
| FLT2UDINT | Converts the specified single-precision real number to 32-bit unsigned binary data.$\xrightarrow[L]{(\mathrm{s})+1,(\mathrm{~s})} \xrightarrow[\text { Real number ( } 0 \text { to } 4294967295 \text { ) }]{\text { Converting to binary }}(\mathrm{d})+1,(\mathrm{~d})$ | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 353 \\ & \text { FLT2UDINT(P) } \end{aligned}$ |
| FLT2UDINTP |  | $\bigcirc$ | $\bigcirc$ |  |

Converting double-precision real number to 16-bit/32-bit signed binary data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| DBL2INT | Converts the specified double-precision real number to 16 -bit signed binary data.$\xrightarrow[\text { (s) }+3,(\mathrm{~s})+2,(\mathrm{~s})+1,(\mathrm{~s})]{\text { Real number ( }-32768 \text { to } 32767 \text { ) }}$ | $\bigcirc$ | $\bigcirc$ | Page 355 DBL2INT(P) |
| DBL2INTP |  | $\bigcirc$ | $\bigcirc$ |  |
| DBL2DINT | Converts the specified double-precision real number to 32-bit signed binary data.$\xrightarrow[4]{(\mathrm{s})+3,(\mathrm{~s})+2,(\mathrm{~s})+1,(\mathrm{~s})} \xrightarrow{\text { Converting to binary }}(\mathrm{d})+1,(\mathrm{~d})$ | $\bigcirc$ | $\bigcirc$ | Page 359 DBL2DINT(P) |
| DBL2DINTP |  | $\bigcirc$ | $\bigcirc$ |  |

Converting double-precision real number to 16-bit/32-bit unsigned binary data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| DBL2UINT | Converts the specified double-precision real number to 16 -bit unsigned binary data.$\xrightarrow[\Delta]{(\mathrm{s})+3,(\mathrm{~s})+2,(\mathrm{~s})+1,(\mathrm{~s})} \xrightarrow{\text { Converting to binary }}(\mathrm{d})$ | $\bigcirc$ | $\bigcirc$ | Page 357 DBL2UINT(P) |
| DBL2UINTP |  | $\bigcirc$ | $\bigcirc$ |  |
| DBL2UDINT | Converts the specified double-precision real number to 32-bit unsigned binary data.$\xrightarrow[\text { Real number ( } 0 \text { to } 4294967295 \text { ) }]{(\mathrm{s})+3,(\mathrm{~s})+2,(\mathrm{~s})+1,(\mathrm{~s})} \xrightarrow{\text { Converting to binary }}(\mathrm{d})+1,(\mathrm{~d})$ | $\bigcirc$ | $\bigcirc$ | Page 361 DBL2UDINT(P) |
| DBL2UDINTP |  | $\bigcirc$ | $\bigcirc$ |  |

Converting 16 -bit signed binary data to 16 -bit/32-bit unsigned binary data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| INT2UINT | Converts the 16-bit signed binary data in the device specified by (s) to 16-bit unsigned binary data, and stores the converted data in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | Page 363 <br> INT2UINT(P) |
| INT2UINTP |  | $\bigcirc$ | $\bigcirc$ |  |
| INT2UDINT | Converts the 16-bit signed binary data in the device specified by (s) to 32-bit unsigned binary data, and stores the converted data in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | Page 366 <br> INT2UDINT(P) |
| INT2UDINTP |  | $\bigcirc$ | $\bigcirc$ |  |

Converting 16-bit signed binary data to 32-bit signed binary data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| INT2DINT | Converts the 16 -bit signed binary data in the device specified by (s) to 32-bit signed binary data, and stores the converted data in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | Page 365 <br> INT2DINT(P) |
| INT2DINTP |  | $\bigcirc$ | $\bigcirc$ |  |

Converting 16-bit unsigned binary data to 16 -bit/32-bit signed binary data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| UINT2INT | Converts the 16-bit unsigned binary data in the device specified by (s) to 16-bit singed binary data, and stores the converted data in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | Page 367 <br> UINT2INT(P) |
| UINT2INTP |  | $\bigcirc$ | $\bigcirc$ |  |
| UINT2DINT | Converts the 16-bit unsigned binary data in the device specified by (s) to 32 -bit singed binary data, and stores the converted data in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | Page 369 UINT2DINT(P) |
| UINT2DINTP |  | $\bigcirc$ | $\bigcirc$ |  |

Converting 16-bit unsigned binary data to 32-bit unsigned binary data

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| UINT2UDINT | Converts the 16-bit unsigned binary data in the device specified by (s) to 32-bit unsigned binary | $\bigcirc$ | $\bigcirc$ | Page 370 |
|  | data, and stores the converted data in the device specified by (d). | $\bigcirc$ | UINT2UDINT(P |  |

Converting 32-bit signed binary data to 16-bit signed binary data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| DINT2INT | Converts the 32 -bit signed binary data in the device specified by (s) to 16-bit signed binary data, and stores the converted data in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | Page 371 DINT2INT(P) |
| DINT2INTP |  | $\bigcirc$ | $\bigcirc$ |  |

Converting 32-bit signed binary data to 16-bit/32-bit unsigned binary data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| DINT2UINT | Converts the 32-bit signed binary data in the device specified by (s) to 16-bit unsigned binary data, and stores the converted data in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | Page 373 <br> DINT2UINT(P) |
| DINT2UINTP |  | $\bigcirc$ | $\bigcirc$ |  |
| DINT2UDINT | Converts the 32-bit signed binary data in the device specified by (s) to 32-bit unsigned binary data, and stores the converted data in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | Page 375 DINT2UDINT(P ) |
| DINT2UDINTP |  | $\bigcirc$ | $\bigcirc$ |  |

Converting 32-bit unsigned binary data to 16-bit/32-bit signed binary data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| UDINT2INT | Converts the 32-bit unsigned binary data in the device specified by (s) to 16-bit singed binary data, and stores the converted data in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | Page 377 <br> UDINT2INT(P) |
| UDINT2INTP |  | $\bigcirc$ | $\bigcirc$ |  |
| UDINT2DINT | Converts the 32-bit unsigned binary data in the device specified by (s) to 32-bit singed binary data, and stores the converted data in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | Page 381 UDINT2DINT(P ) |
| UDINT2DINTP |  | $\bigcirc$ | $\bigcirc$ |  |

Converting 32-bit unsigned binary data to 16-bit unsigned binary data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| UDINT2UINT | Converts the 32-bit unsigned binary data in the device specified by (s) to 16-bit unsigned binary data, and stores the converted data in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | Page 379 UDINT2UINT(P ) |
| UDINT2UINTP |  | $\bigcirc$ | $\bigcirc$ |  |

Converting 16-bit/32-bit binary data to 16 -bit/32-bit binary Gray code data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| GRY | Converts the specified 16-bit binary data to 16 -bit binary Gray code data. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 383 \\ & \operatorname{GRY}(\mathrm{P})\left(\_\mathrm{U}\right) \end{aligned}$ |
| GRYP |  | $\bigcirc$ | $\bigcirc$ |  |
| GRY_U | Converts the specified 16 -bit binary data to 16 -bit binary Gray code data.$\qquad$ BIN (0 to 65535) | $\bigcirc$ | $\bigcirc$ |  |
| GRYP_U |  | $\bigcirc$ | $\bigcirc$ |  |
| DGRY | Converts the specified 32-bit binary data to 32-bit binary Gray code data. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 385 \\ & \text { DGRY(P)(_U) } \end{aligned}$ |
| DGRYP |  | $\bigcirc$ | $\bigcirc$ |  |
| DGRY_U | Converts the specified 32-bit binary data to 32-bit binary Gray code data.$\xrightarrow[4]{(\mathrm{s})+1,(\mathrm{~s})} \xrightarrow{\text { Converting to Gray code }} \text { BIN (0 to 4294967295) }(\mathrm{d})+1 \text {, (d) }$ | $\bigcirc$ | $\bigcirc$ |  |
| DGRYP_U |  | $\bigcirc$ | $\bigcirc$ |  |

Converting 16-bit/32-bit binary Gray code data to 16-bit/32-bit binary data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| GBIN | Converts the specified 16 -bit binary Gray code data to 16 -bit binary data.$\qquad$ Gray code (-32768 to 32767) | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 387 \\ & \text { GBIN }(P)\left(\_U\right) \end{aligned}$ |
| GBINP |  | $\bigcirc$ | $\bigcirc$ |  |
| GBIN_U | Converts the specified 16-bit binary Gray code data to 16-bit binary data. <br> Gray code (0 to 65535) | $\bigcirc$ | $\bigcirc$ |  |
| GBINP_U |  | $\bigcirc$ | $\bigcirc$ |  |
| DGBIN | Converts the specified 32-bit binary Gray code data to 32-bit binary data. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 389 \\ & \text { DGBIN(P)(_U) } \end{aligned}$ |
| DGBINP |  | $\bigcirc$ | $\bigcirc$ |  |
| DGBIN_U | Converts the specified 32-bit binary Gray code data to 32-bit binary data. | $\bigcirc$ | $\bigcirc$ |  |
| DGBINP_U |  | $\bigcirc$ | $\bigcirc$ |  |

Converting 16-bit binary data block to BCD 4-digit data block

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| BKBCD | Batch-converts the ( n ) points of binary data in the device starting from the one specified by ( s ) to BCD data, and stores the converted data in the device specified by (d) and later. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 391 \\ & \text { BKBCD(P) } \end{aligned}$ |
| BKBCDP |  | $\bigcirc$ | $\bigcirc$ |  |

Converting BCD 4-digit data block to 16-bit binary data block

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| BKBIN | Batch-converts the $(\mathrm{n})$ points of BCD data in the device starting from the one specified by ( s ) to binary data, and stores the converted data in the device specified by (d) and later. | $\bigcirc$ | $\bigcirc$ | Page 393 <br> BKBIN(P) |
| BKBINP |  | $\bigcirc$ | $\bigcirc$ |  |

Converting decimal ASCII data to 16 -bit/32-bit binary data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| DABIN | Converts the 5 -digit decimal ASCII value in the device specified by (s) to 1-word binary data, and stores the converted data in the word device number specified by (d). | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 395 \\ & \text { DABIN(P)(_U) } \end{aligned}$ |
| DABINP |  | $\bigcirc$ | $\bigcirc$ |  |
| DABIN_U |  | $\bigcirc$ | $\bigcirc$ |  |
| DABINP_U |  | $\bigcirc$ | $\bigcirc$ |  |
| DDABIN | Converts the 10-digit decimal ASCII value in the device specified by (s) to 2-word binary data, and stores the converted data in the word device number specified by (d). | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 397 \\ & \text { DDABIN(P)(_U } \\ & ) \end{aligned}$ |
| DDABINP |  | $\bigcirc$ | $\bigcirc$ |  |
| DDABIN_U |  | $\bigcirc$ | $\bigcirc$ |  |
| DDABINP_U |  | $\bigcirc$ | $\bigcirc$ |  |

Converting hexadecimal ASCII data to 16-bit/32-bit binary data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| HABIN | Converts the 4-digit hexadecimal ASCII value in the device specified by (s) to 1-word binary data, and stores the converted data in the word device number specified by (d). | $\bigcirc$ | $\bigcirc$ | Page 399 HABIN(P) |
| HABINP |  | $\bigcirc$ | $\bigcirc$ |  |
| DHABIN | Converts the 8-digit hexadecimal ASCII value in the device specified by (s) to 2-word binary data, and stores the converted data in the word device number specified by (d). | $\bigcirc$ | $\bigcirc$ | Page 401 <br> DHABIN(P) |
| DHABINP |  | $\bigcirc$ | $\bigcirc$ |  |

Converting decimal ASCII data to BCD 4-digit/8-digit data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| DABCD | Converts the 4-digit decimal ASCII value in the device specified by (s) to 1-word BCD data, and stores the converted data in the word device number specified by (d). | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 403 \\ & \operatorname{DABCD}(\mathrm{P}) \end{aligned}$ |
| DABCDP |  | $\bigcirc$ | $\bigcirc$ |  |
| DDABCD | Converts the 8-digit decimal ASCII value in the device specified by (s) to 2-word BCD data, and stores the converted data in the word device number specified by (d). | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 405 \\ & \text { DDABCD(P) } \end{aligned}$ |
| DDABCDP |  | $\bigcirc$ | $\bigcirc$ |  |

Converting decimal string data to 16-bit/32-bit binary data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| VAL | Converts a character string including the decimal point in the device specified by (s) to 1-word binary data and the number of decimal positions, and stores the converted data in the device areas specified by (d1) and (d2). | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 407 \\ & \text { VAL(P)(_U) } \end{aligned}$ |
| VALP |  | $\bigcirc$ | $\bigcirc$ |  |
| VAL_U |  | $\bigcirc$ | $\bigcirc$ |  |
| VALP_U |  | $\bigcirc$ | $\bigcirc$ |  |
| DVAL | Converts a character string including the decimal point in the device specified by (s) to 2-word binary data and the number of decimal positions, and stores the converted data in the device areas specified by (d1) and (d2). | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 410 \\ & \text { DVAL(P)(_U) } \end{aligned}$ |
| DVALP |  | $\bigcirc$ | $\bigcirc$ |  |
| DVAL_U |  | $\bigcirc$ | $\bigcirc$ |  |
| DVALP_U |  | $\bigcirc$ | $\bigcirc$ |  |

Converting hexadecimal ASCII data to hexadecimal binary data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| ASC2INT | Converts the hexadecimal ASCII data in the word device specified by (s) and later to binary data by the number of characters specified by ( n ), and stores the converted data in the device number specified by (d) and later. | $\bigcirc$ | $\bigcirc$ | Page 413 <br> ASC2INT(P) |
| ASC2INTP |  | $\bigcirc$ | $\bigcirc$ |  |

Converting single-precision real number to BCD format data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| EMOD | Converts the 32-bit floating-point data in the device specified by ( $s 1$ ) to BCD of the number of decimal positions specified by (s2), and stores the converted data in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | Page 415 <br> EMOD(P) |
| EMODP |  | $\bigcirc$ | $\bigcirc$ |  |

Two's complement of 16-bit/32-bit binary data (sign inversion)

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| NEG | Inverts the sign of 16-bit binary device. | $\bigcirc$ | $\bigcirc$ | Page 417 |
| NEGP |  | $\bigcirc$ | $\bigcirc$ | NEG(P) |
| DNEG | Inverts the sign of 32-bit binary device.$\qquad$ (d) +1 , (d)$\qquad$ Binary data | $\bigcirc$ | $\bigcirc$ | Page 418 <br> DNEG(P) |
| DNEGP |  | $\bigcirc$ | $\bigcirc$ |  |

Decoding 8-bit data to 256-bit data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| DECO | Decodes the lower ( n ) bits of the specified device. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 419 \\ & \text { DECO(P) } \end{aligned}$ |
| DECOP |  | $\bigcirc$ | $\bigcirc$ |  |

Encoding 256-bit data to 8-bit data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| ENCO | Encodes the bit data of ' $n$ 'th power of 2. | $\bigcirc$ | $\bigcirc$ | Page 421 <br> ENCO(P) |
| ENCOP |  | $\bigcirc$ | $\bigcirc$ |  |

Decoding data to seven-segment display data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| SEG | Decodes the data consisting of 0 to F specified by the lower 4 bits of the device to seven-segment display data. $\text { b3 } \cdots \text { b0 }$ <br> (s) $\square$ (d) 7SEG $\square$ | $\bigcirc$ | $\bigcirc$ | Page 423 |
| SEGP |  | $\bigcirc$ | $\bigcirc$ | SEG(P) |

## Separating data in units of 4 bits

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| DIS | Separates the 16-bit in the device specified by (s) in units of 4 bits, and stores the separated data in the ( n ) points of 4 low-order bits in the device starting from the one specified by ( d ). ( $\mathrm{n}<4$ ) | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 426 \\ & \text { DIS(P) } \end{aligned}$ |
| DISP |  | $\bigcirc$ | $\bigcirc$ |  |

Combining data in units of 4 bits

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| UNI | Adds the ( n ) points of 4 low-order bit data in the device starting from the one specified by ( s ), and stores the connected data in the device specified by (d). ( $\mathrm{n}<4$ ) | $\bigcirc$ | $\bigcirc$ | Page 428 <br> UNI(P) |
| UNIP |  | $\bigcirc$ | $\bigcirc$ |  |

## Separating/combining data in units of bits

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| NDIS | Separates the data in the device, specified by (s1) and later, to the bits in the device specified by (s2) and later, and stores the separated data in order in the device starting from the one specified by (d). | $\bigcirc$ | $\bigcirc$ | Page 430 <br> NDIS(P) |
| NDISP |  | $\bigcirc$ | $\bigcirc$ |  |
| NUNI | Connects the data in the device specified by (s1) and later, in units of bits in the device specified by (s2) and later, and stores the connected data in order in the device starting from the one specified by (d). | $\bigcirc$ | $\bigcirc$ | Page 432 <br> NUNI(P) |
| NUNIP |  | $\bigcirc$ | $\bigcirc$ |  |

## Separating/combining data in units of bytes

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| WTOB | Converts the ( n ) points of 16 bit data in the device specified by (s) in units of 8 bits, and stores the converted data in order in the device starting from the one specified by (d). | $\bigcirc$ | $\bigcirc$ | Page 434 <br> WTOB(P) |
| WTOBP |  | $\bigcirc$ | $\bigcirc$ |  |
| BTOW | Connects 8 low-order bits of the ( n ) points of 16 bit data in the device specified by (s) to 16 bits, and stores the connected data in order in the device starting from the one specified by (d). | $\bigcirc$ | $\bigcirc$ | Page 436 <br> BTOW(P) |
| BTOWP |  | $\bigcirc$ | $\bigcirc$ |  |

## Shift instructions

■Shifting 16 -bit binary data to the right/left by $\mathbf{n}$ bit(s)

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| SFR | Shifts the 16 -bit binary data in the specified device to the right. | $\bigcirc$ | $\bigcirc$ | Page 323 |
| SFRP |  | $\bigcirc$ | $\bigcirc$ | SFR(P) |
| SFL | Shifts the 16-bit binary data in the specified device to the left. | $\bigcirc$ | $\bigcirc$ | Page 325 |
| SFLP |  | $\bigcirc$ | $\bigcirc$ | SFL(P) |

Shifting n-bit data to the right/left by one bit


## ■Shifting n-word data to the right/left by one word



## Shifting n-bit data to the right/left by $\mathbf{n}$ bit(s)



Shifting n-word data to the right/left by n word(s)


## Data transfer instructions

ITransferring 16-bit/32-bit binary data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| MOV | Transfers the 16-bit binary data in the device specified. <br> (s) $\qquad$ (d) | $\bigcirc$ | $\bigcirc$ | Page 438 <br> MOV(P) |
| MOVP |  | $\bigcirc$ | $\bigcirc$ |  |
| DMOV | Transfers the 32-bit binary data in the device specified.$(\mathrm{s})+1,(\mathrm{~s}) \longrightarrow(\mathrm{d})+1,(\mathrm{~d})$ | $\bigcirc$ | $\bigcirc$ | Page 439 <br> DMOV(P) |
| DMOVP |  | $\bigcirc$ | $\bigcirc$ |  |

■lnverting and transferring 16-bit/32-bit binary data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| CML | Inverts the specified 16-bit binary data bit by bit, and transfers the inverted data. <br> $\overline{(s)}$ $\qquad$ (d) | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 440 \\ & \text { CML(P) } \end{aligned}$ |
| CMLP |  | $\bigcirc$ | $\bigcirc$ |  |
| DCML | Inverts the specified 32-bit binary data bit by bit, and transfers the inverted data.$\overline{(\mathrm{s})+1,(\mathrm{~s})}$$\qquad$ (d) +1 , (d) | $\bigcirc$ | $\bigcirc$ | Page 441 <br> DCML(P) |
| DCMLP |  | $\bigcirc$ | $\bigcirc$ |  |

## —lnverting and transferring 1-bit data

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| CMLB | Inverts the bit data in the device specified by (s), and stores the inverted data in the device <br> specified by (d). | $\bigcirc$ | $\bigcirc$ | Page 442 |
|  | CMLBP |  | $\bigcirc$ | CMLB(P) |

Transferring 16-bit binary data block (16 bits)

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| BMOV | Batch-transfers the ( $n$ ) points of 16-bit binary data starting from the device specified.$(n)=1 \text { to } 65535$ | $\bigcirc$ | $\bigcirc$ | Page 443 <br> BMOV(P) |
| BMOVP |  | $\bigcirc$ | $\bigcirc$ |  |

Transferring 16-bit binary data block ( 32 bits)

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| BMOVL | Batch-transfers the $(\mathrm{n})$ points of 16-bit binary data starting from the device specified. <br> (s) <br> (d) $\square$ (n) $(n)=1 \text { to } 4294967295$ | $\bigcirc$ | $\bigcirc$ | Page 445 <br> BMOVL(P) |
| BMOVLP |  | $\bigcirc$ | $\bigcirc$ |  |

Transferring the same 16-bit binary data block (16 bits)

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| FMOV | Transfers 16 -bit binary data to the ( n ) points starting from the device specified.$(n)=1 \text { to } 65535$ | $\bigcirc$ | $\bigcirc$ | Page 447 |
| FMOVP |  | $\bigcirc$ | $\bigcirc$ | FMOV(P) |

Transferring the same 16-bit binary data block (32 bits)

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| FMOVL | Transfers 16-bit binary data to the $(\mathrm{n})$ points starting from the device specified.$(n)=1 \text { to } 4294967295$ | $\bigcirc$ | $\bigcirc$ | Page 449 <br> FMOVL(P) |
| FMOVLP |  | $\bigcirc$ | $\bigcirc$ |  |

Transferring the same 32-bit binary data block (16 bits)

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| DFMOV | Transfers 32-bit binary data to the ( n ) points starting from the device specified. <br> (n) $=1$ to 65535 | $\bigcirc$ | $\bigcirc$ | $\text { Page } 451$DFMOV(P) |
| DFMOVP |  | $\bigcirc$ | $\bigcirc$ |  |

Transferring the same 32-bit binary data block (32 bits)

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| DFMOVL | Transfers 32-bit binary data to the ( n ) points starting from the device specified. <br> $(n)=1$ to 4294967295 | $\bigcirc$ | $\bigcirc$ | Page 453 DFMOVL(P) |
| DFMOVLP |  | $\bigcirc$ | $\bigcirc$ |  |

Exchanging 16-bit/32-bit binary data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| XCH | Exchanges the 16-bit binary data specified. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 455 \\ & \mathrm{XCH}(\mathrm{P}) \end{aligned}$ |
| XCHP |  | $\bigcirc$ | $\bigcirc$ |  |
| DXCH | Exchanges the 32-bit binary data specified.$(\mathrm{d} 1)+1,(\mathrm{~d} 1) \longleftrightarrow(\mathrm{d} 2)+1,(\mathrm{~d} 2)$ | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 456 \\ & \text { DXCH(P) } \end{aligned}$ |
| DXCHP |  | $\bigcirc$ | $\bigcirc$ |  |

Exchanging 16-bit binary block data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| BXCH | Exchanges the ( n ) points of 16 -bit binary data starting from the devices specified. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 457 \\ & \mathrm{BXCH}(\mathrm{P}) \end{aligned}$ |
| BXCHP |  | $\bigcirc$ | $\bigcirc$ |  |

Exchanging the upper and lower bytes of 16-bit data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| SWAP | Exchanges upper and lower 8-bit data in the specified device. <br>  | $\bigcirc$ | $\bigcirc$ | Page 459 <br> SWAP(P) |
| SWAPP |  | $\bigcirc$ | $\bigcirc$ |  |
|  | (d)b15 b8 b7 |  |  |  |

Transferring 1-bit data

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| MOVB | Stores the bit data in the device specified by (s) in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | Page 460 |
| MOVBP |  | $O$ | $\bigcirc$ | MOVB(P) |

## Transferring n-bit data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| BLKMOVB | Batch-transfers the ( $n$ ) points of bit data in the device starting from the one specified by (s) to the $(\mathrm{n})$ points of bit data in the device starting from the one specified by (d). | $\bigcirc$ | $\bigcirc$ | Page 461 <br> BLKMOVB(P) |
| BLKMOVBP |  | $\bigcirc$ | $\bigcirc$ |  |

### 2.3 Application Instructions

## Rotation instructions

Rotating 16-bit binary data to the right

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| ROR | Rotates the 16-bit binary data to the right by (n) bit(s) (not including the carry flag). <br> Right rotation by ( n ) bits | $\bigcirc$ | $\bigcirc$ | Page 463 <br> ROR(P), <br> RCR(P) |
| RORP |  | $\bigcirc$ | $\bigcirc$ |  |
| RCR | Rotates the 16-bit binary data to the right by ( n ) bit( s ) (including the carry flag). <br> Right rotation by ( n ) bits | $\bigcirc$ | $\bigcirc$ |  |
| RCRP |  | $\bigcirc$ | $\bigcirc$ |  |

Rotating 16-bit binary data to the left

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| ROL | Rotates the 16-bit binary data to the left by ( n ) bit( s ) (not including the carry flag). | $\bigcirc$ | $\bigcirc$ | Page 466 <br> ROL(P), <br> RCL(P) |
| ROLP |  | $\bigcirc$ | $\bigcirc$ |  |
| RCL | Rotates the 16-bit binary data to the left by ( $n$ ) bit(s) (including the carry flag). | $\bigcirc$ | $\bigcirc$ |  |
| RCLP |  | $\bigcirc$ | $\bigcirc$ |  |

Rotating 32-bit binary data to the right


Rotating 32-bit binary data to the left

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| DROL | Rotates the 32-bit binary data to the left by ( $n$ ) bit(s) (not including the carry flag). | $\bigcirc$ | $\bigcirc$ | Page 471 |
| DROLP |  | $\bigcirc$ | $\bigcirc$ | DROL(P), DRCL(P) |
| DRCL | Rotates the 32-bit binary data to the left by (n) bit(s) (including the carry flag). | $\bigcirc$ | $\bigcirc$ |  |
| DRCLP |  | $\bigcirc$ | $\bigcirc$ |  |

## Program branch instructions

Pointer branch

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| CJ | Executes the program specified by the pointer number within the same program file. | $\bigcirc$ | $\bigcirc$ | Page 473 CJ, SCJ, JMP |
| SCJ | Executes the program specified by the pointer number within the same program file starting with the next scan. | $\bigcirc$ | $\bigcirc$ |  |
| JMP | Unconditionally executes the program specified by the pointer number within the same program file. | $\bigcirc$ | $\bigcirc$ |  |

Jumping to END

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| GOEND | Invokes a jump to the FEND or END instruction within the same program file. | $\bigcirc$ | O | Page 476 <br> GOEND |

## Program execution control instructions

## Disabling/enabling interrupt programs

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| DI | Disables the execution of interrupt programs. | $\bigcirc$ | O | Page 477 DI, |
| EI | Clears the interrupt disabled state. | O | O | El |

Disabling the interrupt program with specified priority or lower

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| DI | Disables the execution of the interrupt program with a priority specified by (s) or lower until the EI <br> instruction is executed. | $\bigcirc$ | Page 479 DI |  |

Interrupt program mask

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| IMASK | Enables or disables the execution of the interrupt program with the specified interrupt pointer <br> number. | $\bigcirc$ | $\bigcirc$ | Page 483 <br> IMASK |

Disabling/enabling the specified interrupt pointer

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| SIMASK | Enables or disables the execution of the interrupt program with the specified interrupt pointer number. | $\bigcirc$ | $\bigcirc$ | Page 485 <br> SIMASK |

Returning from the interrupt program

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| IRET | Indicates the end of the processing of an interrupt program. | $\bigcirc$ | $\bigcirc$ | Page 487 IRET |

Resetting the watchdog timer

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| WDT | Resets the watchdog timer. | $\bigcirc$ | $\bigcirc$ | Page 488 <br> WDT(P) |
| WDTP |  | $\bigcirc$ | $\bigcirc$ |  |

## Structure creation instructions

Performing the FOR to NEXT instruction loop

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| FOR | Executes the processing between FOR to NEXT ( n ) times. | $\bigcirc$ | $\bigcirc$ | Page 489 FOR, NEXT |
| NEXT |  | $\bigcirc$ | $\bigcirc$ |  |

## Forcibly terminating the FOR to NEXT instruction loop

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| BREAK | Forcibly terminates the loop processing between the FOR and NEXT instructions, and passes the control to the specified pointer. | $\bigcirc$ | $\bigcirc$ | Page 491 |
| BREAKP |  | $\bigcirc$ | $\bigcirc$ | BREAK(P) |

Calling a subroutine program

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| CALL | Executes a subroutine program specified by ( $P$ ) when the input condition is met. | $\bigcirc$ | $\bigcirc$ | Page 493 |
| CALLP | (For ( $s$ 1) to (s5), specify the arguments to be passed to the subroutine program.) | $\bigcirc$ | $\bigcirc$ | CALL(P) |

Returning from the subroutine program called

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| RET | Indicates the end of a subroutine program. | $\bigcirc$ | $\bigcirc$ | Page 497 RET |

Calling a subroutine program and turning the output off

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| FCALL | Performs non-execution processing of the subroutine program specified by ( $P$ ) when the input <br> conditions are not met. <br> (For (s1) to (s5), specify the arguments to be passed to the subroutine program.) | $\bigcirc$ | $\bigcirc$ | Page 498 |
|  | FCALLP | $\bigcirc$ | $\bigcirc$ | FCALL(P) |

Calling a subroutine program in the specified program file

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| ECALL | Executes the subroutine program specified by $(\mathrm{P})$ of the specified program when the input conditions are met. <br> (For (s1) to (s5), specify the arguments to be passed to the subroutine program.) | $\bigcirc$ | $\bigcirc$ | Page 502 |
| ECALLP |  | $\bigcirc$ | $\bigcirc$ | ECALL(P) |

Calling a subroutine program in the specified program file and turning the output off

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| EFCALL | Performs non-execution processing of the subroutine program specified by $(\mathrm{P})$ of the specified program when the input conditions are not met. (For (s1) to (s5), specify the arguments to be passed to the subroutine program.) | $\bigcirc$ | $\bigcirc$ | Page 507 <br> EFCALL(P) |
| EFCALLP |  | $\bigcirc$ | $\bigcirc$ |  |

Calling a subroutine program

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| XCALL | Executes a subroutine program specified by $(\mathrm{P})$ when the input condition is met. <br> Perform non-execution processing of the subroutine program specified by $(\mathrm{P})$ when the input conditions are not met. <br> (For (s1) to (s5), specify the arguments to be passed to the subroutine program.) | $\bigcirc$ | $\bigcirc$ | Page 511 XCALL |

## Data table operation instructions

Reading the oldest data from the data table

| Instruction symbol | Processing details |  | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | (1) | (2) |  |
| FIFR | Stores the data first stored in the table in the specified device. |  | $\bigcirc$ | $\bigcirc$ | Page 516 <br> FIFR(P) |
| FIFRP |  |  | $\bigcirc$ | $\bigcirc$ |  |

Reading the newest data from the data table

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| FPOP | Stores the data last stored in the table in the specified device. | $\bigcirc$ | $\bigcirc$ | Page 518 |
| FPOPP |  | $\bigcirc$ | $\bigcirc$ | FPOP(P) |

Writing data to the data table

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| FIFW | Stores 16-bit binary data to the specified data table. <br> (s) $\square$ <br> (d) $\square$ (d) (Number of data) +1 | $\bigcirc$ | $\bigcirc$ | Page 520 |
| FIFWP |  | $\bigcirc$ | $\bigcirc$ | FIFW(P) |

Inserting/deleting data to/from the data table


## Reading/writing data instructions

■Reading 16-bit data from the data memory

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| S.DEVLD | Reads data from the device data storage file in data memory. | $\bigcirc$ | $\bigcirc$ | Page 527 <br> S(P).DEVLD |
| SP.DEVLD |  | $\bigcirc$ | $\bigcirc$ |  |

■Writing 16-bit data to the data memory

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| SP.DEVST | Writes the specified number of points of data to the device data storage file in data memory. | $\bigcirc$ | $\bigcirc$ | Page 529 <br> SP.DEVST |

## Reading 16-bit data from the specified file

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| SP.FREAD | Reads data from the specified file. | O | ○ | Page 531 <br> SP.FREAD |

Writing 16-bit data from the specified file

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| SP.FWRITE | Writes data to the specified file. | $\bigcirc$ | ○ | Page 540 <br> SP.FWRITE |

## Debugging and failure diagnostic instruction

-Displaying the error or resetting the annunciator

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| LEDR | Resets the annunciator. | $\bigcirc$ | O | Page 547 <br> LEDR |

## String processing instructions

■Comparing string data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| LD\$=, AND\$=, OR\$= | Compares the character string specified by (s1) with the character string specified by (s2) (character by character). | $\bigcirc$ | $\bigcirc$ | Page 548 LD\$D, AND\$D, OR\$D |
| $\begin{aligned} & \text { LDS<>, AND\$<>, } \\ & \text { ORS<> } \end{aligned}$ |  | $\bigcirc$ | $\bigcirc$ |  |
| LD\$>, AND\$>, OR\$> |  | $\bigcirc$ | $\bigcirc$ |  |
| $\begin{aligned} & \text { LD\$<=, AND } \$<=, \\ & \text { ORS<= } \end{aligned}$ |  | $\bigcirc$ | $\bigcirc$ |  |
| LD\$<, AND\$<, OR\$< |  | $\bigcirc$ | $\bigcirc$ |  |
| $\begin{aligned} & \text { LD\$>=, AND\$>=, } \\ & \text { OR\$>= } \end{aligned}$ |  | $\bigcirc$ | $\bigcirc$ |  |

Concatenating string data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| \$+ | Connects the character strings in the device specified by (s) to those in the device specified by (d), and stores the connected data in the device specified by (d) and later. | $\bigcirc$ | $\bigcirc$ | Page 551 \$+(P) [when two operands are set] |
| \$+P |  | $\bigcirc$ | $\bigcirc$ |  |
| \$+ | Connects the character strings in the device specified by (s2) to those in the device specified by ( s 1 ), and stores the connected data in the device specified by (d) and later. | $\bigcirc$ | $\bigcirc$ | Page 553 \$+(P) [when three operands are set] |
| \$+P |  | $\bigcirc$ | $\bigcirc$ |  |

## Transferring string data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| \$MOV | Transfers the character strings in the device specified by (s) to the device specified by (d) and later. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 555 \\ & \$ \mathrm{MOV}(\mathrm{P}) \end{aligned}$ |
| \$MOVP |  | $\bigcirc$ | $\bigcirc$ |  |
| \$MOV_WS | Transfers the Unicode character strings in the device specified by (s) to the device specified by (d) and later. | $\bigcirc$ | $\bigcirc$ | Page 557 <br> \$MOV(P)_WS |
| \$MOVP_WS |  | $\bigcirc$ | $\bigcirc$ |  |

Converting 16-bit/32-bit binary data to decimal ASCII

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| BINDA | Converts the 1-word binary data in the device specified by (s) to 5-digit decimal ASCII data, and stores the converted data in the word device specified by (d). | $\bigcirc$ | $\bigcirc$ | Page 559 <br> BINDA(P)(_U) |
| BINDAP |  | $\bigcirc$ | $\bigcirc$ |  |
| BINDA_U |  | $\bigcirc$ | $\bigcirc$ |  |
| BINDAP_U |  | $\bigcirc$ | $\bigcirc$ |  |
| DBINDA | Converts the 2-word binary data in the device specified by (s) to 10-digit decimal ASCII data, and stores the converted data in the word device number specified by (d) and later. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 561 \\ & \text { DBINDA(P)(UU } \\ & \text { ) } \end{aligned}$ |
| DBINDAP |  | $\bigcirc$ | $\bigcirc$ |  |
| DBINDA_U |  | $\bigcirc$ | $\bigcirc$ |  |
| DBINDAP_U |  | $\bigcirc$ | $\bigcirc$ |  |

Converting 16-bit/32-bit binary data to hexadecimal ASCII

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| BINHA | Converts the 1-word binary data in the device specified by (s) to 4-digit hexadecimal ASCII data, and stores the converted data in the word device number specified by (d) and later. | $\bigcirc$ | $\bigcirc$ | Page 563 <br> BINHA(P) |
| BINHAP |  | $\bigcirc$ | $\bigcirc$ |  |
| DBINHA | Converts the 2-word binary data in the device specified by (s) to 8-digit hexadecimal ASCII data, and stores the converted data in the word device number specified by (d) and later. | $\bigcirc$ | $\bigcirc$ | Page 565 <br> DBINHA(P) |
| DBINHAP |  | $\bigcirc$ | $\bigcirc$ |  |

## Converting 16-bit/32-bit binary data to string data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| STR | Converts the 1-word binary data in the device specified by (s2) to a decimal character string consisting of the total number digits and the number of digits in the decimal part in the device specified by ( s 1 ), and stores the converted data in the word device specified by (d). | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 567 \\ & \text { STR(P)(_U) } \end{aligned}$ |
| STRP |  | $\bigcirc$ | $\bigcirc$ |  |
| STR_U |  | $\bigcirc$ | $\bigcirc$ |  |
| STRP_U |  | $\bigcirc$ | $\bigcirc$ |  |
| DSTR | Converts the 2-word binary data in the device specified by (s2) to a decimal character string consisting of the total number digits and the number of digits in the decimal part in the device specified by ( s 1 ), and stores the converted data in the word device specified by (d). | $\bigcirc$ | $\bigcirc$ | Page 570 DSTR(P)(_U) |
| DSTRP |  | $\bigcirc$ | $\bigcirc$ |  |
| DSTR_U |  | $\bigcirc$ | $\bigcirc$ |  |
| DSTRP_U |  | $\bigcirc$ | $\bigcirc$ |  |

Converting BCD 4-digit/8-digit data to decimal ASCII code

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| BCDDA | Converts the 1-word BCD data in the device specified by (s) to 4-digit decimal ASCII data, and stores the converted data in the word device number specified by (d) and later. | $\bigcirc$ | $\bigcirc$ | Page 573 BCDDA(P) |
| BCDDAP |  | $\bigcirc$ | $\bigcirc$ |  |
| DBCDDA | Converts the 2-word BCD data in the device specified by (s) to 8-digit decimal ASCII data, and stores the converted data in the word device number specified by (d) and later. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 575 \\ & \text { DBCDDA(P) } \end{aligned}$ |
| DBCDDAP |  | $\bigcirc$ | $\bigcirc$ |  |

Converting single-precision real number to string data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| ESTR | Converts the single-precision real number in the device specified by ( s 1 ) to a character string, and stores the converted data in the word device specified by (d). | $\bigcirc$ | $\bigcirc$ | Page 577 <br> ESTR(P) |
| ESTRP |  | $\bigcirc$ | $\bigcirc$ |  |

Converting hexadecimal binary data to hexadecimal ASCII code

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| INT2ASC | Converts the 1-word binary data in the device number specified by (s) and later to hexadecimal ASCII, and stores the converted data by the number of characters in the device specified by $(\mathrm{n})$ in the word device number specified by (d) and later. | $\bigcirc$ | $\bigcirc$ | Page 581 <br> INT2ASC(P) |
| INT2ASCP |  | $\bigcirc$ | $\bigcirc$ |  |

IConverting Unicode character string to Shift JIS character string

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| WS2SJIS | Converts the Unicode character string in the device specified by (s) to the shift JIS character string, and stores the converted data in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | Page 583 WS2SJIS(P) |
| WS2SJISP |  | $\bigcirc$ | $\bigcirc$ |  |

Converting shift JIS character string to Unicode character string (without byte order mark)

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| SJIS2WS | Converts the shift JIS character string in the device specified by (s) to a Unicode character string, and stores the converted data in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | Page 585 SJIS2WS(P) |
| SJIS2WSP |  | $\bigcirc$ | $\bigcirc$ |  |

Converting shift JIS to Unicode (with byte order mark)

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| SJIS2WSB | Converts the shift JIS character string in the device specified by (s) to the Unicode character string, add a byte order mark to the head of the converted data, and stores it in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | Page 587 <br> SJIS2WSB(P) |
| SJIS2WSBP |  | $\bigcirc$ | $\bigcirc$ |  |

## Detecting a string length

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| LEN | Stores the length (the number of characters) of the character string data, which is stored in the <br> device specified by (s), in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | Page 589 |
|  | LENP | $\bigcirc$ | $\bigcirc$ | LEN(P) |

## Extracting string data from the right/left

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| RIGHT | Stores the $(\mathrm{n})$ characters from the last character of the character string, which is stored in the device specified by (s), in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | Page 591 <br> RIGHT(P) |
| RIGHTP |  | $\bigcirc$ | $\bigcirc$ |  |
| LEFT | Stores the ( n ) characters from the first character of the character string, which is stored in the device specified by (s), in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 593 \\ & \text { LEFT(P) } \end{aligned}$ |
| LEFTP |  | $\bigcirc$ | $\bigcirc$ |  |

Extracting/replacing the specified string data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| MIDR | Retrieves the character string in the device specified by (s1) by the number of specified characters from the location in the device specified by ( s 2 ), and stores the retrieved data in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 595 \\ & \text { MIDR(P) } \end{aligned}$ |
| MIDRP |  | $\bigcirc$ | $\bigcirc$ |  |
| MIDW | Retrieves the specified number of characters from the character string in the device specified by ( s 1 ), and stores the retrieved data at the location specified by (s2) in the character string stored in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | Page 597 <br> MIDW(P) |
| MIDWP |  | $\bigcirc$ | $\bigcirc$ |  |

## Searching string data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| INSTR | Searches the character string in the device specified by (s2), starting from the (s3)th character, for the character string in the device specified by ( s 1 ), and stores the matching location in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 600 \\ & \text { INSTR(P) } \end{aligned}$ |
| INSTRP |  | $\bigcirc$ | $\bigcirc$ |  |

## —lnserting string data

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| STRINS | Inserts the character string data in the device specified by (s1) to the (s2)th character (insertion | $\bigcirc$ | $\bigcirc$ | Page 602 |
|  | STRINS(P) |  |  |  |
| STRINSP | position) from the head of the character string data in the device specified by (d). | $\bigcirc$ |  |  |

Deleting string data

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| STRDEL | Deletes the $(n)$ characters starting from the position (deletion start position) specified by the (s)th | $\bigcirc$ | $\bigcirc$ | Page 604 |
|  | character from the head of the character string data in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | STRDEL(P) |

## Real number instructions

## Comparing single-precision real numbers

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| LDE=, ANDE=, ORE= | Performs a comparison operation of a single-precision real number. (Devices are used as a normally open contact.) | $\bigcirc$ | $\bigcirc$ | Page 606 LDED, ANDED, ORED |
| LDE<>, ANDE<>, ORE<> |  | $\bigcirc$ | $\bigcirc$ |  |
| LDE>, ANDE>, ORE> |  | $\bigcirc$ | $\bigcirc$ |  |
| $\begin{aligned} & \mathrm{LDE}<=, \mathrm{ANDE}<=\text {, } \\ & \mathrm{ORE}<= \end{aligned}$ |  | $\bigcirc$ | $\bigcirc$ |  |
| LDE<, ANDE<, ORE< |  | $\bigcirc$ | $\bigcirc$ |  |
| $\begin{aligned} & \text { LDE>=, ANDE>=, } \\ & \text { ORE>= } \end{aligned}$ |  | $\bigcirc$ | $\bigcirc$ |  |

Comparing double-precision real numbers

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| $\begin{aligned} & \text { LDED=, ANDED=, } \\ & \text { ORED= } \end{aligned}$ | Performs a comparison operation of a double-precision real number. (Devices are used as a normally open contact.) | $\bigcirc$ | $\bigcirc$ | Page 608 LDEDロ, ANDEDC, OREDD |
| LDED<>, ANDED<>, ORED<> |  | $\bigcirc$ | $\bigcirc$ |  |
| LDED>, ANDED>, ORED> |  | $\bigcirc$ | $\bigcirc$ |  |
| $\begin{aligned} & \mathrm{LDED}<=, \text { ANDED }<=\text {, } \\ & \text { ORED<= } \end{aligned}$ |  | $\bigcirc$ | $\bigcirc$ |  |
| LDED<, ANDED<, ORED< |  | $\bigcirc$ | $\bigcirc$ |  |
| $\begin{aligned} & \text { LDED>=, ANDED>=, } \\ & \text { ORED>= } \end{aligned}$ |  | $\bigcirc$ | $\bigcirc$ |  |

Adding/subtracting single-precision real numbers

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| E+ | Adds single-precision real numbers. (Using two operands) | $\bigcirc$ | $\bigcirc$ | Page 611 $\mathrm{E}+(\mathrm{P})$ [when two operands are set] |
| E+P |  | $\bigcirc$ | $\bigcirc$ |  |
| E+ | Adds single-precision real numbers. (Using three operands) | $\bigcirc$ | $\bigcirc$ | Page 612 $\mathrm{E}+(\mathrm{P})$ [when three operands are set] |
| E+P |  | $\bigcirc$ | $\bigcirc$ |  |
| E- | Performs subtraction between single-precision real numbers. (Using two operands) | $\bigcirc$ | $\bigcirc$ | Page 614 E-(P) <br> [when two operands are set] |
| E-P |  | $\bigcirc$ | $\bigcirc$ |  |
| E- | Performs subtraction between single-precision real numbers. (Using three operands) | $\bigcirc$ | $\bigcirc$ | Page $615 \mathrm{E}-(\mathrm{P})$ <br> [when three operands are set] |
| E-P |  | $\bigcirc$ | $\bigcirc$ |  |

Adding/subtracting double-precision real numbers

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| ED+ | Adds double-precision real numbers. (Using two operands) | $\bigcirc$ | $\bigcirc$ | Page 617 |
| ED+P |  | $\bigcirc$ | $\bigcirc$ | ED+(P) [when two operands are set] |
| ED+ | Adds double-precision real numbers. (Using three operands) | $\bigcirc$ | $\bigcirc$ | Page 618 |
| ED+P |  | $\bigcirc$ | $\bigcirc$ | ED+(P) [when three operands are set] |
| ED- | Performs subtraction between double-precision real numbers. (Using two operands) | $\bigcirc$ | $\bigcirc$ | Page 620 ED- |
| ED-P |  | $\bigcirc$ | $\bigcirc$ | (P) [when two operands are set] |
| ED- | Performs subtraction between double-precision real numbers. (Using three operands) | $\bigcirc$ | $\bigcirc$ | Page 621 ED- |
| ED-P |  | $\bigcirc$ | $\bigcirc$ | ( P ) [when three operands are set] |

## Multiplying/dividing single-precision real numbers

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| E* | Multiplies single-precision real numbers. | $\bigcirc$ | $\bigcirc$ | Page $623 \mathrm{E}^{*}(\mathrm{P})$ |
| E*P |  | $\bigcirc$ | $\bigcirc$ |  |
| E/ | These instructions perform division between single-precision real numbers. | $\bigcirc$ | $\bigcirc$ | Page $625 \mathrm{E} /(\mathrm{P})$ |
| E/P |  | $\bigcirc$ | $\bigcirc$ |  |

■Multiplying/dividing double-precision real numbers

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| ED* | Multiplies double-precision real numbers. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 627 \\ & E D^{*}(P) \end{aligned}$ |
| ED*P |  | $\bigcirc$ | $\bigcirc$ |  |
| ED/ | Performs division between double-precision real numbers. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 629 \text { ED/ } \\ & \text { (P) } \end{aligned}$ |
| ED/P |  | $\bigcirc$ | $\bigcirc$ |  |

Converting 16-bit/32-bit signed binary data to single-precision real number

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| INT2FLT | Converts the 16-bit signed binary data in the device specified by (s) to a single-precision real number, and stores the converted data in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | Page 631 <br> INT2FLT(P) |
| INT2FLTP |  | $\bigcirc$ | $\bigcirc$ |  |
| DINT2FLT | Converts the 32-bit signed binary data in the device specified by (s) to a single-precision real number, and stores the real number in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | Page 633 DINT2FLT(P) |
| DINT2FLTP |  | $\bigcirc$ | $\bigcirc$ |  |

Converting 16-bit/32-bit unsigned binary data to single-precision real number

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| UINT2FLT | Converts the 16-bit unsigned binary data in the device specified by (s) to a single-precision real number, and stores the real number in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | Page 632 UINT2FLT(P) |
| UINT2FLTP |  | $\bigcirc$ | $\bigcirc$ |  |
| UDINT2FLT | Converts the 32-bit unsigned binary data in the device specified by (s) to a single-precision real number, and stores the real number in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | Page 635 UDINT2FLT(P) |
| UDINT2FLTP |  | $\bigcirc$ | $\bigcirc$ |  |

Converting double-precision real number to single-precision real number

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| DBL2FLT | Converts the double-precision real number in the device specified by (s) to a single-precision real number, and stores the real number in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | Page 637 DBL2FLT(P) |
| DBL2FLTP |  | $\bigcirc$ | $\bigcirc$ |  |

Converting 16-bit/32-bit signed binary data to double-precision real number

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| INT2DBL | Converts the 16-bit signed binary data in the device specified by (s) to a double-precision real number, and stores the real number in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | Page 639 <br> INT2DBL(P) |
| INT2DBLP |  | $\bigcirc$ | $\bigcirc$ |  |
| DINT2DBL | Converts the 32-bit signed binary data in the device specified by (s) to a double-precision real number, and stores the real number in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | Page 641 DINT2DBL(P) |
| DINT2DBLP |  | $\bigcirc$ | $\bigcirc$ |  |

Converting 16-bit/32-bit unsigned binary data to double-precision real number

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| UINT2DBL | Converts the 16-bit unsigned binary data in the device specified by (s) to a double-precision real number, and stores the real number in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | Page 640 UINT2DBL(P) |
| UINT2DBLP |  | $\bigcirc$ | $\bigcirc$ |  |
| UDINT2DBL | Converts the 32-bit unsigned binary data in the device specified by (s) to a double-precision real number, and stores the real number in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | Page 642 UDINT2DBL(P) |
| UDINT2DBLP |  | $\bigcirc$ | $\bigcirc$ |  |

Converting single-precision real number to double-precision real number

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| FLT2DBL | Converts the single-precision real number in the device specified by (s) to a double-precision real | $\bigcirc$ | $\bigcirc$ | Page 643 |
|  | number, and stores the double-precision real number in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | FLT2DBL(P) |

Converting string data to single-precision real number

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| EVAL | Converts the character string in the device specified by (s) to a single-precision real number, and stores the converted data in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | Page 644 <br> EVAL(P) |
| EVALP |  | $\bigcirc$ | $\bigcirc$ |  |

■Converting BCD format data to single-precision real number

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| EREXP | Converts the BCD data in the device specified by ( s 1 ) to a single-precision real number with the number of decimal positions specified by (s2), and stores the converted data in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | Page 648 <br> EREXP(P) |
| EREXPP |  | $\bigcirc$ | $\bigcirc$ |  |

Inverting the sign of single-precision real number

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| ENEG | Inverts the sign of single-precision real number data. $\text { (d) }+1 \text {, (d) }$ $\qquad$ (d) +1 , <br> (d) $\qquad$ Real number | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 650 \\ & \text { ENEG(P) } \end{aligned}$ |
| ENEGP |  | $\bigcirc$ | $\bigcirc$ |  |

■Inverting the sign of double-precision real number

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| EDNEG | Inverts the sign of double-precision real number data.$\underset{\text { Real number }}{(\mathrm{d})+3,(\mathrm{~d})+2,(\mathrm{~d})+1,(\mathrm{~d})} \longrightarrow(\mathrm{d})+3,(\mathrm{~d})+2,(\mathrm{~d})+1,(\mathrm{~d})$ | $\bigcirc$ | $\bigcirc$ | Page 651 <br> EDNEG(P) |
| EDNEGP |  | $\bigcirc$ | $\bigcirc$ |  |

Transferring single-precision real number

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| EMOV | Transfers single-precision real number data to the specified device.$\qquad$ Real number | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 652 \\ & \text { EMOV(P) } \end{aligned}$ |
| EMOVP |  | $\bigcirc$ | $\bigcirc$ |  |

Transferring double-precision real number

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| EDMOV | Transfers double-precision real number data to the specified device.$(\mathrm{s})+3,(\mathrm{~s})+2,(\mathrm{~s})+1,(\mathrm{~s}) \longrightarrow(\mathrm{d})+3,(\mathrm{~d})+2,(\mathrm{~d})+1,(\mathrm{~d})$ | $\bigcirc$ | $\bigcirc$ | Page 653 <br> EDMOV(P) |
| EDMOVP |  | $\bigcirc$ | $\bigcirc$ |  |

Calculating the sine of single-precision real number

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| SIN | Calculates the sine of the angle specified by a single-precision real number. | $\bigcirc$ | $\bigcirc$ | Page 654 |
| SINP |  | $O$ | $\bigcirc$ | SIN(P) |

Calculating the cosine of single-precision real number

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| cos | Calculates the cosine of the angle specified by a single-precision real number. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 656 \\ & \cos (P) \end{aligned}$ |
| COSP |  | $\bigcirc$ | $\bigcirc$ |  |

©Calculating the tangent of single-precision real number

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| TAN | Calculates the tangent of the angle specified by a single-precision real number. | $\bigcirc$ | $\bigcirc$ | Page 658 |
| TANP |  | $O$ | $\bigcirc$ | TAN(P) |

Calculating the arc sine of single-precision real number

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| ASIN | Calculates the angle from the sine specified by a single-precision real number. | $\bigcirc$ | $\bigcirc$ | Page 660 <br> ASIN(P) |
| ASINP |  | $\bigcirc$ | $\bigcirc$ |  |

Calculating the arc cosine of single-precision real number

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| ACOS | Calculates the angle from the cosine specified by a single-precision real number. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 662 \\ & \text { ACOS(P) } \end{aligned}$ |
| ACOSP |  | $\bigcirc$ | $\bigcirc$ |  |

## ©Calculating the arc tangent of single-precision real number

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| ATAN | Calculates the angle from the tangent specified by a single-precision real number. | $\bigcirc$ | $\bigcirc$ | Page 664 |
|  |  | $O$ | $\bigcirc$ | ATAN(P) |

Calculating the sine of double-precision real number

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| SIND | Calculates the sine of the angle specified by a double-precision real number. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 666 \\ & \text { SIND(P) } \end{aligned}$ |
| SINDP |  | $\bigcirc$ | $\bigcirc$ |  |

Calculating the cosine of double-precision real number

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| COSD | Calculates the cosine of the angle specified by a double-precision real number. | $\bigcirc$ | $\bigcirc$ | Page 668 |
| $\operatorname{COSDP}$ |  | $\bigcirc$ | COSD(P) |  |

Calculating the tangent of double-precision real number

| Instruction symbol | Processing details | Availability |  | Reference |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| TAND | Calculates the tangent of the angle specified by a double-precision real number. | $\bigcirc$ | $\bigcirc$ | Page 670 |
| TANDP |  | $O$ | $\bigcirc$ | TAND(P) |

Calculating the arc sine of double-precision real number

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| ASIND | Calculates the angle from the sine specified by a double-precision real number. | $\bigcirc$ | $\bigcirc$ | Page 672 <br> ASIND(P) |
| ASINDP |  | $\bigcirc$ | $\bigcirc$ |  |

- Calculating the arc cosine of double-precision real number

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| ACOSD | Calculates the angle from the cosine specified by a double-precision real number. | $\bigcirc$ | $\bigcirc$ | Page 674 |
|  |  | ACOSD(P) |  |  |

Calculating the arc tangent of double-precision real number

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| ATAND | Calculates the angle from the tangent specified by a double-precision real number. | $\bigcirc$ | $\bigcirc$ | Page 676 |
|  |  | $\bigcirc$ | $\bigcirc$ | ATAND(P) |

Calculating the sine of BCD data

| Instruction symbol | Processing details |  |  | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  | (1) | (2) |  |
| BSIN | Calculates the sine of the angle specified by a BCD value. |  |  | $\bigcirc$ | $\bigcirc$ | Page 678 BSIN(P) |
| BSINP |  |  |  | $\bigcirc$ | $\bigcirc$ |  |

■Calculating the cosine of BCD data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| BCOS | Calculates the cosine of the angle specified by a BCD value. | $\bigcirc$ | $\bigcirc$ | Page 680 |
| BCOSP |  | $\bigcirc$ | $\bigcirc$ | BCOS(P) |

Calculating the tangent of BCD data

| Instruction symbol | Processing details |  |  | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  | (1) | (2) |  |
| BTAN | Calculates the tangent of the angle specified by a BCD value. |  |  | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 682 \\ & \text { BTAN(P) } \end{aligned}$ |
| BTANP | Tan (s) | (d) <br> (d) +1 <br> (d) +2 | Sign <br> Integral part Decimal part | $\bigcirc$ | $\bigcirc$ |  |

Calculating the arc sine of BCD data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| BASIN | Calculates the arc sine of the angle specified by a BCD value.$\mathrm{Sin}^{-1}(\mathrm{~s}) \longrightarrow\left(\begin{array}{l} \text { (d) } \\ \text { (d) }+1 \\ \text { (d) }+2 \end{array} \left\lvert\, \begin{array}{ll\|} \hline \text { Sign } \\ \text { Integral part } \\ \hline \end{array}\right.\right.$ | $\bigcirc$ | $\bigcirc$ | Page 684 |
| BASINP |  | $\bigcirc$ | $\bigcirc$ | BASIN(P) |

Calculating the arc cosine of BCD data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| BACOS | Calculates the arc cosine of the angle specified by a $B C D$ value. | $\bigcirc$ | $\bigcirc$ | Page 686 |
| BACOSP |  | $\bigcirc$ | $\bigcirc$ | BACOS(P) |

Calculating the arc tangent of BCD data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| BATAN | Calculates the arc tangent of the angle specified by a BCD value. | $\bigcirc$ | $\bigcirc$ | Page 688 |
| BATANP |  | $\bigcirc$ | $\bigcirc$ | BATAN(P) |

Converting single-precision real number angle to radian

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| RAD | Converts the unit of the measure of angle from the degree specified by a single-precision real number to radian. $(\mathrm{s})+1,(\mathrm{~s}) \longrightarrow(\mathrm{d})+1,(\mathrm{~d})$ <br> Converting degree to radian | $\bigcirc$ | $\bigcirc$ | Page 690 |
| RADP |  | $\bigcirc$ | $\bigcirc$ | RAD(P) |

Converting single-precision real number radian to angle

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| DEG | Converts the unit of the measure of angle from the radian specified by a single-precision real number to the degree. $(\mathrm{s})+1,(\mathrm{~s}) \longrightarrow(\mathrm{d})+1,(\mathrm{~d})$ <br> Converting radian to degree | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 692 \\ & \text { DEG(P) } \end{aligned}$ |
| DEGP |  | $\bigcirc$ | $\bigcirc$ |  |

Converting double-precision real number angle to radian

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| RADD | Converts the unit of the measure of angle from the degree specified by a single-precision real number to radian. $(\mathrm{s})+3,(\mathrm{~s})+2,(\mathrm{~s})+1,(\mathrm{~s}) \longrightarrow(\mathrm{d})+3,(\mathrm{~d})+2,(\mathrm{~d})+1,(\mathrm{~d})$ <br> Converting degree to radian | $\bigcirc$ | $\bigcirc$ | Page 694 <br> RADD(P) |
| RADDP |  | $\bigcirc$ | $\bigcirc$ |  |

Converting double-precision real number radian to angle

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| DEGD | Converts the unit of the measure of angle from the radian specified by a double-precision real number to the degree. $(\mathrm{s})+3,(\mathrm{~s})+2,(\mathrm{~s})+1,(\mathrm{~s}) \longrightarrow(\mathrm{d})+3,(\mathrm{~d})+2,(\mathrm{~d})+1,(\mathrm{~d})$ <br> Converting radian to degree | $\bigcirc$ | $\bigcirc$ | Page 696 |
| DEGDP |  | $\bigcirc$ | $\bigcirc$ | DEGD(P) |

Calculating the square root of single-precision real number

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| ESQRT | Calculates the square root of the value specified by a single-precision real number.$\sqrt{(\mathrm{s})+1,(\mathrm{~s})} \longrightarrow(\mathrm{d})+1,(\mathrm{~d})$ | $\bigcirc$ | $\bigcirc$ | Page 698 ESQRT(P) |
| ESQRTP |  | $\bigcirc$ | $\bigcirc$ |  |

Calculating the square root of double-precision real number

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| EDSQRT | Calculates the square root of the value specified by a double-precision real number.$\sqrt{(\mathrm{s})+3,(\mathrm{~s})+2,(\mathrm{~s})+1,(\mathrm{~s})} \longrightarrow(\mathrm{d})+3,(\mathrm{~d})+2,(\mathrm{~d})+1,(\mathrm{~d})$ | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 700 \\ & \text { EDSQRT(P) } \end{aligned}$ |
| EDSQRTP |  | $\bigcirc$ | $\bigcirc$ |  |

Calculating the exponent of single-precision real number

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| EXP | Calculates the exponent of the value specified by a single-precision real number. | $\bigcirc$ | $\bigcirc$ | Page 702 |
| EXPP |  | $O$ | $\bigcirc$ | EXP(P) |

Calculating the exponent of double-precision real number

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| EXPD | Calculates the exponent of the value specified by a double-precision real number. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 704 \\ & \text { EXPD(P) } \end{aligned}$ |
| EXPDP |  | $\bigcirc$ | $\bigcirc$ |  |

Calculating the natural logarithm of single-precision real number

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| LOG | Calculates the logarithm using the natural logarithm (e) of the value specified by a single-precision real number as the base. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 706 \\ & \text { LOG(P) } \end{aligned}$ |
| LOGP |  | $\bigcirc$ | $\bigcirc$ |  |

Calculating the natural logarithm of double-precision real number

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| LOGD | Calculates the logarithm using the natural logarithm (e) of the value specified by a double-precision real number as the base. | $\bigcirc$ | $\bigcirc$ | Page 708 |
| LOGDP |  | $\bigcirc$ | $\bigcirc$ | LOGD(P) |

Calculating the square root of BCD 4-digit/8-digit data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| BSQRT | Calculates the square root of the value specified by a BCD 4-digit data. | $\bigcirc$ | $\bigcirc$ | Page 710 |
| BSQRTP |  | $\bigcirc$ | $\bigcirc$ | BSQRT(P) |
| BDSQRT | Calculates the square root of the value specified by a BCD 8-digit data. | $\bigcirc$ | $\bigcirc$ | Page 711 |
| BDSQRTP |  | $\bigcirc$ | $\bigcirc$ | BDSQRT(P) |

Calculating the exponentiation of single-precision real number

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| POW | Calculates the exponentiation of a single-precision real number. | $\bigcirc$ | $\bigcirc$ | Page 712 <br> POW(P) |
| POWP |  | $\bigcirc$ | $\bigcirc$ |  |

■alculating the exponentiation of double-precision real number

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| POWD | Calculates the exponentiation of a double-precision real number. | $\bigcirc$ | $\bigcirc$ | Page 714 <br> POWD(P) |
| POWDP |  | $\bigcirc$ | $\bigcirc$ |  |

Calculating the common logarithm of single-precision real number

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| LOG10 | Calculates the logarithm using the common logarithm (using 10 as the base) of the value specified <br> by a single-precision real number. | $\bigcirc$ | $\bigcirc$ | Page 716 |
|  | LOG10(P) |  |  |  |

Calculating the common logarithm of double-precision real number

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| LOG10D | Calculates the logarithm using the common logarithm (using 10 as the base) of the value specified <br> by a double-precision real number. | $\bigcirc$ | $\bigcirc$ | Page 718 |
|  | LOG10D(P) | $O$ | $O$ |  |

Searching the maximum value of single-precision real number

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| EMAX | Searches for the maximum value in the ( $n$ ) points of single-precision real number block data in the device starting from the one specified by (s), and stores the maximum value in the search result (maximum value) in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 720 \\ & \text { EMAX(P) } \end{aligned}$ |
| EMAXP |  | $\bigcirc$ | $\bigcirc$ |  |

Searching the maximum value of double-precision real number

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| EDMAX | Searches for the maximum value in the ( n ) points of double-precision real number block data in the device starting from the one specified by ( $s$ ), and stores the maximum value in the search result (maximum value) in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 722 \\ & \text { EDMAX(P) } \end{aligned}$ |
| EDMAXP |  | $\bigcirc$ | $\bigcirc$ |  |

Searching the minimum value of single-precision real number

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| EMIN | Searches for the minimum value in the ( $n$ ) points of single-precision real number block data in the device starting from the one specified by (s), and stores the maximum value in the search result (minimum value) in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 724 \\ & \text { EMIN(P) } \end{aligned}$ |
| EMINP |  | $\bigcirc$ | $\bigcirc$ |  |

## Searching the minimum value of double-precision real number

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| EDMIN | Searches for the minimum value in the ( n ) points of double-precision real number block data in the device starting from the one specified by (s), and stores the maximum value in the search result (minimum value) in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 726 \\ & \text { EDMIN(P) } \end{aligned}$ |
| EDMINP |  | $\bigcirc$ | $\bigcirc$ |  |

## Random number instructions

Generating random number, changing random sequence

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| RND | Generates a random number between 0 and less than 32767, and stores the random number in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 728 \\ & \text { RND(P) } \end{aligned}$ |
| RNDP |  | $\bigcirc$ | $\bigcirc$ |  |
| SRND | Changes the random number sequence according to the content of the 16-bit binary data stored in the device specified by (s). | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 729 \\ & \text { SRND(P) } \end{aligned}$ |
| SRNDP |  | $\bigcirc$ | $\bigcirc$ |  |

## Index register instructions

Saving/returning all data of the index register

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| ZPUSH | Saves data of the index register to the area specified by (d). | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 730 \\ & \text { ZPUSH(P) } \end{aligned}$ |
| ZPUSHP |  | $\bigcirc$ | $\bigcirc$ |  |
| ZPOP | Reads the data, which has been saved to the area specified by (d)and later, into the index register. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 732 \\ & \text { ZPOP(P) } \end{aligned}$ |
| ZPOPP |  | $\bigcirc$ | $\bigcirc$ |  |

Saving/returning the selected data of the index register and long index register

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| ZPUSH | Saves the contents of the index register and long index register specified by (s) to the area specified by (d). | $\bigcirc$ | $\bigcirc$ | $\text { Page } 733$ZPUSH(P) |
| ZPUSHP |  | $\bigcirc$ | $\bigcirc$ |  |
| ZPOP | Reads the data, which has been saved to the area specified by (d), into the index register and long index register. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 736 \\ & \text { ZPOP(P) } \end{aligned}$ |
| ZPOPP |  | $\bigcirc$ | $\bigcirc$ |  |

## Data control instructions

■Upper and lower limit control of 16-bit/32-bit binary data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| LIMIT | Controls the output value depending on whether the specified 16 -bit binary bit value is within the upper and lower limits. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 738 \\ & \text { LIMIT(P)(U) } \end{aligned}$ |
| LIMITP |  | $\bigcirc$ | $\bigcirc$ |  |
| LIMIT_U |  | $\bigcirc$ | $\bigcirc$ |  |
| LIMITP_U |  | $\bigcirc$ | $\bigcirc$ |  |
| DLIMIT | Controls the output value depending on whether the specified 32 -bit binary bit value is within the upper and lower limits. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 740 \\ & \operatorname{DLIMIT(P)(\_ U)} \end{aligned}$ |
| DLIMITP |  | $\bigcirc$ | $\bigcirc$ |  |
| DLIMIT_U |  | $\bigcirc$ | $\bigcirc$ |  |
| DLIMITP_U |  | $\bigcirc$ | $\bigcirc$ |  |

## Dead band control of 16-bit/32-bit binary data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| BAND | Controls the output value depending on whether the specified 16 -bit binary bit value is within the upper and lower limits of the dead band. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 742 \\ & \text { BAND(P)(_U) } \end{aligned}$ |
| BANDP |  | $\bigcirc$ | $\bigcirc$ |  |
| BAND_U |  | $\bigcirc$ | $\bigcirc$ |  |
| BANDP_U |  | $\bigcirc$ | $\bigcirc$ |  |
| DBAND | Controls the output value depending on whether the specified 32 -bit binary bit value is within the upper and lower limits of the dead band. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 744 \\ & \text { DBAND(P)(_U) } \end{aligned}$ |
| DBANDP |  | $\bigcirc$ | $\bigcirc$ |  |
| DBAND_U |  | $\bigcirc$ | $\bigcirc$ |  |
| DBANDP_U |  | $\bigcirc$ | $\bigcirc$ |  |

Zone control of 16-bit/32-bit binary data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| ZONE | Adds a bias value to the specified input value (16-bit binary). | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 746 \\ & \text { ZONE(P)(_U) } \end{aligned}$ |
| ZONEP |  | $\bigcirc$ | $\bigcirc$ |  |
| ZONE_U |  | $\bigcirc$ | $\bigcirc$ |  |
| ZONEP_U |  | $\bigcirc$ | $\bigcirc$ |  |
| DZONE | Adds a bias value to the specified input value (32-bit binary). | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 748 \\ & \text { DZONE(P)(_U) } \end{aligned}$ |
| DZONEP |  | $\bigcirc$ | $\bigcirc$ |  |
| DZONE_U |  | $\bigcirc$ | $\bigcirc$ |  |
| DZONEP_U |  | $\bigcirc$ | $\bigcirc$ |  |

Scaling 16-bit/32-bit binary data (point coordinates)

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| SCL | Scales the scaling conversion data (16-bit data) in the device specified by ( s 2 ) on the basis of the input value in the device specified by (s1), and stores the operation result in the device specified by (d). <br> Scaling conversion is performed based on the scaling conversion data stored in the device specified by (s2) and later. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 750 \\ & \text { SCL(P)(_U) } \end{aligned}$ |
| SCLP |  | $\bigcirc$ | $\bigcirc$ |  |
| SCL_U |  | $\bigcirc$ | $\bigcirc$ |  |
| SCLP_U |  | $\bigcirc$ | $\bigcirc$ |  |
| DSCL | Scales the scaling conversion data (32-bit data) in the device specified by (s2) on the basis of the input value in the device specified by (s1), and stores the operation result in the device specified by (d). <br> Scaling conversion is performed based on the scaling conversion data stored in the device specified by (s2) and later. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 753 \\ & \text { DSCL(P)(U) } \end{aligned}$ |
| DSCLP |  | $\bigcirc$ | $\bigcirc$ |  |
| DSCL_U |  | $\bigcirc$ | $\bigcirc$ |  |
| DSCLP_U |  | $\bigcirc$ | $\bigcirc$ |  |

UScaling 16-bit/32-bit binary data (XY coordinates)

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| SCL2 | Scales the scaling conversion data (16-bit data) in the device specified by ( s 2 ) on the basis of the input value in the device specified by ( $\mathbf{s} 1$ ), and stores the operation result in the device specified by (d). <br> Scaling conversion is performed based on the scaling conversion data stored in the device specified by (s2) and later. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 756 \\ & \text { SCL2(P)(_U) } \end{aligned}$ |
| SCL2P |  | $\bigcirc$ | $\bigcirc$ |  |
| SCL2_U |  | $\bigcirc$ | $\bigcirc$ |  |
| SCL2P_U |  | $\bigcirc$ | $\bigcirc$ |  |
| DSCL2 | Scales the scaling conversion data (32-bit data) in the device specified by (s2) on the basis of the input value in the device specified by (s1), and stores the operation result in the device specified by (d). <br> Scaling conversion is performed based on the scaling conversion data stored in the device specified by ( s 2 ) and later. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 758 \\ & \text { DSCL2(P)(_U) } \end{aligned}$ |
| DSCL2P |  | $\bigcirc$ | $\bigcirc$ |  |
| DSCL2_U |  | $\bigcirc$ | $\bigcirc$ |  |
| DSCL2P_U |  | $\bigcirc$ | $\bigcirc$ |  |

## Special counter instructions

## Counting up or down the current value (1-phase input)

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| UDCNT1 | Updates the current value of the specified counter. | $\bigcirc$ | $\bigcirc$ | Page 760 <br> UDCNT1 |

Counting up or down the current value (2-phase input)

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| UDCNT2 | Updates the current value of the counter depending on the status of phases $A$ and $B$ pulses. | $\bigcirc$ | $\bigcirc$ | Page 762 UDCNT2 |

## Special timer instructions

## -Teaching timer

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| TTMR | Measures the on time of the measurement command in seconds, multiplies it by a multiplier, and stores the operation result. | $\bigcirc$ | $\bigcirc$ | Page 764 <br> TTMR |

Special function timer

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| STMR | According to on/off of the input condition of the STMR instruction, the following operations are performed at the four points from the bit device specified by (d). <br> - (d) +0 : Off delay timer output <br> - (d)+1: After-off one-shot timer output <br> - (d)+2: After-on one-shot timer output <br> - (d) +3 : On delay + off delay timer | $\bigcirc$ | $\bigcirc$ | Page 766 <br> STMR |

## Shortcut control instruction

## ■Rotary table shortest direction control

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| ROTC | Controls the rotary table divided by ( n 1 ) so that it makes shortcut rotation from the stop position to the position specified by $(\mathrm{s})+1$. | $\bigcirc$ | $\bigcirc$ | Page 769 <br> ROTC |

## Ramp signal instruction

Ramp signal

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| RAMPQ | Changes the value specified by ( s 1 ) to the value specified by ( s 2 ) by the number of times specified by ( n ). <br> The current value is stored in the device specified by (d1) +0 . | $\bigcirc$ | $\bigcirc$ | Page 772 <br> RAMPQ |

## Pulse related instructions

Measuring the density of pulses

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| SPD | Counts the input pulses in the device specified by (s1) for the period specified by (s2), and stores <br> the result data in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | Page 774 SPD |

©Outputting pulses at regular intervals

| Instruction symbol | Processing details | Availability | Reference |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
| PLSY | Outputs the pulses at the frequency specified by (s), by the number of times specified by (n), to the <br> output number $(Y)$ in the device specified by (d). | $\bigcirc$ | (2) | $\bigcirc$ | Page 776 <br> PLSY |

Performing the pulse width modulation

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| PWM | Outputs the on time specified by (s1) and the pulses in the period specified by (s2) to the output <br> number $(Y)$ in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | Page 778 PWM |

## Matrix input instruction

Matrix input

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| MTR | Reads 16 points by ( $n$ ) columns of data from the device specified by ( $s$ ), and stores it in the device <br> specified by ( d 2 ) and later. | $\bigcirc$ | $\bigcirc$ | Page 780 MTR |

## Data processing instructions

USearching 16-bit/32-bit binary data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| SERDATA | Searches ( n ) points from 16-bit binary data using the specified 16-bit binary data as a keyword. <br> (d): Location <br> (d) +1 : Number of matches | $\bigcirc$ | $\bigcirc$ | Page 782 |
| SERDATAP |  | $\bigcirc$ | $\bigcirc$ | SERDATA(P) |
| DSERDATA | Searches ( n ) points from 32-bit binary data using the specified 32-bit binary data as a keyword. | $\bigcirc$ | $\bigcirc$ | Page 784 |
| DSERDATAP |  | $\bigcirc$ | $\bigcirc$ | DSERDATA(P) |

Checking 16-bit/32-bit binary data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| SUM | Stores the total number of "1" bits in the 16-bit binary data stored in the specified device. <br> (s) <br> b15 <br> ... b0 (d): Number of 1 s | $\bigcirc$ | $\bigcirc$ | Page 786 |
| SUMP |  | $\bigcirc$ | $\bigcirc$ | SUM(P) |
| DSUM | Stores the total number of "1" bits in the 32-bit binary data stored in the specified device. $(s)+1$ <br> (s) $\square$ <br> (d): Number of 1 s | $\bigcirc$ | $\bigcirc$ | Page 787 |
| DSUMP |  | $\bigcirc$ | $\bigcirc$ | DSUM(P) |

Searching for the maximum value of 16-bit/32-bit binary data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| MAX | Searches the ( n ) points of data in the device specified by ( s ) in units of 16 bits, and stores the maximum value in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 788 \\ & \operatorname{MAX(P)(\_ U)} \end{aligned}$ |
| MAXP |  | $\bigcirc$ | $\bigcirc$ |  |
| MAX_U |  | $\bigcirc$ | $\bigcirc$ |  |
| MAXP_U |  | $\bigcirc$ | $\bigcirc$ |  |
| DMAX | Searches the ( n ) points of data in the device specified by (s) in units of 32 bits, and stores the maximum value in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 790 \\ & \text { DMAX(P)(_U) } \end{aligned}$ |
| DMAXP |  | $\bigcirc$ | $\bigcirc$ |  |
| DMAX_U |  | $\bigcirc$ | $\bigcirc$ |  |
| DMAXP_U |  | $\bigcirc$ | $\bigcirc$ |  |

Searching for the minimum value of 16-bit/32-bit binary data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| MIN | Searches the ( n ) points of data in the device specified by ( s ) in units of 16 bits, and stores the minimum value in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 792 \\ & \operatorname{MIN}(P)\left(\_U\right) \end{aligned}$ |
| MINP |  | $\bigcirc$ | $\bigcirc$ |  |
| MIN_U |  | $\bigcirc$ | $\bigcirc$ |  |
| MINP_U |  | $\bigcirc$ | $\bigcirc$ |  |
| DMIN | Searches the ( n ) points of data in the device specified by ( s ) in units of 32 bits, and stores the minimum value in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 794 \\ & \text { DMIN(P)(_U) } \end{aligned}$ |
| DMINP |  | $\bigcirc$ | $\bigcirc$ |  |
| DMIN_U |  | $\bigcirc$ | $\bigcirc$ |  |
| DMINP_U |  | $\bigcirc$ | $\bigcirc$ |  |

Sorting 16-bit/32-bit binary data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| SORTD | Sorts the ( n ) points of data in the device specified by (s) in units of 16 bits. ("(n) $\times((n)-1) \div 2$ " scanning required) | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 796 \\ & \text { SORTD(_U) } \end{aligned}$ |
| SORTD_U |  | $\bigcirc$ | $\bigcirc$ |  |
| DSORTD | Sorts the ( n ) points of data in the device specified by (s) in units of 32 bits. ("(n) $\times((n)-1) \div 2$ " scanning required) | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \hline \text { Page } 798 \\ & \text { DSORTD(_U) } \end{aligned}$ |
| DSORTD_U |  | $\bigcirc$ | $\bigcirc$ |  |

Adding 16-bit binary data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| WSUM | Adds the $(\mathrm{n})$ points of 16-bit binary data in the device starting from the one specified by ( s ), and stores the result in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 800 \\ & \text { WSUM(P)(_U) } \end{aligned}$ |
| WSUM_U |  | $\bigcirc$ | $\bigcirc$ |  |
| WSUMP |  | $\bigcirc$ | $\bigcirc$ |  |
| WSUMP_U |  | $\bigcirc$ | $\bigcirc$ |  |

Adding 32-bit binary data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| DWSUM | Adds the ( n ) points of 32-bit binary data in the device starting from the one specified by (s), and stores the result in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | ```Page }80 DWSUM(P)(_U )``` |
| DWSUM_U |  | $\bigcirc$ | $\bigcirc$ |  |
| DWSUMP |  | $\bigcirc$ | $\bigcirc$ |  |
| DWSUMP_U |  | $\bigcirc$ | $\bigcirc$ |  |

Calculating the mean value of 16-bit/32-bit binary data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| MEAN | Calculates the average value of the $(\mathrm{n})$ points of 16-bit data in the device starting from the one specified by (s), and stores the average value in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | Page 804 <br> MEAN(P)(_U) |
| MEANP |  | $\bigcirc$ | $\bigcirc$ |  |
| MEAN_U |  | $\bigcirc$ | $\bigcirc$ |  |
| MEANP_U |  | $\bigcirc$ | $\bigcirc$ |  |
| DMEAN | Calculates the average value of the (n) points of 32-bit data in the device starting from the one specified by ( s ), and stores the average value in the device specified by (d). | $\bigcirc$ | $\bigcirc$ | Page 806 DMEAN(P)(_U) |
| DMEANP |  | $\bigcirc$ | $\bigcirc$ |  |
| DMEAN_U |  | $\bigcirc$ | $\bigcirc$ |  |
| DMEANP_U |  | $\bigcirc$ | $\bigcirc$ |  |

## Database access instructions

Opening the data base

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| DBOPEN | Connects to the database specified by (s), and makes it available. | $\bigcirc$ | $\bigcirc$ | Page 812 <br> DBOPEN(P) |
| DBOPENP |  | $\bigcirc$ | $\bigcirc$ |  |

■Closing the data base

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| DBCLOSE | Releases the identification number specified by (s) and the allocation of the database. | $\bigcirc$ | $\bigcirc$ | Page 814 |
|  |  | $O$ | $\bigcirc$ | DBCLOSE(P) |

■Adding a record to the data base

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| DBINSERT | Adds a record to the table specified by (s2) in the database corresponding to the identification number specified by ( s 1 ). | $\bigcirc$ | $\bigcirc$ | Page 816 DBINSERT(P) |
| DBINSERTP |  | $\bigcirc$ | $\bigcirc$ |  |

Uupdating the record in the data base

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| DBUPDATE | Updates all record that meets the condition specified by (s5) in the table specified by ( s 2 ) in the database specified by the identification number specified by ( s 1 ). | $\bigcirc$ | $\bigcirc$ | Page 820 DBUPDATE(P) |
| DBUPDATEP |  | $\bigcirc$ | $\bigcirc$ |  |

Searching the record in the data base

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| DBSELECT | Searches the records in the table specified by (s2) in the database corresponding to the identification number specified by (s1). | $\bigcirc$ | $\bigcirc$ | Page 826 DBSELECT(P) |
| DBSELECTP |  | $\bigcirc$ | $\bigcirc$ |  |

Deleting the record in the data base

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| DBDELETE | Deletes the record that meets the condition specified by ( s 3 ) in the table specified by ( s 2 ) in the database corresponding to the identification number specified by (s1). | $\bigcirc$ | $\bigcirc$ | Page 834 DBDELETE(P) |
| DBDELETEP |  | $\bigcirc$ | $\bigcirc$ |  |

Importing data to the data base

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| DBIMPORT | Imports the data set in the Unicode text file stored in the path specified by (s) to construct a <br> database. | $\bigcirc$ | $\bigcirc$ | Page 808 |
|  |  | $\bigcirc$ | $\bigcirc$ | DBIMPORT(P) |

Exporting data from the data base

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| DBEXPORT | Exports the data stored in the database to the Unicode text file stored in the path specified by (s). | $\bigcirc$ | $\bigcirc$ | Page 810 DBEXPORT(P) |
| DBEXPORTP |  | $\bigcirc$ | $\bigcirc$ |  |

Starting a transaction

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| DBTRANS | Declares the start of a transaction in relation to the database corresponding to the identification number specified by (s). | $\bigcirc$ | $\bigcirc$ | Page 838 DBTRANS(P) |
| DBTRANSP |  | $\bigcirc$ | $\bigcirc$ |  |

Committing a transaction

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| DBCOMMIT | Commits the transaction in relation to the database corresponding to the identification number specified by (s). | $\bigcirc$ | $\bigcirc$ | Page 840 DBCOMMIT(P) |
| DBCOMMITP |  | $\bigcirc$ | $\bigcirc$ |  |

Performing a rollback

| Instruction symbol | Processing details | Availability | Reference |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |  |
| DBROLBAK | Executes the rollback in relation to the database corresponding to the identification number | $\bigcirc$ | $\bigcirc$ | Page 842 |  |
| Specified by (s). | $\bigcirc$ | $\bigcirc$ | DBROLBAK(P) |  |  |
| DBROLBAKP |  |  |  |  |  |

## File register operation instructions

## ©Switching the file register block number

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| RSET | Switches the block number of the file register used in the program to that stored in the device specified by (s). | $\bigcirc$ | $\bigcirc$ | Page 847 <br> RSET(P) |
| RSETP |  | $\bigcirc$ | $\bigcirc$ |  |

Changing the file register file name

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| QDRSET | Changes the file name of the file register used in the program. | $\bigcirc$ | $\bigcirc$ | Page 849 QDRSET(P) |
| QDRSETP |  | $\bigcirc$ | $\bigcirc$ |  |

## File register read/write instructions

■Reading 1-byte data from the file register

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| ZRRDB | Reads the data from the file register with the specified serial byte number. | $\bigcirc$ | $\bigcirc$ | Page 851 |
| ZRRDBP | 0 0 <br>  Lower 8 bits <br> 1 Upper 8 bits <br> 2 Lower 8 bits <br> 3 Upper 8 bits <br>  ZR0 <br>  ZR1 <br> (s) <br>  (d) | $\bigcirc$ | $\bigcirc$ | ZRRDB(P) |

Writing 1-byte data to the file register


## Indirect address read instructions

■Reading the indirect address

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| ADRSET | Reads the indirect address of the specified device. <br> Indirect address of specified device <br> Device name | $\bigcirc$ | $\bigcirc$ | Page 855 ADRSET(P) |
| ADRSETP |  | $\bigcirc$ | $\bigcirc$ |  |

## Clock instructions

## ■Reading clock data

| Instruction symbol | Processing details |  | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | (1) | (2) |  |
| DATERD | Reads "year, month, day, hour, minute, second, and day of week" from the clock element of the CPU module. |  | $\bigcirc$ | $\bigcirc$ | Page 857 |
| DATERDP |  |  | $\bigcirc$ | $\bigcirc$ | DATERD(P) |
|  | $\begin{aligned} \text { (Clock elements) } \rightarrow & \text { (d) } \\ & (\text { d })+1 \\ & \text { (d) }+2 \\ & \text { (d) }+3 \\ & (\text { d })+4 \\ & \text { (d) }+5 \\ & \text { (d) }+6 \end{aligned}$ | Year |  |  |  |
|  |  | Month |  |  |  |
|  |  | Day |  |  |  |
|  |  | Hour |  |  |  |
|  |  | Minute |  |  |  |
|  |  | Second |  |  |  |
|  |  | Day of week |  |  |  |

Writing clock data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| DATEWR | Writes the clock data stored in the specified device number and later to the clock element of the CPU module. | $\bigcirc$ | $\bigcirc$ | Page 859 DATEWR(P) |
| DATEWRP |  | $\bigcirc$ | $\bigcirc$ |  |
|  | $\rightarrow$ (Clock elements) |  |  |  |
|  |  |  |  |  |
|  |  |  |  |  |
|  |  |  |  |  |
|  |  |  |  |  |
|  |  |  |  |  |
|  |  |  |  |  |

Adding clock data


## Subtracting clock data

| Instruction symbol | Processing details |  |  |  | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  | （1） | （2） |  |
| DATE－ | Subtracts time data． |  | （d） |  | $\bigcirc$ | $\bigcirc$ | Page 863 DATE－（P） |
| DATE－P | （s1） <br> Hour <br> Minute <br> Second | （s2） |  |  | $\bigcirc$ | $\bigcirc$ | DATE－（P） |
|  |  | Hour | $\rightarrow$ | Hour |  |  |  |
|  |  | Minute |  | Minute |  |  |  |
|  |  | Second |  | Second |  |  |  |

Converting time data from hour／minute／second to second

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | （1） | （2） |  |
| TIME2SEC | Converts time data to seconds． | $\bigcirc$ | $\bigcirc$ | Page 865 |
| TIME2SECP | （s）（d） | $\bigcirc$ | $\bigcirc$ | TIME2SEC（P） |
|  | Hour Second（lower bits） |  |  |  |
|  | Minute $\rightarrow$ Second（upper bits） |  |  |  |
|  | Second |  |  |  |

Converting time data from second to hour／minute／second

| Instruction symbol | Processing details |  | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | （1） | （2） |  |
| SEC2TIME | Converts seconds data to hour／minute／second data． <br> （s） <br> （d） |  | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 867 \\ & \text { SEC2TIME(P) } \end{aligned}$ |
| SEC2TIMEP |  |  | $\bigcirc$ | $\bigcirc$ |  |
|  | 年econd（lower bits） | Hour |  |  |  |
|  |  | Minute |  |  |  |
|  |  | Second |  |  |  |

Comparing date data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | （1） | （2） |  |
| $\begin{aligned} & \text { LDDT=, ANDDT=, } \\ & \text { ORDT= } \end{aligned}$ | Compares the specified date data，or compares the date data with the current date． | $\bigcirc$ | $\bigcirc$ | Page 869 LDDTロ， ANDDTロ， ORDTD |
| LDDT＜＞，ANDDT＜＞， ORDT＜＞ |  | $\bigcirc$ | $\bigcirc$ |  |
| LDDT＞，ANDDT＞， ORDT＞ |  | $\bigcirc$ | $\bigcirc$ |  |
| ```LDDT<=, ANDDT<=, ORDT<=``` |  | $\bigcirc$ | $\bigcirc$ |  |
| LDDT＜，ANDDT＜， ORDT＜ |  | $\bigcirc$ | $\bigcirc$ |  |
| $\begin{aligned} & \text { LDDT>=, ANDDT>=, } \\ & \text { ORDT>= } \end{aligned}$ |  | $\bigcirc$ | $\bigcirc$ |  |

Comparing time data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| $\begin{aligned} & \text { LDTM }=\text {, ANDTM=, } \\ & \text { ORTM= } \end{aligned}$ | Compares the specified time data, or compares the specified time data with the current time. | $\bigcirc$ | $\bigcirc$ | Page 873 LDTMD, ANDTMD, ORTMD |
| LDTM<>, ANDTM<>, ORTM<> |  | $\bigcirc$ | $\bigcirc$ |  |
| LDTM>, ANDTM>, ORTM> |  | $\bigcirc$ | $\bigcirc$ |  |
| LDTM<=, ANDTM<=, ORTM<= |  | $\bigcirc$ | $\bigcirc$ |  |
| LDTM<, ANDTM<, ORTM< |  | $\bigcirc$ | $\bigcirc$ |  |
| $\begin{aligned} & \text { LDTM>=, ANDTM>=, } \\ & \text { ORTM>= } \end{aligned}$ |  | $\bigcirc$ | $\bigcirc$ |  |

Reading expansion clock data

| Instruction symbol | Processing details |  | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | (1) | (2) |  |
| S.DATERD | Reads clock data including millisecond from the clock elements in the CPU module. |  | $\bigcirc$ | $\bigcirc$ | Page 876 |
| SP.DATERD | (Clock elements) $\rightarrow$ (d) <br> (d) +1 <br> (d) +2 <br> (d) +3 <br> (d) +4 <br> (d) +5 <br> (d) +6 <br> (d) +7 | Year <br> Month <br> Day <br> Hour <br> Minute <br> Second <br> Day of week <br> Millisecond | $\bigcirc$ | $\bigcirc$ | S(P).DATERD |

Adding expansion clock data


Subtracting expansion clock data

| Instruction symbol | Processing details |  |  |  | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  | (1) | (2) |  |
| S.DATE- | Subtracts time data. | (s2) | (d) |  | $\bigcirc$ | $\bigcirc$ | Page 880 S(P).DATE- |
| SP.DATE- | (s1) |  |  |  | $\bigcirc$ | $\bigcirc$ | S(P).DATE- |
|  | Hour | Hour | $\rightarrow$ | Hour |  |  |  |
|  | Minute | Minute |  | Minute |  |  |  |
|  | Second | Second |  | Second |  |  |  |
|  | - | - |  | - |  |  |  |
|  | Millisecond | Millisecond |  | Millisecond |  |  |  |

## Timing check instructions

Generating timing pulses

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| DUTY | Turns on the user timing clock for the specified number of scans and off for the specified number of scans. <br> (d) | $\bigcirc$ | $\bigcirc$ | Page 882 DUTY |

## -Measuring time of the specified data

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| TIMCHK | Measures the on time of the input condition and, if the on time has continued as specified or longer, <br> turns on the device specified by (d). | $\bigcirc$ | Page 884 |  |

## Module access instructions

Performing I/O refresh

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| RFS | Performs partial refresh of the relevant input/output during one scan. | $\bigcirc$ | $\bigcirc$ | Page 885 |
| RFSP |  | $O$ | $\bigcirc$ | RFS(P) |

Selecting refresh to be performed

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| COM | Performs refresh and service processing for various modules when the input condition is met. | $\bigcirc$ | $\bigcirc$ | Page 887 |
|  |  | $\bigcirc$ | COM(P) |  |

Performing module refresh

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| S.ZCOM | Performs refresh processing for the specified modules. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 889 \\ & \text { S(P).ZCOM } \end{aligned}$ |
| SP.ZCOM |  | $\bigcirc$ | $\bigcirc$ |  |

Reading 1-word/2-word data from another module (16-bit specification)

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| FROM | Reads ( n ) words of data in units of 16 bits from the buffer memory areas of the intelligent function module and other CPU modules. | $\bigcirc$ | $\bigcirc$ | Page 891 <br> FROM(P), <br> DFROM(P) |
| FROMP |  | $\bigcirc$ | $\bigcirc$ |  |
| DFROM | Reads $(\mathrm{n}) \times 2$ words of data in units of 16 bits from the buffer memory areas of the intelligent function module and other CPU modules. | $\bigcirc$ | $\bigcirc$ |  |
| DFROMP |  | $\bigcirc$ | $\bigcirc$ |  |

Writing 1-word/2-word data to a module (16-bit specification)

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| TO | Writes ( n ) words of data in units of 16 bits to the buffer memory areas of the intelligent function module and own CPU module. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 894 \\ & \text { TO(P), DTO(P) } \end{aligned}$ |
| TOP |  | $\bigcirc$ | $\bigcirc$ |  |
| DTO | Writes $(\mathrm{n}) \times 2$ words of data in units of 16 bits to the buffer memory areas of the intelligent function module and own CPU module. | $\bigcirc$ | $\bigcirc$ |  |
| DTOP |  | $\bigcirc$ | $\bigcirc$ |  |

Reading 1-word/2-word data from another module (32-bit specification)

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| FROMD | Reads ( n ) words of data in units of 32 bits from the buffer memory areas of the intelligent function module and other CPU modules. | $\bigcirc$ | $\bigcirc$ | Page 899 FROMD(P), DFROMD(P) |
| FROMDP |  | $\bigcirc$ | $\bigcirc$ |  |
| DFROMD | Reads ( n ) $\times 2$ words of data in units of 32 bits from the buffer memory areas of the intelligent function module and other CPU modules. | $\bigcirc$ | $\bigcirc$ |  |
| DFROMDP |  | $\bigcirc$ | $\bigcirc$ |  |

Writing 1-word/2-word data to a module (32-bit specification)

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| TOD | Writes ( n ) words of data in units of 32 bits to the buffer memory areas of the intelligent function module and own CPU module. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 902 \\ & \text { TOD(P), } \\ & \text { DTOD(P) } \end{aligned}$ |
| TODP |  | $\bigcirc$ | $\bigcirc$ |  |
| DTOD | Writes ( n ) $\times 2$ words of data in units of 32 bits to the buffer memory areas of the intelligent function module and own CPU module. | $\bigcirc$ | $\bigcirc$ |  |
| DTODP |  | $\bigcirc$ | $\bigcirc$ |  |

Reading the module model name

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| TYPERD | Reads the module model name mounted on the slot specified by $(\mathrm{H})$, and stores the model name in the device areas specified by (d) and later. | $\bigcirc$ | $\bigcirc$ | Page 907 <br> TYPERD(P) |
| TYPERDP |  | $\bigcirc$ | $\bigcirc$ |  |

Reading module specific information

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| UNIINFRD | Reads the module information by the number of points specified by ( n ) from the module specified by $(\mathrm{H})$, and stores the information in the device areas specified by (d) and later. | $\bigcirc$ | $\bigcirc$ | Page 911 <br> UNIINFRD(P) |
| UNIINFRDP |  | $\bigcirc$ | $\bigcirc$ |  |

## Routing information instructions

■ Reading routing information

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.RTREAD | Reads the data set by routing parameters. | $\bigcirc$ | $\bigcirc$ | Page 916 |
| SP.RTREAD |  | S(P).RTREAD |  |  |

Registering routing information

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.RTWRITE | Registers the routing information to the area specified by a routing parameter. | $\bigcirc$ | $\bigcirc$ | Page 918 |
| SP.RTWRITE |  | S(P).RTWRITE |  |  |

## Logging instructions

Setting/resetting trigger logging

| Instruction symbol | Processing details | Availability | Reference |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
| LOGTRG | Generates a trigger for trigger logging. Data sampled for the number of records (specified in the <br> trigger logging setting parameter using the engineering tool) are stored in the data logging file. | (1) | (2) | ○ | Page 920 <br> LOGTRG |
| LOGTRGR | Resets the trigger condition. | O | Page 921 <br> LOGTRGR |  |  |

## Program control instructions

Changing the program execution type to standby type

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| PSTOP | Changes the type of the specified program to standby type. | $\bigcirc$ | $\bigcirc$ | Page 922 |
|  |  | PSTOP(P) |  |  |

Changing the program execution type to standby type (output off)

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| POFF | Turns off the coil of the OUT instruction used in the specified program and changes the type of the specified program to standby type. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 924 \\ & \text { POFF(P) } \end{aligned}$ |
| POFFP |  | $\bigcirc$ | $\bigcirc$ |  |

Changing the program execution type to scan execution type

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| PSCAN | Changes the type of the specified program to scan execution type. | $\bigcirc$ | $\bigcirc$ | Page 926 |
|  |  |  | PSCAN(P) |  |

### 2.4 Built-in Ethernet Function Instructions

Open/close processing instructions

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| SP.SOCOPEN | Opens the connection specified by (s1). | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 928 \\ & \text { SP.SOCOPEN } \end{aligned}$ |

Closing a connection

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| SP.SOCCLOSE | Closes the connection specified by (s1). (Closing a connection) | O | O | Page 931 <br> SP.SOCCLOS <br> E |

## Socket communications instructions

Reading receive data during the END processing

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| SP.SOCRCV | Reads the receive data of the connection specified by (s1) during END processing from the socket <br> communication receive data area. | $\bigcirc$ | $\bigcirc$ | Page 933 <br> SP.SOCRCV |

## ■Reading receive data when the instruction is executed

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.SOCRCVS | Reads the receive data of the connection specified by (s) during instruction execution from the <br> socket communication receive data area. | $\bigcirc$ | $\bigcirc$ | Page 936 <br> S.SOCRCVS |

## Sending data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| SP.SOCSND | Sends the data in the device specified by ( s 3 ) to the external device of the connection specified by (s1). | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 939 \\ & \text { SP.SOCSND } \end{aligned}$ |

Reading connection information

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| SP.SOCCINF | Reads the connection information of the connection specified by (s1). | $\bigcirc$ | $\bigcirc$ | Page 942 <br> SP.SOCCINF |

Changing the communication target (UDP/IP)

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| SP.SOCCSET | Changes the communication target IP address and port number of the connection specified by <br> (s1). <br> (UDP/IP communications only) | $\bigcirc$ | $\bigcirc$ | Page 944 <br> SP.SOCCSET |

Changing the receive mode

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| SP.SOCRMODE | Changes the TCP receive mode and receive data size for the connection specified by (s1). | $\bigcirc$ | $\bigcirc$ | Page 946 SP.SOCRMOD E |

-Reading socket communications receive data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| S.SOCRDATA | Reads data by the number of words specified by ( n ) from the socket communication receive data area of the connection specified by ( s 1 ), and stores them in the device specified by (d) and later. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 950 \\ & \text { S(P).SOCRDA } \\ & \text { TA } \end{aligned}$ |
| SP.SOCRDATA |  | $\bigcirc$ | $\bigcirc$ |  |

## Predefined protocol support function instruction

Executing the registered protocols

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| SP.ECPRTCL | Executes the protocol specified by the communication protocol support tool of the engineering tool. | $\bigcirc$ | $\bigcirc$ | Page 952 SP.ECPRTCL |

### 2.5 PID Control Instructions

## PID control instructions (inexact differential)

Registering the PID control data to the CPU module

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| S.PIDINIT | Stores the PID control data by the number of loops used that is set in the device number specified by (s) and later altogether in the CPU module to enable PID control. | $\bigcirc$ | $\bigcirc$ | Page 960 S(P).PIDINIT |
| SP.PIDINIT |  | $\bigcirc$ | $\bigcirc$ |  |

Performing PID operation

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| S.PIDCONT | Measures the sampling cycle and performs PID operation. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 962 \\ & \text { S(P).PIDCONT } \end{aligned}$ |
| SP.PIDCONT |  | $\bigcirc$ | $\bigcirc$ |  |

Stopping/starting the operation of specified loop number

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| S.PIDSTOP | Stops the PID operation of the loop number in the device specified by (s). | $\bigcirc$ | $\bigcirc$ | Page 965 <br> S(P).PIDSTOP |
| SP.PIDSTOP |  | $\bigcirc$ | $\bigcirc$ |  |
| S.PIDRUN | Starts the PID operation of the loop number in the device specified by (s). | $\bigcirc$ | $\bigcirc$ | Page 966 S(P).PIDRUN |
| SP.PIDRUN |  | $\bigcirc$ | $\bigcirc$ |  |

Changing the parameters of specified loop number

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| S.PIDPRMW | Changes the operation parameter of the loop number in the device specified by (s1) to the PID control data stored in the device number specified by (s2) and later. | $\bigcirc$ | $\bigcirc$ | Page 967 S(P).PIDPRM W |
| SP.PIDPRMW |  | $\bigcirc$ | $\bigcirc$ |  |

## PID control instructions (exact differential)

## Registering the PID control data to the CPU module

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| PIDINIT | Stores the PID control data by the number of loops used that is set in the device number specified by (s) and later altogether in the CPU module to enable PID control. | $\bigcirc$ | $\bigcirc$ | Page 969 PIDINIT(P) |
| PIDINITP |  | $\bigcirc$ | $\bigcirc$ |  |

## Performing PID operation

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| PIDCONT | Measures the sampling cycle and performs PID operation. | $\bigcirc$ | $\bigcirc$ | Page 971 PIDCONT(P) |
| PIDCONTP |  | $\bigcirc$ | $\bigcirc$ |  |

Stopping/starting the operation of specified loop number

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| PIDSTOP | Stops the PID operation of the loop number in the device specified by (s). | $\bigcirc$ | $\bigcirc$ | Page 974 <br> PIDSTOP(P) |
| PIDSTOPP |  | $\bigcirc$ | $\bigcirc$ |  |
| PIDRUN | Starts the PID operation of the loop number in the device specified by (s). | $\bigcirc$ | $\bigcirc$ | Page 975 PIDRUN(P) |
| PIDRUNP |  | $\bigcirc$ | $\bigcirc$ |  |

Changing the parameters of specified loop number

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| PIDPRMW | Changes the operation parameter of the loop number in the device specified by (s1) to the PID control data stored in the device number specified by (s2) and later. | $\bigcirc$ | $\bigcirc$ | Page 976 PIDPRMW(P) |
| PIDPRMWP |  | $\bigcirc$ | $\bigcirc$ |  |

### 2.6 Process Control Instructions

## I/O control instructions

■Analog input processing

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.IN | Performs following processing to the input data (PV): range check, input limiter, engineering value <br> transformation, and digital filter. | $\times$ | $\bigcirc$ | Page 993 S.IN |

■Output processing 1 with mode switching

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.OUT1 | Calculates MV $(0$ to $100 \%)$ from the input data $(\triangle M V)$, and performs the variation rate \& upper/ <br> lower limiter processing and output conversion processing. | $\times$ | $\bigcirc$ | Page 998 <br> S.OUT1 |

Output processing 2 with mode switching

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.OUT2 | Performs variation rate \& upper/lower limiter processing and output conversion processing to the <br> input data (MV). | $\times$ | $\bigcirc$ | Page 1004 <br> S.OUT2 |

## ■Manual output

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.MOUT | Reads the manipulated value (MV) from the loop tag memory, and performs output conversion <br> processing and alarm clear processing. | $\times$ | $\bigcirc$ | Page 1009 <br> S.MOUT |

Time proportioning

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.DUTY | Outputs ON and OFF by changing the ON/OFF ratio in a given cycle in proportion to the input data <br> (0 to $100 \%)$. | $\times$ | $\bigcirc$ | Page 1012 <br> S.DUTY |

## Batch counter

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.BC | Compares the input data with the set value, and outputs bit data when it reaches the set value. | $\times$ | $\bigcirc$ | Page 1018 <br> S.BC |

Pulse integration

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.PSUM | Integrates the number of input pulses, and outputs the operation result. | $\times$ | $\bigcirc$ | Page 1022 <br> S.PSUM |

## Control operation instructions

Basic PID control

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.PID | Performs process value derivative type (inexact differential) PID operation. <br> The instruction performs the following processing steps: SV setting, tracking, gain (Kp) operation, <br> PID operation, and deviation check. | $\times$ | Page 1027 <br> S.PID |  |

## Two-degree-of-freedom PID control

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.2PID | Performs two-degree-of-freedom PID control operation (inexact differential). <br> The instruction performs the following processing steps: SV setting, tracking, gain (Kp) operation, <br> two-degree-of-freedom PID control operation, and deviation check. | $\times$ | $\bigcirc$ | Page 1034 <br> S.2PID |

## Position type PID control

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.PIDP | Performs position type PID operation. <br> The instruction performs the following processing steps: SV setting, tracking, gain (Kp) operation, <br> PID operation, deviation check, and operation mode determination. <br> Depending on the operation result up to the mode determination processing, the instruction <br> performs either variation rate \& upper/lower limiter and output conversion, or alarm clear and <br> output conversion processing. | $\times$ | Page 1041 |  |
| S.PIDP |  |  |  |  |

## Sample PI control

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.SPI | Monitors the operating time (operating time (ST) or hold time (HT)), and performs the following <br> processing steps: SV setting, tracking, gain (Kp) operation, SPI operation, and deviation check if <br> the operating time is in ST. | $\times$ | O | Page 1049 <br> S.SPI |

## II-PD control

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.IPD | Performs I-PD operation. The instruction performs the following processing steps: SV setting, <br> tracking, gain Kp operation, IPD operation, and deviation check. | $\times$ | $\bigcirc$ | Page 1056 <br> S.IPD |

## Blend PI control

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.BPI | Performs blend PI operation. The instruction performs the following processing steps: SV setting, <br> tracking, gain Kp operation, BPI operation, and deviation check. | $\times$ | $\bigcirc$ | Page 1063 <br> S.BPI |

## Ratio calculation

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| S.R | Performs the following processing steps to the input data: engineering value transformation, tracking, variation rate limiter, and ratio calculation. | $\times$ | $\bigcirc$ | Page 1069 S.R |

## Upper/lower limit alarm

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.PHPL | Checks the range of the PV that has been output by the S.IN instruction. | $\times$ | $\bigcirc$ | Page 1074 <br> S.PHPL |

Lead-lag compensation

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.LLAG | Performs lead-lag compensation to the input data, and outputs the operation result. | $\times$ | $\bigcirc$ | Page 1080 <br> S.LLAG |

Integral control

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| S.I | Performs integration operation to the input data, and outputs the operation result. | $\times$ | $\bigcirc$ | Page 1082 S.I |

## Derivative control

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.D | Performs differential operation to the input data, and outputs the operation result. | $\times$ | $\bigcirc$ | Page 1084 S.D |

## Dead time

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.DED | Delays the output of the input data by the specified dead time. | $\times$ | $\bigcirc$ | Page 1086 <br> S.DED |

## ■High selector

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.HS | Outputs the maximum value of the input data. | $\times$ | $\bigcirc$ | Page 1089 <br> S.HS |

Low selector

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.LS | Outputs the minimum value of the input data. | $\times$ | $\bigcirc$ | Page 1091 <br> S.LS |

Middle value selector

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.MID | Outputs the intermediate value (the value between the maximum and minimum values) of the input <br> data. | $\times$ | $\bigcirc$ | Page 1093 <br> S.MID |

## Average value calculation

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.AVE | Calculates the mean (average) value of the input data, and outputs the operation result. | $\times$ | $\bigcirc$ | Page 1095 <br> S.AVE |

## ■Upper/Iower limiter

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.LIMT | Applies a limiter with hysteresis to the output value. | $\times$ | ○ | Page 1097 <br> S.LIMT |

Variation rate limiter 1

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.VLMT1 | Limits the variation speed when the variation rate of the input (E1) exceeds the limit. | $\times$ | $\bigcirc$ | Page 1099 <br> S.VLMT1 |

## Variation rate limiter 2

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.VLMT2 | Holds the last value when the variation rate of the input (E1) exceeds the limit. | $\times$ | $\bigcirc$ | Page 1101 <br> S.VLMT2 |

Two-position (on/off) control

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.ONF2 | Performs two-position (on/off) control. <br> The instruction performs the following processing steps: SV setting, tracking, MV correction, MV <br> output, and two-position (on/off) control. | $\times$ | ○ | Page 1103 <br> S.ONF2 |

Three-position (on/off) control

| Instruction symbol | Processing details | Reference |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.ONF3 | Performs three-position (on/off) control. <br> The instruction performs the following processing steps: SV setting, tracking, MV correction, MV <br> output, and three-position (on/off) control. | Page 1108 <br> S.ONF3 |  |  |

Dead band

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.DBND | Provides a dead band and performs output processing. | $\times$ | $\bigcirc$ | Page 1114 <br> S.DBND |

## Program setter

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.PGS | Provides control output according to the SV and MV patterns. | $\times$ | O | Page 1116 <br> S.PGS |

## ■Loop selector

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.SEL | Outputs the value selected by the selection signal from the input data in automatic mode, and <br> outputs the manipulated value (MV) in the loop tag memory in manual mode. | $\times$ | $\bigcirc$ | Page 1120 <br> S.SEL |

## Bumpless transfer

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.BUMP | Gradually brings the output value (BW) closer to the output set value (E1) from the output control <br> value (E2) when the mode switching signal (e1) changes from manual to automatic. | $\times$ | $\bigcirc$ | Page 1126 <br> S.BUMP |

## Analog memory

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.AMR | Increases or decreases the output value (BW) at a fixed rate. | $\times$ | $\bigcirc$ | Page 1129 <br> S.AMR |

## Correction operation instructions

IFunction generator

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.FG | Outputs the input data values according to the specified function generator pattern. | $\times$ | $\bigcirc$ | Page 1132 <br> S.FG |

## Inverse function generator

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.IFG | Outputs the input data values according to the specified inverse function generator pattern. | $\times$ | $\bigcirc$ | Page 1135 <br> S.IFG |

Standard filter

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.FLT | Outputs the mean (average) value of the ' $n$ ' pieces of data collected at the specified data collection <br> intervals (ST). | $\times$ | $\bigcirc$ | Page 1137 <br> S.FLT |

## Integration

| Instruction symbol | Processing details | Availability |  | Reference |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.SUM | Integrates the input data, and outputs the operation result. | $\times$ | $\bigcirc$ | Page 1140 <br> S.SUM |

## Temperature/pressure correction

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.TPC | Performs temperature/pressure correction processing to the input data. | $\times$ | O | Page 1142 <br> S.TPC |

## Engineering value transformation

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.ENG | Performs engineering value transformation processing to the input data. | $\times$ | $\bigcirc$ | Page 1145 <br> S.ENG |

Engineering value inverse transformation

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.IENG | Performs engineering value inverse transformation processing to the input data. | $\times$ | $\bigcirc$ | Page 1147 <br> S.IENG |

## Arithmetic operation instructions

## ■Addition

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.ADD | Adds input data with a coefficient. | $\times$ | $\bigcirc$ | Page 1149 <br> S.ADD |

Subtraction

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.SUB | Subtracts input data with a coefficient. | $\times$ | ○ | Page 1151 <br> S.SUB |

## ■Multiplication

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| S.MUL | Multiplies input data with a coefficient. | $\times$ | $\bigcirc$ | Page 1153 <br> S.MUL |

Division

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.DIV | Divides input data with a coefficient. | $\times$ | $\bigcirc$ | Page 1155 <br> S.DIV |

## Square root

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.SQR | Outputs the square root $(\sqrt{ })$ of input data. | $\times$ | $\bigcirc$ | Page 1157 <br> S.SQR |

## ■Absolute value

| Instruction symbol | Processing details | Availability | Reference |
| :--- | :--- | :--- | :--- | :--- |
| S.ABS | Outputs the absolute value of input data. | (1) | (2) |

## Comparison operation instructions

Comparing data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| S.> | Compares input data, and outputs the comparison result. | $\times$ | $\bigcirc$ | Page 1161 S.> |
| S.< |  | $\times$ | $\bigcirc$ | Page 1162 S.< |
| S. $=$ |  | $\times$ | $\bigcirc$ | Page 1164 S.= |
| S.>= |  | $\times$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 1165 \\ & \text { S.>= } \end{aligned}$ |
| S.<= |  | $\times$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 1167 \\ & \text { S.<= } \end{aligned}$ |

## Auto tuning

| Instruction symbol | Processing details | Availability |  | Reference |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| S.AT1 | Performs auto tuning to make initial setting of PID constants. | $\times$ | $\bigcirc$ | Page 1172 <br> S.AT1 |

### 2.7 Multiple CPU Dedicated Instructions

## Reading device data from another CPU module

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| D.DDRD | Reads the data in the device of another CPU module specified by ( n ), and stores the data to the read-source CPU module in a multiple CPU system. | $\bigcirc$ | $\bigcirc$ | Page 1183 D(P).DDRD, M(P).DDRD |
| DP.DDRD |  | $\bigcirc$ | $\bigcirc$ |  |
| M.DDRD |  | $\bigcirc$ | $\bigcirc$ |  |
| MP.DDRD |  | $\bigcirc$ | $\bigcirc$ |  |

Writing device data to another CPU module

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| D.DDWR | Writes the data specified in the own CPU module to another CPU module specified by ( n ) in a multiple CPU system. | $\bigcirc$ | $\bigcirc$ | Page 1186 D(P).DDWR, M(P).DDWR |
| DP.DDWR |  | $\bigcirc$ | $\bigcirc$ |  |
| M.DDWR |  | $\bigcirc$ | $\bigcirc$ |  |
| MP.DDWR |  | $\bigcirc$ | $\bigcirc$ |  |

## Motion CPU dedicated instructions

For available Motion CPU dedicated instructions, refer to the following.
( $\mathbb{C}]$ MELSEC iQ-R Motion Controller Programming Manual (Program Design)) MODULE DEDICATED INSTRUCTIONS

How to read the list is shown below.

| Item | Description |
| :--- | :--- |
| Instruction symbol | Indicates the instruction name. |
| Processing details | Indicates the overview of an instruction. |
| Availability | Indicates the availability of each model. <br> (1): Programmable controller CPU, (2): Process CPU <br> $\bigcirc:$ Available, $\triangle:$ Available with restrictions, $\times:$ Not available |
| Reference | Indicates the reference of detailed information. |

### 3.1 Network Common Instructions

## Link dedicated instructions

## ■Reading data from the programmable controller on another station

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| JP.READ | Reads data in units of words from a device in the programmable controller of another station. | $\bigcirc$ | $\bigcirc$ | Page 1195 |
| GP.READ |  | $\bigcirc$ | $\bigcirc$ | JP.READ, GP.READ |

■Reading data from the programmable controller on another station (with notification)

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| JP.SREAD | Reads data in units of words from a device in the programmable controller of another station. After the data reading is completed, the device of another station is turned on. (The other station can recognize that data has been read by the SREAD instruction.) | $\bigcirc$ | $\bigcirc$ | Page 1202 JP.SREAD, GP.SREAD |
| GP.SREAD |  | $\bigcirc$ | $\bigcirc$ |  |

Writing data to the programmable controller on another station

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| JP.WRITE | Writes data in units of words to a device in the programmable controller of another station. | $\bigcirc$ | $\bigcirc$ | Page 1209 |
|  |  | JP.WRITE, |  |  |

Writing data to the programmable controller on another station (with notification)

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| JP.SWRITE | Writes data in units of words to a device in the programmable controller of another station. After the data writing is completed, the device of another station is turned on. (The other station can recognize that data has been written by the SWRITE instruction.) | $\bigcirc$ | $\bigcirc$ | Page 1217 JP.SWRITE, GP.SWRITE |
| GP.SWRITE |  | $\bigcirc$ | $\bigcirc$ |  |

Sending data to the programmable controller on another station

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| JP.SEND | Sends data to the programmable controller of another station. | $\bigcirc$ | $\bigcirc$ | Page 1225 |
| GP.SEND |  | $\bigcirc$ | JP.SEND, |  |

Receiving data from the programmable controller on another station

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| JP.RECV | Reads data received from the programmable controller of another station. (For the main routine program) | $\bigcirc$ | $\bigcirc$ | Page 1232 JP.RECV, GP.RECV |
| GP.RECV |  | $\bigcirc$ | $\bigcirc$ |  |

■Receiving data from the programmable controller on another station (for interrupt programs)

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| G.RECVS | Reads data received from the programmable controller of another station. (For interrupt programs) | $\bigcirc$ | $\bigcirc$ | Page 1237 |
| Z.RECVS |  | $\bigcirc$ | $\bigcirc$ | G.RECVS, <br> Z.RECVS |

-Reading data from the programmable controller (Q series-compatible)

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| J.ZNRD | Reads data in units of words from a device in the programmable controller of another station. | $\bigcirc$ | $\bigcirc$ | Page 1241 <br> J(P).ZNRD |
| JP.ZNRD |  | $\bigcirc$ | $\bigcirc$ |  |

Writing data to the programmable controller (Q series-compatible)

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| J.ZNWR | Writes data in units of words to a device in the programmable controller of another station. | $\bigcirc$ | $\bigcirc$ | Page 1245 J(P).ZNWR |
| JP.ZNWR |  | $\bigcirc$ | $\bigcirc$ |  |

Remote RUN/STOP

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| J.REQ | Executes remote RUN or STOP for the programmable controller of another station. | $\bigcirc$ | $\bigcirc$ | Page 1250 <br> $J(P)$.REQ, <br> G(P).REQ |
| JP.REQ |  | $\bigcirc$ | $\bigcirc$ |  |
| G.REQ |  | $\bigcirc$ | $\bigcirc$ |  |
| GP.REQ |  | $\bigcirc$ | $\bigcirc$ |  |

## ■Reading/writing clock data

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| J.REQ | Reads/writes clock data from/to the programmable controller of another station. | $\bigcirc$ | $\bigcirc$ | Page 1257 <br> $J(P)$.REQ, <br> G(P).REQ |
| JP.REQ |  | $\bigcirc$ | $\bigcirc$ |  |
| G.REQ |  | $\bigcirc$ | $\bigcirc$ |  |
| GP.REQ |  | $\bigcirc$ | $\bigcirc$ |  |

## CC-Link dedicated instructions

Reading data from the target station

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| J.RIRD | Reads the specified number of points of data from a device of the target station. | $\bigcirc$ | $\bigcirc$ | Page 1264 J(P).RIRD, $G(P)$.RIRD |
| JP.RIRD |  | $\bigcirc$ | $\bigcirc$ |  |
| G.RIRD |  | $\bigcirc$ | $\bigcirc$ |  |
| GP.RIRD |  | $\bigcirc$ | $\bigcirc$ |  |

■Writing data to the target station

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| J.RIWT | Writes the specified number of points of data to a device of the target station. | $\bigcirc$ | $\bigcirc$ | Page 1269 <br> J(P).RIWT, <br> G(P).RIWT |
| JP.RIWT |  | $\bigcirc$ | $\bigcirc$ |  |
| G.RIWT |  | $\bigcirc$ | $\bigcirc$ |  |
| GP.RIWT |  | $\bigcirc$ | $\bigcirc$ |  |

## Restriction

- When the target station is QSCPU, the following network common instructions cannot be used.

SEND, RECV, RECVS, J(P).ZNRD, J(P).ZNWR, and REQ (remote RUN/STOP)

- Data cannot be written from the CPU module on another station to the QSCPU by using the following network common instructions.
WRITE, SWRITE, REQ (reading/writing clock data), and RIWT
- If the CPU module on the target station is AnUCPU, A2USCPU(-S1), or A2ASCPU(-S1) when the $J(P) . Z N R D$ or $J(P) . Z N W R$ instruction is executed, the CPU module must be the one with the following version or later.
AnUCPU: Version AY (manufactured in July 1995) or later
A2USCPU(-S1): Version CP (manufactured in July 1995) or later A2ASCPU(-S1): Version CP (manufactured in July 1995) or later


### 3.2 Ethernet Instructions

## Open/Close Processing Instructions

-Opening a connection

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| GP.CONOPEN | Establishes (opens) a connection with an external device for data communication. | $\bigcirc$ | $\bigcirc$ | Page 1274 <br> GP.CONOPEN |

Closing a connection

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| GP.CONCLOSE | Disconnects (closes) the connection from the external device during data communication. | O | O | Page 1278 <br> GP.CONCLOS |
|  |  |  | E |  |

Opening a connection

| Instruction symbol | Processing details | Availability |  | Reference |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| GP.OPEN | Establishes (opens) a connection with an external device for data communication. | $\bigcirc$ | $\bigcirc$ | Page 1280 |
|  |  | $\bigcirc$ | GP.OPEN, |  |

Closing a connection

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| GP.CLOSE | Disconnects (closes) the connection from the external device during data communication. | $\bigcirc$ | $\bigcirc$ | Page 1283 <br> GP.CLOSE, <br> ZP.CLOSE |
| ZP.CLOSE |  | $\bigcirc$ | $\bigcirc$ |  |

## Socket communications instructions

Reading receive data

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| GP.SOCRCV | Reads receive data from the external device through socket communications or fixed buffer <br> communications. This instruction is used in the main program. | $\bigcirc$ | $\bigcirc$ | Page 1285 <br> GP.SOCRCV |

Reading receive data (for interrupt programs)

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| G.SOCRCVS | Reads receive data from the external device through socket communications or fixed buffer <br> communications. This instruction is used in the interrupt program. | $\bigcirc$ | $\bigcirc$ | Page 1287 <br> G.SOCRCVS |

## Sending data

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| GP.SOCSND | Sends data to the external device through socket communications or fixed buffer communications. <br> This instruction is used in the main program. | $\bigcirc$ | $\bigcirc$ | Page 1289 <br> GP.SOCSND |

## Fixed buffer communications instructions

Reading receive data

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| GP.BUFRCV | Reads receive data from the external device through fixed buffer communications. This instruction <br> is used in the main program. | $\bigcirc$ | $\bigcirc$ | Page 1291 |
|  |  | $\bigcirc$ | GP.BUFRCV, |  |
| ZP.BUFRCV |  | ZP.BUFRCV |  |  |

## ©Reading receive data (for interrupt programs)

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| G.BUFRCVS | Reads receive data from the external device through fixed buffer communications. This instruction <br> is used in the interrupt program. | $\bigcirc$ | $\bigcirc$ | Page 1294 |
|  |  | $\bigcirc$ | G.BUFRCVS, |  |
| Z.BUFRCVS |  | Z.BUFRCVS |  |  |

Sending data

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| GP.BUFSND | Sends data to the external device through fixed buffer communications. | $\bigcirc$ | $\bigcirc$ | Page 1296 |
| ZP.BUFSND |  | $O$ | GP.BUFSND, |  |

## Reinitializing the module

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| G.UINI | Changes the setting details such as Ethernet operation settings or reinitializes the module. | $\bigcirc$ | $\bigcirc$ | Page 1299 G(P).UINI, Z(P).UINI |
| GP.UINI |  | $\bigcirc$ | $\bigcirc$ |  |
| Z.UINI |  | $\bigcirc$ | $\bigcirc$ |  |
| ZP.UINI |  | $\bigcirc$ | $\bigcirc$ |  |

## Executing the protocols registered for the predefined protocol support function

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| GP.ECPRTCL | Executes the protocol that has been set by the predefined protocol support function. | $\bigcirc$ | $\bigcirc$ | Page 1302 GP.ECPRTCL |

## Clearing error information

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| GP.ERRCLEAR | Clears error information. | $\bigcirc$ | $\bigcirc$ | Page 1307 <br> GP.ERRCLEA <br> R, <br> ZP.ERRCLEAR |
| ZP.ERRCLEAR |  | $\bigcirc$ | $\bigcirc$ |  |

## Reading error information

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| GP.ERRRD | Reads error information. | $\bigcirc$ | $\bigcirc$ | Page 1309 GP.ERRRD, ZP.ERRRD |
| ZP.ERRRD |  | $\bigcirc$ | $\bigcirc$ |  |

### 3.3 CC-Link IE Controller Network Instructions

## Remote RUN

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| J.RRUN | Sends a remote RUN request to the programmable controller of another station. | $\bigcirc$ | $\bigcirc$ | Page 1311 $J(P)$.RRUN, G(P).RRUN, Z(P).RRUN |
| JP.RRUN |  | $\bigcirc$ | $\bigcirc$ |  |
| G.RRUN |  | $\bigcirc$ | $\bigcirc$ |  |
| GP.RRUN |  | $\bigcirc$ | $\bigcirc$ |  |
| Z.RRUN |  | $\bigcirc$ | $\bigcirc$ |  |
| ZP.RRUN |  | $\bigcirc$ | $\bigcirc$ |  |

## Remote STOP

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| J.RSTOP | Sends a remote STOP request to the programmable controller of another station. | $\bigcirc$ | $\bigcirc$ | Page 1315 J(P).RSTOP, G(P).RSTOP, Z(P).RSTOP |
| JP.RSTOP |  | $\bigcirc$ | $\bigcirc$ |  |
| G.RSTOP |  | $\bigcirc$ | $\bigcirc$ |  |
| GP.RSTOP |  | $\bigcirc$ | $\bigcirc$ |  |
| Z.RSTOP |  | $\bigcirc$ | $\bigcirc$ |  |
| ZP.RSTOP |  | $\bigcirc$ | $\bigcirc$ |  |

Reading clock data from the programmable controller on another station

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| J.RTMRD | Reads clock data from the programmable controller of another station. | $\bigcirc$ | $\bigcirc$ | Page 1319 J(P).RTMRD, G(P).RTMRD, Z(P).RTMRD |
| JP.RTMRD |  | $\bigcirc$ | $\bigcirc$ |  |
| G.RTMRD |  | $\bigcirc$ | $\bigcirc$ |  |
| GP.RTMRD |  | $\bigcirc$ | $\bigcirc$ |  |
| Z.RTMRD |  | $\bigcirc$ | $\bigcirc$ |  |
| ZP.RTMRD |  | $\bigcirc$ | $\bigcirc$ |  |

Writing clock data to the programmable controller on another station

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| J.RTMWR | Writes clock data to the programmable controller of another station. | $\bigcirc$ | $\bigcirc$ | Page 1324 J(P).RTMWR, G(P).RTMWR, Z(P).RTMWR |
| JP.RTMWR |  | $\bigcirc$ | $\bigcirc$ |  |
| G.RTMWR |  | $\bigcirc$ | $\bigcirc$ |  |
| GP.RTMWR |  | $\bigcirc$ | $\bigcirc$ |  |
| Z.RTMWR |  | $\bigcirc$ | $\bigcirc$ |  |
| ZP.RTMWR |  | $\bigcirc$ | $\bigcirc$ |  |

## Setting the station number to own station

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| G.UINI | Sets the station number of the own station. | $\bigcirc$ | $\bigcirc$ | Page 1330 <br> G(P).UINI, <br> Z(P).UINI |
| GP.UINI |  | $\bigcirc$ | $\bigcirc$ |  |
| Z.UINI |  | $\bigcirc$ | $\bigcirc$ |  |
| ZP.UINI |  | $\bigcirc$ | $\bigcirc$ |  |

- When the target station is QSCPU, the RRUN and RSTOP instructions cannot be used.
- The RTMWR instruction cannot write to QSCPU of other stations.


### 3.4 CC-Link IE Field Network Instructions

## Reading data from the intelligent device station/remote device station

| Instruction symbol | Processing details | Availability |  | Reference |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| JP.REMFR | Reads data in units of words from the buffer memory in the intelligent device station or remote <br> device station. | $\bigcirc$ | $\bigcirc$ | Page 1333 |
|  |  | $\bigcirc$ | O | JP.REMFR, <br> ZP.REMFR |

## Writing data to the intelligent device station/remote device station

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| JP.REMTO | Writes data in units of words to the buffer memory in the intelligent device station or remote device station. | $\bigcirc$ | $\bigcirc$ | Page 1338 JP.REMTO, ZP.REMTO |
| ZP.REMTO |  | $\bigcirc$ | $\bigcirc$ |  |

## Setting parameters

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| G.CCPASET | Sets parameters to the master, submaster, and local stations. <br> Use the $G(P)$.CCPASET instruction to set parameters in the following cases. <br> - To change parameters without resetting the CPU module <br> - To install more modules than can be set by the engineering tool | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 1343 \\ & \text { G(P).CCPASE } \\ & \mathrm{T} \end{aligned}$ |
| GP.CCPASET |  | $\bigcirc$ | $\bigcirc$ |  |

Setting the station number to own station

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| G.UINI | Sets the station number to the local station (own station). <br> Use the engineering tool or UINI instruction to set network parameters excluding the station number. | $\bigcirc$ | $\bigcirc$ | Page 1350 <br> G(P).UINI, <br> Z(P).UINI |
| GP.UINI |  | $\bigcirc$ | $\bigcirc$ |  |
| Z.UINI |  | $\bigcirc$ | $\bigcirc$ |  |
| ZP.UINI |  | $\bigcirc$ | $\bigcirc$ |  |

## Restriction

The REMFR and REMTO instructions cannot be executed in the local station. Execute them in the master station.

The submaster function can be executed only in the master operating station.
Use the own station master/submaster function operating status (SB004E) to apply an interlock so that the submaster function is executed only in the master operating station. (Master operating station when SB004E is off)

### 3.5 CC-Link Instructions

## Reading data from the target station

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| G.RIRD | Reads data of the specified number of points from the buffer memory area of the target station or the device in the CPU module on the target station. | $\bigcirc$ | $\bigcirc$ | Page 1353 <br> G(P).RIRD |
| GP.RIRD |  | $\bigcirc$ | $\bigcirc$ |  |

## Writing data to the target station

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| G.RIWT | Writes data of the specified number of points to the buffer memory area of the target station or the device in the CPU module on the target station. | $\bigcirc$ | $\bigcirc$ | Page 1357 <br> G(P).RIWT |
| GP.RIWT |  | $\bigcirc$ | $\bigcirc$ |  |

Reading data from the buffer memory of the specified intelligent device station

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| G.RIRCV | Automatically performs handshake with the specified intelligent device station, and reads data from its buffer memory. It is available for communication with a module with a handshake signal (e.g. AJ65BT-R2N). | $\bigcirc$ | $\bigcirc$ | Page 1361 <br> G(P).RIRCV |
| GP.RIRCV |  | $\bigcirc$ | $\bigcirc$ |  |

Writing data to the buffer memory of the specified intelligent device station

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| G.RISEND | Automatically performs handshake with the specified intelligent device station, and writes data to | $\bigcirc$ | $\bigcirc$ | Page 1364 |
| GP.RISEND | its buffer memory. <br> It is available for communication with a module with a handshake signal (e.g. AJ65BT-R2N). | $\bigcirc$ | $\bigcirc$ |  |

## Reading data from the automatic update buffer

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| G.RIFR | Reads data from an automatic update or random access buffer. It is available for communication with a module with an automatic update buffer (e.g. AJ65BTR2N). | $\bigcirc$ | $\bigcirc$ | Page 1367 G(P).RIFR |
| GP.RIFR |  | $\bigcirc$ | $\bigcirc$ |  |

## Writing data to the automatic update buffer

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| G.RITO | Writes data to an automatic update or random access buffer. |  |  |  |
| GP.RITO | It is available for communication with a module with an automatic update buffer (e.g. AJ65BT- <br> R2N). | $\bigcirc$ | ○ | Page 1369 |
|  | G(P).RITO |  |  |  |

## Setting network parameters

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| G.RLPASET | Sets the network parameters to the master station, and starts up the data link. | $\bigcirc$ | $\bigcirc$ | Page 1371 |
| GP.RLPASET |  | $O$ | O | G(P).RLPASET |

## Sending/receiving a message to/from a remote device station

| Instruction symbol | Processing details | Reference |
| :--- | :--- | :--- |
| G.RDMSG | Sends/receives a message to/from a remote device station. | Page 1377 |
| GP.RDMSG |  | G(P).RDMSG |

### 3.6 Serial Communication Instructions

## Sending data using the on-demand function

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| G.ONDEMAND | Sends data using the on-demand function of SLMP (MC protocol). | $\bigcirc$ | $\bigcirc$ | Page 1382 G(P).ONDEMA ND |
| GP.ONDEMAND |  | $\bigcirc$ | $\bigcirc$ |  |

## Executing the protocols registered for the predefined protocol support function

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| G.CPRTCL | Executes the protocol registered using the engineering tool. | $\bigcirc$ | $\bigcirc$ | Page 1385 |
| GP.CPRTCL |  | $\bigcirc$ | $\bigcirc$ | G(P).CPRTCL |

## Sending data using the nonprocedural protocol

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| G.OUTPUT | Sends data in user-specified message format through communications using the nonprocedural protocol. | $\bigcirc$ | $\bigcirc$ | Page 1389 G(P).OUTPUT |
| GP.OUTPUT |  | $\bigcirc$ | $\bigcirc$ |  |

Receiving data using the nonprocedural protocol

| Instruction symbol | Processing details | Availability |  | Reference |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| G.INPUT | Receives data in user-specified message format through communications using the nonprocedural <br> protocol. | $\bigcirc$ | $\bigcirc$ | Page 1392 <br> G.INPUT |

## Sending data using the bidirectional protocol

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| G.BIDOUT | Sends the specified amount of data through communications using the bidirectional protocol. | $\bigcirc$ | $\bigcirc$ | Page 1395 |
|  |  | G(P).BIDOUT |  |  |

## Receiving data using the bidirectional protocol

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| G.BIDIN | Receives data through communications using the bidirectional protocol. | $\bigcirc$ | $\bigcirc$ | Page 1398 <br> G(P).BIDIN |
| GP.BIDIN |  | $\bigcirc$ | $\bigcirc$ |  |

## Reading the data send/receive status

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| G.SPBUSY | Reads the status of data sent/received by using the serial communication instruction. | $\bigcirc$ | $\bigcirc$ | Page 1401 |
| GP.SPBUSY |  | $O$ | $\bigcirc$ | G(P).SPBUSY |

## Receiving data using the interrupt program

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| Z.BUFRCVS | Receives data using the interrupt program through communications under control of the <br> nonprocedural protocol or bidirectional protocol. | $\bigcirc$ | $\bigcirc$ | Page 1403 <br> Z.BUFRCVS |

## Sending data by using user frames

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| G.PRR | Sends data with user frames according to the specification in the user frame specification area for | $\bigcirc$ | $\bigcirc$ | Page 1406 |
|  | Gending, through communication with the nonprocedural protocol. | G(P).PRR |  |  |

## Clearing receive data

| Instruction symbol | Processing details | Availability |  | Reference |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| ZP.CSET | Clears the receive data area without stopping send processing by using the nonprocedural <br> protocol. | $\bigcirc$ | $\bigcirc$ | Page 1409 <br> ZP.CSET |

## Registering/canceling the programmable controller CPU monitoring

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| ZP.CSET | Registers programmable controller CPU monitoring to use the programmable controller CPU <br> monitoring function, or cancels programmable controller CPU monitoring. | $\bigcirc$ | $\bigcirc$ | Page 1412 <br> ZP.CSET |

Initial setting

| Instruction symbol | Processing details | Availability |  | Reference |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| ZP.CSET | Sets the unit (word/byte) of data to be sent or received. | $\bigcirc$ | ○ | Page 1417 <br> ZP.CSET |

## Registering user frames

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | $(\mathbf{1 )}$ | (2) |  |
| G.PUTE | Registers user frames. | $\bigcirc$ | $\bigcirc$ | Page 1421 |
| GP.PUTE |  | $O$ | $\bigcirc$ | G(P).PUTE |

## Reading user frames

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| G.GETE | Reads user frames. | $\bigcirc$ | $\bigcirc$ |  |
| GP.GETE |  | Page 1424 |  |  |

## Switching the mode

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| ZP.UINI | Switches the serial communication module mode, transmission specifications, and own station <br> number. | $\bigcirc$ | $\bigcirc$ | Page 1427 <br> ZP.UINI |

### 3.7 A/D Conversion Instructions

## Switching the mode

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| G.OFFGAN | Switches the analog module mode. <br> - Normal mode to offset/gain setting mode <br> - Offset/gain setting mode to normal mode | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 1430 \\ & \text { G(P).OFFGAN } \end{aligned}$ |
| GP.OFFGAN |  | $\bigcirc$ | $\bigcirc$ |  |

## Reading the user range setting values

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| G.OGLOAD | Reads the offset/gain setting values of the user range settings of an analog module into the CPU module. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 1433 \\ & \text { G(P).OGLOAD } \end{aligned}$ |
| GP.OGLOAD |  | $\bigcirc$ | $\bigcirc$ |  |

## Restoring the user range setting values

| Instruction symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| G.OGSTOR | Restores the offset/gain settings in the user range setting stored in the CPU module into an analog <br> module. | $\bigcirc$ | $\bigcirc$ | Page 1452 |
|  | G(P).OGSTOR |  |  |  |

### 3.8 Positioning Instructions

## Restoring the absolute position

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| G.ABRST1 | Restores the absolute position of specified axis. | $\bigcirc$ | $\bigcirc$ | Page 1471 G.ABRSTD, Z.ABRSTD |
| G.ABRST2 |  | $\bigcirc$ | $\bigcirc$ |  |
| G.ABRST3 |  | $\bigcirc$ | $\bigcirc$ |  |
| G.ABRST4 |  | $\bigcirc$ | $\bigcirc$ |  |
| Z.ABRST1 |  | $\bigcirc$ | $\bigcirc$ |  |
| Z.ABRST2 |  | $\bigcirc$ | $\bigcirc$ |  |
| Z.ABRST3 |  | $\bigcirc$ | $\bigcirc$ |  |
| Z.ABRST4 |  | $\bigcirc$ | $\bigcirc$ |  |

## Starting the positioning

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| GP.PSTRT1 | Starts positioning of the specified axis. | $\bigcirc$ | $\bigcirc$ | Page 1475 GP.PSTRTD, ZP.PSTRTD |
| GP.PSTRT2 |  | $\bigcirc$ | $\bigcirc$ |  |
| GP.PSTRT3 |  | $\bigcirc$ | $\bigcirc$ |  |
| GP.PSTRT4 |  | $\bigcirc$ | $\bigcirc$ |  |
| ZP.PSTRT1 |  | $\bigcirc$ | $\bigcirc$ |  |
| ZP.PSTRT2 |  | $\bigcirc$ | $\bigcirc$ |  |
| ZP.PSTRT3 |  | $\bigcirc$ | $\bigcirc$ |  |
| ZP.PSTRT4 |  | $\bigcirc$ | $\bigcirc$ |  |


| Teaching |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Instruction symbol | Processing details | Availability |  | Reference |
|  |  | (1) | (2) |  |
| GP.TEACH1 | Performs teaching for the specified axis. | $\bigcirc$ | $\bigcirc$ | Page 1478 |
| GP.TEACH2 |  | $\bigcirc$ | $\bigcirc$ | GP.TEACHD, |
| GP.TEACH3 |  | $\bigcirc$ | $\bigcirc$ |  |
| GP.TEACH4 |  | $\bigcirc$ | $\bigcirc$ |  |
| ZP.TEACH1 |  | $\bigcirc$ | $\bigcirc$ |  |
| ZP.TEACH2 |  | $\bigcirc$ | $\bigcirc$ |  |
| ZP.TEACH3 |  | $\bigcirc$ | $\bigcirc$ |  |
| ZP.TEACH4 |  | $\bigcirc$ | $\bigcirc$ |  |

## Backing up module data (writing data to the flash ROM)

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| GP.PFWRT | Writes the positioning data and block start data in the buffer memory to the flash ROM. | $\bigcirc$ | $\bigcirc$ | Page 1481 |
| ZP.PFWRT |  | $\bigcirc$ | $\bigcirc$ | GP.PFWRT, ZP.PFWRT |

## Initializing the module

| Instruction symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| GP.PINIT | Initializes the setting data in the buffer memory and flash ROM. | $\bigcirc$ | $\bigcirc$ | Page 1483 |
| ZP.PINIT |  | $\bigcirc$ | $\bigcirc$ | GP.PINIT, ZP.PINIT | STANDARD FUNCTIONS/FUNCTION BLOCKS

How to read the list is shown below.

| Item | Description |
| :--- | :--- |
| Function symbol and function block symbol | A function and function block name are shown. |
| Processing details | An overview of the functions and function blocks is explained. |
| Availability | Indicates the availability of each model. <br> (1): Programmable controller CPU, (2): Process CPU <br> O: Available, $\triangle:$ Available with restrictions, $\times:$ Not available |
| Reference | Indicates the reference of detailed information. |

### 4.1 Standard Functions

## Type conversion functions

■Converting BOOL to WORD/DWORD

| Function symbol | Processing details | Availability | Reference |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |

Converting BOOL to INT/DINT

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| BOOL_TO_INT | Converts a value from BOOL data type to INT data type. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 1488 \\ & \text { BOOL_TO_INT } \\ & \text { (_E) } \end{aligned}$ |
| BOOL_TO_INT_E |  | $\bigcirc$ | $\bigcirc$ |  |
| BOOL_TO_DINT | Converts a value from BOOL data type to DINT data type. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 1489 \\ & \text { BOOL_TO_DI } \\ & \text { NT(_E) } \end{aligned}$ |
| BOOL_TO_DINT_E |  | $\bigcirc$ | $\bigcirc$ |  |

## Converting BOOL to TIME

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| BOOL_TO_TIME | Converts a value from BOOL data type to TIME data type. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 1490 \\ & \text { BOOL_TO_TI } \\ & \text { ME(_E) } \end{aligned}$ |
| BOOL_TO_TIME_E |  | $\bigcirc$ | $\bigcirc$ |  |

Converting BOOL to STRING

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| BOOL_TO_STRING | Converts a value from BOOL data type to STRING data type. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 1491 \\ & \text { BOOL_TO_ST } \\ & \text { RING(_E) } \end{aligned}$ |
| $\begin{aligned} & \text { BOOL_TO_STRING_ } \\ & \text { E } \end{aligned}$ |  | $\bigcirc$ | $\bigcirc$ |  |

Converting WORD to BOOL

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| WORD_TO_BOOL | Converts a value from WORD data type to BOOL data type. | $\bigcirc$ | $\bigcirc$ | Page 1492 <br> WORD_TO_B <br> OOL(_E) |
| WORD_TO_BOOL_E |  | $\bigcirc$ | $\bigcirc$ |  |

■Converting WORD to DWORD

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| WORD_TO_DWORD | Converts a value from WORD data type to DWORD data type. | $\bigcirc$ | $\bigcirc$ | Page 1493 <br> WORD_TO_D <br> WORD(_E) |
| WORD_TO_DWORD_ E |  | $\bigcirc$ | $\bigcirc$ |  |

## Converting WORD to INT/DINT

| Function symbol | Processing details | Availability | Reference |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |

Converting WORD to TIME

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| WORD_TO_TIME | Converts a value from WORD data type to TIME data type. | $\bigcirc$ | $\bigcirc$ | Page 1496 <br> WORD_TO_TI <br> ME(_E) |
| WORD_TO_TIME_E |  | $\bigcirc$ | $\bigcirc$ |  |

Converting WORD to STRING

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| WORD_TO_STRING | Converts a value from WORD data type to STRING data type. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 1497 \\ & \text { WORD_TO_ST } \\ & \text { RING(_E) } \end{aligned}$ |
| WORD_TO_STRING_ E |  | $\bigcirc$ | $\bigcirc$ |  |

## Converting DWORD to BOOL

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| DWORD_TO_BOOL | Converts a value from DWORD data type to BOOL data type. | $\bigcirc$ | $\bigcirc$ | Page 1498 <br> DWORD_TO_ BOOL(_E) |
| $\begin{aligned} & \text { DWORD_TO_BOOL_ } \\ & \text { E } \end{aligned}$ |  | $\bigcirc$ | $\bigcirc$ |  |

## Converting DWORD to WORD

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| DWORD_TO_WORD | Converts a value from DWORD data type to WORD data type. | $\bigcirc$ | $\bigcirc$ | Page 1499 <br> DWORD_TO_ <br> WORD(_E) |
| $\begin{aligned} & \text { DWORD_TO_WORD_ } \\ & \text { E } \end{aligned}$ |  | $\bigcirc$ | $\bigcirc$ |  |

Converting DWORD to INT/DINT

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| DWORD_TO_INT | Converts a value from DWORD data type to INT data type. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 1501 \\ & \text { DWORD_TO_I } \\ & \text { NT(_E) } \end{aligned}$ |
| DWORD_TO_INT_E |  | $\bigcirc$ | $\bigcirc$ |  |
| DWORD_TO_DINT | Converts a value from DWORD data type to DINT data type. | $\bigcirc$ | $\bigcirc$ | Page 1503 <br> DWORD_TO_ <br> DINT(_E) |
| DWORD_TO_DINT_E |  | $\bigcirc$ | $\bigcirc$ |  |

## Converting DWORD to TIME

| Function symbol | Processing details |  |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
| DWORD_TO_TIME | Converts a value from DWORD data type to TIME data type. | $\bigcirc$ | $\bigcirc$ | Page 1504 DWORD_TO_ TIME(_E) |
| DWORD_TO_TIME_E |  | $\bigcirc$ | $\bigcirc$ |  |

Converting DWORD to STRING

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| DWORD_TO_STRING | Converts a value from DWORD data type to STRING data type. | $\bigcirc$ | $\bigcirc$ | Page 1505 <br> DWORD_TO_ <br> STRING(_E) |
| $\begin{aligned} & \text { DWORD_TO_STRING } \\ & \text { _E } \end{aligned}$ |  | $\bigcirc$ | $\bigcirc$ |  |

Converting INT to BOOL

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| INT_TO_BOOL | Converts a value from INT data type to BOOL data type. | $\bigcirc$ | $\bigcirc$ | Page 1506 <br> INT_TO_BOOL <br> (_E) |
| INT_TO_BOOL_E |  | $\bigcirc$ | $\bigcirc$ |  |

Converting INT to WORD/DWORD

| Function symbol | Processing details | Availability | Reference |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |

Converting INT to DINT

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| INT_TO_DINT | Converts a value from INT data type to DINT data type. | $\bigcirc$ | $\bigcirc$ | Page 1509 INT_TO_DINT( _E) |
| INT_TO_DINT_E |  | $\bigcirc$ | $\bigcirc$ |  |

## Converting INT to BCD

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| INT_TO_BCD | Converts a value from INT data type to BCD data type. | $\bigcirc$ | $\bigcirc$ | Page 1510 <br> INT_TO_BCD( _E) |
| INT_TO_BCD_E |  | $\bigcirc$ | $\bigcirc$ |  |

Converting INT to REAL/LREAL

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| INT_TO_REAL | Converts a value from INT data type to REAL data type. | $\bigcirc$ | $\bigcirc$ | Page 1512 <br> INT_TO_REAL <br> (_E) |
| INT_TO_REAL_E |  | $\bigcirc$ | $\bigcirc$ |  |
| INT_TO_LREAL | Converts a value from INT data type to LREAL data type. | $\bigcirc$ | $\bigcirc$ | Page 1513 <br> INT_TO_LREA <br> L(_E) |
| INT_TO_LREAL_E |  | $\bigcirc$ | $\bigcirc$ |  |

## Converting INT to TIME

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| INT_TO_TIME | Converts a value from INT data type to TIME data type. | $\bigcirc$ | $\bigcirc$ | Page 1514 INT_TO_TIME( E) |
| INT_TO_TIME_E |  | $\bigcirc$ | $\bigcirc$ |  |

Converting INT to STRING

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| INT_TO_STRING | Converts a value from INT data type to STRING data type. | $\bigcirc$ | $\bigcirc$ | Page 1515 <br> INT_TO_STRI <br> NG(_E) |
| INT_TO_STRING_E |  | $\bigcirc$ | $\bigcirc$ |  |

Converting DINT to BOOL

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| DINT_TO_BOOL | Converts a value from DINT data type to BOOL data type. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 1517 \\ & \text { DINT_TO_BO } \\ & \text { OL(_E) } \end{aligned}$ |
| DINT_TO_BOOL_E |  | $\bigcirc$ | $\bigcirc$ |  |

Converting DINT to WORD/DWORD

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| DINT_TO_WORD | Converts a value from DINT data type to WORD data type. | $\bigcirc$ | $\bigcirc$ | Page 1518 |
| DINT_TO_WORD_E |  | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { DINT_TO_WO } \\ & \text { RD(_E) } \end{aligned}$ |
| DINT_TO_DWORD | Converts a value from DINT data type to DWORD data type. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 1520 \\ & \text { DINT_TO_DW } \\ & \text { ORD(_E) } \end{aligned}$ |
| DINT_TO_DWORD_E |  | $\bigcirc$ | $\bigcirc$ |  |

Converting DINT to INT

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| DINT_TO_INT | Converts a value from DINT data type to INT data type. | $\bigcirc$ | $\bigcirc$ | Page 1521 DINT_TO_INT( _E) |
| DINT_TO_INT_E |  | $\bigcirc$ | $\bigcirc$ |  |

Converting DINT to BCD

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| DINT_TO_BCD | Converts a value from DINT data type to BCD data type. | $\bigcirc$ | $\bigcirc$ | Page 1522 <br> DINT_TO_BCD <br> (_E) |
| DINT_TO_BCD_E |  | $\bigcirc$ | $\bigcirc$ |  |

■Converting DINT to REAL/LREAL

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| DINT_TO_REAL | Converts a value from DINT data type to REAL data type. | $\bigcirc$ | $\bigcirc$ | Page 1524 DINT_TO_REA L(_E) |
| DINT_TO_REAL_E |  | $\bigcirc$ | $\bigcirc$ |  |
| DINT_TO_LREAL | Converts a value from DINT data type to LREAL data type. | $\bigcirc$ | $\bigcirc$ | Page 1525 DINT_TO_LRE AL(_E) |
| DINT_TO_LREAL_E |  | $\bigcirc$ | $\bigcirc$ |  |

Converting DINT to TIME

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| DINT_TO_TIME | Converts a value from DINT data type to TIME data type. | $\bigcirc$ | $\bigcirc$ | Page 1526 <br> DINT_TO_TIM <br> E(_E) |
| DINT_TO_TIME_E |  | $\bigcirc$ | $\bigcirc$ |  |

## Converting DINT to STRING

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| DINT_TO_STRING | Converts a value from DINT data type to STRING data type. | $\bigcirc$ | $\bigcirc$ | Page 1527 <br> DINT_TO_STR <br> ING(_E) |
| DINT_TO_STRING_E |  | $\bigcirc$ | $\bigcirc$ |  |

Converting BCD to INT/DINT

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| BCD_TO_INT | Converts a value from BCD data type to INT data type. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 1529 \\ & \text { BCD_TO_INT( } \\ & \text { _E) } \end{aligned}$ |
| BCD_TO_INT_E |  | $\bigcirc$ | $\bigcirc$ |  |
| BCD_TO_DINT | Converts a value from BCD data type to DINT data type. | $\bigcirc$ | $\bigcirc$ | Page 1531 BCD_TO_DINT (_E) |
| BCD_TO_DINT_E |  | $\bigcirc$ | $\bigcirc$ |  |

Converting BCD to STRING

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| BCD_TO_STRING | Converts a value from BCD data type to STRING data type. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 1534 \\ & \text { BCD_TO_STRI } \\ & \text { NG(_E) } \end{aligned}$ |
| BCD_TO_STRING_E |  | $\bigcirc$ | $\bigcirc$ |  |

Converting REAL to INT/DINT

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| REAL_TO_INT | Converts a value from REAL data type to INT data type. | $\bigcirc$ | $\bigcirc$ | Page 1536 <br> REAL_TO_INT <br> (_E) |
| REAL_TO_INT_E |  | $\bigcirc$ | $\bigcirc$ |  |
| REAL_TO_DINT | Converts a value from REAL data type to DINT data type. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 1537 \\ & \text { REAL_TO_DIN } \\ & \text { T(_E) } \end{aligned}$ |
| REAL_TO_DINT_E |  | $\bigcirc$ | $\bigcirc$ |  |

Converting REAL to LREAL

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| REAL_TO_LREAL | Converts a value from REAL data type to LREAL data type. | $\bigcirc$ | $\bigcirc$ | Page 1538 <br> REAL_TO_LR <br> EAL(_E) |
| REAL_TO_LREAL_E |  | $\bigcirc$ | $\bigcirc$ |  |

## Converting REAL to STRING

| Function symbol | Processing details | Availability | Reference |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |
| REAL_TO_STRING | Converts a REAL data type value to STRING data type (exponential form). | $\bigcirc$ | $\bigcirc$ |
| REAL_TO_STRING_E |  | Page 1539 |  |
|  |  | REAL_TO_ST <br> RING(_E) |  |

## Converting LREAL to INT/DINT

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| LREAL_TO_INT | Converts a value from LREAL data type to INT data type. | $\bigcirc$ | $\bigcirc$ | Page 1542 |
| LREAL_TO_INT_E |  | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { LREAL_TO_IN } \\ & \text { T(_E) } \end{aligned}$ |
| LREAL_TO_DINT | Converts a value from LREAL data type to DINT data type. | $\bigcirc$ | $\bigcirc$ | Page 1543 <br> LREAL_TO_DI <br> NT(_E) |
| LREAL_TO_DINT_E |  | $\bigcirc$ | $\bigcirc$ |  |

Converting LREAL to REAL

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| LREAL_TO_REAL | Converts a value from LREAL data type to REAL data type. | $\bigcirc$ | $\bigcirc$ | Page 1544 <br> LREAL_TO_R <br> EAL(_E) |
| LREAL_TO_REAL_E |  | $\bigcirc$ | $\bigcirc$ |  |

Converting TIME to BOOL

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| TIME_TO_BOOL | Converts a value from TIME data type to BOOL data type. | $\bigcirc$ | $\bigcirc$ | Page 1545 TIME_TO_BO OL(_E) |
| TIME_TO_BOOL_E |  | $\bigcirc$ | $\bigcirc$ |  |

## Converting TIME to WORD/DWORD

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| TIME_TO_WORD | Converts a value from TIME data type to WORD data type. | $\bigcirc$ | $\bigcirc$ | Page 1546 |
| TIME_TO_WORD_E |  | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { TIME_TO_WO } \\ & \text { RD(_E) } \end{aligned}$ |
| TIME_TO_DWORD | Converts a value from TIME data type to DWORD data type. | $\bigcirc$ | $\bigcirc$ | Page 1547 <br> TIME_TO_DW ORD(_E) |
| TIME_TO_DWORD_E |  | $\bigcirc$ | $\bigcirc$ |  |

Converting TIME to INT/DINT

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| TIME_TO_INT | Converts a value from TIME data type to INT data type. | $\bigcirc$ | $\bigcirc$ | Page 1548 <br> TIME_TO_INT( _E) |
| TIME_TO_INT_E |  | $\bigcirc$ | $\bigcirc$ |  |
| TIME_TO_DINT | Converts a value from TIME data type to DINT data type. | $\bigcirc$ | $\bigcirc$ | Page 1549 <br> TIME_TO_DIN <br> T(_E) |
| TIME_TO_DINT_E |  | $\bigcirc$ | $\bigcirc$ |  |

Converting TIME to STRING

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| TIME_TO_STRING | Converts a value from TIME data type to STRING data type. | $\bigcirc$ | $\bigcirc$ | Page 1550 <br> TIME_TO_STR <br> ING(_E) |
| TIME_TO_STRING_E |  | $\bigcirc$ | $\bigcirc$ |  |

## Converting STRING to BOOL

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| STRING_TO_BOOL | Converts a value from STRING data type to BOOL data type. | $\bigcirc$ | $\bigcirc$ | Page 1552 <br> STRING_TO_B <br> OOL(_E) |
| $\begin{aligned} & \text { STRING_TO_BOOL_} \\ & \mathrm{E} \end{aligned}$ |  | $\bigcirc$ | $\bigcirc$ |  |

## Converting STRING to WORD/DWORD

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| STRING_TO_WORD | Converts a value from STRING data type to WORD data type. | $\bigcirc$ | $\bigcirc$ | Page 1553 <br> STRING_TO_ <br> WORD(_E) |
| $\begin{aligned} & \text { STRING_TO_WORD_ } \\ & \text { E } \end{aligned}$ |  | $\bigcirc$ | $\bigcirc$ |  |
| STRING_TO_DWORD | Converts a value from STRING data type to DWORD data type. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 1554 \\ & \text { STRING_TO_ } \\ & \text { DWORD(_E) } \end{aligned}$ |
| $\begin{aligned} & \text { STRING_TO_DWORD } \\ & \text { _E } \end{aligned}$ |  | $\bigcirc$ | $\bigcirc$ |  |

## Converting STRING to INT/DINT

| Function symbol | Processing details | Availability | Reference |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |

Converting STRING to BCD

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| STRING_TO_BCD | Converts a value from STRING data type to BCD data type. | $\bigcirc$ | $\bigcirc$ | Page 1559 <br> STRING_TO_B <br> CD(_E) |
| STRING_TO_BCD_E |  | $\bigcirc$ | $\bigcirc$ |  |

■Converting STRING to REAL

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| STRING_TO_REAL | Converts a value from STRING data type to REAL data type. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 1561 \\ & \text { STRING_TO_ } \\ & \text { REAL__E) } \\ & \hline \end{aligned}$ |
| STRING_TO_REAL_E |  | $\bigcirc$ | $\bigcirc$ |  |

Converting STRING to TIME

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| STRING_TO_TIME | Converts a value from STRING data type to TIME data type. | $\bigcirc$ | $\bigcirc$ | Page 1564 STRING_TO_T IME(_E) |
| STRING_TO_TIME_E |  | $\bigcirc$ | $\bigcirc$ |  |

Converting bit array to INT/DINT

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| BITARR_TO_INT | Converts the specified number of bits in a bit array to an INT data type value. | $\bigcirc$ | $\bigcirc$ | Page 1565 <br> BITARR_TO_I <br> NT(_E) |
| BITARR_TO_INT_E |  | $\bigcirc$ | $\bigcirc$ |  |
| BITARR_TO_DINT | Converts the specified number of bits in a bit array to a DINT data type value. | $\bigcirc$ | $\bigcirc$ | Page 1566 <br> BITARR_TO_D <br> INT(_E) |
| BITARR_TO_DINT_E |  | $\bigcirc$ | $\bigcirc$ |  |

Converting INT/DINT to bit array

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| INT_TO_BITARR | Outputs the lower n bits of the INT data type value to the bit array. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 1567 \\ & \text { INT_TO_BITA } \\ & \text { RR(_E) } \end{aligned}$ |
| INT_TO_BITARR_E |  | $\bigcirc$ | $\bigcirc$ |  |
| DINT_TO_BITARR | Outputs the lower n bits of the DINT data type value to the bit array. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 1568 \\ & \text { DINT_TO_BIT } \\ & \text { ARR(_E) } \end{aligned}$ |
| DINT_TO_BITARR_E |  | $\bigcirc$ | $\bigcirc$ |  |

## Copying the bit array

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| CPY_BITARR | Copies the bit array by the specified number of bits. | $\bigcirc$ | $\bigcirc$ | Page 1569 CPY_BITARR( _E) |
| CPY_BITARR_E |  | $\bigcirc$ | $\bigcirc$ |  |

## Reading/writing/copying the specified bit of the word label

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| GET_BIT_OF_INT | Reads a value from the specified bit of a word label. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 1570 \\ & \text { GET_BIT_OF_I } \\ & \text { NT(_E) } \end{aligned}$ |
| GET_BIT_OF_INT_E |  | $\bigcirc$ | $\bigcirc$ |  |
| SET_BIT_OF_INT | Writes a value to the specified bit of a word label. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 1572 \\ & \text { SET_BIT_OF_I } \\ & \text { NT(_E) } \end{aligned}$ |
| SET_BIT_OF_INT_E |  | $\bigcirc$ | $\bigcirc$ |  |
| CPY_BIT_OF_INT | Copies the specified bit of the word label to the specified bit of another word label. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 1574 \\ & \text { CPY_BIT_OF_I } \\ & \text { NT(_E) } \end{aligned}$ |
| CPY_BIT_OF_INT_E |  | $\bigcirc$ | $\bigcirc$ |  |
| Getting the start data |  |  |  |  |
| Function symbol | Processing details | Availability  <br> (1) $(2)$ |  | Reference |
|  |  | (1) | (2) |  |
| GET_BOOL_ADDR | Outputs the top data of the specified data as the BOOL, INT, or WORD type data. | $\bigcirc$ | $\bigcirc$ | Page 1576GET_BOOL_ADDR,GET_INT_ADDR,GET_WORD_ADDR |
| GET_INT_ADDR |  | $\bigcirc$ | $\bigcirc$ |  |
| GET_WORD_ADDR |  | $\bigcirc$ | $\bigcirc$ |  |

## Single variable functions

Calculating the absolute value

| Function symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| ABS | Outputs the absolute value of an input value. | $\bigcirc$ | $\bigcirc$ | Page 1577 |
|  | ABS_(_E) |  |  |  |

## ■Calculating the square root

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| SQRT | Outputs the square root of an input value. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 1579 \\ & \text { SQRT(_E) } \end{aligned}$ |
| SQRT_E |  | $\bigcirc$ | $\bigcirc$ |  |

## ©Calculating the natural logarithm

| Function symbol | Processing details | Availability | Reference |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |

Calculating the common logarithm

| Function symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| LOG | Outputs the common logarithm (logarithm with base 10) of an input value. | $\bigcirc$ | $\bigcirc$ | Page 1581 |
|  | LOG_E |  | $\bigcirc$ | LOG(_E) |

Calculating the exponent

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| EXP | Outputs the exponent of an input value. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 1583 \\ & \text { EXP(_E) } \end{aligned}$ |
| EXP_E |  | $\bigcirc$ | $\bigcirc$ |  |

■Calculating the sine/cosine/tangent

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| SIN | Outputs the sine of an input value. | $\bigcirc$ | $\bigcirc$ | Page 1584 <br> SIN(_E) |
| SIN_E |  | $\bigcirc$ | $\bigcirc$ |  |
| cos | Outputs the cosine of an input value. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 1585 \\ & \text { COS(_E) } \end{aligned}$ |
| COS_E |  | $\bigcirc$ | $\bigcirc$ |  |
| TAN | Outputs the tangent of an input value. | $\bigcirc$ | $\bigcirc$ | Page 1586 <br> TAN(_E) |
| TAN_E |  | $\bigcirc$ | $\bigcirc$ |  |

Calculating the arc sine/arc cosine/arc tangent

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| ASIN | Outputs the arc sine ( $\mathrm{SIN}^{-1}$ ) of an input value. | $\bigcirc$ | $\bigcirc$ | Page 1587 <br> ASIN(_E) |
| ASIN_E |  | $\bigcirc$ | $\bigcirc$ |  |
| ACOS | Outputs the arc cosine ( $\mathrm{COS}^{-1}$ ) of an input value. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 1588 \\ & \text { ACOS(_E) } \end{aligned}$ |
| ACOS_E |  | $\bigcirc$ | $\bigcirc$ |  |
| ATAN | Outputs the arc tangent ( $\mathrm{TAN}^{-1}$ ) of an input value. | $\bigcirc$ | $\bigcirc$ | Page 1589 ATAN(_E) |
| ATAN_E |  | $\bigcirc$ | $\bigcirc$ |  |

## Arithmetic operation functions

## Addition

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| ADD | Outputs the sum of input values ((s1)+(s2)+..+(s28)). | $\bigcirc$ | $\bigcirc$ | Page 1590 <br> ADD(_E) |
| ADD_E |  | $\bigcirc$ | $\bigcirc$ |  |

## -Multiplication

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| MUL | Outputs the product of input values ((s1) $\times(\mathrm{s} 2) \times \cdots \times(\mathrm{s} 28)$ ). | $\bigcirc$ | $\bigcirc$ | Page 1592 <br> MUL(_E) |
| MUL_E |  | $\bigcirc$ | $\bigcirc$ |  |

## Subtraction

| Function symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| SUB | Outputs the difference between input values ((s1)-(s2)). | $\bigcirc$ | $\bigcirc$ | Page 1594 |
| SUB_E |  | $\bigcirc$ | $\bigcirc$ | SUB(_E) |

## Division

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| DIV | Outputs the quotient of input values ((s1) $\div(\mathrm{s} 2)$ ). | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 1596 \\ & \text { DIV(_E) } \end{aligned}$ |
| DIV_E |  | $\bigcirc$ | $\bigcirc$ |  |

## ■Remainder

| Function symbol | Processing details | Availability | Reference |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |

Exponentiation

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| EXPT | Outputs the exponentiation of an input value. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 1600 \\ & \text { EXPT(_E) } \end{aligned}$ |
| EXPT_E |  | $\bigcirc$ | $\bigcirc$ |  |

Assignment (move operation)

| Function symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| MOVE | Outputs the assignment value of an input value. | $\bigcirc$ | $\bigcirc$ | Page 1601 |
| MOVE_E |  | $\bigcirc$ | $\bigcirc$ | MOVE(_E) |

## Bit shift functions

- Shifting data to the left/right by $\mathbf{n}$ bit(s)

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| SHL | Shifts the input value to the left by (n) bit(s), and outputs the operation result. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 1603 \\ & \text { SHL(_E) } \end{aligned}$ |
| SHL_E |  | $\bigcirc$ | $\bigcirc$ |  |
| SHR | Shifts the input value to the right by ( n ) bit(s), and outputs the operation result. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 1605 \\ & \text { SHR(_E) } \end{aligned}$ |
| SHR_E |  | $\bigcirc$ | $\bigcirc$ |  |

Rotating data to the left/right by $\mathbf{n}$ bit(s)

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| ROL | Rotates the input value to the left by ( n ) bit(s), and outputs the operation result. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 1607 \\ & \text { ROL(_E) } \end{aligned}$ |
| ROL_E |  | $\bigcirc$ | $\bigcirc$ |  |
| ROR | Rotates the input value to the right by ( n ) bit(s), and outputs the operation result. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 1609 \\ & \text { ROR(_E) } \end{aligned}$ |
| ROR_E |  | $\bigcirc$ | $\bigcirc$ |  |

## Boolean functions

IAND operation, OR operation, XOR operation

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| AND | Outputs the logical product of input values. | $\bigcirc$ | $\bigcirc$ | Page 1611 <br> AND(_E), <br> OR(_E), <br> XOR(_E) |
| AND_E |  | $\bigcirc$ | $\bigcirc$ |  |
| OR | Outputs the logical sum of input values. | $\bigcirc$ | $\bigcirc$ |  |
| OR_E |  | $\bigcirc$ | $\bigcirc$ |  |
| XOR | Outputs the exclusive logical sum of input values. | $\bigcirc$ | $\bigcirc$ |  |
| XOR_E |  | $\bigcirc$ | $\bigcirc$ |  |

## ■NOT operation

| Function symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| NOT | Outputs the logical NOT of input values. | $\bigcirc$ | $\bigcirc$ | Page 1614 |
|  |  | NOT(_E) |  |  |

## Selection functions

Selecting a value

| Function symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| SEL | Outputs the selected input value. | $\bigcirc$ | $\bigcirc$ | Page 1615 |
|  |  | SEL(_E) |  |  |

## Selecting the maximum/minimum value

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| MAX | Outputs the maximum input value. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 1617 \\ & \text { MAX(_E), } \\ & \text { MIN(_E) } \end{aligned}$ |
| MAX_E |  | $\bigcirc$ | $\bigcirc$ |  |
| MIN | Outputs the minimum input value. | $\bigcirc$ | $\bigcirc$ |  |
| MIN_E |  | $\bigcirc$ | $\bigcirc$ |  |

## Controlling the upper/lower limit

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| LIMIT | Outputs an input value that has been controlled in terms of the upper and lower limits. | $\bigcirc$ | $\bigcirc$ | Page 1619 <br> LIMIT(_E) |
| LIMIT_E |  | $\bigcirc$ | $\bigcirc$ |  |

■Multiplexer

| Function symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| MUX | Outputs one of the input values. | $\bigcirc$ | $\bigcirc$ | Page 1622 |
| MUX_E |  | O | O | MUX(_E) |

## Comparison functions

## Comparing data

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| GT | Outputs the comparison result of input values. | $\bigcirc$ | $\bigcirc$ | Page 1624GT(_E),GE(_E),EQ(_E),LE(_E), LT(_E) |
| GT_E |  | $\bigcirc$ | $\bigcirc$ |  |
| GE |  | $\bigcirc$ | $\bigcirc$ |  |
| GE_E |  | $\bigcirc$ | $\bigcirc$ |  |
| EQ |  | $\bigcirc$ | $\bigcirc$ |  |
| EQ_E |  | $\bigcirc$ | $\bigcirc$ |  |
| LE |  | $\bigcirc$ | $\bigcirc$ |  |
| LE_E |  | $\bigcirc$ | $\bigcirc$ |  |
| LT |  | $\bigcirc$ | $\bigcirc$ |  |
| LT_E |  | $\bigcirc$ | $\bigcirc$ |  |
| NE |  | $\bigcirc$ | $\bigcirc$ | Page 1626 |
| NE_E |  | $\bigcirc$ | $\bigcirc$ | NE(_E) |

## String functions

## ■Detecting a string length

| Function symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| LEN | Detects and outputs the length of the string input. | $\bigcirc$ | $\bigcirc$ | Page 1628 |
| LEN_E |  | $O$ | $\bigcirc$ | LEN(_E) |

## Extracting string data from the left/right

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| LEFT | Extracts and outputs the specified number of characters, starting from the left end of the string input. | $\bigcirc$ | $\bigcirc$ | Page 1629 <br> LEFT(_E), <br> RIGHT(_E) |
| LEFT_E |  | $\bigcirc$ | $\bigcirc$ |  |
| RIGHT | Extracts and outputs the specified number of characters, starting from the right end of the string input. | $\bigcirc$ | $\bigcirc$ |  |
| RIGHT_E |  | $\bigcirc$ | $\bigcirc$ |  |

## Extracting string data

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| MID | Extracts and outputs the specified number of characters, starting from the specified position of the string input. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 1631 \\ & \text { MID(_E) } \end{aligned}$ |
| MID_E |  | $\bigcirc$ | $\bigcirc$ |  |

## Concatenating string data

| Function symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | $(\mathbf{1 )}$ | (2) |  |
| CONCAT | Concatenates character strings, and outputs the operation result. | $\bigcirc$ | $\bigcirc$ | Page 1633 |
| CONCAT(_E) |  |  |  |  |

## Inserting string data

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| INSERT | Inserts a character string into another string, and outputs the operation result. | $\bigcirc$ | $\bigcirc$ | Page 1635 <br> INSERT(_E) |
| INSERT_E |  | $\bigcirc$ | $\bigcirc$ |  |

## Deleting string data

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| DELETE | Deletes the specified range in a character string, and outputs the operation result. | $\bigcirc$ | $\bigcirc$ | Page 1637 <br> DELETE(_E) |
| DELETE_E |  | $\bigcirc$ | $\bigcirc$ |  |

-Replacing string data

| Function symbol | Processing details | Availability |  | Reference |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| REPLACE | Replaces the specified range in a character string, and outputs the operation result. | $\bigcirc$ | $\bigcirc$ | Page 1639 |
|  | REPLACE(_E) |  |  |  |

Searching string data

| Function symbol | Processing details | Availability | Reference |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |  |
| FIND | Searches a character string, and outputs the operation result. | $\bigcirc$ | $\bigcirc$ | Page 1642 |  |
|  | FIND_E |  | $\bigcirc$ | $\bigcirc$ | FIND(_E) |

## Time data type functions

■Addition

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| ADD_TIME | Outputs the sum ((s1)+(s2)) of the TIME data type input values. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 1644 \\ & \text { ADD_TIME(_E) } \end{aligned}$ |
| ADD_TIME_E |  | $\bigcirc$ | $\bigcirc$ |  |

Subtraction

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| SUB_TIME | Outputs the difference ((s1)-(s2)) between the TIME data type input values. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 1646 \\ & \text { SUB_TIME(_E) } \end{aligned}$ |
| SUB_TIME_E |  | $\bigcirc$ | $\bigcirc$ |  |

## ■Multiplication

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| MUL_TIME | Outputs the product ((s1) $\times$ (s2)) of the TIME data type input values. | $\bigcirc$ | $\bigcirc$ | Page 1648 <br> MUL_TIME(_E) |
| MUL_TIME_E |  | $\bigcirc$ | $\bigcirc$ |  |

## Division

| Function symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| DIV_TIME | Outputs the quotient ((s1) $\div(\mathrm{s} 2)$ )of the TIME data type input values. | $\bigcirc$ | $\bigcirc$ | Page 1650 <br> DIV_TIME(_E) |
| DIV_TIME_E |  | $\bigcirc$ | $\bigcirc$ |  |

### 4.2 Standard Function Flocks

## Bistable function blocks

Bistable function block (set-dominant)

| Function block <br> symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| SR | Discriminates between two input values, and outputs1 (TRUE) or 0 (FALSE). | $\bigcirc$ | $\bigcirc$ | Page 1654 |
| SR_E |  | $O$ | SR(_E) |  |

Bistable function block (reset-dominant)

| Function block symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| RS | Discriminates between two input values, and outputs1 (TRUE) or 0 (FALSE). | $\bigcirc$ | $\bigcirc$ | Page 1656 <br> RS(_E) |
| RS_E |  | $\bigcirc$ | $\bigcirc$ |  |

## Edge detection function blocks

Detecting a rising edge

| Function block symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| R_TRIG | Detects a signal rising edge, and outputs the pulse signal. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 1658 \\ & \text { R_TRIG(_E) } \end{aligned}$ |
| R_TRIG_E |  | $\bigcirc$ | $\bigcirc$ |  |

## Detecting a falling edge

| Function block <br> symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| F_TRIG | Detects a signal falling edge, and outputs the pulse signal. | $\bigcirc$ | $\bigcirc$ | Page 1660 |
| F_TRIG_E |  | $O$ | $\bigcirc$ | F_TRIG(_E) |

## Counter function blocks

■Up counter

| Function block <br> symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| CTU | Counts up the number of rising edges of a signal. | $\bigcirc$ | ○ | Page 1662 |
|  | CTU(_E) |  |  |  |

## Down counter

| Function block <br> symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| CTD | Counts down the number of rising edges of a signal. | $\bigcirc$ | ○ | Page 1664 |
| CTD_E |  | $O$ | O | CTD(_E) |

■Up/down counter

| Function block <br> symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| CTUD | Counts up or down the number of rising edges of a signal. | $\bigcirc$ | $\bigcirc$ | Page 1666 |
| CTUD_E |  | $\bigcirc$ | $\bigcirc$ | CTUD(_E) |

Counter function block

| Function block <br> symbol | Processing details | Availability | Reference |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |
| COUNTER_FB_M | Starts counting up when the execution condition is satisfied. | O | Page 1669 <br> COUNTER_FB <br> M |

## Timer function blocks

Pulse timer

| Function block <br> symbol | Processing details | Availability | Reference |
| :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |
| TP | Keeps the signal on for the specified period of time. | $\bigcirc$ | $\bigcirc$ |
| TP_E |  | Page 1671 |  |

On delay timer

| Function block <br> symbol | Processing details | Availability | Reference |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
|  |  | (1) | (2) |  |
| TON | Turns on a signal after the specified period of time. | $\bigcirc$ | $\bigcirc$ | Page 1673 |
| TON_E |  | $\bigcirc$ | $\bigcirc$ |  |

## -Off delay timer

| Function block symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| TOF | Turns off a signal after the specified period of time. | $\bigcirc$ | $\bigcirc$ | $\begin{aligned} & \text { Page } 1675 \\ & \text { TOF(_E) } \end{aligned}$ |
| TOF_E |  | $\bigcirc$ | $\bigcirc$ |  |

## Timer function block

| Function block symbol | Processing details | Availability |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  |  | (1) | (2) |  |
| TIMER_10_FB_M | Starts counting a timer when the execution condition is satisfied, and continues counting until the timer reaches the set value. | $\bigcirc$ | $\bigcirc$ | Page 1677 <br> TIMER_口_M |
| TIMER_100_FB_M |  | $\bigcirc$ | $\bigcirc$ |  |
| TIMER_HIGH_FB_M |  | $\bigcirc$ | $\bigcirc$ |  |
| TIMER_LOW_FB_M |  | $\bigcirc$ | $\bigcirc$ |  |
| TIMER_CONT_FB_M |  | $\bigcirc$ | $\bigcirc$ |  |
| TIMER_CONTHFB_M |  | $\bigcirc$ | $\bigcirc$ |  |

## PART 3 CPU MODULE INSTRUCTIONS

Part 3 consists of the following chapters.

5 SEQUENCE INSTRUCTIONS

6 BASIC INSTRUCTIONS

7 APPLICATION INSTRUCTIONS

8 BUILT-IN ETHERNET FUNCTION INSTRUCTIONS

9 PID CONTROL INSTRUCTIONS

10 PROCESS CONTROL INSTRUCTIONS

11 MULTIPLE CPU DEDICATED INSTRUCTIONS

## 5 SEQUENCE INSTRUCTIONS

### 5.1 Contact Instructions

## Operation start, series connection, parallel connection

## LD, LDI, AND, ANI, OR, ORI

- LD: Normally open contact operation start, LDI: Normally closed contact operation start

These instructions output the on/off information of the specified device as the operation result.

- AND: Normally open contact series connection, ANI: Normally closed contact series connection

These instructions perform an AND operation between the on/off information of the specified device and the previous operation result, and output the operation result.

- OR: Single normally open contact parallel connection, ORI: Single normally closed contact parallel connection These instructions perform an OR operation between the on/off information of the specified device and the previous operation result, and output the operation result.


Setting data
Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Device used as a contact | - | Bit | ANY＿BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others(DX) |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロロロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | － | － | － | $\bigcirc$ |

## Processing details

■LD，LDI
－LD is a normally open contact operation start instruction and LDI is a normally closed contact operation start instruction． These instructions read the on／off information ${ }^{* 1}$ of the specified bit device，and output it as the operation result．
＊1 When a bit of a word device is specified，the instruction outputs on or off according to the status（1 or 0 ）of the specified bit．

## IAND，ANI

－AND is a normally open contact series connection instruction and ANI is a normally closed contact series connection instruction．These instructions read the on／off information ${ }^{* 1}$ of the specified bit device，perform an AND operation with the previous operation result，and output the operation result．
＊1 When a bit of a word device is specified，the instruction outputs on or off according to the status（1 or 0 ）of the specified bit．
－Note the following when creating or displaying a program using the engineering tool（in ladder edit mode）．
－Write mode：When the AND and ANI instructions are connected in series，a ladder with up to 24 steps can be created．
－Read mode：When the AND and ANI instructions are connected in series，a ladder with up to 24 steps can be displayed．If there are more than 24 steps，up to 24 steps are displayed．

## חOR，ORI

－OR is a single normally open contact parallel connection instruction and ORI is a single normally open contact parallel connection instruction．These instructions read the on／off information ${ }^{* 1}$ of the specified bit device，perform an OR operation with the previous operation result，and output the operation result．
＊1 When a bit of a word device is specified，the instruction outputs on or off according to the status（1 or 0 ）of the specified bit．
－Note the following when creating or displaying a program using the engineering tool（in ladder edit mode）．
－Write mode：Up to 23 OR and ORI instructions can be connected consecutively．
－Read mode：Up to 23 OR and ORI instructions connected consecutively can be displayed．A ladder with more than 23 instructions cannot be displayed correctly．

$$
\text { Point } \xlongequal{\rho} \text { Specify a bit in a word device in hexadecimal. (For example, specify "DO.0B" for b11 in D0.) }
$$

## Operation error

There is no operation error．

## Pulse operation start, pulse series connection, pulse parallel connection

## LDP, LDF, ANDP, ANDF, ORP, ORF

- LDP: Rising edge pulse operation start

This instruction turns on only at the rising edge (off to on) of the specified bit device.

- LDF: Falling edge pulse operation start

This instruction turns on only at the falling edge (on to off) of the specified bit device.

- ANDP: Rising edge pulse series connection, ANDF: Falling edge pulse series connection

These instructions perform an AND operation with the previous operation result.

- ORP: Rising edge pulse parallel connection, ORF: Falling edge pulse parallel connection

These instructions perform an OR operation with the previous operation result.



Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| LDP | Every scan |
| LDF |  |
| ANDP |  |
| ANDF |  |
| ORP |  |
| ORF |  |

## Setting data

■Description, range, data type

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| $(\mathrm{s})$ | Device used as a contact | - | Bit | ANY_BOOL |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others(DX) |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3E미（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | － | － | － | $\bigcirc$ |

## Processing details

## ■LDP，LDF

－LDP is a rising edge pulse operation start instruction，and turns on only at the rising edge（off to on）of the specified bit device．When a bit－specified word device is used，this instruction turns on only when the specified bit changes from 0 to 1 ． In cases where there is an LDP instruction only，it acts identically to instructions for conversion to pulses that are executed during on（ロP）．

Ladder diagram using the LDP instruction Ladder diagram not using the LDP instruction


－LDF is a falling edge pulse operation start instruction and turns on only at the falling edge（on to off）of the specified bit device．When a bit－specified word device is used，this instruction turns on only when the specified bit changes from 1 to 0 ．
－If the LDP instruction is used in the program written in ST language or FBD／LD，ENO turns on at the rising edge（off to on） of the specified bit device（s）．
－If the LDF instruction is used in the program written in ST language or FBD／LD，ENO turns on at the falling edge（on to off） of the specified bit device（s）．
－If the LDP or LDF instruction is used in the program written in ST language or FBD／LD，always set EN to TRUE．

## ■ANDP，ANDF

－ANDP is a rising edge pulse series connection instruction and ANDF is a falling edge pulse series connection instruction． These instructions perform an AND operation with the previous operation result，and output the operation result．The following table lists the on／off information used by the ANDP and ANDF instructions．

| Device specified by ANDP or ANDF | ANDP status | ANDF status |  |
| :--- | :--- | :--- | :--- |
| Bit device | Bit－specified word device |  |  |
| Off $\rightarrow$ On | $0 \rightarrow 1$ | On | Off |
| Off | 0 | Off | Off |
| On | 1 | Off | Off |
| On $\rightarrow$ Off | $1 \rightarrow 0$ | Off | On |

－If the ANDP instruction is used in the program written in ST or FBD／LD language，ENO turns on when the result of AND operation between EN and the rising edge of the specified bit device（s）is on．EN will not be an execution condition．
－If the ANDF instruction is used in the program written in ST or FBD／LD language，ENO turns on when the result of AND operation between EN and the falling edge of the specified bit device（s）is on．EN will not be an execution condition．

## ORP, ORF

- ORP is a rising edge pulse parallel connection instruction and ORF is a falling edge pulse parallel connection instruction. These instructions perform an OR operation with the previous operation result, and output the operation result. The following table lists the on/off information used by the ORP and ORF instructions.

| Device specified by ORP or ORF | ORP status | ORF status |  |
| :--- | :--- | :--- | :--- |
| Bit device | Bit-specified word device |  |  |
| Off $\rightarrow$ On | $0 \rightarrow 1$ | On | Off |
| Off | 0 | Off | Off |
| On | 1 | Off | Off |
| On $\rightarrow$ Off | $1 \rightarrow 0$ | Off | On |

- If the ORP instruction is used in the program written in ST or FBD/LD language, ENO turns when the result of OR operation between EN and the rising edge of the specified bit device (s) is on. EN will not be an execution condition.
- If the ORF instruction is used in the program written in ST or FBD/LD language, ENO turns when the result of OR operation between EN and the falling edge of the specified bit device (s) is on. EN will not be an execution condition.


## Operation error

There is no operation error.

## Pulse NOT operation start, pulse NOT series connection, pulse NOT parallel connection

## LDPI, LDFI, ANDPI, ANDFI, ORPI, ORFI

- LDPI: Rising edge pulse NOT operation start

This instruction turns on when the specified device is off, on, or at the falling edge (on to off).

- LDFI: Falling edge pulse NOT operation start

This instruction turns on when the specified bit device is at the rising edge (off to on), off, or on.

- ANDPI: Rising edge pulse NOT series connection, ANDFI: Falling edge pulse NOT series connection

These instructions perform an AND operation with the previous operation result.

- ORPI: Rising edge pulse NOT parallel connection, ORFI: Falling edge pulse NOT parallel connection

These instructions perform an OR operation with the previous operation result.

| Ladder |  | ST |
| :---: | :---: | :---: |
| LDPI <br> LDFI |  | $\begin{aligned} & \text { ENO:=LDPI(EN,s); } \\ & \text { ENO:=LDFI(EN,s); } \\ & \text { ENO:=ANDPI(EN,s); } \\ & \text { ENO:=ANDI(EN,s); } \\ & \text { ENO:=ORPI(EN,s); } \\ & \text { ENO:=ORFI(EN,s); } \end{aligned}$ |
| ANDPI <br> ANDFI |  |  |
| ORPI <br> ORFI |  |  |

FBD/LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| LDPI | Every scan |
| LDFI |  |
| ANDPI |  |
| ANDFI |  |
| ORPI |  |
| ORFI |  |

## Setting data

Description, range, data type

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| $(s)$ | Device used as a contact | - | Bit | ANY_BOOL |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others(DX) |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，Jㅁㅁ， U3EDI（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | － | － | － | $\bigcirc$ |

## Processing details

## LDPI，LDFI

－LDFI is a rising edge pulse NOT operation start instruction，and turns on when the specified device is off，on，or at the falling edge（on to off）．When a bit－specified word device is used，this instruction turns on when the specified bit is 0 or 1 or when the bit changes from 1 to 0 ．
－LDFI is a falling edge pulse NOT operation start instruction，and turns on when the specified bit device is at the rising edge （off to on），off，or on．When a bit－specified word device is used，this instruction turns on when the specified bit is 0 or 1 or when the bit changes from 0 to 1 ．The following table lists the on／off information used by the LDPI and LDFI instructions．

| Device specified by LDPI or LDFI | LDPI status | LDFI status |  |
| :--- | :--- | :--- | :--- |
| Bit device | Bit－specified word device |  |  |
| Off $\rightarrow$ On | $0 \rightarrow 1$ | Off | On |
| Off | 0 | On | On |
| On | 1 | On | On |
| On $\rightarrow$ Off | $1 \rightarrow 0$ | On | Off |

－If the LDPI instruction is used in the program written in ST language or FBD／LD，ENO turns on at the timing except the rising edge（off to on）of the specified bit device（s）．
－If the LDFI instruction is used in the program written in ST language or FBD／LD，ENO turns on at the timing except the falling edge（on to off）of the specified bit device（s）．
－If the LDPI or LDFI instruction is used in the program written in ST language or FBD／LD，always set EN to TRUE．

## －ANDPI，ANDFI

－ANDPI is a rising edge pulse NOT series connection instruction and ANDFI is a falling edge pulse NOT series connection instruction．These instructions perform an AND operation with the previous operation result，and output the operation result． The following table lists the on／off information used by the ANDPI and ANDFI instructions．

| Device specified by ANDPI or ANDFI | ANDPI status | ANDFI status |  |
| :--- | :--- | :--- | :--- |
| Bit device | Bit－specified word device |  |  |
| Off $\rightarrow$ On | $0 \rightarrow 1$ | Off | On |
| Off | 0 | On | On |
| On | 1 | On | On |
| On $\rightarrow$ Off | $1 \rightarrow 0$ | On | Off |

－If the ANDPI instruction is used in the program written in ST or FBD／LD language，ENO turns on when the result of AND operation between EN and the rising edge of the specified bit device（s）is not on．EN will not be an execution condition．
－If the ANDFI instruction is used in the program written in ST or FBD／LD language，ENO turns on when the result of AND operation between EN and the falling edge of the specified bit device（s）is not on．EN will not be an execution condition．

## -ORPI, ORFI

- ORPI is a rising edge pulse NOT parallel connection instruction and ORFI is a falling edge pulse NOT parallel connection instruction. These instructions perform an OR operation with the previous operation result, and output the operation result. The following table lists the on/off information used by the ORPI and ORFI instructions.

| Device specified by ORPI or ORFI | ORPI status | ORFI status |  |
| :--- | :--- | :--- | :--- |
| Bit device | Bit-specified word device |  |  |
| Off $\rightarrow$ On | $0 \rightarrow 1$ | Off | On |
| Off | 0 | On | On |
| On | 1 | On | On |
| On $\rightarrow$ Off | $1 \rightarrow 0$ | On | Off |

- If the ORPI instruction is used in the program written in ST or FBD/LD language, ENO turns when the result of OR operation between EN and the rising edge of the specified bit device (s) is not on. EN will not be an execution condition.
- If the ORFI instruction is used in the program written in ST or FBD/LD language, ENO turns when the result of OR operation between EN and the falling edge of the specified bit device (s) is not on. EN will not be an execution condition.


## Operation error

There is no operation error.

### 5.2 Association Instructions

## Ladder block series/parallel connection

## ANB, ORB

- ANB: Ladder block series connection

This instruction performs an AND operation between block $A$ and block $B$.

- ORB: Ladder block parallel connection

This instruction performs an OR operation between block $A$ and block $B$.


## Processing details

## ■ANB

- This instruction performs an AND operation between block $A$ and block $B$, and outputs the operation result.
- The symbol of the ANB instruction is not a contact but a connection.


## ORB

- This instruction performs an OR operation between block $A$ and block $B$, and outputs the operation result.
- Ladder blocks, each having two or more contacts, are connected in parallel. Use the OR or ORI instruction for connection of blocks, each having only one contact. The ORB instruction is not required in this case.
- The symbol of the ORB instruction is not a contact but a connection.


## Operation error

There is no operation error

## Storing/reading/clearing the operation result

## MPS, MRD, MPP

- MPS: Storing the operation result

This instruction stores the operation result (on/off) immediately before the MPS instruction.

- MRD: Reading the operation result

This instruction reads the operation result stored by using the MPS instruction.

- MPP: Clearing the operation result

This instruction clears the operation result stored by using the MPS instruction.



Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| MPS | Every scan |
| MRD |  |
| MPP |  |

## Processing details

## [MPS

- This instruction stores the operation result (on/off) immediately before the MPS instruction.
- Up to 16 MPS instructions can be used consecutively. If the MPP instruction is used in the middle of the program, the number of MPS instructions used is decremented by one.


## CMRD

- This instruction reads the operation result stored by using the MPS instruction, and performs operations from the next step based on the operation result.


## -MPP

- This instruction reads the operation result stored by using the MPS instruction, and performs operations from the next step based on the operation result.
- This instruction clears the operation result stored by using the MPS instruction.
- This instruction decrements the number of MPS instructions used in the program by one.


## Operation error

There is no operation error.

## Point

- The following are the ladder program examples.
[Ladder program using the MPS, MRD, and MPP instructions]

[Ladder program not using the MPS, MRD, or MPP instruction]

- Use the same number of MPS instructions as that of MPP instructions. If the numbers of MPS and MPP instructions are different, the ladder is not displayed correctly on the engineering tool (ladder mode).


## Inverting the operation result

## INV

This instruction inverts the operation result up to just before the INV instruction.

| Ladder |  | ST |
| :---: | :---: | :---: |
| $\mid \ggg$ |  | ENO:=INV(EN); |
| FBD/LD |  |  |
|  |  |  |
| Execution condition |  |  |
| Instruction | Execution condition |  |
| INV | Every scan |  |

## Processing details

- This instruction inverts the operation result up to just before the INV instruction.

| Operation result up to just before the INV instruction | Operation result after execution of the INV instruction |
| :--- | :--- |
| Off | On |
| On | Off |

## Operation error

There is no operation error.

## Point ${ }^{\rho}$

- The INV instruction operates based on the result of operations performed up to just before the INV instruction. Use it at the same position as the AND instruction. The instruction cannot be used at the LD or OR instruction position.
- When a ladder block is used, the operation result is inverted within the range of the ladder block. When the INV instruction and the ANB instruction are used together in the same ladder, pay attention to the inversion range.


For details on the ANB instruction, refer to the following.
$\longmapsto$ Page 170 ANB, ORB

## Converting the operation result into a pulse

## MEP, MEF

- MEP: Converting the operation result into a pulse (rising edge)

This instruction turns on at the rising edge (off to on) of the operation result up to the MEP instruction.

- MEP: Converting the operation result into a pulse (falling edge)

This instruction turns on at the falling edge (on to off) of the operation result up to the MEF instruction.


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| MEP | Every scan |
| MEF |  |

## Processing details

■MEP

- This instruction turns on (continuity state) at the rising edge (off to on) of the operation result up to the MEP instruction. The instruction turns off (non-continuity state) when the operation result is in another state (not rising edge).
- Use of the MEP instruction eases pulse conversion processing when multiple contacts are connected in series.


## MEF

- This instruction turns on (continuity state) at the falling edge (on to off) of the operation result up to the MEF instruction. The instruction turns off (non-continuity state) when the operation result is in another state (not falling edge).
- Use of the MEF instruction eases pulse conversion processing when multiple contacts are connected in series.


## Operation error

There is no operation error.

- The MEP or MEF instruction may not operate correctly if pulse conversion is performed for an indexmodified contact in the subroutine program or in the area between the FOR and NEXT instructions. To perform pulse conversion for an index-modified contact in the subroutine program or in the area between the FOR and NEXT instructions, refer to the following
$\longmapsto$ Page 175 EGP, EGF
- The MEP and MEF instructions operate based on the result of operations performed from the LD instruction just before the MEP or MEF instruction to just before the MEP or MEF instruction. Use them at the same position as the AND instruction. The instructions cannot be used at the LD or OR instruction position.


## Converting the edge relay operation result into a pulse

## EGP，EGF

－EGP：Converting the edge relay operation result into a pulse（rising edge）
This instruction stores the operation result up to the EGP instruction in the edge relay（V）．The instruction turns on at the rising edge（off to on）of the operation result．
－EGF：Converting the edge relay operation result into a pulse（falling edge）
This instruction stores the operation result up to the EGF instruction in the edge relay（V）．The instruction turns on at the falling edge（on to off）of the operation result．


## FBD／LD



Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| EGP | $\uparrow$ |
| EGF | $\uparrow$ |

## Setting data

## ■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| $(\mathrm{d})$ | Edge relay number for storing operation result | - | Bit | ANY＿BOOL＊1 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

[^1]
## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （V） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J미， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （d） | － | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ |

## Processing details

## EGP

- This instruction stores the operation result up to the EGP instruction in the edge relay (V).
- The instruction turns on (continuity state) at the rising edge (off to on) of the operation result up to the EGP instruction. The instruction turns off (non-continuity state) when the operation result is in another state (staying on, falling edge (on to off), or staying off).
- The instruction is used to perform pulse conversion for index-modified programs in the subroutine program or in the area between the FOR and NEXT instructions.
- The instruction can be used in the same way as the AND instruction.
- The following figure shows the operation performed when the instruction is used in the subroutine program.



## EGF

- This instruction stores the operation result up to the EGF instruction in the edge relay (V).
- The instruction turns on (continuity state) at the falling edge (on to off) of the operation result up to the EGF instruction. The instruction turns off (non-continuity state) when the operation result is in another state (staying on, rising edge (off to on), or staying off).
- The instruction is used to perform pulse conversion for index-modified programs in the subroutine program or in the area between the FOR and NEXT instructions.
- The instruction can be used in the same way as the AND instruction.


## Operation error

There is no operation error.

## Point ${ }^{\rho}$

- The EGP and EGF instructions operate based on the result of operations performed from the LD instruction just before the EGP or EGF instruction to just before the EGP or EGF instruction. Use them at the same position as the AND instruction. The instructions cannot be used at the LD or OR instruction position.
- The instructions cannot be used at the ladder block position shown below.



## 5．3 Output Instructions

## Out（excluding the timer，counter，and annunciator）

## OUT

This instruction outputs the operation result to the specified device．

| Ladder | ST |
| :--- | :--- | :--- |
|  | ENO：＝OUT（EN，d）； |
|  |  |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| OUT | Every scan |

## Setting data

## Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （d） | On／off target device number | - | Bit | ANY＿BOOL |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## EApplicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others （DY） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3E미（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （d） | $\bigcirc{ }^{* 1}$ | $\bigcirc$ | $\bigcirc{ }^{*}$ | $\bigcirc$ | － | － | － | $\bigcirc$ | － | － | － | $\bigcirc$ |

＊1 When $F$ is used，refer to the following．
W Page 189 OUT F
＊2 When T or ST is used，refer to the following．
$\longmapsto$ Page 179 OUT T，OUTH T，OUT ST，OUTH ST
When C is used，refer to the following．
W Page 185 OUT C

## Processing details

－This instruction outputs the operation result up to the OUT instruction to the specified device．

| Condition | Operation result | Coil／Specified bit |
| :--- | :--- | :--- |
| When a bit device is used | Off | Off |
|  | On | On |
| When a bit－specified word device is used | Off | 0 |
|  | On | 1 |

－When indirect specification is used，specify the bit as shown below．

（1）The operation result is output to bit 0 of the indirect address stored in DO．

## Operation error

There is no operation error

## Timer

## OUT T，OUTH T，OUT ST，OUTH ST

－OUT T：Low－speed timer instruction
－OUTH T：High－speed timer instruction
－OUT ST：Low－speed retentive timer instruction
－OUTH ST：High－speed retentive timer instruction
These instructions start time measurement when the operation result up to the OUT instruction is on．When time is up，the normally open contact turns on（continuity state）and the normally closed contact turns off（non－continuity state）．


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| OUT T | Every scan |
| OUTH T |  |
| OUT ST |  |
| OUTH ST |  |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| $(\mathrm{d})^{* 1}$ | Timer device or timer type label | - | Bit | ANY＿BOOL |
| ${\text {（Set value）})^{*}}^{2}$ | Value set for the timer | 0 to 65535 | 16 －bit unsigned <br> binary | ANY16 |

＊1 If the program is written in ST language or FBD／LD，this operand is displayed as＂Coil＂on the program editor．
＊2 If the program is written in ST language or FBD／LD，this operand is displayed as＂Value＂on the program editor．
＊3 If the program is written in ST language or FBD／LD，the data type will be ANY＿INT．

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others （DY） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDI（H）Gロ | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （d） | － | － | $\bigcirc{ }^{* 1}$ | － | － | － | － | － | － | － | － | － |
| （Set value） | － | － | $0^{* 2}$ | $\bigcirc$ | － | － | － | － | O＊3 | － | － | － |

[^2]
## Processing details

- These instructions start time measurement, triggered by the coil specified by (d), when the operation result up to the OUT instruction is on. When time is up (current value $\geq$ set value), the normally open contact turns on (continuity state) and the normally closed contact turns off (non-continuity state).
- When the operation result up to the OUT instruction turns off, the contact responds as shown below.

| Type | Timer coil | Current value | Before time is up |  |  | Normally open <br> contact |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Low-speed timer | Off | Normally closed <br> contact | Normally open <br> contact | Normally closed <br> contact |  |  |
| High-speed timer <br> Low-speed retentive | Off | 0 | Non-continuity | Continuity | Non-continuity | Continuity |
| High-speed <br> retentive timer |  | Current value <br> retained | Non-continuity | Continuity | Continuity | Non-continuity |

- To clear the current value of the retentive timer and turn off the contact after time is up, use the RST instruction.
- When the timer set value is 0 , the time will be up at execution of the OUT instruction.
- The following operations are performed at execution of the OUT instruction.
- The coil used as a trigger of the OUT T, OUTH T, OUT ST, or OUTH ST instruction turns on or off.
- The contact used as a trigger of the OUT T, OUTH T, OUT ST, or OUTH ST instruction turns on or off.
- The current value of the OUT T, OUTH T, OUT ST, or OUTH ST instruction is changed.
- If the OUT T instruction is skipped by using such as the JMP instruction while the OUT T, OUTH T, OUT ST, or OUTH ST instruction is on, the current value is not updated or the contact is not turned on or off.
- If the same OUT T, OUTH T, OUT ST, or OUTH ST instruction is executed two times or more in a single scan, the current value is updated by the number of times the instruction is executed.


## Point/

- The timer limit value is set in parameter using the engineering tool.

Low-speed timer/low-speed retentive timer: 1 to 1000 ms (in increments of 1 ms ) (Default: 100 ms )
High-speed timer/high-speed retentive timer: 0.01 to 100.0 ms (in increments of 0.01 ms ) (Default: 10.0 ms )

- For the counting method, refer to the following.
$\square \square$ MELSEC iQ-R CPU Module User's Manual (Application)


## Precautions

To create a program in which the operation of a timer contact triggers the operation of another timer, program the timers in order from the one that operates last.
In the following cases, if a program is created in order of timer measurements, all timers turn on in the same scan.

- The set value is smaller than the scan time.
- The set value is 1 .

Ex.
When timers T0 to T2 are programmed in order from the one that measures last

(1) Timer T2 starts measurement from the next scan after the contact of timer T1 turns on.
(2) Timer T1 starts measurement from the next scan after the contact of timer T0 turns on.
(3) Timer T0 starts measurement when X0 turns on.

## Ex.

When timers T0 to T2 are programmed in order of measurement

(1) Timer T0 starts measurement when X0 turns on.
(2) When the contact of timer T0 turns on, the contacts of timers T1 and T2 also turn on.

## Operation error

There is no operation error.

## Long timer

## OUT LT, OUT LST

- OUT LT: Low-speed long timer instruction
- OUT LST: Low-speed long retentive timer instruction

These instructions start time measurement when the operation result up to the OUT instruction is on. When time is up, the normally open contact turns on (continuity state) and the normally closed contact turns off (non-continuity state).


## ■xecution condition

| Instruction | Execution condition |
| :--- | :--- |
| OUT LT | Every scan |
| OUT LST |  |

## Setting data

## Description, range, data type

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (d) $^{* 1}$ | Long timer device or long timer type label | - | Bit | ANY_BOOL |
| (Set value) $^{* 2}$ | Value set for the long timer | 0 to 4294967295 | 32-bit unsigned <br> binary | ANY32 ${ }^{* 3}$ |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

*1 If the program is written in ST language or FBD/LD, this operand is displayed as "Coil" on the program editor.
*2 If the program is written in ST language or FBD/LD, this operand is displayed as "Value" on the program editor.
*3 If the program is written in ST language or FBD/LD, the data type will be ANY_INT.

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | U밈, J밈, U3EDI(H)GD | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (d) | - | - | - | - | - | O*1 | - | - | - | - | - | - |
| (Set value) | - | - | $\mathrm{O}^{*}$ | $\bigcirc$ | - | - | - | - | O*3 | - | - | - |

*1 Only LT and LST can be used.
*2 T, ST, and C cannot be used.
*3 Only K (decimal constant) can be used.

## Processing details

- These instructions start time measurement, triggered by the coil specified by (d), when the operation result up to the OUT instruction is on. When time is up (current value $\geq$ set value), the normally open contact turns on (continuity state) and the normally closed contact turns off (non-continuity state).
- When the operation result up to the OUT instruction turns off, the contact responds as shown below.

| Type | Timer coil | Current value | Before time is up |  | After time is up |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
|  |  |  | Normally open <br> contact | Normally closed <br> contact | Normally open <br> contact | Normally closed <br> contact |
| Long timer | Off | 0 | Non-continuity | Continuity | Non-continuity | Continuity |
| Long retentive timer | Off | Current value <br> retained | Non-continuity | Continuity | Continuity | Non-continuity |

- To clear the current value of the long retentive timer and turn off the contact after time is up, use the RST instruction.
- When the timer set value is 0 , the time will be up at execution of the OUT instruction.
- The following operations are performed at execution of the OUT instruction.
- The coil used as a trigger of the OUT LT or OUT LST instruction turns on or off.
- The contact used as a trigger of the OUT LT or OUT LST instruction turns on or off.
- The current value of the OUT LT or OUT LST instruction is changed.
- If the OUT LT instruction is skipped by using such as the JMP instruction while the OUT LT or OUT LST instruction is on, the current value is not updated or the contact is not turned on or off.
- If the same OUT LT or OUT LST instruction is executed two times or more in a single scan, the current value is updated by the number of times the instruction is executed.
- The timer limit value is set in parameter using the engineering tool.

Long timer/long retentive timer: 0.001 to 1000 ms (in increments of 0.001 ms ) (Default: 0.001 ms )

- For the counting method, refer to the following.
[] MELSEC iQ-R CPU Module User's Manual (Application)


## Precautions

To create a program in which the operation of a long timer contact triggers the operation of another long timer, program the long timers in order from the one that operates last.
In the following cases, if a program is created in order of timer measurements, all timers turn on in the same scan.

- The set value is smaller than the scan time.
- The set value is 1 .


## Ex.

When timers LT0 to LT2 are programmed in order from the one that measures last

(1) Long timer LT2 starts measurement from the next scan after the contact of long timer LT1 turns on.
(2) Long timer LT1 starts measurement from the next scan after the contact of long timer LT0 turns on.
(3) Long timer LTO starts measurement when X0 turns on.

## Ex.

When long timers LTO to LT2 are programmed in order of measurement

(1) Long timer LT0 starts measurement when X0 turns on.
(2) When the contact of timer LT0 turns on, the contacts of timers LT1 and LT2 also turn on.

## Operation error

There is no operation error.

## Counter

## OUT C

This instruction increments the current counter value（count value）by one when the operation result up to the OUT instruction turns on．When the count value reaches the set value，the normally open contact of the counter turns on（continuity state）and the normally closed contact turns off（non－continuity state）．


FBD／LD


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| OUT C | Every scan |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （d）$^{* 1}$ | Counter number | - | Bit | ANY＿BOOL $^{* 3}$ |
| （Set value）$^{* 2}$ | Value set for the counter | 0 to $65535^{* 4}$ | 16－bit unsigned <br> binary ${ }^{*}$ | ANY16 ${ }^{* 4}$ |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

＊1 If the program is written in ST language or FBD／LD，this operand is displayed as＂Coil＂on the program editor．
＊2 If the program is written in ST language or FBD／LD，this operand is displayed as＂Value＂on the program editor．
＊3 Only counter type labels can be used．
＊4 If the program is written in ST language or FBD／LD，the data type will be ANY＿INT．The range will be 0 to 4294967295.

## applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （d） | － | － | O＊ | － | － | － | － | － | － | － | － | － |
| （Set value） | － | － | ${ }^{*}{ }^{2}$ | $\bigcirc$ | － | － | － | － | O＊ | － | － | － |

＊1 Only C can be used．
＊2 T，ST，and C cannot be used．
＊3 Only K（decimal constant）can be used．

## Processing details

－This instruction increments the current counter value（count value）in the device specified by（d）by one on the rising edge （off to on）of the operation result up to the OUT instruction．When the count value reaches the set value（current value $\geq$ set value），the normally open contact turns on（continuity state）and the normally closed contact turns off（non－continuity state）．
－Counting is disabled while the operation result remains on．（Count input does not need to be converted into pulses．）
－After counting－up，the count value and contact status remain unchanged until the RST instruction is executed．
－When the set value is 0 ，the same processing is performed as when it is set to 1 ．

## Operation error

There is no operation error

## Long counter

## OUT LC

This instruction increments the current long counter value（count value）by one on the rising edge（off to on）of the operation result up to the OUT instruction．When the count value reaches the set value，the normally open contact of the long counter turns on（continuity state）and the normally closed contact turns off（non－continuity state）．

| Ladder | ST |
| :---: | :---: |
|  | ENO：＝OUT＿C（EN，d，set value）； |
| - －－ （d） （Set value） |  |

FBD／LD

（ $\square$ is to be replaced by OUT＿C．）
■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| OUT LC | Every scan |

## Setting data

## Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （d）$^{* 1}$ | Long counter number | - | Bit | ANY＿BOOL $^{* 3}$ |
| （Set value）$^{* 2}$ | Set value for the long counter | 0 to 4294967295 | 32－bit unsigned <br> binary ${ }^{*}$ | ANY32 ${ }^{* 4}$ |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

＊1 If the program is written in ST language or FBD／LD，this operand is displayed as＂Coil＂on the program editor．
＊2 If the program is written in ST language or FBD／LD，this operand is displayed as＂Value＂on the program editor．
＊3 Only long counter type labels can be used．
＊4 If the program is written in ST language or FBD／LD，the data type will be ANY＿INT．

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGㅁ，J밈， U3E미（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （d） | － | － | － | － | － | O＊1 | － | － | － | － | － | － |
| （Set value） | － | － | $0^{*}$ | $\bigcirc$ | － | － | － | － | O＊3 | － | － | － |

＊1 Only LC can be used．
＊2 T，ST，and C cannot be used．
＊3 Only K（decimal constant）can be used．

## Processing details

－This instruction increments the current long counter value（count value）in the device specified by（d）by one on the rising edge（off to on）of the operation result up to the OUT instruction．When the count value reaches the set value（current value $\geq$ set value），the normally open contact turns on（continuity state）and the normally closed contact turns off（non－ continuity state）．
－Counting is disabled while the operation result remains on．（Count input does not need to be converted into pulses．）
－After counting－up，the count value and contact status remain unchanged until the RST instruction is executed．
－When the set value is 0 ，the same processing is performed as when it is set to 1 ．

## Operation error

There is no operation error

## Annunciator

## OUT F

This instruction outputs the operation result up to the OUT F instruction to the specified annunciator．

| Ladder | ST |
| :---: | :---: |
|  | ENO：＝OUT（EN，d）； |
| FBD／LD |  |
| （ $\square$ is to be replaced by OUT．） |  |

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| OUT F | Every scan |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| $(\mathrm{d})$ | Target annunciator number | - | Bit | ANY＿BOOL＊1 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

＊1 Only labels assigned to the annunciator can be used．

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J미， U3EDI（H）GD | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （d） | $\bigcirc{ }^{* 1}$ | － | － | － | － | － | － | － | － | － | － | － |

＊1 Only F can be used．

## Processing details

－This instruction outputs the operation result up to the OUT F instruction to the specified annunciator．
－When the annunciator（ F ）is turned on by this instruction，the following are performed．
－The USER LED of the CPU module turns on．
－The annunciator number（F number）to be turned on is stored in the special register（SD64 to SD79）．
－The value in SD63 is incremented by one．
－If the value in SD63 is 16 （meaning 16 annunciators are already on），the annunciator number will not be stored in the special register（SD64 to SD79）even when a new annunciator turns on．
－When the annunciator $(F)$ is turned off by this instruction，the following are performed．
－The coil turns off，but the USER LED status and the data in SD63 to SD79 remain unchanged．
－To turn off the USER LED or delete the annunciator number that has been turned off by this instruction from SD63 to SD79，use the RST F instruction．

## Operation error

There is no operation error．

## Setting devices (excluding annunciator)

## SET

This instruction turns on the specified bit.

| Ladder | ST |  |
| :--- | :--- | :--- |
| $-\square-\square$ (d) <br>   |  |  |

FBD/LD


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| SET | $-\square$ |

Setting data

## ■Description, range, data type

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (d) | Set target bit device number or bit specification of word <br> device | - | Bit | ANY_BOOL |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> (DY) |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | J밈 | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | U밈, J밈, U3EDI(H)G口 | z | LT, LST, LC | LZ |  | K, H | E | \$ |  |
| (d) | $O^{* 1}$ | $\bigcirc$ | - | - | - | - | - | $\bigcirc$ | - | - | - | $\bigcirc$ |

*1 When $F$ is used, refer to the following
$\longmapsto$ Page 194 SET F

## Processing details

- This instruction changes the device status as follows when the execution command turns on.

| Device | Status |
| :--- | :--- |
| Bit device | Turns on the coil or contact. |
| Bit-specified word device | Sets the specified bit to 1. |

- The device that has been turned on remains on even after the execution command turns off. The device that has been turned on can be turned off by using the RST instruction.

- When the execution command is off, the device status does not change.


## Operation error

There is no operation error
$\qquad$
When $X$ is used, specify a device number that is not used in actual input. If the number that is used in actual input is specified, the data of actual input is written over the input device $(X)$ specified by the SET instruction.

## Resetting devices（excluding annunciator）

## RST

This instruction turns off the specified device．For the timer and counter，the instruction clears the current value to 0 and turns off the contact or coil．


## FBD／LD



## －Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| RST | $-\square$ |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （d） | Reset target bit device number，bit specification of word <br> device，or reset target word device number | - | Bit／Word／Double word | ANY＿ELEMENTARY |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （DY） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （d） | $\bigcirc{ }^{* 1}$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | $\bigcirc$ |

＊1 When $F$ is used，refer to the following．
$\leftrightarrows$ Page 196 RST F

## Processing details

－This instruction changes the device status as follows when the execution command turns on．

| Device | Status |
| :--- | :--- |
| Bit device | Turns off the coil or contact． |
| Timer，counter | Clears the current value to 0 and turns off the coil or contact． |
| Bit－specified word device | Sets the specified bit to 0. |
| Word device other than timer and counter | Clears the data to 0. |

－When the execution command is off，the device status does not change．
－The RST instruction specifying a word device operates in the same way as the following ladder．


## Operation error

There is no operation error

## Setting annunciator

## SET F

This instruction turns on the specified annunciator.

| Ladder | ST |  |  |
| :--- | :--- | :---: | :---: |
| $-\square-\square$ (d) <br>  ENO:=SET(EN,d); |  |  |  |

FBD/LD

( $\square$ is to be replaced by SET.)

## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| SET F | $\ddots$ |
|  | - |

## Setting data

■Description, range, data type

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (d) | Set target annunciator number (F number) | - | Bit | ANY_BOOL |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## -Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | U밈, J밈, U3E미(H)Gㅁ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (d) | $\bigcirc^{* 1}$ | - | - | - | - | - | - | - | - | - | - | - |

[^3]
## Processing details

- This instruction turns on the annunciator specified by (d) when the execution command turns on.
- When the annunciator $(F)$ is turned on, the following are performed.
- The USER LED turns on
- The annunciator number (F number) turned on is stored in the special register (SD64 to SD79).
- The value in SD63 is incremented by one.
- If the value in SD63 is 16 (meaning 16 annunciators are already on), the annunciator number will not be stored in the special register (SD64 to SD79) even when a new annunciator turns on.

| SD63 | 16 |
| :---: | :---: |
| SD64 | 233 |
| SD65 | 90 |
| SD66 | 700 |
|  | , |
| SD78 | 145 |
| SD79 | 1027 |



## Operation error

There is no operation error.

## Resetting annunciator

## RST F

This instruction turns off the specified annunciator.


## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| RST F | $\ddots$ |
|  | - |

## Setting data

■Description, range, data type

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| $(\mathrm{d})$ | Reset target annunciator number (F number) | - | Bit $^{* 1}$ | ANY_BOOL*1 $^{* 1}$ |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

*1 If the program is written in ST language or FBD/LD, the data type will be ANY_ELEMENTARY.

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | J미미 | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | UपIGロ, J미, U3E미(H)Gロ | Z | LT, LST, | LZ |  | K, H | E | \$ |  |
| (d) | $\bigcirc{ }^{* 1}$ | - | - | - | - | - | - | - | - | - | - | - |

*1 Only F can be used.

## Processing details

- This instruction turns off the annunciator specified by (d) when the execution command turns on.
- The annunciator number (F number) turned off is deleted from the special register (SD64 to SD79), and the value in SD63 is decremented by one.
- If the value in SD63 is 16 , the corresponding annunciator number is deleted from SD64 to SD79 by the RST instruction. If an annunciator with a number not registered in SD64 to SD79 has been turned on, the number is newly registered. If all annunciator numbers in SD64 to SD79 are reset (turned off), the USER LED of the CPU module turns off.


## Ex.

When the value in SD63 is 16 and there is an annunciator number that is not registered


## Operation error

There is no operation error.

## Rising edge output

## PLS

This instruction turns on the specified device for one scan on the rising edge（off to on）of the execution command．

| Ladder | ST |  |
| :--- | :--- | :--- |
|  | ENO：＝PLS（EN，d）； |  |
| $\square-\square-\square$ | （d） |  |
|  |  |  |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| PLS | $\uparrow$ |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （d） | Pulse conversion target device number | - | Bit | ANY＿BOOL |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （DY） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロום | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EロI（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | $\bigcirc$ | － | － | － | $\bigcirc$ |

## Processing details

- This instruction turns on the specified device on the rising edge (off to on) of the execution command. When the execution command is in another state (staying on, falling edge (on to off), or staying off), the instruction turns off the specified device. If only one PLS instruction in the device specified by (d) is executed in a single scan, the specified device turns on for one scan. For the operation to be performed if more than one PLS instruction is executed during one scan, refer to the following.
$\longmapsto$ Page 51 Operations arising when the OUT, SET/RST, and PLS/PLF instructions of the same device are used

- Once after execution of the PLS instruction, even if the switch of the CPU module is moved to the STOP position and then the RUN position again, the PLS instruction is not executed.

(1) M0 turns on for one scan.
(2) The CPU module operation stops.
(3) Change the RUN/STOP/RESET switch of the CPU module from RUN to STOP.
(4) Change the RUN/STOP/RESET switch of the CPU module from STOP to RUN.
- If the latch relay $(\mathrm{L})$ is specified as the execution command and the system is powered on while the latch relay is on, the execution command turns on in the first scan, triggering execution of the PLS instruction and turning on the specified device. The device that has been turned on in the first scan after power-on can be turned off by the next PLS instruction.


## Point $\rho$

- Note that if the PLS instruction is jumped by using the CJ instruction or the executed subroutine program is not called by using the CALL(P) instruction, the device specified by (d) may be on for more than one scan.


## Operation error

There is no operation error.

## Falling edge output

## PLF

This instruction turns on the specified device for one scan on the falling edge（on to off）of the execution command．

| Ladder | ST |
| :--- | :--- | :--- |
|  | ENO：＝PLF（EN，d）； |
| $\square-\square$ |  |
|  |  |

## FBD／LD



Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| PLF | $-\quad$ |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （d） | Pulse conversion target device number | - | Bit | ANY＿BOOL |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others （DY） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U미미，J밈， U3EDl（H）Gロ | z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | $\bigcirc$ | － | － | － | $\bigcirc$ |

## Processing details

－This instruction turns on the specified device on the falling edge（on to off）of the execution command．When the execution command is in another state（staying off，rising edge（off to on），or staying on），the instruction turns off the specified device． If only one PLF instruction in the device specified by（d）is executed during one scan，the specified device turns on for one scan．For the operation to be performed if more than one PLF instruction is executed during one scan，refer to the following．
$\longmapsto$ Page 51 Operations arising when the OUT，SET／RST，and PLS／PLF instructions of the same device are used

－Once after execution of the PLF instruction，even if the switch of the CPU module is moved to the STOP position and then the RUN position again，the PLF instruction is not executed．
－Note that if the PLF instruction is jumped by using the CJ instruction or the executed subroutine program is not called by using the CALL $(\mathrm{P})$ instruction，the device specified by（d）may be on for more than one scan．

## Operation error

There is no operation error

## Inverting the bit device output

## FF

This instruction inverts the status of the specified device．

| Ladder | ST |  |
| :--- | :--- | :--- |
|  | ENO：＝FF（EN，d）； |  |
| $\square-\square-\square$ | （d）$-\quad$ |  |
|  |  |  |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| FF | - |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （d） | Inversion target device number | - | Bit | ANY＿BOOL |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （DY） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3E미（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | $\bigcirc$ | － | － | － | $\bigcirc$ |

## Processing details

－This instruction inverts the status of the device specified by（d）on the rising edge of the execution command．

| Device | Device status |  |
| :--- | :--- | :--- |
|  | Before execution of the FF instruction | After execution of the FF instruction |
| Bit device | Off | On |
|  | On | Off |
| Bit－specified word device | 0 | 1 |
|  | 1 | 0 |

## Operation error

There is no operation error．

## Converting the direct access output into a pulse

## DELTA（P）

These instructions convert the specified direct access output（DY）into pulse output．

| Ladder | ST |  |
| :--- | :--- | :--- |
| $-\square-\square$ （d） <br>   |  |  |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DELTA | - |
|  | $\boxed{-}$ |
| DELTAP | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （d） | Pulse conversion target device number | - | Bit | ANY＿BOOL |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

＊1 Only labels assigned to the device（DY）can be used．

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （DY） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathbf{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathbf{S M}, \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロID， U3E미（H）G口 | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （d） | － | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ |

## Processing details

－These instructions convert the direct access output（DY）specified by（d）into pulse output．If DY0 is specified by（d），the program operates in the same way as the one that uses the SET and RST instructions．


## Precautions

These instructions are used as an execution command (rising edge execution) for intelligent function modules. These instructions cannot be used as an actual output command for output modules.

## Operation error

There is no operation error.

## Point 9

The DELTA(P) instruction is used to set a preset value of the high-speed counter module.
[Example]
A program that presets the CH 1 of the high-speed counter module (RD62P2) mounted in slot 0 of the base unit when X20 turns on

(1) Store the preset value (0) in the buffer memory areas 0 and 1 of the RD62P2.
(2) Output the preset command.

## 5．4 Shift Instructions

## Shifting bit devices

## SFT（P）

These instructions shift the on／off state of the device area just before the one specified to the specified device area，and turn off the shift source device．

| Ladder | ST |
| :---: | :---: |
|  | $\begin{aligned} & \text { ENO:=SFT(EN,d); } \\ & \text { ENO:=SFTP(EN,d); } \end{aligned}$ |
| FBD／LD |  |



## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| SFT | - |
|  | $\boxed{ }$ |
| SFTP | $\boxed{ }$ |

Setting data
■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （d） | Shift target device | - | Bit | ANY＿BOOL |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others （DY） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathbf{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathbf{S M}, \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3E미（H）Gㅁ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | $\bigcirc$ | － | － | － | $\bigcirc$ |

## Processing details

## Bit device

- These instructions shift the on/off state of the device area just before the one specified by (d) to the device area specified by (d). After the data is shifted, the data of the shift source device area is turned off.


## Ex.

When the SFTP instruction that specifies M11 is executed, it shifts the on/off state of M10 to M11, and turns off M10.

- Turn on the shift target start device by using the SET instruction.
- When the $\operatorname{SFT}(\mathrm{P})$ instruction is used consecutively, program devices in descending order of the device numbers.

(1) X 02 ON
(2) After the 1st shift input
(3) After the 2nd shift input
(4) X02 ON
(5) After the 3rd shift input
(6) After the 4th shift input
(7) After the 5th shift input


## Bit-specified word device

- These instructions shift the $1 / 0$ state of the bit just before the one specified by (d) to the bit specified by (d). After the data is shifted, the data of the shift source bit is set to 0 .


## Ex.

The SFT(P) instruction that specifies D0.5 (b5 in D0) is executed, it shifts the $1 / 0$ state of b4 in D0 to b5, and sets b4 to 0 .


## Operation error

There is no operation error

### 5.5 Master Control Instructions

## Setting/resetting a master control

## MC, MCR

- MC: This instruction starts a master control.
- MCR: This instruction ends a master control.


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| MC | Every scan |
| MCR |  |

## Setting data

Description, range, data type

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (N) | Nesting | NO to N14 | Device name | ANY16_S*1 |
| (d) | Number of the device to be turned on | - | Bit | ANY_BOOL |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

*1 Only labels assigned to the device ( N ) or to which constants are assigned can be used.

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | U밈, J밈, U3E미(H)Gㅁ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ | N | DY |
| (N) | - | - | - | - | - | - | - | - | - | - | - | $\bigcirc$ | - |
| (d) | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ |

## Processing details

These instructions are used to create an efficient ladder switching program by opening and closing the common rails of the ladder.
The following is the program example using the master control instructions.


## ■MC

- If the execution command of the MC instruction is on when a master control starts, the operation result between the MC and MCR instructions will be the one as programmed. If the execution command is off, the operation result between the MC and MCR instructions will be as follows.

| Device | Status |
| :--- | :--- |
| High-speed timer <br> Low-speed timer | The count value is set to 0, and both the coil and contact are turned off. |
| High-speed retentive timer <br> Low-speed retentive timer <br> Counter | The coil is turned off, but both the count value and contact maintain the current status. |
| Device used by the OUT instruction | Forcibly turned off. |
| Device used by the SET and RST instructions <br> Device used by the SFT(P) instruction <br> Device used by basic instructions and application <br> instructions | Maintains the current status. |

- Even if the MC instruction is off, the instructions between the MC and MCR instructions are executed and therefore the scan time is not shortened.


## Point ${ }^{\rho}$

- When a ladder performing a master control includes an instruction which does not require a contact instruction (such as the FOR to NEXT instruction), the CPU module executes the instruction regardless of the execution command of the MC instruction.
- To create an easy-to-understand program, use the MC and MCR instructions within a single program block.
- The MC instruction can use the same nesting ( N ) number as many times as needed by specifying different devices in (d).
- When the MC instruction is on, the coil of the device specified by (d) turns on. Using the same device for the OUT instruction causes double coils. Do not use the device specified by (d) in other instructions.


## ■MCR

- This instruction is a master control reset instruction which indicates the end of the master control area.
- Do not place any contact instruction before the MCR instruction.
- Use the MC and MCR instructions with the same nesting number as a set. Note that if the MCR instructions are nested in one place, all master controls can be terminated by specifying the lowest nesting ( N ) number. (Refer to "Precautions".)


## Operation error

There is no operation error.

## Point 9

The master control instructions can be nested. Individual master control areas are distinguished by nesting (N) numbers. Nesting can be set from N0 to N14.

Using the nesting structure enables the creation of a ladder which can sequentially constrain the program execution conditions.
The following figure shows a ladder program example using the nesting structure.

(1) Executed when $A$ is on.
(2) Executed when $A$ and $B$ are on.
(3) Executed when A, B, and C are on.
(4) Executed regardless of the status of A, B, and C

## Precautions

- Up to 15 nests (N0 to N14) are allowed. When nesting is performed, the MC instruction should use nesting (N) numbers in order from lower numbers and the MCR instruction should use them in order from higher numbers.
- If the MCR instructions are nested in one place, all master controls can be terminated by specifying the lowest nesting (N) number



### 5.6 Termination Instructions

## Ending the main routine program

## FEND

This instruction is used to separate the main routine program from subroutine programs and interrupt programs in a program file.

| Ladder | ST |  |
| :--- | :--- | :--- |
|  |  | Not supported |
|  |  |  |
| FBD/LD |  |  |
| Not supported |  |  |
| EXXCution Condition |  |  |
| Instruction | Execution condition |  |
| FEND | Every scan |  |

## Processing details

- This instruction is used to divide sequence program operations by using a program branch instruction such as the CJ instruction or to separate the main routine program from subroutine programs and interrupt programs specified by the interrupt pointer (I).
- When the instruction is executed, the CPU module terminates the running program.
- Sequence programs following the FEND instruction can be displayed on the engineering tool (ladder mode).

(a) When the CJ instruction is used

(b) When there are subroutine and interrupt programs
(1) Operation performed when the $C J$ instruction is not executed
(2) Jump caused by the CJ instruction
(3) Operation performed when the CJ instruction is executed


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3340 H | After execution of the FOR instruction, the FEND instruction is executed before the NEXT instruction. |
| 3381 H | After execution of the CALL(P), FCALL(P), ECALL(P), or EFCALL(P) instruction, the FEND instruction is executed before the RET <br> instruction. |
| $33 A 1 \mathrm{H}$ | Within the interrupt program specified by the interrupt pointer (I), the FEND instruction is executed before the IRET instruction. |

## Ending the sequence program

## END

This instruction indicates the end of a program.

| Ladder | ST |
| :--- | :--- | :--- |
|  | Not supported |
| $\square \square$ |  |
| FBD/LD |  |
| Not supported |  |

Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| END | Every scan |

## Processing details

- This instruction indicates the end of a program including a main routine program, subroutine programs, and interrupt programs.
- When the instruction is executed, the CPU module terminates the running program.

- If END processing is required in the middle of a program, use the FEND instruction.
- If the program is created using the engineering tool (in ladder edit mode), the END instruction is automatically input and cannot be edited.
- The following figure shows how to use the termination instructions when a main routine program, subroutine program, and interrupt program exist.


When a program is divided into multiple program blocks, the END instruction indicates the end of a program block.
The END instruction within the program registered at the end of the program setting performs END processing

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3340 H | After execution of the FOR instruction, the END instruction is executed before the NEXT instruction. |
| 3381 H | After execution of the CALL(P), FCALL(P), ECALL(P), or EFCALL(P) instruction, the END instruction is executed before the RET <br> instruction. |
| $33 A 1 \mathrm{H}$ | Within the interrupt program specified by the interrupt pointer (I), the END instruction is executed before the IRET instruction. |

### 5.7 Stop Instruction

## Stopping the sequence program

## STOP

This instruction stops the operation of the CPU module. (The operation of this instruction is the same as setting the switch of the CPU module to the STOP position.)

| Ladder | ST |
| :---: | :---: |
|  | ENO:=STOP(EN); |
|  |  |
| FBD/LD |  |


$-$| $[---\square$ |
| :---: |
| $\mathrm{EN} \quad \mathrm{ENO}$ |

Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| STOP | $-\square$ |

## Processing details

- This instruction resets the output $(\mathrm{Y})$ and stops the operation of the CPU module when the execution command turns on. (The operation of this instruction is the same as setting the switch of the CPU module to the STOP position.)
- To restart the operation of the CPU module after execution of the STOP instruction, set the switch back to STOP, and then set it to RUN again.


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3340 H | After execution of the FOR instruction, the STOP instruction is executed before the NEXT instruction. |
| 3381 H | After execution of the CALL(P), FCALL(P), ECALL(P), EFCALL(P), or XCALL instruction, the STOP instruction is executed before the <br> RET instruction. |
| 33A1H | Within the interrupt program specified by the interrupt pointer (I), the STOP instruction is executed before the IRET instruction. |
| 33A3H | The STOP instruction is executed within a fixed scan execution type program. |

### 5.8 No Operation Instruction

## No operation (NOP)

## NOP

This instruction is used to insert a space for debugging.

| Ladder | ST |
| :--- | :--- |
| - | Not supported |
| FBD/LD |  |
| Not supported |  |
| Execution condition |  |
| Instruction | Execution condition |
| NOP | Every scan |

## Processing details

- This instruction is a no-operation instruction and has no impact on the previous operations.
- The instruction is used for the following purposes:
- To insert a space for debugging
- To delete an instruction without changing the number of steps (The relevant instruction is replaced with the NOP instruction.)
- To delete an instruction temporarily

> Point For inserting or deleting the NOP instruction, refer to the following. GX Works3 Operating Manual

## Operation error

[^4]
## 6．1 Comparison Operation Instructions

## Comparing 16－bit binary data

## LDロ（＿U），ANDロ（＿U），ORロ（＿U）

These instructions compare the two sets of 16－bit binary data specified．（Devices are used as normally open contacts．）

＜（＿U），＞＝（＿U）．）
FBD／LD

| ［－－－－- ］ |  |
| :---: | :---: |
| EN | ENO |
| s1 |  |
| s2 |  |

（ $\square$ is to be replaced by combination of any of the following：$L D_{\_}, A N D_{\_}, O R \_$and $E Q\left(\_U\right), N E\left(\_U\right), G T\left(\_U\right), \operatorname{LE}\left(\_U\right), \operatorname{LT}\left(\_U\right), G E\left(\_U\right)$ ）．

## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| LDロ（＿U），ANDロ（＿U），ORロ（＿U） | Every scan |

DDescription，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :---: | :---: | :---: | :---: | :---: | :---: |
| （s1） | LDC，ANDロ， ORD | Comparison data or the device where comparison data is stored | －32768 to 32767 | 16－bit signed binary | ANY16＿S |
|  | LDロ＿U， <br> ANDI＿U， <br> ORD＿U |  | 0 to 65535 | 16－bit unsigned binary | ANY16＿U |
| （s2） | LDD，ANDロ， ORD | Comparison data or the device where comparison data is stored | -32768 to 32767 | 16－bit signed binary | ANY16＿S |
|  | $\begin{aligned} & \text { LDD_U, } \\ & \text { ANDロ_U, } \\ & \text { ORD_U } \end{aligned}$ |  | 0 to 65535 | 16－bit unsigned binary | ANY16＿U |
| EN |  | Execution condition | － | Bit | BOOL |
| ENO |  | Execution result | － | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | J밈 | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDI（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

－These instructions perform a comparison operation between the 16 －bit binary data in the device specified by（s1）and the 16－bit binary data in the device specified by（s2）．（Devices are used as normally open contacts．）
－The following table lists the comparison operation results of each instruction．

| Instruction symbol（ladder，FBD／LD） | Condition | Result |
| :---: | :---: | :---: |
| ＝（＿U），EQ（＿U） | （s1）＝（s2） | Continuity state（ENO is on．） |
| ＜＞（＿U），NE（＿U） | （s1）$\ddagger$（s2） |  |
| ＞（＿U），GT（＿U） | （s1）＞（s2） |  |
| ＜＝（＿U），LE（＿U） | $(\mathrm{s} 1) \leq$（s2） |  |
| ＜（＿U），LT（＿U） | （s1）＜（s2） |  |
| ＞＝（＿U），GE（＿U） | （s1）$\geq$（s2） |  |
| ＝（＿U），EQ（＿U） | （s1）$=(\mathrm{s} 2)$ | Non－continuity state（ENO is off．） |
| ＜＞（＿U），NE（＿U） | （s1）＝（s2） |  |
| ＞（＿U），GT（＿U） | （s1）$\leq$（s2） |  |
| ＜＝（＿U），LE（＿U） | （s1）＞（s2） |  |
| ＜（＿U），LT（＿U） | （s1）$\geq$（s2） |  |
| ＞＝（＿U），GE（＿U） | （s1）＜（s2） |  |

－When hexadecimal constants are used for（ s 1 ）and（ s 2 ）and the numerical value（ 8 to F ）whose most significant bit（b15）is 1 is specified as a constant，the value is considered as a negative binary value in comparison operation．
－If the LDD instruction is used in the program written in FBD／LD，always set EN to TRUE．
－If the ORD instruction is used in the program written in FBD／LD and EN is set to TRUE，ENO becomes always on．

## Operation error

There is no operation error．

## Comparing 32－bit binary data

## LDDD（＿U），ANDDD（＿U），ORDD（＿U）

These instructions compare the two sets of 32－bit binary data specified．（Devices are used as normally open contacts．）

| Ladder |
| :--- |

FBD／LD

（ $\square$ is to be replaced by combination of any of the following：LDD＿，ANDD＿，ORD＿and EQ（＿U），NE（＿U），GT（＿U），LE（＿U），LT（＿U），GE（＿U）．）

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| LDDD（＿U），ANDDロ（＿U）， <br> ORDロ（＿U） | Every scan |

## Setting data

Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :---: | :---: | :---: | :---: | :---: | :---: |
| （s1） | LDDD， <br> ANDDC， ORDD | Comparison data or the start device where the comparison data is stored | －2147483648 to 2147483647 | 32－bit signed binary | ANY32＿S |
|  | $\begin{aligned} & \text { LDDD_U, } \\ & \text { ANDDロ_U, } \\ & \text { ORDロ_U } \end{aligned}$ |  | 0 to 4294967295 | 32－bit unsigned binary | ANY32＿U |
| （s2） | LDDロ， ANDDD， ORDD | Comparison data or the start device where the comparison data is stored | －2147483648 to 2147483647 | 32－bit signed binary | ANY32＿S |
|  | $\begin{aligned} & \text { LDDD_U, } \\ & \text { ANDDロ_U, } \\ & \text { ORDロ_U, } \end{aligned}$ |  | 0 to 4294967295 | 32－bit unsigned binary | ANY32＿U |
| EN |  | Execution condition | － | Bit | BOOL |
| ENO |  | Execution result | － | Bit | BOOL |

## －Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathbf{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JपIロ， U3EDI（H）GD | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- These instructions perform a comparison operation between the 32-bit binary data in the device specified by (s1) and the 32-bit binary data in the device specified by (s2). (Devices are used as normally open contacts.)
- The following table lists the comparison operation results of each instruction.

| Instruction symbol (ladder, FBD/LD) | Condition | Result |
| :---: | :---: | :---: |
| D=(_U), EQ(_U) | (s1)=(s2) | Continuity state (ENO is on.) |
| D<>(_U), NE(_U) | (s1) $=$ (s2) |  |
| D>(_U), GT(_U) | (s1)>(s2) |  |
| D<=(_U), LE( $\quad$ U) | (s1) $\leq$ (s2) |  |
| D<(_U), LT(_U) | (s1)<(s2) |  |
| D>=(_U), GE(_U) | (s1) $\geq$ (s2) |  |
| D=(_U), EQ(_U) | (s1) $=(\mathrm{s} 2)$ | Non-continuity state (ENO is off.) |
| D<>(_U), NE(_U) | (s1)=(s2) |  |
| D>(_U), GT(_U) | (s1) $\leq$ (s2) |  |
| D<=(_U), LE(_U) | (s1)>(s2) |  |
| $\mathrm{D}<\left(\_\mathrm{U}\right), \mathrm{LT}\left(\_\mathrm{U}\right)$ | (s1) $\geq$ (s2) |  |
| D>=(_U), GE(_U) | (s1)<(s2) |  |

- When hexadecimal constants are specified for ( s 1 ) and ( s 2 ) and the numerical value ( 8 to F ) whose most significant bit (b31) is 1 is specified as a constant, the value is considered as a negative binary value in comparison operation.
- To specify the compare target data, use an instruction which handles 32-bit data, such as the DMOV(P) instruction. If an instruction which handles 16-bit data, such as the $\operatorname{MOV}(\mathrm{P})$ instruction, is used, comparison cannot be performed normally.
- If the LDDD instruction is used in the program written in FBD/LD, always set EN to TRUE.
- If the ORDD instruction is used in the program written in FBD/LD and EN is set to TRUE, ENO becomes always on.


## Operation error

There is no operation error.

## Comparing 16－bit binary block data

## BKCMPロ（P）（＿U）

These instructions compare the two sets of 16－bit binary block data specified．

| Ladder |  |  |  |  |  | ST |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| －． （s1） （s2） （d） （n） <br> is replaced by any of the following：BKCMP＝（P）（＿U），BKCMP＜＞（P）（＿U）， BKCMP＞（P）（＿U），BKCMP＜＝（P）（＿U），BKCMP＜（P）（＿U），BKCMP＞＝（P）（＿U）．） |  |  |  |  |  | Not supported |
|  |  |  |  |  |  |  |
| （ $\square$ is replaced by any of the following：$B K C M P=(P)\left(\_U\right), B K C M P<>(P)\left(\_U\right)$ ， BKCMP＞（P）（＿U），BKCMP＜＝（P）（＿U），BKCMP＜（P）（U），BKCMP＞＝（P）（＿U）．） |  |  |  |  |  |  |

FBD／LD

（ $\square$ is to be replaced by combination of any of the following：$B K C M P \_$and $E Q(P)\left(\_U\right), N E(P)\left(\_U\right), G T(P)\left(\_U\right), L E(P)\left(\_U\right), \operatorname{LT}(P)\left(\_U\right), G E(P)(U)$ ）

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| BKCMPロ（＿U） | - |
| BKCMPロP（＿U） | - |

## Setting data

Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :---: | :---: | :---: | :---: | :---: | :---: |
| （s1） | BKCMPロ（P） | Comparison data or the start device where the comparison data is stored | －32768 to 32767 | 16－bit signed binary | ANY16＿S |
|  | $\begin{aligned} & \text { BKCMPロ(P)_ } \\ & \text { U } \end{aligned}$ |  | 0 to 65535 | 16－bit unsigned binary | ANY16＿U |
| （s2） | BKCMPD（P） | Start device where the comparison data is stored | － | 16－bit signed binary | ANY16＿S |
|  | $\begin{aligned} & \text { BKCMPロ(P)_ } \\ & \cup \end{aligned}$ |  |  | 16－bit unsigned binary | ANY16＿U |
| （d） |  | Start device for storing the comparison operation result | － | Bit | ANY＿BOOL |
| （ n ） |  | Number of data points to be compared | 0 to 65535 | 16－bit unsigned binary | ANY16 |
| EN |  | Execution condition | － | Bit | BOOL |
| ENO |  | Execution result | － | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGㅁ，J밈， U3EDl（H）GD | z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- These instructions compare the $(\mathrm{n})$ points of 16 -bit binary data from the device specified by ( s 1 ) with the $(\mathrm{n})$ points of 16-bit binary data from the device specified by ( s 2 ), and stores the operation result in the device specified by (d) and later.
- If the comparison condition is satisfied, the relevant device specified by (d) turns on; otherwise, the device turns off.



- Specify data in units of 16 bits.
- A constant can be specified for (s1).

- The following table lists the comparison operation results of each instruction.

| Instruction symbol (ladder, FBD/LD) | Condition | Result |
| :---: | :---: | :---: |
| BKCMP=(P)(_U), BKCMP_EQ(P)(_U) | (s1)=(s2) | On (1) |
| BKCMP<>(P)(_U), BKCMP_NE(P)(_U) | (s1) $=(\mathrm{s} 2)$ |  |
| BKCMP>(P)(_U), BKCMP_GT(P)(_U) | (s1)>(s2) |  |
| BKCMP<=(P)(_U), BKCMP_LE(P)(_U) | (s1) $\leq$ (s2) |  |
| BKCMP<(P)(_U), BKCMP_LT(P)(_U) | (s1)<(s2) |  |
| BKCMP>=(P)(_U), BKCMP_GE(P)(_U) | (s1) $\geq$ (s2) |  |
| BKCMP=(P)(_U), BKCMP_EQ(P)(_U) | (s1) $\boldsymbol{\prime}$ ( s 2$)$ | Off (0) |
| BKCMP<>(P)(_U), BKCMP_NE(P)(_U) | (s1)=(s2) |  |
| BKCMP>(P)(_U), BKCMP_GT(P)(_U) | (s1) $\leq$ (s2) |  |
| BKCMP<=(P)(_U), BKCMP_LE(P)(_U) | (s1)>(s2) |  |
| BKCMP<(P)(_U), BKCMP_LT(P)(_U) | (s1) $\geq$ (s2) |  |
| BKCMP>=(P)(_U), BKCMP_GE(P)(_U) | (s1)<(s2) |  |

- When the comparison operation results stored in ( $n$ ) points from the device specified by (d) are all on (1), SM704 turns on.


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2821 H | The device ranges starting from the ones specified by (s1) and (d) are overlapping. |
|  | The device ranges starting from the ones specified by (s2) and (d) are overlapping. |

Comparing 32－bit binary block data

## DBKCMPロ（P）（＿U）

These instructions compare the two sets of 32－bit binary block data specified．


FBD／LD

（ $\square$ is to be replaced by combination of any of the following：$D B K C M P \_$and $E Q(P)\left(\_U\right), N E(P)\left(\_U\right), G T(P)\left(\_U\right), L E(P)\left(\_U\right), L T(P)\left(\_U\right), G E(P)\left(\_U\right)$ ．）

## －Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DBKCMPロ（＿U） | - |
|  | $\boxed{\square}$ |
| DBKCMPロP（＿U） | - |

Setting data

## Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :---: | :---: | :---: | :---: | :---: | :---: |
| （s1） | DBKCMPロ（P） | Comparison data or the start device where the comparison data is stored | －2147483648 to 2147483647 | 32－bit signed binary | ANY32＿S |
|  | $\begin{aligned} & \text { DBKCMPD(P)_ } \\ & \text { U } \end{aligned}$ |  | 0 to 4294967295 | 32－bit unsigned binary | ANY32＿U |
| （s2） | DBKCMPロ（P） | Start device where the comparison data is stored | － | 32－bit signed binary | ANY32＿S |
|  | $\begin{aligned} & \text { DBKCMPD(P)_ } \\ & \text { U } \end{aligned}$ |  |  | 32－bit unsigned binary | ANY32＿U |
| （d） |  | Start device for storing the comparison operation result | － | Bit | ANY＿BOOL |
| （ n ） |  | Number of data points to be compared | 0 to 65535 | 16－bit unsigned binary | ANY16 |
| EN |  | Execution condition | － | Bit | BOOL |
| ENO |  | Execution result | － | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGㅁ，J밈， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- These instructions compare the $(\mathrm{n})$ points of 32-bit binary data from the device specified by ( s 1 ) with the ( n ) points of 32-bit binary data from the device specified by ( s 2 ), and stores the operation result in the device specified by (d) and later.
- If the comparison condition is satisfied, the relevant device specified by (d) turns on; otherwise, the device turns off.

- Comparison operation is performed in units of 32 bits.
- A constant can be specified for (s1).

- Specify (d) outside the device ranges for ( n ) points from the device specified by ( s 1 ) and those from the device specified by (s2).
- The following table lists the comparison operation results of each instruction.

| Instruction symbol (ladder, FBD/LD) | Condition | Result |
| :---: | :---: | :---: |
| DBKCMP=(P)(_U), DBKCMP_EQ(P)(_U) | (s1)=(s2) | On (1) |
| DBKCMP<>(P)(_U), DBKCMP_NE(P)(_U) | (s1) $=(\mathrm{s} 2)$ |  |
| DBKCMP>(P)(_U), DBKCMP_GT(P)(_U) | (s1)>(s2) |  |
| DBKCMP<=(P)(_U), DBKCMP_LE(P)(_U) | (s1) $\leq$ (s2) |  |
| DBKCMP<(P)(_U), DBKCMP_LT(P)(_U) | (s1)<(s2) |  |
| DBKCMP>=(P)(_U), DBKCMP_GE(P)(_U) | (s1) $\geq$ (s2) |  |
| DBKCMP=(P)(_U), DBKCMP_EQ(P)(_U) | (s1) $=(\mathrm{s} 2)$ | Off (0) |
| DBKCMP<>(P)(_U), DBKCMP_NE(P)(_U) | (s1)=(s2) |  |
| DBKCMP>(P)(_U), DBKCMP_GT(P)(_U) | (s1) $\leq$ (s2) |  |
| DBKCMP<=(P)(_U), DBKCMP_LE(P)(_U) | (s1)>(s2) |  |
| DBKCMP<(P)(_U), DBKCMP_LT(P)(_U) | (s1) $\geq$ (s2) |  |
| DBKCMP>=(P)(_U), DBKCMP_GE(P)(_U) | (s1)<(s2) |  |

- When the comparison operation results stored in ( $n$ ) points from the device specified by (d) are all on (1), SM704 turns on.
- If $(\mathrm{n})$ is 0 , no processing is performed.


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2821 H | The device ranges starting from the ones specified by (s1) and (d) are overlapping. |
|  | The device ranges starting from the ones specified by (s2) and (d) are overlapping. |

## Point 8

When bits of a word device are specified, the bits other than the specified ones for storing the operation result do not change.


## 6．2 Arithmetic Operation Instructions

## Adding 16－bit binary data

## ＋（P）（＿U）［when two operands are set］

These instructions add the two sets of 16－bit binary data specified．

| Ladder | ST |
| :---: | :---: |
|  | Not supported |
| ■－二－$\square$ （s） （d） |  |

## FBD／LD

Not supported
Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| + | - |
| $+\_U$ | - |
| $+P$ | - |
| $+P+U$ | - |

## Setting data

Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- | :--- |
| （s） | $+(\mathrm{P})$ | Second addend data or the device where the | -32768 to 32767 | 16－bit signed binary | ANY16＿S |
|  | $+(P) \_U$ | second addend data is stored | 0 to 65535 | 16－bit unsigned binary | ANY16＿U |
|  | $+(P)$ | Device where the first addend data is stored | -32768 to 32767 | 16－bit signed binary | ANY16＿S |
|  | $+(P) \_U$ | 0 to 65535 | 16 －bit unsigned binary | ANY16＿U |  |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J미， U3Eपl（H）G口 | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions add the 16-bit binary data in the device specified by ( d ) and the 16 -bit binary data in the device specified by (s), and store the operation result in the device specified by (d).
(d)
 $+$
(d)

| b15 $\quad \ldots$ | b0 |
| :---: | :---: |
| $6912(B I N)$ |  |

- If an overflow occurs in the result, the carry bit is ignored. In this case, SM700 does not turn on. [+(P) instruction]

(s)
(d)

(d)


$+$| b15 $\quad \cdots \quad$ b0 |
| :---: |
| $+\quad 23456(\mathrm{BIN})$ |


[+(P)_U instruction]

## (d)

(s)
(d)


## Operation error

There is no operation error

## $+(P)\left(\_U\right)$［when three operands are set］

These instructions add the two sets of 16－bit binary data specified．


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| + | - |
| $+\_U$ | - |
| $+P$ |  |
| $+P \_U$ | - |

## Setting data

Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :---: | :---: | :---: | :---: | :---: | :---: |
| （s1） | ＋（P） | First addend data or the device where the first addend data is stored | －32768 to 32767 | 16－bit signed binary | ANY16＿S |
|  | ＋（P）＿U |  | 0 to 65535 | 16－bit unsigned binary | ANY16＿U |
| （s2） | $+(\mathrm{P})$ | Second addend data or the device where the second addend data is stored | －32768 to 32767 | 16－bit signed binary | ANY16＿S |
|  | ＋（P）＿U |  | 0 to 65535 | 16－bit unsigned binary | ANY16＿U |
| （d） | ＋（P） | Device for storing the operation result | － | 16－bit signed binary | ANY16＿S |
|  | ＋（P）＿U |  |  | 16－bit unsigned binary | ANY16＿U |
| EN |  | Execution condition | － | Bit | BOOL |
| ENO |  | Execution result | － | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions add the 16-bit binary data in the device specified by ( s 1 ) and the 16 -bit binary data in the device specified by ( s 2 ), and store the operation result in the device specified by (d).
(s1)
 $+$
b15 ... b0
$\square$
(d)

| $\overbrace{15} \quad \ldots$ | b0 |
| :--- | :--- | :--- |
| $6912($ BIN $)$ |  |

- If an overflow occurs in the result, the carry bit is ignored. In this case, SM700 does not turn on. [+(P) instruction]

(s2)
(d)

(s1)

[+(P)_U instruction]

(s2)
(d)



## Operation error

There is no operation error.

## Subtracting 16-bit binary data

## -(P)(_U) [when two operands are set]

These instructions perform subtraction between the two sets of 16-bit binary data specified.


## Setting data

Description, range, data type

| Operand |  | Description | Range | Data type | Data type (label) |
| :---: | :---: | :---: | :---: | :---: | :---: |
| (s) | -(P) | Subtrahend data or the device where subtrahend data is stored | -32768 to 32767 | 16-bit signed binary | ANY16_S |
|  | -(P)_U |  | 0 to 65535 | 16-bit unsigned binary | ANY16_U |
| (d) | -(P) | Device where minuend data is stored | -32768 to 32767 | 16-bit signed binary | ANY16_S |
|  | -(P)_U |  | 0 to 65535 | 16-bit unsigned binary | ANY16_U |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | J밈 | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | UपIGㅁ, J밈, U3EDl(H)Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (s) | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - | $\bigcirc$ | $\bigcirc$ | - | - | - |
| (d) | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - | $\bigcirc$ | - | - | - | - |

## Processing details

- These instructions subtract the 16-bit binary data in the device specified by (s) from the 16 -bit binary data in the device specified by (d), and store the operation result in the device specified by (d).
(d)

(s)

(d)

- If an underflow occurs in the result, the borrow bit is ignored. In this case, SM700 does not turn on. [-(P) instruction]
(d)
(s)
(d)

(s)
(d)

 $\stackrel{\mathrm{b} 0}{\mathrm{~b} 1}$
 3034 (BIN)
[-(P)_U instruction]



## Operation error

There is no operation error.

## －（P）（＿U）［when three operands are set］

These instructions perform subtraction between the two sets of 16－bit binary data specified．


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| - | - |
| $--U$ | - |
| $-P$ | - |
| $-P \_U$ | - |

## Setting data

■Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :---: | :---: | :---: | :---: | :---: | :---: |
| （s1） | －（P） | Minuend data or the device where minuend data is stored | －32768 to 32767 | 16－bit signed binary | ANY16＿S |
|  | －（P）＿U |  | 0 to 65535 | 16－bit unsigned binary | ANY16＿U |
| （s2） | －（P） | Subtrahend data or the device where subtrahend data is stored | －32768 to 32767 | 16－bit signed binary | ANY16＿S |
|  | －（P）＿U |  | 0 to 65535 | 16－bit unsigned binary | ANY16＿U |
| （d） | －（P） | Device for storing the operation result | － | 16－bit signed binary | ANY16＿S |
|  | －（P）＿U |  |  | 16－bit unsigned binary | ANY16＿U |
| EN |  | Execution condition | － | Bit | BOOL |
| ENO |  | Execution result | － | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | Jロロロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）GD | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions subtract the 16-bit binary data in the device specified by (s2) from the 16-bit binary data in the device specified by ( s 1 ), and store the operation result in the device specified by (d).
(s1)

| b15 $\quad$... |
| :---: |
| $6678(\mathrm{BIN})$ |

(s2)

(d)
b15
b15 ... b0

- If an underflow occurs in the result, the borrow bit is ignored. In this case, SM700 does not turn on. [-(P) instruction]



## Operation error

There is no operation error.

## Adding 32－bit binary data

## D＋（P）（＿U）［when two operands are set］

These instructions add the two sets of 32－bit binary data specified．


## FBD／LD

Not supported

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| $\mathrm{D}+$ | - |
| $\mathrm{D}+\mathrm{U}$ | - |
| $\mathrm{D}+\mathrm{P}$ |  |
| $\mathrm{D}+\mathrm{P}$－ U | - |

## Setting data

Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- | :--- |
| （s） | D＋（P） | Second addend data or the start device where | -2147483648 to 2147483647 | 32－bit signed binary | ANY32＿S |
|  | t＋（P）＿U | the second addend data is stored | O to 4294967295 | 32－bit unsigned binary | ANY32＿U |
| （d） | D＋（P） | Start device where the first addend data is stored | -2147483648 to 2147483647 | 32－bit signed binary | ANY32＿S |
|  | D＋（P）＿U |  | 0 to 4294967295 | 32－bit unsigned binary | ANY32＿U |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | J밈 | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions add the 32-bit binary data in the device specified by ( d ) and the 32-bit binary data in the device specified by (s), and store the operation result in the device specified by (d).

- If an overflow occurs in the result, the carry bit is ignored. In this case, SM700 does not turn on. [D+(P) instruction]

| $(\mathrm{d})+1 \quad(\mathrm{~d})$ |  | $(\mathrm{s})+1 \quad(\mathrm{~s})$ | $(\mathrm{d})+1 \quad(\mathrm{~d})$ |
| :---: | :---: | :---: | :---: |
| $\overbrace{\sim}^{\sim} \overbrace{}^{\text {(d) }}$ |  | $\overbrace{}^{\text {c }}$ | $\overbrace{}^{(d)}$ |
| b31 … b16 b15 ... b0 |  | b31 $\cdots$ b16 b15 $\cdots$ b0 | b31 $\cdots$ b16 b15 $\cdots$ b0 |
| 1234567890 (BIN) | + | 987654321 (BIN) | 74738564 (BIN) |


[D+(P)_U instruction]

| $(\mathrm{d})+1 \quad(\mathrm{~d})$ |  | $(\mathrm{s})+1 \quad(\mathrm{~s})$ | $(\mathrm{d})+1 \quad(\mathrm{~d})$ |
| :---: | :---: | :---: | :---: |
|  |  |  |  |
| b31 ... b16 b15 ... b0 |  | b31 ... b16 b15 ... b0 | b31 $\cdots$ b16 b15 $\cdots$ b0 |
| 3456789012 (BIN) | + | 1234567890 (BIN) | 396389607 (BIN) |

## Operation error

There is no operation error

## D＋（P）（＿U）［when three operands are set］

These instructions add the two sets of 32－bit binary data specified．


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| D＋ | - |
| $D+\_U$ | - |
| $D+P$ | - |
| $D+P \_U$ | - |

## Setting data

Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- | :--- |
| （s1） | D＋（P） | First addend data or the start device where the | -2147483648 to 2147483647 | 32－bit signed binary | ANY32＿S |
|  | D＋（P）＿U | first addend data is stored | Second addend data or the start device where | -2147483648 to 2147483647 | 32－bit signed binary |
| （s2） | D＋（P） | the second addend data is stored | ANY32＿S |  |  |
|  | D＋（P）＿U | Start device for storing the operation result | - | 32－bit unsigned binary | ANY32＿U |
| （d） | D＋（P） |  |  | 32－bit signed binary | ANY32＿S |
|  | D＋（P）＿U |  | - | 32－bit unsigned binary | ANY32＿U |
| EN | Execution condition | - | Bit | BOOL |  |
|  | Execution result |  | BOOL |  |  |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | Jロロロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）GD | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions add the 32-bit binary data in the device specified by ( s 1 ) and the 32-bit binary data in the device specified by (s2), and store the operation result in the device specified by (d).

- If an overflow occurs in the result, the carry bit is ignored. In this case, SM700 does not turn on. [D+(P) instruction]

$\left[D+(P) \_U\right.$ instruction $]$

| $(\mathrm{s} 1)+1 \quad(\mathrm{~s} 1)$ | $(\mathrm{s} 2)+1$ |  | $(\mathrm{d})+1 \quad(\mathrm{~d})$ |
| :---: | :---: | :---: | :---: |
|  | + |  |  |
| b31 ... b16 b15 ... b0 |  | b31 … b16 b15 ... b0 | b31 $\cdots$ b16 b15 $\cdots$ b0 |
| 3456789012 (BIN) |  | 1234567890 (BIN) | 396389607 (BIN) |

## Operation error

There is no operation error.

## Subtracting 32－bit binary data

## D－（P）（＿U）［when two operands are set］

These instructions perform subtraction between the two sets of 32－bit binary data specified．

| Ladder | ST |
| :---: | :---: |
|  | Not supported |
|  |  |

## FBD／LD

Not supported

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| D－ | - |
| D－＿U | - |
| D－P | - |
| D－P＿U |  |

## Setting data

Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- | :--- |
| （s） | D－（P） | Subtrahend data or the start device where | -2147483648 to 2147483647 | 32－bit signed binary | ANY32＿S |
|  | D－（P）＿U | subtrahend data is stored | O to 4294967295 | 32－bit unsigned binary | ANY32＿U |
| （d） | D－（P） | Start device where minuend data is stored | -2147483648 to 2147483647 | 32－bit signed binary | ANY32＿S |
|  | D－（P）＿U |  | 0 to 4294967295 | 32－bit unsigned binary | ANY32＿U |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions subtract the 32-bit binary data in the device specified by (s) from the 32-bit binary data in the device specified by (d) and, and store the operation result in the device specified by (d).

| (d)+1 (d) | $(\mathrm{s})+1$ ( s$)$ | (d) +1 (d) |
| :---: | :---: | :---: |
| 1 … b16 b15 ... b0 | b31 ... b16 b15 ... b0 | b31 ... b16 b15 ... |
| 567890 (BIN) | 123456 (BIN) | 444434 (BIN) |

- If an underflow occurs in the result, the carry bit is ignored. In this case, SM700 does not turn on. [D-(P) instruction]

| (d) +1 (d) | (s) $+1 \quad(\mathrm{~s})$ | (d) +1 (d) |
| :---: | :---: | :---: |
| $31 \cdots$ b16 b15 ... b0 | b31 ... b16 b15 ... b0 | b31 ... b16 b15 |
| 1234567890 (BIN) | -987654321 (BIN) | 74738564 (BIN) |


| (d) $+1 \quad$ (d) | $(\mathrm{s})+1 \quad(\mathrm{~s})$ | (d) +1 (d) |
| :---: | :---: | :---: |
| b31 … b16 b15 … b0 | b31 … b16 b15 | b31 … b16 b15 |
| -1234567890 (BIN) | 987654321 (BIN) | -74738563 (BIN) |

[D-(P)_U instruction]

| (d) +1 ( d$)$ | (s)+1 (s) | (d)+1 (d) |
| :---: | :---: | :---: |
| b31 ‥ b16 b15 … b0 | b31 ... b16 b15 ... b0 | b31 … b16 b15 ... b0 |
| 3456789012 (BIN) | -1234567890 (BIN) | 396389607 (BIN) |

## Operation error

There is no operation error.

## D－（P）（＿U）［when three operands are set］

These instructions perform subtraction between the two sets of 32－bit binary data specified．


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| D－ | - |
| D－＿U | - |
| D－P | $\boxed{ }$ |
| D－P＿U | $\boxed{ }$ |

## Setting data

Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :---: | :---: | :---: | :---: | :---: | :---: |
| （s1） | D－（P） | Minuend data or the start device where minuend data is stored | －2147483648 to 2147483647 | 32－bit signed binary | ANY32＿S |
|  | D－（P）＿U |  | 0 to 4294967295 | 32－bit unsigned binary | ANY32＿U |
| （s2） | D－（P） | Subtrahend data or the start device where subtrahend data is stored | －2147483648 to 2147483647 | 32－bit signed binary | ANY32＿S |
|  | D－（P）＿U |  | 0 to 4294967295 | 32－bit unsigned binary | ANY32＿U |
| （d） | D－（P） | Start device for storing the operation result | － | 32－bit signed binary | ANY32＿S |
|  | D－（P）＿U |  |  | 32－bit unsigned binary | ANY32＿U |
| EN |  | Execution condition | － | Bit | BOOL |
| ENO |  | Execution result | － | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions subtracts the 32-bit binary data in the device specified by (s2) from the 32-bit binary data in the device specified by ( s 1 ), and store the operation result in the device specified by (d).

- If an underflow occurs in the result, the carry bit is ignored. In this case, SM700 does not turn on. [D-(P) instruction]


| (s1)+1 (s1) |  | (s2)+1 (s2) | (d)+1 | (d) |
| :---: | :---: | :---: | :---: | :---: |
| $\overbrace{\sim} \overbrace{}^{(1)}$ |  | $\overbrace{}$ |  |  |
| b31 $\cdots$ b16 b15 ‥ b0 |  | b31 … b16 b15 ... b0 | b31 ... b1 | $5 \cdots$ b0 |
| -1234567890 (BIN) |  | 987654321 (BIN) | -74738 | (BIN) |

[D-(P)_U instruction]


## Operation error

There is no operation error.

## Multiplying 16－bit binary data

## ＊（P）（＿U）

These instructions multiply the two sets of 16－bit binary data specified．


FBD／LD

（ $\square$ is to be replaced by any of the following：MULTI，MULTIP，MULTI＿U，MULTIP＿U．）

## ■Execution condition

| Instruction | Execution condition |
| :---: | :---: |
| ＊ |  |
| ＊＿U | $\square$ |
| ＊P |  |
| ＊P＿U | $\uparrow$ |

## Setting data

## DDescription，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :---: | :---: | :---: | :---: | :---: | :---: |
| （s1） | ＊（P） | Multiplicand data or the device where multiplicand data is stored | －32768 to 32767 | 16－bit signed binary | ANY16＿S |
|  | ＊（P）＿U |  | 0 to 65535 | 16－bit unsigned binary | ANY16＿U |
| （s2） | ＊（P） | Multiplier data or the device where multiplier data is stored | －32768 to 32767 | 16－bit signed binary | ANY16＿S |
|  | ＊（P）＿U |  | 0 to 65535 | 16－bit unsigned binary | ANY16＿U |
| （d） | ＊（P） | Start device for storing the operation result | － | 32－bit signed binary | ANY32＿S |
|  | ＊（P）＿U |  |  | 32－bit unsigned binary | ANY32＿U |
| EN |  | Execution condition | － | Bit | BOOL |
| ENO |  | Execution result | － | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J밈， U3EDl（H）GD | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions multiply the 16-bit binary data in the device specified by ( s 1 ) by the 16 -bit binary data in the device specified by (s2), and store the operation result in the device specified by (d).

| (s1) | $\mathrm{s}^{\text {(s2) }}$ |  |  | (d) +1 | (d) |
| :---: | :---: | :---: | :---: | :---: | :---: |
| b15 ... b0 |  | b15 | b0 | b31 ... b16 | $5 \cdots$ b0 |
| 5678 (BIN) | $\times$ | 1234 (BIN) |  | 70066 | (BIN) |

- When (d) is a bit device, data should be specified in order from lower bits.


## Ex.

Operation result when (d) is a bit device

- K1…Lower 4 bits (b0 to b3)
- K4…Lower 16 bits (b0 to b15
- K8...Lower 32 bits (b0 to b31)


## Operation error

There is no operation error.

## Dividing 16－bit binary data

## $l(P)(\mathbf{U})$

These instructions perform division between the two sets of 16－bit binary data specified．


FBD／LD

（ $\square$ is to be replaced by any of the following：DIVISION，DIVISIONP，DIVISION＿U，DIVISIONP＿U．）

## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| I | - |
| I＿U | - |
| IP | $\boxed{ }$ |
| IP＿U | - |

## Setting data

## －Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :---: | :---: | :---: | :---: | :---: | :---: |
| （s1） | ／（P） | Dividend data or the device where dividend data is stored | －32768 to 32767 | 16－bit signed binary | ANY16＿S |
|  | ／（P）＿U |  | 0 to 65535 | 16－bit unsigned binary | ANY16＿U |
| （s2） | $1(\mathrm{P})$ | Divisor data or the device where divisor data is stored | －32768 to 32767 | 16－bit signed binary | ANY16＿S |
|  | ／（P）＿U |  | 0 to 65535 | 16－bit unsigned binary | ANY16＿U |
| （d） | ／（P） | Start device for storing the operation result | － | 32－bit signed binary | ANY16＿S＿ARRAY <br> （Number of elements： <br> 2） |
|  | ／（P）＿U |  |  | 32－bit unsigned binary | ANY16＿U＿ARRAY <br> （Number of elements： <br> 2） |
| EN |  | Execution condition | － | Bit | BOOL |
| ENO |  | Execution result | － | Bit | BOOL |

## EApplicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3EDI（H）G口 | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions divide the 16 -bit binary data in the device specified by (s1) by the 16 -bit binary data in the device specified by ( s 2 ), and store the operation result in the device specified by (d).

| (s1) | (s2) |  |  | Quotient <br> (d) | Remainder $\text { (d) }+1$ |
| :---: | :---: | :---: | :---: | :---: | :---: |
| $\overbrace{\text { b15 ... b0 }}$ |  | b15 ... | b0 | b15 ... b0 | b15 ... b0 |
| 5678 (BIN) | $\div$ | 1234 (BIN) |  | 4 (BIN) | 742 (BIN) |

- As the operation result, the quotient and remainder are stored in 32 bits. When a bit device is specified, the number of digitspecified bits is used to store the quotient and remainder.
- Quotient…Stored in lower 16 bits.
- Remainder...Stored in upper 16 bits.


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3400 H | The value (divisor) in the device specified by (s2) is 0. |

## Multiplying 32-bit binary data

## D*(P)(_U)

These instructions multiply the two sets of 32-bit binary data specified.


FBD/LD

( $\square$ is to be replaced by any of the following: DMULTI, DMULTIP, DMULTI_U, DMULTIP_U.)

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| $\mathrm{D}^{*}$ | - |
| $\mathrm{D}^{*} \mathrm{U}$ | - |
| $\mathrm{D}^{*} \mathrm{P}$ | - |
| $\mathrm{D}^{*} \mathrm{P}_{-} \mathrm{U}$ | $\boxed{ }$ |

## Setting data

## DDescription, range, data type

| Operand |  | Description <br> Multiplicand data or the start device where multiplicand data is stored | Range | Data type | Data type (label) |
| :---: | :---: | :---: | :---: | :---: | :---: |
| (s1) | $\mathrm{D}^{*}(\mathrm{P})$ |  | -2147483648 to 2147483647 | 32-bit signed binary | ANY32_S |
|  | $\mathrm{D}^{*}(\mathrm{P})$ _U |  | 0 to 4294967295 | 32-bit unsigned binary | ANY32_U |
| (s2) | $\mathrm{D}^{*}(\mathrm{P})$ | Multiplier data or the start device where multiplier data is stored | -2147483648 to 2147483647 | 32-bit signed binary | ANY32_S |
|  | $\mathrm{D}^{*}(\mathrm{P})$ _U |  | 0 to 4294967295 | 32-bit unsigned binary | ANY32_U |
| (d) | D* ${ }^{*}$ ) | Start device for storing the operation result | - | 64-bit signed binary | ANY32_S_ARRAY <br> (Number of elements: 2) |
|  | D* $(P)$ _ U |  |  | 64-bit unsigned binary | ANY32_U_ARRAY <br> (Number of elements: 2) |
| EN |  | Execution condition | - | Bit | BOOL |
| ENO |  | Execution result | - | Bit | BOOL |

## EApplicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM} \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ\|ロ | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | U밈ㅁ, J밈, U3EDl(H)GD | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (s1) | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - | - |
| (s2) | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - | - |
| (d) | $\bigcirc$ | - | $\bigcirc$ | - | - | $\bigcirc$ | - | $\bigcirc$ | - | - | - | - |

## Processing details

- These instructions multiply the 32-bit binary data in the device specified by ( s 1 ) by the 32-bit binary data in the device specified by (s2), and store the operation result in the device specified by (d).
$\overbrace{}^{(\mathrm{s} 1)+1} \overbrace{}^{(\mathrm{s} 1)} \overbrace{}^{(\mathrm{s} 2)+1} \overbrace{}^{(\mathrm{s} 2)} \overbrace{}^{(\mathrm{d})+3} \overbrace{}^{(\mathrm{d})+2} \overbrace{}^{(\mathrm{d})+1} \overbrace{}^{(\mathrm{d})}$

| b31 $\cdots$ b16 b15 $\cdots$ b0 |
| :---: |
| $567890($ BIN $)$ |
| b31 $\cdots$ b16 b15 $\cdots$ b0 |
| $123456($ BIN $)$ |
| b63 $\cdots$ b48 b47 $\cdots$ b32 b31 $\cdots$ b16 b15 $\cdots$ b0 |
| $70109427840($ BIN $)$ |

- When (d) is a bit device, only the lower 32 bits of the operation result are stored. If the upper 32 bits of the operation result are required, temporarily store the result in a word device, and transfer the data stored in (d) +2 and (d) +3 to the specified bit devices.


## Ex.

Operation result when (d) is a bit device

- K1 …Lower 4 bits (b0 to b3)
- K4…Lower 16 bits (b0 to b15)
- K8...Lower 32 bits (b0 to b31)


## Operation error

There is no operation error.

## Dividing 32－bit binary data

## D／（P）（＿U）

These instructions perform division between the two sets of 32－bit binary data specified．

| Ladder | ST |
| :---: | :---: |
|  | Not supported |
| ■－—— $-\square$ （s1） （s2） （d） |  |

FBD／LD

（ $\square$ is to be replaced by any of the following：DDIVISION，DDIVISIONP，DDIVISION＿U，DDIVISIONP＿U．）

## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| D／ | - |
| D／U | - |
| D／P | $\boxed{ }$ |
| D／P＿U | - |

## Setting data

## －Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :---: | :---: | :---: | :---: | :---: | :---: |
| （s1） | $\mathrm{D} /(\mathrm{P})$ | Dividend data or the start device where dividend data is stored | －2147483648 to 2147483647 | 32－bit signed binary | ANY32＿S |
|  | $\mathrm{D} /(\mathrm{P})_{-} \mathrm{U}$ |  | 0 to 4294967295 | 32－bit unsigned binary | ANY32＿U |
| （s2） | D／（P） | Divisor data or the start device where divisor data is stored | －2147483648 to 2147483647 | 32－bit signed binary | ANY32＿S |
|  | $\mathrm{D} /(\mathrm{P})$＿ U |  | 0 to 4294967295 | 32－bit unsigned binary | ANY32＿U |
| （d） | D／（P） | Start device for storing the operation result | － | 64－bit signed binary | ANY32＿S＿ARRAY <br> （Number of elements： 2） |
|  | $\mathrm{D} /(\mathrm{P})$＿U |  |  | 64－bit unsigned binary | ANY32＿U＿ARRAY <br> （Number of elements： 2） |
| EN |  | Execution condition | － | Bit | BOOL |
| ENO |  | Execution result | － | Bit | BOOL |

## EApplicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J미， U3EDI（H）G口 | z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions divide the 32-bit binary data in the device specified by (s1) by the 32-bit binary data in the device specified by (s2), and store the operation result in the device specified by (d).

| (s1) +1 (s1) |  | ( s 2$)+1$ ( s 2$)$ | (d) +1 (d) | (d) $+3 \quad$ (d) +2 |
| :---: | :---: | :---: | :---: | :---: |
| b31 ... b16 b15 ... b0 |  | b31 ... b16 b15 ... b0 | b31 ... b16 b15 ... b0 | b31 $\cdots$ b16 b15 $\ldots$ b0 |
| 567890 (BIN) | $\div$ | 123456 (BIN) | 4 (BIN) | 74066 (BIN) |

- As the operation result when a word device is specified, the quotient and remainder are stored in 64 bits. The quotient is stored in lower 32 bits, and the remainder is stored in upper 32 bits. When a bit device is specified, only quotient is stored in 32 bits.


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3400 H | The value (divisor) in the device specified by (s2) is 0. |

## Adding BCD 4－digit data

## $B+(P)$［when two operands are set］

These instructions add the two sets of BCD 4－digit data specified．


FBD／LD
Not supported
Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| $\mathrm{B}+$ | - |
|  | $\boxed{B}+\mathrm{P}$ |
|  | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Second addend data or the device where the second <br> addend data is stored | 0 to 9999 | BCD 4－digit | ANY16 |
| （d） | Device where the first addend data is stored | 0 to 9999 | BCD 4－digit | ANY16 |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロID， U3EDI（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions add the BCD 4－digit data in the device specified by（d）and the BCD 4－digit data in the device specified by（s），and store the operation result in the device specified by（d）．

## （d）

（s）
（d）

－If the result exceeds 9999 ，the carry bit is ignored．In this case，SM700 does not turn on．


## Operation error

| Error code（SDO） | Description |
| :--- | :--- |
| 3405 H | The BCD data in the device specified by（s）is out of the range， 0 to 9999. |
|  | The BCD data in the device specified by $(\mathrm{d})$ is out of the range， 0 to 9999. |

## $B+(P)$［when three operands are set］

These instructions add the two sets of BCD 4－digit data specified．


FBD／LD

（ $\square$ is to be replaced by either of the following：BPLUS，BPLUSP．）

## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| $\mathrm{B}+$ | - |
| $\mathrm{B}+\mathrm{P}$ | $\boxed{ }$ |
|  | - |

## Setting data

## －Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | First addend data or the device where the first addend data <br> is stored | 0 to 9999 | BCD 4－digit | ANY16 |
| （s2） | Second addend data or the device where the second <br> addend data is stored | 0 to 9999 | BCD 4－digit | ANY16 |
| （d） | Device for storing the operation result | - | BCD 4－digit | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3E미（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions add the BCD 4－digit data in the device specified by（s1）and the BCD 4－digit data in the device specified by（s2），and store the operation result in the device specified by（d）．

－If the result exceeds 9999，the carry bit is ignored．In this case，SM700 does not turn on．


## Operation error

| Error code (SD0) | Description |
| :--- | :--- |
| 3405 H | The BCD data in the device specified by (s1) is out of the range, 0 to 9999. |
|  | The BCD data in the device specified by $(\mathrm{s} 2)$ is out of the range, 0 to 9999. |

## Subtracting BCD 4－digit data

## B－（P）［when two operands are set］

These instructions perform subtraction between the two sets of BCD 4－digit data specified．

| Ladder | ST |
| :---: | :---: |
|  | Not supported |
| $\square-\square-\square$ （s） （d） |  |

FBD／LD
Not supported

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| B－ | - |
| B－P | $\boxed{ }$ |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Subtrahend data or the device where subtrahend data is <br> stored | 0 to 9999 | BCD 4－digit | ANY16 |
| （d） | Device where minuend data is stored | 0 to 9999 | BCD 4－digit | ANY16 |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions subtract the BCD 4－digit data in the device specified by（s）from the 32－bit binary data in the device specified by（d），and store the operation result in the device specified by（d）．
（d）

－If an underflow occurs，the result will be as follows．In this case，SM700 does not turn on．


Operation error

| Error code（SDO） | Description |
| :--- | :--- |
| 3405 H | The BCD data in the device specified by（s）is out of the range， 0 to 9999. |
|  | The BCD data in the device specified by $(\mathrm{d})$ is out of the range， 0 to 9999. |

## $B-(P)$［when three operands are set］

These instructions perform subtraction between the two sets of BCD 4－digit data specified．

| Ladder | ST |
| :---: | :---: |
| ■－——च （s1） （s2） （d） | $\begin{aligned} & \text { ENO:=BMINUS(EN,s1,s2,d); } \\ & \text { ENO:=BMINUSP(EN,s1,s2,d); } \end{aligned}$ |

FBD／LD

（ $\square$ is to be replaced by either of the following：BMINUS，BMINUSP．）

## －Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| B－ | - |
|  | $\boxed{\square}$ |
| B－P | - |

## Setting data

## Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Minuend data or the device where minuend data is stored | 0 to 9999 | BCD 4－digit | ANY16 |
| （s2） | Subtrahend data or the device where subtrahend data is <br> stored | 0 to 9999 | BCD 4－digit | ANY16 |
| （d） | Device for storing the operation result | - | BCD 4－digit | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，Jपاロ， U3EDl（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions subtract the BCD 4－digit data in the device specified by（s2）from the BCD 4－digit data in the device specified by（ s 1 ），and store the operation result in the device specified by（d）．

－If an underflow occurs，the result will be as follows．In this case，SM700 does not turn on．


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3405 H | The BCD data in the device specified by (s1) is out of the range, 0 to 9999. |
|  | The BCD data in the device specified by (s2) is out of the range, 0 to 9999. |

## Adding BCD 8－digit data

## DB＋（P）［when two operands are set］

These instructions add the two sets of BCD 8－digit data specified．

| Ladder | ST |
| :---: | :---: |
|  | Not supported |
| - －－ （s） （d） |  |

FBD／LD
Not supported

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| $\mathrm{DB}+$ | - |
|  | $\boxed{ }$ |
| $\mathrm{DB}+\mathrm{P}$ | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Second addend data or the start device where the second <br> addend data is stored | 0 to 99999999 | BCD 8－digit | ANY32 |
| （d） | Start device where the first addend data is stored | 0 to 99999999 | BCD 8－digit | ANY32 |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロID， U3EDl（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions add the BCD 8－digit data in the device specified by（d）and the BCD 8－digit data in the device specified by（s），and store the operation result in the device specified by（d）．

－If the result exceeds 99999999 ，the carry bit is ignored．In this case，SM700 does not turn on．


## Operation error

| Error code（SDO） | Description |
| :--- | :--- |
| 3405 H | The BCD data in the device specified by（s）is out of the range， 0 to 99999999. |
|  | The BCD data in the device specified by（d）is out of the range， 0 to 99999999. |

## DB＋（P）［when three operands are set］

These instructions add the two sets of BCD 8－digit data specified．

| Ladder | ST |
| :---: | :---: |
| ■－——— （s1） （s2） （d） | $\begin{aligned} & \text { ENO:=DBPLUS(EN,s1,s2,d); } \\ & \text { ENO:=DBPLUSP(EN,s1,s2,d); } \end{aligned}$ |

FBD／LD

（ $\square$ is to be replaced by either of the following：DBPLUS，DBPLUSP．）

## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| $\mathrm{DB}+$ | - |
|  | $\boxed{ }$ |
| $\mathrm{DB}+\mathrm{P}$ | - |

## Setting data

## Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | First addend data or the start device where the first addend <br> data is stored | 0 to 99999999 | BCD 8－digit | ANY32 |
| （s2） | Second addend data or the start device where the second <br> addend data is stored | 0 to 99999999 | BCD 8－digit | ANY32 |
| （d） | Start device for storing the operation result | - | BCD 8－digit | ANY32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）Gロ | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions add the BCD 8－digit data in the device specified by（s1）and the BCD 8－digit data in the device specified by（s2），and store the operation result in the device specified by（d）．

－If the result exceeds 99999999 ，the carry bit is ignored．In this case，SM700 does not turn on．


## Operation error

| Error code (SD0) | Description |
| :--- | :--- |
| 3405 H | The BCD data in the device specified by $(\mathrm{s} 1)$ is out of the range, 0 to 99999999. |
|  | The BCD data in the device specified by $(\mathrm{s} 2)$ is out of the range, 0 to 99999999. |

## Subtracting BCD 8－digit data

## DB－（P）［when two operands are set］

These instructions perform subtraction between the two sets of BCD 8－digit data specified．


## FBD／LD

Not supported

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DB－ | - |
| DB－P | - |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Subtrahend data or the start device where subtrahend data <br> is stored | 0 to 99999999 | BCD 8－digit | ANY32 |
| （d） | Minuend data or the start device where minuend data is <br> stored | 0 to 99999999 | BCD 8－digit | ANY32 |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロID， U3EDI（H）Gㅁ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions subtract the BCD 8－digit data in the device specified by（s）from the BCD 8－digit data in the device specified by（d），and store the operation result in the device specified by（d）．

－If an underflow occurs，the result will be as follows．In this case，SM700 does not turn on．

| 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| 1 | 2 | 3 | 4 | 5 | 6 | 7 | 9 |
| 9 | 9 | 9 | 9 | 9 | 9 | 9 | 9 |

## Operation error

| Error code（SDO） | Description |
| :--- | :--- |
| 3405 H | The BCD data in the device specified by（s）is out of the range， 0 to 99999999. |
|  | The BCD data in the device specified by（d）is out of the range， 0 to 99999999. |

## DB－（P）［when three operands are set］

These instructions perform subtraction between the two sets of BCD 8－digit data specified．


FBD／LD

（ $\square$ is to be replaced by either of the following：DBMINUS，DBMINUSP．）

## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DB－ | - |
|  | $\boxed{ }$ |
| DB－P | - |

## Setting data

## ■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Minuend data or the start device where minuend data is <br> stored | 0 to 99999999 | BCD 8－digit | ANY32 |
| （s2） | Subtrahend data or the start device where subtrahend data <br> is stored | 0 to 99999999 | BCD 8－digit | ANY32 |
| （d） | Start device for storing the operation result | - | BCD 8－digit | ANY32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3E미（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions subtract the BCD 8－digit data in the device specified by（s2）from the BCD 8－digit data in the device specified by（ s 1 ），and store the operation result in the device specified by（d）．

－If an underflow occurs，the result will be as follows．In this case，SM700 does not turn on．

| 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| 1 | 2 | 3 | 4 | 5 | 6 | 7 | 9 |
| 9 | 9 | 9 | 9 | 9 | 9 | 9 | 9 |

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3405 H | The BCD data in the device specified by (s1) is out of the range, 0 to 99999999. |
|  | The BCD data in the device specified by (s2) is out of the range, 0 to 99999999. |

## Multiplying BCD 4-digit data

## $B^{*}(P)$

These instructions multiply the two sets of BCD 4-digit data specified.


FBD/LD

( $\square$ is to be replaced by either of the following: BMULTI, BMULTIP.)

## ■Execution condition

| Instruction | Execution condition |
| :---: | :---: |
| B* |  |
| B*P |  |

## Setting data

## ■Description, range, data type

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (s1) | Multiplicand data or the device where multiplicand data is <br> stored | 0 to 9999 | BCD 4-digit | ANY16 |
| (s2) | Multiplier data or the device where multiplier data is stored | 0 to 9999 | BCD 4-digit | ANY16 |
| (d) | Start device for storing the operation result | - | BCD 8-digit | ANY32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | J밈 | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | UपIGロ, JपIロ, U3EDI(H)GD | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (s1) | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - | $\bigcirc$ | $\bigcirc$ | - | - | - |
| (s2) | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - | $\bigcirc$ | $\bigcirc$ | - | - | - |
| (d) | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - | - | - |

## Processing details

- These instructions multiply the BCD 4-digit data in the device specified by ( s 1 ) by the BCD 4-digit data in the device specified by ( s 2 ), and store the operation result in the device specified by (d).



## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3405 H | The BCD data in the device specified by (s1) is out of the range, 0 to 9999. |
|  | The BCD data in the device specified by (s2) is out of the range, 0 to 9999. |

## Dividing BCD 4－digit data

## B／（P）

These instructions perform division between the two sets of BCD 4－digit data specified．


FBD／LD

（ $\square$ is to be replaced by either of the following：BDIVISION，BDIVISIONP．）

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| B／ | - |
|  | $\boxed{B}$ |
| B／P | - |

## Setting data

## DDescription，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Dividend data or the device where dividend data is stored | 0 to 9999 | BCD 4－digit | ANY16 |
| （s2） | Divisor data or the device where divisor data is stored | 0 to 9999 | BCD 4－digit | ANY16 |
| （d） | Start device for storing the operation result | - | BCD 8－digit | ANY16＿ARRAY <br> （Number of elements： <br> $2)$ |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## EApplicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDl（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions divide the BCD 4-digit data in the device specified by (s1) by the BCD 4-digit data in the device specified by (s2), and store the operation result in the device specified by (d).

- As the operation result, the quotient and remainder are stored in 32 bits.
- Quotient (BCD 4 digits)...Stored in lower 16 bits.
- Remainder (BCD 4 digits)…Stored in upper 16 bits.


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3400 H | The value (divisor) in the device specified by (s2) is 0. |
| 3405 H | The BCD data in the device specified by (s1) is out of the range, 0 to 9999. |
|  | The BCD data in the device specified by (s2) is out of the range, 0 to 9999. |

## Multiplying BCD 8－digit data

## DB＊${ }^{*}$（P）

These instructions multiply the two sets of BCD 8－digit data specified．


FBD／LD

（ $\square$ is to be replaced by either of the following：DBMULTI，DBMULTIP．）

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| $\mathrm{DB}^{*}$ | - |
|  | $\boxed{ }$ |
| $\mathrm{DB} * \mathrm{P}$ | - |

## Setting data

## DDescription，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Multiplicand data or the start device where multiplicand <br> data is stored | 0 to 99999999 | BCD 8－digit | ANY32 |
| （s2） | Multiplier data or the start device where multiplier data is <br> stored | 0 to 99999999 | BCD 8－digit | ANY32 |
| （d） | Start device for storing the operation result | - | BCD 16－digit | ANY32＿ARRAY <br> （Number of elements： <br> 2） |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## －Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）GD | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions multiply the BCD 8-digit data in the device specified by ( s 1 ) by the BCD 8 -digit data in the device specified by ( s 2 ), and store the operation result in the device specified by (d).

- When (d) is a bit device, only the lower 8 digits (lower 32 bits) of the operation result are stored.


## Ex.

Operation result when (d) is a bit device

- K1...Lower 1 digit (b0 to b3)
- K4...Lower 4 digits (b0 to b15)
- K8...Lower 8 digits (b0 to b31)


## Operation error

| Error code (SD0) | Description |
| :--- | :--- |
| 3405 H | The BCD data in the device specified by (s1) is out of the range, 0 to 99999999. |
|  | The BCD data in the device specified by (s2) is out of the range, 0 to 99999999. |

## Dividing BCD 8-digit data

## DB/(P)

These instructions perform division between the two sets of BCD 8-digit data specified.


FBD/LD

( $\square$ is to be replaced by either of the following: DBDIVISION, DBDIVISIONP.)

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| $\mathrm{DB} /$ | - |
|  | $\boxed{ }$ |
| $\mathrm{DB} / \mathrm{P}$ | - |

## Setting data

## DDescription, range, data type

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (s1) | Dividend data or the start device where dividend data is <br> stored | 0 to 99999999 | BCD 8-digit | ANY32 |
| (s2) | Divisor data or the start device where divisor data is stored | 0 to 99999999 | BCD 8-digit | ANY32 |
| (d) | Start device for storing the operation result | - | BCD 16-digit | ANY32_ARRAY <br> (Number of elements: <br> $2)$ |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | UपIGㅁ, J밈, U3E미(H)Gㅁ | Z | LT, LST, LC | LZ |  | K, H | E | \$ |  |
| (s1) | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - | - |
| (s2) | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - | - |
| (d) | $\bigcirc$ | - | $\bigcirc$ | - | - | $\bigcirc$ | - | $\bigcirc$ | - | - | - | - |

## Processing details

- These instructions divide the BCD 8-digit data in the device specified by ( s 1 ) by the BCD 8 -digit data in the device specified by (s2), and store the operation result in the device specified by (d).


As the operation result, the quotient and remainder are stored in 64 bits.

- Quotient (BCD 8 digits)...Stored in lower 32 bits.
- Remainder (BCD 8 digits) $\cdots$.. Stored in upper 32 bits.
- When (d) is a bit device, the remainder is not stored.


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3400 H | The value (divisor) in the device specified by (s2) is 0. |
| 3405 H | The BCD data in the device specified by (s1) is out of the range, 0 to 99999999. |
|  | The BCD data in the device specified by (s2) is out of the range, 0 to 99999999. |

## Adding 16－bit binary block data

## BK＋（P）（U）

These instructions add the two 16－bit binary data blocks specified．


FBD／LD

（ $\square$ is to be replaced by any of the following：DMINUS，DMINUSP，DMINUS＿U，DMINUSP＿U．）
■Execution condition

| Instruction | Execution condition |
| :--- | :---: |
| $B K+$ | - |
| $B K+\_U$ | - |
| $B K+P$ | $\boxed{ }$ |
| $B K+P \_U$ | - |

## Setting data

Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :---: | :---: | :---: | :---: | :---: | :---: |
| （s1） | BK＋（P） | First addend data or the start device where the first addend data is stored | －32768 to 32767 | 16－bit signed binary | ANY16＿S |
|  | BK＋（P）＿U |  | 0 to 65535 | 16－bit unsigned binary | ANY16＿U |
| （s2） | BK＋（P） | Second addend data or the start device where the second addend data is stored | －32768 to 32767 | 16－bit signed binary | ANY16＿S |
|  | BK＋（P）＿U |  | 0 to 65535 | 16－bit unsigned binary | ANY16＿U |
| （d） | $\mathrm{BK}+(\mathrm{P})$ | Start device for storing the operation result | － | 16－bit signed binary | ANY16＿S |
|  | BK＋（P）＿U |  |  | 16－bit unsigned binary | ANY16＿U |
| （ n ） |  | Number of data points | 0 to 65535 | 16－bit unsigned binary | ANY16 |
| EN |  | Execution condition | － | Bit | BOOL |
| ENO |  | Execution result | － | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロID， U3EDI（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- These instructions add the $(\mathrm{n})$ points of 16-bit binary data from the device specified by ( s 1 ) and the $(\mathrm{n})$ points of 16-bit binary data from the device specified by (s2) or the constant, and store the operation result in the device specified by (d) and later.
- Specify data in units of 16 bits.


## Ex.

When a device is specified in (s2) (signed value specification)


When a constant is specified in (s2) (signed value specification)


- If an overflow occurs, the result will be as follows. In this case, SM700 does not turn on.

| When a signed value is specified |  |  |  |  | When an unsigned value is specified |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\begin{aligned} & \text { K32767 } \\ & \text { (H7FFF) } \end{aligned}$ | + | $\begin{aligned} & \text { K2 } \\ & \text { (H0002) } \end{aligned}$ |  | $\begin{aligned} & \text { K-32767 } \\ & \text { (H8001) } \end{aligned}$ | $\begin{aligned} & \text { K65535 } \\ & \text { (HFFFF) } \end{aligned}$ | $+$ | $\begin{aligned} & \text { K1 } \\ & \text { (H0001) } \end{aligned}$ | $\leadsto \underset{(\mathrm{HOOOO})}{\mathrm{KO}}$ |
| $\begin{aligned} & \text { K-32767 } \\ & \text { (H8001) } \end{aligned}$ | + | K-2 <br> (HFFFE) | $5$ | $\begin{aligned} & \text { K32767 } \\ & \text { (H7FFF) } \end{aligned}$ |  |  |  |  |

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2821 H | The device ranges starting from the ones specified by ( s 1 ) and (d) are overlapping (except when the same device is specified for (s1) and <br> (d)). |
|  | The device ranges starting from the ones specified by (s2) and (d) are overlapping (except when the same device is specified for (s2) and <br> (d)). |

## Subtracting 16-bit binary block data

## BK-(P)(_U)

These instructions perform subtraction between the two 16-bit binary data blocks specified.

|  |  |  |  |  | ST <br> Not supported |
| :---: | :---: | :---: | :---: | :---: | :---: |
| ----] | Ladder |  |  |  |  |
|  | (s1) | (s2) | (d) | ( n ) |  |

FBD/LD

( $\square$ is to be replaced by any of the following: BKMINUS, BKMINUSP, BKMINUS_U, BKMINUSP_U.)
■Execution condition

| Instruction | Execution condition |
| :--- | :---: |
| BK- | - |
| BK-_U | - |
| BK-P | $\boxed{ }$ |
| BK-P_U | - |

## Setting data

Description, range, data type

| Operand |  | Description | Range | Data type | Data type (label) |
| :---: | :---: | :---: | :---: | :---: | :---: |
| (s1) | BK-(P) | Minuend data or the start device where minuend data is stored | -32768 to 32767 | 16-bit signed binary | ANY16_S |
|  | BK-(P)_U |  | 0 to 65535 | 16-bit unsigned binary | ANY16_U |
| (s2) | BK-(P) | Subtrahend data or the start device where subtrahend data is stored | -32768 to 32767 | 16-bit signed binary | ANY16_S |
|  | BK-(P)_U |  | 0 to 65535 | 16-bit unsigned binary | ANY16_U |
| (d) | BK-(P) | Start device for storing the operation result | - | 16-bit signed binary | ANY16_S |
|  | BK-(P)_U |  |  | 16-bit unsigned binary | ANY16_U |
| ( n ) |  | Number of data points | 0 to 65535 | 16-bit unsigned binary | ANY16 |
| EN |  | Execution condition | - | Bit | BOOL |
| ENO |  | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | J밈 | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | UपIGロ, J밈, U3EDI(H)GD | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (s1) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (s2) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | $\bigcirc$ | - | - | - |
| (d) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| ( n ) | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - | $\bigcirc$ | $\bigcirc$ | - | - | - |

## Processing details

- These instructions subtract the ( n ) points of 16-bit binary data from the device specified by ( s 2 ) or the constant from the ( n ) points of 16-bit binary data from the device specified by ( s 1 ), and store the operation result in the device specified by (d) and later.
- Specify data in units of 16 bits.


## Ex.

When a device is specified in (s2)


| (s2) | b15 | b0 |  |
| :---: | :---: | :---: | :---: |
|  | 1234 | (BIN) | 4 |
| (s2)+1 | 5678 | (BIN) |  |
| (s2)+2 | 9876 | (BIN) | ( n |
| : |  | 7 |  |
| (s2)+(n)-2 | 4321 | (BIN) |  |
| (s2)+(n)-1 | 4000 | (BIN) | $\downarrow$ |



When a constant is specified in (s2)


- If an overflow occurs, the result will be as follows. In this case, SM700 does not turn on.

| When a signed value is specified |  |  |  |  | When an unsigned value is specified |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\begin{aligned} & \text { K-32767 } \\ & \text { (H8001) } \end{aligned}$ |  | $\begin{aligned} & \text { K2 } \\ & \text { (H0002) } \end{aligned}$ | $\underset{\nabla}{B}$ | $\begin{aligned} & \text { K32766 } \\ & \text { (H7FFE) } \end{aligned}$ | $\begin{aligned} & \text { KO } \\ & \text { (H0000) } \end{aligned}$ |  | $\begin{aligned} & \text { K1 } \\ & \text { (H0001) } \end{aligned}$ | $\square$ | $\begin{aligned} & \text { K65535 } \\ & \text { (HFFFF) } \end{aligned}$ |
| $\begin{aligned} & \text { K32767 } \\ & \text { (H7FFF) } \end{aligned}$ |  | K-2 <br> (HFFFE) |  | $\begin{aligned} & \text { K-32767 } \\ & \text { (H8001) } \end{aligned}$ |  |  |  |  |  |

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2821 H | The device ranges starting from the ones specified by (s1) and (d) are overlapping (except when the same device is specified for (s1) and <br> (d)). |
|  | The device ranges starting from the ones specified by (s2) and (d) are overlapping (except when the same device is specified for (s2) and <br> (d)). |

## Adding 32－bit binary block data

## DBK＋（P）（U）

These instructions add the two 32－bit binary data blocks specified．


FBD／LD

（ $\square$ is to be replaced by any of the following：DBKPLUS，DBKPLUSP，DBKPLUS＿U，DBKPLUSP＿U．）
Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DBK＋ | - |
| DBK＋＿U | - |
| DBK＋P | $\boxed{ }$ |
| DBK $+P_{-} U$ | - |

## Setting data

■Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :---: | :---: | :---: | :---: | :---: | :---: |
| （s1） | DBK＋（P） | First addend data or the start device where the first addend data is stored | －2147483648 to 2147483647 | 32－bit signed binary | ANY32＿S |
|  | DBK＋（P）＿U |  | 0 to 4294967295 | 32－bit unsigned binary | ANY32＿U |
| （s2） | DBK＋（P） | Second addend data or the start device where the second addend data is stored | －2147483648 to 2147483647 | 32－bit signed binary | ANY32＿S |
|  | DBK＋（P）＿U |  | 0 to 4294967295 | 32－bit unsigned binary | ANY32＿U |
| （d） | DBK＋（P） | Start device for storing the operation result | － | 32－bit signed binary | ANY32＿S |
|  | DBK＋（P）＿U |  |  | 32－bit unsigned binary | ANY32＿U |
| （ n ） |  | Number of data points | 0 to 65535 | 16－bit unsigned binary | ANY16 |
| EN |  | Execution condition | － | Bit | BOOL |
| ENO |  | Execution result | － | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3Eपl（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- These instructions add the $(\mathrm{n})$ points of 32-bit binary data from the device specified by ( s 1 ) and the ( n ) points of 32-bit binary data from the device specified by (s2) or the constant, and store the operation result in the device specified by (d) and later.
- Specify data in units of 32 bits.


## Ex.

When a device is specified in (s2) (signed value specification)


When a constant is specified in (s2) (signed value specification)


- Operation is possible when the same device is specified in (s1) or (s2) and (d). However, if the device range of (n) points from ( s 1 ) or ( s 2 ) and the device range of ( n ) points from ( d ) are partly overlapped, an error results.


## Ex.

When the four points of device from that specified by (s2) and (d) exactly match

(1) Operation is possible because they exactly match.

When four points of device from that specified by ( s 2 ) and (d) are partly overlapped

(1) An operation error results because they partly match.

- If $(\mathrm{n})$ is 0 , no processing is performed.
- If an overflow occurs, the result will be as follows. In this case, SM700 does not turn on.

| When a signed value is specified |  |  |  |  | When an unsigned value is specified |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\begin{aligned} & \text { K2147483647 } \\ & \text { (H7FFFFFFF) } \end{aligned}$ | + | $\begin{aligned} & \text { K2 } \\ & \text { (H00000002) } \end{aligned}$ | $\forall$ | $\begin{aligned} & \text { K-2147483647 } \\ & \text { (H80000001) } \end{aligned}$ | K4294967295 <br> (HFFFFFFFF) | $+$ | $\begin{aligned} & \text { K1 } \\ & \text { (H00000001) } \end{aligned}$ | $\sqrt{2}$ | $\begin{aligned} & \text { K0 } \\ & \text { (H00000000) } \end{aligned}$ |
| $\begin{aligned} & \text { K-2147483647 } \\ & \text { (H80000001) } \end{aligned}$ | $+$ | $\begin{aligned} & \text { K-2 } \\ & \text { (HFFFFFFFE) } \end{aligned}$ | $\Delta$ | $\begin{aligned} & \text { K2147483647 } \\ & \text { (H7FFFFFFF) } \end{aligned}$ |  |  |  |  |  |

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2821 H | The device ranges starting from the ones specified by (s1) and (d) are overlapping (except when the same device is specified for (s1) and <br> (d)). |
|  | The device ranges starting from the ones specified by (s2) and (d) are overlapping (except when the same device is specified for (s2) and <br> (d)). |

## Subtracting 32－bit binary block data

## DBK－（P）（＿U）

These instructions perform subtraction between the two 32－bit binary data blocks specified．


FBD／LD

（ $\square$ is to be replaced by any of the following：DBKMINUS，DBKMINUSP，DBKMINUS＿U，DBKMINUSP＿U．）

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DBK－ | - |
| DBK－＿U | - |
| DBK－P | $\boxed{ }$ |
| DBK－P＿U |  |

## Setting data

Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :---: | :---: | :---: | :---: | :---: | :---: |
| （s1） | DBK－（P） | Minuend data or the start device where minuend data is stored | －2147483648 to 2147483647 | 32－bit signed binary | ANY32＿S |
|  | DBK－（P）＿U |  | 0 to 4294967295 | 32－bit unsigned binary | ANY32＿U |
| （s2） | DBK－（P） | Subtrahend data or the start device where subtrahend data is stored | －2147483648 to 2147483647 | 32－bit signed binary | ANY32＿S |
|  | DBK－（P）＿U |  | 0 to 4294967295 | 32－bit unsigned binary | ANY32＿U |
| （d） | DBK－（P） | Start device for storing the operation result | － | 32－bit signed binary | ANY32＿S |
|  | DBK－（P）＿U |  |  | 32－bit unsigned binary | ANY32＿U |
| （ n ） |  | Number of data points | 0 to 65535 | 16－bit unsigned binary | ANY16 |
| EN |  | Execution condition | － | Bit | BOOL |
| ENO |  | Execution result | － | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3Eपl（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- These instructions subtract the ( n ) points of 32-bit binary data from the device specified by ( s 2 ) or the constant from the ( n ) points of 32-bit binary data from the device specified by ( s 1 ), and store the operation result in the device specified by (d) and later.
- Specify data in units of 32 bits.


## Ex.

When a device is specified in (s2) (signed value specification)


When a constant is specified in (s2) (signed value specification)


- Operation is possible when the same device is specified in (s1) or (s2) and (d). However, if the device range of (n) points from ( s 1 ) or ( s 2 ) and the device range of ( n ) points from ( d ) are partly overlapped, an error results.


## Ex.

When the four points of device from that specified by (s2) and (d) exactly match

(1) Operation is possible because they exactly match.

When four points of device from that specified by ( s 2 ) and (d) are partly overlapped

(1) An operation error results because they partly match.

- If $(\mathrm{n})$ is 0 , no processing is performed.
- If an overflow occurs, the result will be as follows. In this case, SM700 does not turn on.

| When a signed value is specified |  |  |  |  | When an unsigned value is specified |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| K2147483647 <br> (H7FFFFFFF) |  | $\begin{aligned} & \text { K-2 } \\ & \text { (HFFFFFFFE) } \end{aligned}$ | $\sqrt[2]{2}$ | $\begin{aligned} & \text { K-2147483647 } \\ & \text { (H80000001) } \end{aligned}$ | $\begin{aligned} & \text { K0 } \\ & \text { (H00000000) } \end{aligned}$ |  | $\begin{aligned} & \text { K1 } \\ & \text { (H00000001) } \end{aligned}$ | K4294967295 (HFFFFFFFF) |
| $\begin{aligned} & \text { K-2147483647 } \\ & \text { (H80000001) } \end{aligned}$ | - | $\begin{aligned} & \text { K2 } \\ & \text { (H00000002) } \end{aligned}$ | $\stackrel{\square}{2}$ | $\begin{aligned} & \text { K2147483647 } \\ & \text { (H7FFFFFFF) } \end{aligned}$ |  |  |  |  |

## Operation error

| Error code (SD0) | Description |
| :--- | :--- |
| 2821 H | The device ranges starting from the ones specified by (s1) and (d) are overlapping (except when the same device is specified for (s1) and <br> (d)). |
|  | The device ranges starting from the ones specified by (s2) and (d) are overlapping (except when the same device is specified for (s2) and <br> (d)). |

## Incrementing 16－bit binary data

## INC（P）（U）

These instructions increment the specified 16－bit binary data by one．

| Ladder | ST |  |
| :---: | :---: | :---: |
|  | $\begin{aligned} & \text { ENO:=INC(EN,d); } \\ & \text { ENO:=INCP(EN,d); } \end{aligned}$ | $\begin{aligned} & \text { ENO:=INC_U(EN,d); } \\ & \text { ENO:=INCP_U(EN,d); } \end{aligned}$ |

FBD／LD


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| INC | - |
| INC＿U | - |
| INCP | - |
| INCP＿U |  |

## Setting data

## ■Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- | :--- |
| （d） | INC（P） | Increment target device | -32768 to 32767 | 16－bit signed binary | ANY16＿S |
|  |  |  | INC（P）＿U |  | 0 to 65535 |
| EN | Execution condition | - | 16－bit unsigned binary | ANY16＿U |  |
| ENO |  | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3E미（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions increment the 16－bit binary data in the device specified by（d）by one．

－When the $\operatorname{INC}(P)$ instruction is executed while the data in the device specified by（d）is $32767,-32768$ is stored in the device specified by（d）．
－When the INC（P）＿U instruction is executed while the data in the device specified by（d）is 65535,0 is stored in the device specified by（d）．

## Operation error

There is no operation error

## Decrementing 16－bit binary data

## DEC（P）（＿U）

These instructions decrement the specified 16－bit binary data by one．

| Ladder | ST |  |
| :---: | :---: | :---: |
|  | $\begin{aligned} & \text { ENO:=DEC(EN,d); } \\ & \text { ENO:=DECP(EN,d); } \end{aligned}$ | $\begin{aligned} & \text { ENO:=DEC_U(EN,d); } \\ & \text { ENO:=DECP_U(EN,d); } \end{aligned}$ |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DEC | - |
| DEC＿U | - |
| DECP | - |
| DECP＿U |  |

## Setting data

－Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- | :--- |
| （d） | DEC（P） | Decrement target device | -32768 to 32767 | 16－bit signed binary | ANY16＿S |
|  | DEC（P）＿U |  | 0 to 65535 | 16－bit unsigned binary | ANY16＿U |
| EN | Execution condition | - | Bit | BOOL |  |
| ENO | Execution result | - | Bit | BOOL |  |

■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGㅁ，Jपاロ， U3EDl（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions decrement the 16 －bit binary data in the device specified by（d）by one．
（d）
（d）

－When the $\operatorname{DEC}(P)$ instruction is executed while the data in the device specified by $(\mathrm{d})$ is $-32768,32767$ is stored in the device specified by（d）．
－When the $\operatorname{DEC}(P)$＿U instruction is executed while the data in the device specified by（d）is 0,65535 is stored in the device specified by（d）．

## Operation error

There is no operation error

## Incrementing 32－bit binary data

## DINC（P）（＿U）

These instructions increment the specified 32－bit binary data by one．

| Ladder | ST |  |  |
| :--- | :--- | :--- | :--- |
|  |  | ENO：＝DINC（EN，d）； |  |
| $\square-\square-\square$ | （d） |  | ENO：＝DINC＿U（EN，d）； <br> ENO：＝DINCP＿U（EN，d）； |

FBD／LD


■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DINC | - |
| DINC＿U | - |
| DINCP | $\boxed{ }$ |
| DINCP＿U |  |

## Setting data

Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- | :--- |
| （d） | DINC（P） | Increment target start device | -2147483648 to 2147483647 | 32－bit signed binary | ANY32＿S |
|  |  |  | DINC（P）＿U |  | 0 to 4294967295 |
| EN | Execution condition | - | 32－bit unsigned binary | ANY32＿U |  |
| ENO |  | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロID， U3EDI（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions increment the 32－bit binary data in the device specified by（d）by one．

－When the $\operatorname{DINC}(P)$ instruction is executed while the data in the device specified by（d）is $2147483647,-2147483648$ is stored in the device specified by（d）．
－When the DINC（P）＿U instruction is executed while the data in the device specified by（d）is 4294967295,0 is stored in the device specified by（d）．

## Operation error

There is no operation error

## Decrementing 32－bit binary data

## DDEC（P）（＿U）

These instructions decrement the specified 32－bit binary data by one．

| Ladder | ST |  |
| :---: | :---: | :---: |
| （d） | $\begin{aligned} & \text { ENO:=DDEC(EN,d); } \\ & \text { ENO:=DDECP(EN,d); } \end{aligned}$ | $\begin{aligned} & \text { ENO:=DDEC_U(EN,d); } \\ & \text { ENO:=DDECP_U(EN,d); } \end{aligned}$ |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DDEC | - |
| DDEC＿U | - |
| DDECP | $\boxed{ }$ |
| DDECP＿U | - |

Setting data
Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- | :--- |
| （d） | DDEC（P） | Decrement target start device | -2147483648 to 2147483647 | 32－bit signed binary | ANY32＿S |
|  |  |  | 0 to 4294967295 | 32－bit unsigned binary | ANY32＿U |
| EN |  | Execution condition | - | Bit | BOOL |
| ENO |  | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDl（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions decrement the 32－bit binary data in the device specified by（d）by one．

| （d） | $\overbrace{\text { b31 } \cdots \text { b16 }}^{(\mathrm{d})+1} \overbrace{15}^{(\mathrm{c}} \mathrm{c}_{\text {b0 }}^{(\mathrm{d})}$ |  |  |
| :---: | :---: | :---: | :---: |
| ．．．b16 b15 ．．．b0 |  |  |  |
| 73500 （BIN） |  |  |  |

－When the DDEC $(P)$ instruction is executed while the data in the device specified by（ d ）is $-2147483648,2147483647$ is stored in the device specified by（d）．
－When the $\operatorname{DDEC}(P)$ instruction is executed while the data in the device specified by（d）is $0,-1$ is stored in the device specified by（d）．
－When the DDEC（P）＿U instruction is executed while the data in the device specified by（d）is 0,4294967295 is stored in the device specified by（d）．

## Operation error

There is no operation error．

## 6．3 Logical Operation Instructions

## Performing an AND operation on 16－bit data

## WAND（P）［when two operands are set］

These instructions perform an AND operation on the two sets of 16－bit binary data specified．

| Ladder |  | ST |
| :---: | :---: | :---: |
|  |  | Not supported |
|  |  |  |
| FBD／LD |  |  |
| Not supported |  |  |

## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| WAND | - |
|  | $\boxed{ }$ |
| WANDP | - |

Setting data
Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Logical AND data or the device where logical AND data is <br> stored | -32768 to 32767 | 16－bit signed binary | ANY16 |
| （d） | Device for storing the operation result | -32768 to 32767 | 16－bit signed binary | ANY16 |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | Jロロロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J밈， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions perform an AND operation（bit－by－bit）on the 16－bit binary data in the device specified by（d）and the 16－ bit binary data in the device specified by（s），and store the operation result in the device specified by（d）．
（d）

（s）

（d）

－When a bit device is specified，the instruction performs an operation by assuming that the ones after the number of digit－ specified points are 0.

## Operation error

There is no operation error．

## WAND（P）［when three operands are set］

These instructions perform an AND operation on the two sets of 16－bit binary data specified．

| Ladder | ST |
| :---: | :---: |
| ■－二－$\square$ （s1） （s2） （d） | $\begin{aligned} & \text { ENO:=WAND(EN,s1,s2,d); } \\ & \text { ENO:=WANDP(EN,s1,s2,d); } \end{aligned}$ |

FBD／LD


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| WAND | - |
|  | $\boxed{ }$ |
| WANDP | - |

## Setting data

DDescription，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Logical AND data or the device where logical AND data is <br> stored | -32768 to 32767 | 16－bit signed binary | ANY16 |
| （s2） | Logical AND data or the device where logical AND data is <br> stored | -32768 to 32767 | 16－bit signed binary | ANY16 |
| （d） | Device for storing the operation result | - | 16－bit signed binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，Jㅁㅁ， U3EDl（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions perform an AND operation（bit－by－bit）on the 16－bit binary data in the device specified by（s1）and the 16－bit binary data in the device specified by（ s 2 ），and store the operation result in the device specified by（d）．

（s2）

| b15 | ．．． | b8 | b7 | ．．． | b0 |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  |  |


－When a bit device is specified，the instruction performs an operation by assuming that the ones after the number of digit－ specified points are 0.

## Operation error

There is no operation error.

## Performing an AND operation on 32－bit data

## DAND（P）［when two operands are set］

These instructions perform an AND operation on the two sets of 32－bit binary data specified．


FBD／LD
Not supported
Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DAND | - |
|  | $\boxed{ }$ |
| DANDP | $\boxed{ }$ |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Logical AND data or the start device where logical AND <br> data is stored | -2147483648 to 2147483647 | 32－bit signed binary | ANY32 |
| （d） | Start device for storing the operation result | -2147483648 to 2147483647 | 32－bit signed binary | ANY32 |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロID， U3EDl（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions perform an AND operation（bit－by－bit）on the 32－bit binary data in the device specified by（d）and the 32－ bit binary data in the device specified by（s），and store the operation result in the device specified by（d）．
$\qquad$
（d）+1
（d）
（d）

$\qquad$
（s）

（d）
（d） $\square$
（d）+1
－When a bit device is specified，the instruction performs an operation by assuming that the ones after the number of digit－ specified points are 0.

## Operation error

There is no operation error．

## DAND（P）［when three operands are set］

These instructions perform an AND operation on the two sets of 32－bit binary data specified．

| Ladder | ST |
| :---: | :---: |
| $\square^{-\square-\square}-\square$ （s1） （s2） （d） | $\begin{aligned} & \text { ENO:=DAND(EN,s1,s2,d); } \\ & \text { ENO:=DANDP(EN,s1,s2,d); } \end{aligned}$ |

FBD／LD


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DAND | - |
|  | $\boxed{ }$ |
| DANDP | - |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Logical AND data or the start device where logical AND <br> data is stored | -2147483648 to 2147483647 | 32－bit signed binary | ANY32 |
| （s2） | Logical AND data or the start device where logical AND <br> data is stored | -2147483648 to 2147483647 | 32－bit signed binary | ANY32 |
| （d） | Start device for storing the operation result | - | 32－bit signed binary | ANY32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，Jㅁㅁ， U3EDl（H）Gロ | z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions perform an AND operation (bit-by-bit) on the 32-bit binary data in the device specified by (s1) and the 32-bit binary data in the device specified by (s2), and store the operation result in the device specified by (d).

(d) +1
(d)
(d)

- When a bit device is specified, the instruction performs an operation by assuming that the ones after the number of digitspecified points are 0 .


## Operation error

There is no operation error.

## Performing an AND operation on 16－bit block data

## BKAND（P）

These instructions perform an AND operation on the two 16－bit binary data blocks specified．


FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| BKAND | - |
|  |  |
| BKANDP |  |

## Setting data

DDescription，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Logical AND data or the start device where logical AND <br> data is stored | - | 16－bit signed binary | ANY16 |
| （s2） | Logical AND data or the start device where logical AND <br> data is stored | -32768 to 32767 | 16 －bit signed binary | ANY16 |
| （d） | Start device for storing the operation result | - | 16－bit signed binary | ANY16 |
| （n） | Number of data points | 0 to 65535 | 16－bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## －Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UㅁIGロ，J밈， U3EDI（H）G口 | z | LT，LST， LC | LZ |  | K，H | E | \＄ |  |
| （s1）${ }^{* 1}$ | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2）${ }^{* 1}$ | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d）${ }^{*}$ | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

[^5]
## Processing details

- These instructions perform an AND operation on the ( n ) points of data from the device specified by ( s 1 ) and the ( n ) points of data from the device specified by ( s 2 ), and store the operation result in the device specified by (d) and later.
(s1)
(s1)+1
(s1)+2


- A constant from -32768 to 32767 (16-bit signed binary) can be specified for (s2).
(s1)
(s1)+1
$(s 1)+2$

(n) AND
$\vdots$
$(\mathrm{s} 1)+(\mathrm{n})-2001: 0: 1$
$(\mathrm{s} 1)+(\mathrm{n})-1 \quad 1: 1: 1: 1,0: 0: 0: 0 ~ 1: 1: 1: 1: 0: 0: 0$
(s2)

$\qquad$
(d)

| $(d)+1$ | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| 1 | 1 | 1 | 1 |  |  |  |  |  |  |

(d)+2 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |

(d)+(n)-2 | 0 | 0 | 1 | 0 | 1 | 0 | 1 | 0 | 0 | 0 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | 0

(d)+(n)-1 $1: 1: 1: 1,0: 0: 0: 0,0: 0: 0$

## Operation error

| Error code (SD0) | Description |
| :--- | :--- |
| 2821 H | The device ranges starting from the ones specified by (s1) and (d) are partially overlapping (except when the same device is specified for <br> $(\mathrm{s} 1)$ and (d)). |
|  | The device ranges starting from the ones specified by (s2) and (d) are partially overlapping (except when the same device is specified for <br> $(\mathrm{s} 2)$ and (d)). |

## Performing an OR operation on 16－bit data

## WOR（P）［when two operands are set］

These instructions perform an OR operation on the two sets of 16－bit binary data specified．


FBD／LD
Not supported
Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| WOR | - |
|  | $\boxed{ }$ |
| WORP | - |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Logical OR data or the device where logical OR data is <br> stored | -32768 to 32767 | 16 －bit signed binary | ANY16 |
| （d） | Device for storing the operation result | -32768 to 32767 | 16 －bit signed binary | ANY16 |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロID， U3EDl（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions perform an OR operation（bit－by－bit）on the 16 －bit binary data in the device specified by（d）and the 16－ bit binary data in the device specified by（s），and store the operation result in the device specified by（d）．
（d）

（s）

（d）

－When a bit device is specified，the instruction performs an operation by assuming that the ones after the number of digit－ specified points are 0.

## Operation error

There is no operation error．

## WOR（P）［when three operands are set］

These instructions perform an OR operation on the two sets of 16－bit binary data specified．

| Ladder | ST |
| :---: | :---: |
| ■－——च （s1） （s2） （d） | $\begin{aligned} & \mathrm{ENO}:=\mathrm{WOR}(\mathrm{EN}, \mathrm{~s} 1, \mathrm{~s} 2, \mathrm{~d}) ; \\ & \mathrm{ENO}:=\mathrm{WORP}(\mathrm{EN}, \mathrm{~s} 1, \mathrm{~s} 2, \mathrm{~d}) ; \end{aligned}$ |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| WOR | $-\square$ |
| WORP | - |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Logical OR data or the device where logical OR data is <br> stored | -32768 to 32767 | 16 －bit signed binary | ANY16 |
| （s2） | Logical OR data or the device where logical OR data is <br> stored | -32768 to 32767 | 16－bit signed binary | ANY16 |
| （d） | Device for storing the operation result | - | 16 －bit signed binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J미， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions perform an OR operation（bit－by－bit）on the 16－bit binary data in the device specified by（s1）and the 16－ bit binary data in the device specified by（s2），and store the operation result in the device specified by（d）．



－When a bit device is specified，the instruction performs an operation by assuming that the ones after the number of digit－ specified points are 0.

## Operation error

There is no operation error

## Performing an OR operation on 32－bit data

## DOR（P）［when two operands are set］

These instructions perform an OR operation on the two sets of 32－bit binary data specified．


FBD／LD
Not supported

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DOR | - |
|  | $\boxed{ }$ |
| DORP | $\boxed{ }$ |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Logical OR data or the start device where logical OR data <br> is stored | -2147483648 to 2147483647 | 32－bit signed binary | ANY32 |
| （d） | Start device for storing the operation result | -2147483648 to 2147483647 | 32－bit signed binary | ANY32 |

## －Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロID， U3EDl（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions perform an OR operation（bit－by－bit）on the 32－bit binary data in the device specified by（d）and the 32－ bit binary data in the device specified by（s），and store the operation result in the device specified by（d）．
（d）+1
（d）
（d）

$\square$
（s）+1
（s）

（d）+1
（d）
（d） $\square$

| b16 |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 1 | 0 | 0 | 0 | 1 | 1 | 1 |


|  | $b 0$ |  |  |
| :--- | :--- | :--- | :--- |
|  | 0 | 0 | 1 |

－When a bit device is specified，the instruction performs an operation by assuming that the ones after the number of digit－ specified points are 0.

## Operation error

There is no operation error．

## DOR（P）［when three operands are set］

These instructions perform an OR operation on the two sets of 32－bit binary data specified．


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DOR | $-\square$ |
| DORP | $\uparrow$ |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Logical OR data or the start device where logical OR data <br> is stored | -2147483648 to 2147483647 | 32－bit signed binary | ANY32 |
| （s2） | Logical OR data or the start device where logical OR data <br> is stored | -2147483648 to 2147483647 | 32－bit signed binary | ANY32 |
| （d） | Start device for storing the operation result | - | 32－bit signed binary | ANY32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J미， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions perform an OR operation (bit-by-bit) on the 32-bit binary data in the device specified by (s1) and the 32bit binary data in the device specified by (s2), and store the operation result in the device specified by (d).


(d) +1
(d)

| b31 | b16 b15 |  |
| :---: | :---: | :---: |
| 0 0 1 1 | $\int\left[\begin{array}{ll:l:l}1 \\ 1 & 1 & 1 & 1\end{array}\right.$ | 1 0 0 |

- When a bit device is specified, the instruction performs an operation by assuming that the ones after the number of digitspecified points are 0.


## Operation error

There is no operation error

## Performing an OR operation on 16－bit block data

## BKOR（P）

These instructions perform an OR operation on the two 16－bit binary data blocks specified．


FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| BKOR | - |
|  | $\boxed{ }$ |
| BKORP | - |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Logical OR data or the start device where logical OR data <br> is stored | - | 16－bit signed binary | ANY16 |
| （s2） | Logical OR data or the start device where logical OR data <br> is stored | -32768 to 32767 | 16－bit signed binary | ANY16 |
| （d） | Start device for storing the operation result | - | 16－bit signed binary | ANY16 |
| （n） | Number of data points | 0 to 65535 | 16 －bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロロロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J미， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1）${ }^{* 1}$ | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2）${ }^{* 1}$ | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d）${ }^{* 1}$ | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

＊1 The same device number can be specified for（s1）and（d）or（s2）and（d）．

## Processing details

- These instructions perform an OR operation on the ( n ) points of data from the device specified by ( s 1 ) and the ( n ) points of data from the device specified by (s2), and store the operation result in the device specified by (d) and later.

(d)
(d) +1


(d)+(n)-1 $1: 1: 1: 1: 1: 1: 1: 1$ 0:0:0:0 $1: 1: 1: 1$
- A constant from -32768 to 32767 (16-bit signed binary) can be specified for (s2).





## Operation error

| Error code (SD0) | Description |
| :--- | :--- |
| 2821 H | The device ranges starting from the ones specified by (s1) and (d) are partially overlapping (except when the same device is specified for <br> $(\mathrm{s} 1)$ and (d)). |
|  | The device ranges starting from the ones specified by (s2) and (d) are partially overlapping (except when the same device is specified for <br> $(\mathrm{s} 2)$ and (d)). |

## Performing an XOR operation on 16－bit data

## WXOR（P）［when two operands are set］

These instructions perform an XOR operation on the two sets of 16－bit binary data specified．

| Ladder | ST |
| :---: | :---: |
|  | Not supported |
| $\square--\square$ （s） （d） |  |

FBD／LD
Not supported
Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| WXOR | - |
|  | $\boxed{T}$ |
| WXORP | - |

Setting data
Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Exclusive OR data or the device where exclusive OR data <br> is stored | -32768 to 32767 | 16 －bit signed binary | ANY16 |
| （d） | Device for storing the operation result | -32768 to 32767 | 16 －bit signed binary | ANY16 |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | J밈 | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロID， U3E미（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions perform an XOR operation（bit－by－bit）on the 16－bit binary data in the device specified by（d）and the 16－ bit binary data in the device specified by（s），and store the operation result in the device specified by（d）．
（d）

（s）

（d）

－When a bit device is specified，the instruction performs an operation by assuming that the ones after the number of digit－ specified points are 0.

## Operation error

There is no operation error．

## WXOR（P）［when three operands are set］

These instructions perform an XOR operation on the two sets of 16－bit binary data specified．


FBD／LD

| ᄃ：二－」 |  |
| :---: | :---: |
| En | eno |
| s1 | d |
| s2 |  |

Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| WXOR | $-\square$ |
| WXORP | $\ddots$ |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Exclusive OR data or the device where exclusive OR data <br> is stored | -32768 to 32767 | 16－bit signed binary | ANY16 |
| （s2） | Exclusive OR data or the device where exclusive OR data <br> is stored | -32768 to 32767 | 16－bit signed binary | ANY16 |
| （d） | Device for storing the operation result | - | 16－bit signed binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathbf{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J미， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions perform an XOR operation（bit－by－bit）on the 16－bit binary data in the device specified by（s1）and the 16－bit binary data in the device specified by（ s 2 ），and store the operation result in the device specified by（d）．
（s1）

（s2）

（d）

－When a bit device is specified，the instruction performs an operation by assuming that the ones after the number of digit－ specified points are 0.

## Operation error

There is no operation error

## Performing an XOR operation on 32－bit data

## DXOR（P）［when two operands are set］

These instructions perform an XOR operation on the two sets of 32－bit binary data specified．

| Ladder | ST |
| :---: | :---: |
|  | Not supported |
| $-\square-\square$ （s） （d） |  |

FBD／LD
Not supported
Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DXOR | - |
|  | $\boxed{ }$ |
| DXORP | - |

Setting data
Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Exclusive OR data or the start device where exclusive OR <br> data is stored | -2147483648 to 2147483647 | 32－bit signed binary | ANY32 |
| （d） | Start device for storing the operation result | -2147483648 to 2147483647 | 32－bit signed binary | ANY32 |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3EDl（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions perform an XOR operation（bit－by－bit）on the 32－bit binary data in the device specified by（d）and the 32－ bit binary data in the device specified by（s），and store the operation result in the device specified by（d）．

（d） \begin{tabular}{l}
b31 <br>

| 0 | 1 | 0 |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  |  |  | <br>

\hline
\end{tabular} $\qquad$ （d）

（d）

| b16 b15 |  |
| :---: | :---: |
| $\begin{array}{ll:l:l}1 & 0 & 1 & 0\end{array}$ | 1 0 1 |
| XOR |  |

$\square$
（s）+1
（s）

（d）+1
（d）



－When a bit device is specified，the instruction performs an operation by assuming that the ones after the number of digit－ specified points are 0.

## Operation error

There is no operation error．

## DXOR（P）［when three operands are set］

These instructions perform an XOR operation on the two sets of 32－bit binary data specified．

| Ladder | ST |
| :---: | :---: |
| ■－二－$\square$ （s1） （s2） （d） | $\begin{aligned} & \text { ENO:=DXOR(EN,s1,s2,d); } \\ & \text { ENO:=DXORP(EN,s1,s2,d); } \end{aligned}$ |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DXOR | - |
|  | $\boxed{ }$ |
| DXORP | - |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Exclusive OR data or the start device where exclusive OR <br> data is stored | -2147483648 to 2147483647 | 32－bit signed binary | ANY32 |
| （s2） | Exclusive OR data or the start device where exclusive OR <br> data is stored | -2147483648 to 2147483647 | 32－bit signed binary | ANY32 |
| （d） | Start device for storing the operation result | - | 32－bit signed binary | ANY32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3EDI（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions perform an XOR operation (bit-by-bit) on the 32-bit binary data in the device specified by (s1) and the 32-bit binary data in the device specified by (s2), and store the operation result in the device specified by (d).

(d) +1
(d)
(d)

- When a bit device is specified, the instruction performs an operation by assuming that the ones after the number of digitspecified points are 0.


## Operation error

There is no operation error

## Performing an XOR operation on 16－bit block data

## BKXOR（P）

These instructions perform an XOR operation on the two 16－bit binary data blocks specified．


## EExecution condition

| Instruction | Execution condition |
| :--- | :--- |
| BKXOR | - |
| BKXORP | $\boxed{ }$ |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Start device where the logical operation data is stored | - | 16－bit signed binary | ANY16 |
| （s2） | Logical operation data or the start device where the logical <br> operation data is stored | -32768 to 32767 | 16－bit signed binary | ANY16 |
| （d） | Start device for storing the operation result | - | 16－bit signed binary | ANY16 |
| （n） | Number of data points | 0 to 65535 | 16－bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3Eपl（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1）${ }^{* 1}$ | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2）${ }^{* 1}$ | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d）${ }^{*}$ | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

[^6]
## Processing details

- These instructions perform an XOR operation on the $(\mathrm{n})$ points of data from the device specified by ( s 1 ) and the ( n ) points of data from the device specified by ( s 2 ), and store the operation result in the device specified by (d) and later.

|  | b15 | b8b | b7 | b0 |  |  | b15 | b8 | b7 | b0 |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| (s1) | 000111 | 00011 | 0000 | 00011 | $\underbrace{4}_{(n)}$ | (s2) | 000:1:1 | 11100 | 0,0:11 | 1110 0 | 4 |
| (s1) +1 | 1110:0 | 11100 | 11100 | 1:110 |  | (s2)+1 | 000111 | 1:1000 | 0:0:0 0 | 1:1:00 |  |
| (s1) +2 | 1:1:1:1 | 0 O 010 | 1:1:1:1 | 0000 |  | (s2)+2 | 0000 | $00_{0} 0$ | 1!1:1:1 | 1:1:1:1 | (n) |
| : |  |  |  |  |  | : |  |  |  |  | ( n ) |
| $(\mathrm{s} 1)+(\mathrm{n})-2$ | 0100 | 0,0ioio | 1:1:1:1 | 1:1:1:1 | ${ }^{\text {(n) }}$ | (s2)+(n)-2 | 1:1:1:1 | 1:1:1:1 | 010:010 | 1:1:1:1 |  |
| $(\mathrm{s} 1)+(\mathrm{n})-1$ | 0.01010 | 1:1:1:1 | 1:1:1:1 | 01000 |  | (s2)+(n)-1 | 0000 | 1:1:1:1 | 1:1:1:1 | 1:1:1:1 | $\downarrow$ |


| b15 | b8b7 |  | b0 |  |
| :---: | :---: | :---: | :---: | :---: |
| $00^{010} 0$ | 1:1:1:1 | 0,0:1:1 | 11111 | 4 |
| 1:1:1:1 | 0000 | 1:1:0 0 | 0,0:0 0 |  |
| 1:1:1:1 | $00_{0} 0$ | $00_{0} 0$ | 1:1:1:1 |  |




- A constant from -32768 to 32767 (16-bit signed binary) can be specified for (s2).



## Operation error

| Error code (SD0) | Description |
| :--- | :--- |
| 2821 H | The device ranges starting from the ones specified by (s1) and (d) are partially overlapping (except when the same device is specified for <br> $(\mathrm{s} 1)$ and (d)). |
|  | The device ranges starting from the ones specified by (s2) and (d) are partially overlapping (except when the same device is specified for <br> $(\mathrm{s} 2)$ and (d)). |

## Performing an XNOR operation on 16－bit data

## WXNR（P）［when two operands are set］

These instructions perform an XNOR operation on the two sets of 16－bit binary data specified．

| Ladder | ST |
| :---: | :---: |
|  | Not supported |
| $\square--\square$ （s） （d） |  |

FBD／LD
Not supported
Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| WXNR | - |
|  | $\boxed{ }$ |
| WXNRP | - |

Setting data
Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Exclusive NOR data or the device where exclusive NOR <br> data is stored | -32768 to 32767 | 16 －bit signed binary | ANY16 |
| （d） | Device for storing the operation result | -32768 to 32767 | 16 －bit signed binary | ANY16 |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | J밈 | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロID， U3E미（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions perform an XNOR operation on the 16 －bit binary data in the device specified by（d）and the 16－bit binary data in the device specified by（s），and store the operation result in the device specified by（d）．
（d）

（s）

（d）

－When a bit device is specified，the instruction performs an operation by assuming that the ones after the number of digit－ specified points are 0.

## Operation error

There is no operation error

## WXNR（P）［when three operands are set］

These instructions perform an XNOR operation on the two sets of 16－bit binary data specified．


FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| WXNR | - |
|  | $\boxed{ }$ |
| WXNRP | - |

## Setting data

## Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1），（s2） | Exclusive NOR data or the device where exclusive NOR <br> data is stored | -32768 to 32767 | 16－bit signed binary | ANY16 |
| （d） | Device for storing the operation result | - | 16－bit signed binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J밈， U3Eपl（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions perform an exclusive NOR operation on the 16－bit binary data in the device specified by（s1）and the 16－ bit binary data in the device specified by（s2），and store the operation result in the device specified by（d）．



－When a bit device is specified，the instruction performs an operation by assuming that the ones after the number of digit－ specified points are 0.

## Operation error

There is no operation error.

## Performing an XNOR operation on 32－bit data

## DXNR（P）［when two operands are set］

These instructions perform an XNOR operation on the two sets of 32－bit binary data specified．

| Ladder | ST |
| :---: | :---: |
|  | Not supported |
| $\square--$－$\square$ （s） （d） |  |

FBD／LD
Not supported
Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DXNR | - |
|  | $\boxed{ }$ |
| DXNRP | - |

Setting data
Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Exclusive NOR data or the start device where exclusive <br> NOR data is stored | -2147483648 to 2147483647 | 32－bit signed binary | ANY32 |
| （d） | Start device for storing the operation result | -2147483648 to 2147483647 | 32－bit signed binary | ANY32 |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3E［l（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions perform an XNOR operation on the 32－bit binary data in the device specified by（d）and the 32－bit binary data in the device specified by（s），and store the operation result in the device specified by（d）．
（d）+1
（d）

$(\mathrm{s})+1$
（s）

（d）+1
（d）

－When a bit device is specified，the instruction performs an operation by assuming that the ones after the number of digit－ specified points are 0.

## Operation error

There is no operation error．

## DXNR（P）［when three operands are set］

These instructions perform an XNOR operation on the two sets of 32－bit binary data specified．

| Ladder | ST |
| :---: | :---: |
| $\square-\square-\square$ （s1） （s2） （d） | $\begin{aligned} & \text { ENO:=DXNR(EN,s1,s2,d); } \\ & \text { ENO:=DXNRP(EN,s1,s2,d); } \end{aligned}$ |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DXNR | - |
|  | $\boxed{ }$ |
| DXNRP | - |

## Setting data

## Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1），（s2） | Exclusive NOR data or the start device where exclusive <br> NOR data is stored | -2147483648 to 2147483647 | 32－bit signed binary | ANY32 |
| （d） | Start device for storing the operation result | - | 32－bit signed binary | ANY32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，Jロ\ロ， U3EDI（H）GD | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions perform an XNOR operation on the 32-bit binary data in the device specified by (s1) and the 32-bit binary data in the device specified by (s2), and store the operation result in the device specified by (d).
s1)

$\square$
(s2)

(s2)
d) +1
(d)
(d)

- When a bit device is specified, the instruction performs an operation by assuming that the ones after the number of digitspecified points are 0.


## Operation error

There is no operation error.

## Performing an XNOR operation on 16-bit block data

## BKXNR(P)

These instructions perform an XNOR operation on the two 16-bit binary data blocks specified.


■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| BKXNR | - |
|  | $\boxed{ }$ |
| BKXNRP | - |

Setting data
Description, range, data type

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (s1) | Start device where the logical operation data is stored | - | 16-bit signed binary | ANY16 |
| (s2) | Logical operation data or the start device where the logical <br> operation data is stored | -32768 to 32767 | 16-bit signed binary | ANY16 |
| (d) | Start device for storing the operation result | - | 16-bit signed binary | ANY16 |
| (n) | Number of data points | 0 to 65535 | 16-bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ\|ロ | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | U밈, J밈, U3E미(H)Gㅁ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (s1) ${ }^{* 1}$ | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (s2) ${ }^{* 1}$ | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | $\bigcirc$ | - | - | - |
| (d) ${ }^{*}$ | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| ( n ) | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - | $\bigcirc$ | $\bigcirc$ | - | - | - |

[^7]
## Processing details

- These instructions perform an exclusive NOR operation on the ( n ) points of data from the device specified by ( s 1 ) and the $(\mathrm{n})$ points of data from the device specified by (s2), and store the operation result in the device specified by (d) and later.

(d)
(d) +1

| b15 | b8b7 |  | b0 |
| :---: | :---: | :---: | :---: |
| 1:1:1:1 | 000:0 | 1:1:1:1 | 1:1:1,1 |
| 0101010 | 00:00 | 1:1:1:1 | 1:1:1:1 |
| 10:110 | 10:10 | 01101 | 0:10:1 |

$\vdots$

(d)+(n)-1 $1: 0: 1: 0$ 1:0:1:0 $0: 1: 0: 10: 1: 0: 1$

- A constant from -32768 to 32767 (16-bit signed binary) can be specified for (s2).

(s2)



## Operation error

| Error code (SD0) | Description |
| :--- | :--- |
| 2821 H | The device ranges starting from the ones specified by (s1) and (d) are partially overlapping (except when the same device is specified for <br> $(\mathrm{s} 1)$ and (d)). |
|  | The device ranges starting from the ones specified by (s2) and (d) are partially overlapping (except when the same device is specified for <br> $(\mathrm{s} 2)$ and (d)). |

## 6．4 Bit Processing Instructions

## Setting a bit in the word device

## BSET（P）

These instructions set the＇n＇th bit in the specified word device to 1 ．

| Ladder | ST |
| :---: | :---: |
|  | ENO：＝BSET（EN，n，d）； <br> ENO：＝BSETP（EN，n，d）； |

## FBD／LD



■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| BSET | - |
|  | $\boxed{ }$ |
| BSETP | - |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （d） | Set target device | - | 16－bit signed binary | ANY16 |
| （n） | Set target bit position | 0 to 15 | 16－bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## －Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3EDl（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

－These instructions set the＇n＇th bit in the word device specified by（d）to 1.
－If（ $n$ ）exceeds 15 ，the instruction sets lower 4 bits of data．


## Operation error

There is no operation error.

## Resetting a bit in the word device

## BRST(P)

These instructions reset the 'n'th bit in the specified word device to 0 .


■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| BRST | - |
|  | $\boxed{ }$ |
| BRSTP | - |

## Setting data

Description, range, data type

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (d) | Reset target device | - | 16-bit signed binary | ANY16 |
| $(\mathrm{n})$ | Reset target bit position | 0 to 15 | 16-bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | J미미 | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | U밈, J밈, U3Eㅁ(H)G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (d) | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - | $\bigcirc$ | - | - | - | - |
| ( n ) | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - | $\bigcirc$ | $\bigcirc$ | - | - | - |

## Processing details

- These instructions reset the ' $n$ 'th bit in the word device specified by (d) to 0 .
- If ( n ) exceeds 15 , the instruction sets lower 4 bits of data.




## Operation error

There is no operation error.

## Performing a 16-bit test

## TEST(P)

These instructions extract the 'n'th bit in the specified word device.


FBD/LD


## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| TEST | - |
|  | $\boxed{ }$ |
| TESTP | - |

## Setting data

■Description, range, data type

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (s1) | Device where the extract target bit data is stored | - | 16-bit signed binary | ANY16 |
| (s2) | Extract target bit position | 0 to 15 | 16-bit unsigned binary | ANY16 |
| (d) | Device for storing the extracted bit data | - | Bit | ANY_BOOL |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | J미민 | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | UपIGロ, JपIप, U3E미(H)G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (s1) | - | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - | $\bigcirc$ | - | - | - | - |
| (s2) | - | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - | $\bigcirc$ | $\bigcirc$ | - | - | - |
| (d) | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - | - | $\bigcirc$ | - | - | - | - |

## Processing details

- These instructions extract the bit data at the position specified by (s2) of the word device specified by (s1), and write it to the bit device specified by (d).

- The bit device specified by (d) turns off when the extracted bit data is 0 and turns on when the bit data is 1 .
- Specify the bit position (0 to 15 ) of the word data in ( s 2 ). When 16 or a greater value is specified in ( s 2 ), the remainder of (s2) $\div 16$ becomes the bit position.


## Ex.

When (s2)=18: The remainder of $18 \div 16$ is 2 , and therefore the data in bit 2 will be extracted.

## Operation error

There is no operation error.

## Performing a 32－bit test

## DTEST（P）

These instructions extract the＇n＇th bit in the specified double－word device．

| Ladder | ST |
| :---: | :---: |
| ■－—— $\square$ （s1） （s2） （d） | $\begin{aligned} & \text { ENO:=DTEST(EN,s1,s2,d); } \\ & \text { ENO:=DTESTP(EN,s1,s2,d); } \end{aligned}$ |

## FBD／LD



## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DTEST | - |
|  | $\boxed{ }$ |
| DTESTP | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Device where the extract target bit data is stored | - | 32－bit signed binary | ANY32 |
| （s2） | Extract target bit position | 0 to 31 | 16－bit unsigned binary | ANY16 |
| （d） | Device for storing the extracted bit data | - | Bit | ANY＿BOOL |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDI（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |
| （s2） | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions extract the bit data at the position specified by (s2) of the double-word device specified by (s1), and write it to the bit device specified by (d).

- The bit device specified by (d) turns off when the extracted bit data is 0 and turns on when the bit data is 1 .
- Specify the bit position (0 to 31 ) of the double-word data in ( s 2 ). When 32 or a greater value is specified in ( s 2 ), the remainder of ( s 2 ) $\div 32$ becomes the bit position.


## Ex.

When (s2)=34: The remainder of $34 \div 32$ is 2 , and therefore the data in bit 2 will be extracted.

## Operation error

There is no operation error.

## Batch－resetting bit devices

## BKRST（P）

These instructions reset the $(\mathrm{n})$ points of bit devices starting from the bit device specified．

| Ladder | ST |
| :---: | :---: |
| $\begin{array}{\|c\|c\|c\|} \hline-\square-\square & \text { (d) } & \text { (n) } \\ \hline \end{array}$ | $\begin{aligned} & \text { ENO:=BKRST(EN,n,d); } \\ & \text { ENO:=BKRSTP(EN,n,d); } \end{aligned}$ |

## FBD／LD



## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| BKRST | - |
|  | $\boxed{Z}$ |
| BKRSTP | $\boxed{ }$ |

Setting data
■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （d） | Start device to be reset | - | Bit | ANY＿BOOL |
| （n） | Number of reset target devices | 0 to 65535 | 16－bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J밈， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （d） | $\bigcirc$ | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

－These instructions reset the（ n ）points of bit devices starting from the bit device specified by（d）．
－The following table lists the reset status of the bit devices．

| Device | Status |
| :--- | :--- |
| Annunciator（F） | • The（ $n$ ）points of data starting from the annunciator（F）number in the device specified by（d）turn off． <br> －The annunciator numbers that turned off are deleted from SD64 to SD79，and the remaining data are <br> compressed forward． |
|  | －The number of annunciators stored in SD64 to SD79 is stored in SD63． |

－When the specified device is off，the device status does not change．

## Operation error

There is no operation error．

## 6．5 Shift Instructions

## Shifting 16－bit binary data to the right by n bit（s）

## SFR（P）

These instructions shift the 16 －bit binary data in the specified device to the right．

| Ladder |  | ST |
| :---: | :---: | :---: |
|  | （n） | $\begin{aligned} & \mathrm{ENO}:=\mathrm{SFR}(\mathrm{EN}, \mathrm{n}, \mathrm{~d}) ; \\ & \mathrm{ENO}:=\mathrm{SFRP}(\mathrm{EN}, \mathrm{n}, \mathrm{~d}) ; \end{aligned}$ |
| FBD／LD |  |  |
|  | － |  |



| Instruction | Execution condition |
| :--- | :--- |
| SFR | - |
|  | $\boxed{Z}$ |
| SFRP | - |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （d） | Shift target device | - | 16－bit signed binary | ANY16 |
| （n） | Number of shifts | 0 to 15 | 16－bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J미， U3EDI（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- These instructions shift the 16 -bit binary data in the device specified by (d) to the right by ( n ) bit(s). The ( n ) bit( s ) from the most significant bit is/are filled with 0 (s).

- When (d) is a bit device, bits are shifted to the right within the device range specified by digit specification.


Filled with 0s.

- The number of bits actually to be shifted is the remainder of $(\mathrm{n}) \div($ specified number of bits). For example, when $(\mathrm{n})$ is 15 and the specified number of bits is 8,7 bits are shifted because 15 divided by 8 equals 1 with a remainder of 7 .
- Specify any value between 0 and 15 for ( n ). If a value 16 or bigger is specified, bits are shifted by the remainder value of $\mathrm{n} \div 16$. For example, when $(\mathrm{n})$ is 18,2 bits are shifted to the right because 18 divided by 16 equals 1 with a remainder of 2 .


## Operation error

There is no operation error.

## Shifting 16－bit binary data to the left by $\mathbf{n}$ bit（s）

## SFL（P）

These instructions shift the 16－bit binary data in the specified device to the left．

| Ladder | ST |
| :---: | :---: |
| $\begin{array}{l\|l\|l\|} \hline-\square-\square & \text { (d) } & \text { (n) } \\ \hline \end{array}$ | $\begin{aligned} & \text { ENO:=SFL(EN,n,d); } \\ & \text { ENO:=SFLP(EN,n,d); } \end{aligned}$ |

## FBD／LD



■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| SFL | - |
|  | $\boxed{L}$ |
| SFLP | - |

Setting data
■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （d） | Shift target device | - | 16－bit signed binary | ANY16 |
| （n） | Number of shifts | 0 to 15 | 16－bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J밈， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- This instruction shifts the 16-bit binary data in the device specified by (d) to the left by ( n ) bit( s ). The ( n ) bit( s ) from the least significant bit is/are filled with $0(\mathrm{~s})$.


Filled with 0s.

- When (d) is a bit device, bits are shifted to the left within the device range specified by digit specification.

- The number of bits actually to be shifted is the remainder of $(\mathrm{n}) \div($ specified number of bits). For example, when $(\mathrm{n})$ is 15 and the specified number of bits is 8,7 bits are shifted because 15 divided by 8 equals 1 with a remainder of 7 .
- Specify any value between 0 and 15 for ( n ). If a value 16 or larger is specified, the value is shifted by the remainder value of $\mathrm{n} \div 16$ to the left. For example, when $(\mathrm{n})$ is 18,2 bits are shifted to the left because 18 divided by 16 equals 1 with a remainder of 2 .


## Operation error

There is no operation error.

## Shifting n－bit data to the right by one bit

## BSFR（P）

These instructions shift the n points of data starting from the specified device to the right by one bit．

| Ladder |  | ST |
| :--- | :--- | :--- |
| $-\square$ （d） （n） <br>   ENO：＝BSFR（EN，n，d）； |  |  |

FBD／LD


## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| BSFR | - |
|  | $\boxed{ }$ |
| BSFRP | - |

Setting data
■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （d） | Shift target device | - | Bit | ANY＿BOOL |
| $(\mathrm{n})$ | Number of devices to be shifted | 0 to 65535 | 16－bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3E미（H）Gㅁ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （d） | $\mathrm{O}^{* 1}$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

＊1 T，C，and ST cannot be used．

## Processing details

－These instructions shift the $(\mathrm{n})$ points of data starting from the device specified by（d）to the right by one bit．

－The value of the device specified by $(\mathrm{d})+(\mathrm{n})-1$ is set to 0 ．

## Operation error

There is no operation error．

## Shifting n－bit data to the left by one bit

## BSFL（P）

These instructions shift the n points of data starting from the specified device to the left by one bit．

| Ladder |  | ST |
| :--- | :--- | :--- |
| $-\square$ （d） （n） <br>   ENO：＝BSFL（EN，n，d）； |  |  |

FBD／LD


## －Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| BSFL | - |
|  | $\boxed{ }$ |
| BSFLP | - |

Setting data
■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （d） | Shift target device | - | Bit | ANY＿BOOL |
| $(\mathrm{n})$ | Number of devices to be shifted | 0 to 65535 | 16－bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3E미（H）Gㅁ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （d） | $\mathrm{O}^{* 1}$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

＊1 T，C，and ST cannot be used．

## Processing details

－These instructions shift the（ n ）points of data starting from the device specified by（d）to the left by one bit．

－The value of the device specified by（d）is set to 0 ．

## Operation error

There is no operation error．

## Shifting n－word data to the right by one word

## DSFR（P）

These instructions shift the n points of data starting from the specified device to the right by one word．

| Ladder | ST |
| :---: | :---: |
|  | $\begin{aligned} & \text { ENO:=DSFR(EN,n,d); } \\ & \text { ENO:=DSFRP(EN,n,d); } \end{aligned}$ |

FBD／LD


## －Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DSFR | - |
|  | $\boxed{ }$ |
| DSFRP | $\boxed{ }$ |

Setting data
■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （d） | Shift target device | - | Word | ANY16 |
| $(\mathrm{n})$ | Number of devices to be shifted | 0 to 65535 | 16－bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3E미（H）Gㅁ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

－These instructions shift the（ n ）points of data starting from the device specified by（d）to the right by one word．

$(\mathrm{n}) \longrightarrow$


| $(\mathrm{d})+(\mathrm{n})-1$ | （d）$+(\mathrm{n})-2$ | （d）＋（n）－3 | （d）＋（n）－4 | $\cdots$ | $(\mathrm{d})+1$ | （d） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 0 | 555 | 212 | 325 | $\square$ | 100 | 50 |

－The value of the device specified by $(\mathrm{d})+(\mathrm{n})-1$ is set to 0 ．

## Operation error

There is no operation error．

## Shifting n－word data to the left by one word

## DSFL（P）

These instructions shift the n points of data starting from the specified device to the left by one word．

| Ladder | ST |
| :---: | :---: |
|  | $\begin{aligned} & \text { ENO:=DSFL(EN,n,d); } \\ & \text { ENO:=DSFLP(EN,n,d); } \end{aligned}$ |

FBD／LD


## －Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DSFL | - |
|  | $\boxed{ }$ |
| DSFLP | - |

Setting data
■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （d） | Shift target device | - | Word | ANY16 |
| （n） | Number of devices to be shifted | 0 to 65535 | 16－bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロID， U3EDl（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

－These instructions shift the（ n ）points of data starting from the device specified by（d）to the left by one word．

－The value of the device specified by（d）is set to 0 ．

## Operation error

There is no operation error．

## Shifting n－bit data to the right by n bit（s）

## SFTBR（P）

These instructions shift the n －bit data starting from the specified device to the right by n bit（s）．


FBD／LD

| ■－－－ |  |
| :---: | :---: |
| EN | ENO |
| n1 | d |
| n2 |  |

■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| SFTBR | - |
|  | - |
| SFTBRP | - |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| $(\mathrm{d})$ | Shift target device | - | Bit | ANY＿BOOL |
| $(\mathrm{n} 1)$ | Number of bits to be shifted | 0 to 64 | 16－bit unsigned binary | ANY16 |
| （n2） | Number of shifts | 0 to 65535 | 16 －bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J미， U3Eपl（H）G口 | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （d） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ n 1 ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （n2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- These instructions shift the ( n 1 ) bit(s) of data starting from the specified device to the right by ( n 2 ) bit( s ).

Ex.
When ( n 1 ) $=10$ and ( n 2 ) $=4$


- Specify ( $n 1$ ) and ( $n 2$ ) so that the following condition is satisfied: $(\mathrm{n} 1)>(\mathrm{n} 2)$. In the case of $(\mathrm{n} 1) \leq(\mathrm{n} 2)$, data is shifted by the value of the remainder of $(n 2) \div(n 1)$. However, if the remainder value is 0 , no processing is performed.
- Specify ( n 1 ) within the range of 1 to 64.
- The ( n 2 ) bit(s) from the most significant bit is/are filled with $0(\mathrm{~s})$. In the case of $(\mathrm{n} 1)<(\mathrm{n} 2)$, the bits are filled with 0 s by the value of the remainder of $(\mathrm{n} 2) \div(\mathrm{n} 1)$.
- If ( n 1 ) or ( n 2 ) is 0 , no processing is performed.


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3405 H | The value specified by ( n 1 ) is out of the range, 0 to 64. |

## Shifting n－bit data to the left by $\mathbf{n}$ bit（s）

## SFTBL（P）

These instructions shift the n －bit data starting from the specified device to the left by n bit（s）．


FBD／LD

| ■－－－］ |  |
| :---: | :---: |
| EN | ENO |
| n1 | d |
| n2 |  |

■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| SFTBL | - |
|  | $\boxed{~ S F T B L P ~}$ |
|  | - |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （d） | Shift target device | - | Bit | ANY＿BOOL |
| $(\mathrm{n} 1)$ | Number of bits to be shifted | 0 to 64 | 16－bit unsigned binary | ANY16 |
| $(\mathrm{n} 2)$ | Number of shifts | 0 to 65535 | 16－bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3E미（H）G口 | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （d） | $\bigcirc{ }^{* 1}$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （n1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （n2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

[^8]
## Processing details

- These instructions shift the ( n 1 ) bit(s) of data starting from the specified device to the left by ( n 2 ) bit(s).


## Ex.

When ( n 1 ) $=10$ and ( n 2 ) $=4$


- Specify ( $n 1$ ) and ( $n 2$ ) so that the following condition is satisfied: $(n 1)>(n 2)$. In the case of $(n 1) \leq(n 2)$, data is shifted by the value of the remainder of $(\mathrm{n} 2) \div(\mathrm{n} 1)$. However, if the remainder value is 0 , no processing is performed.
- Specify ( n 1 ) within the range of 1 to 64 .
- The ( n 2 ) bit(s) from the least significant bit is/are filled with $0(\mathrm{~s})$. In the case of $(\mathrm{n} 1)<(\mathrm{n} 2)$, the bits are filled with 0 s by the value of the remainder of $(\mathrm{n} 2) \div(\mathrm{n} 1)$.
- If ( n 1 ) or ( n 2 ) is 0 , no processing is performed.


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3405 H | The value specified by $(\mathrm{n} 1)$ is out of the range, 0 to 64. |

## Shifting n－word data to the right by n word（s）

## SFTWR（P）

These instructions shift the n －word data starting from the specified device to the right by n word（s）．


FBD／LD

| ■－－－］ |  |
| :---: | :---: |
| EN | ENO |
| n1 | d |
| n2 |  |

■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| SFTWR | - |
|  | - |
| SFTWRP |  |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （d） | Shift target device | - | Word | ANY16 |
| $(\mathrm{n} 1)$ | Number of words to be shifted | 0 to 65535 | 16－bit unsigned binary | ANY16 |
| $(\mathrm{n} 2)$ | Number of shifts | 0 to 65535 | 16－bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J미， U3Eपl（H）G口 | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ n 1 ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （n2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- These instructions shift the ( n 1 ) word( s ) of data starting from the specified device to the right by ( n 2 ) word( s ).

Ex.

## When ( n 1 ) $=9$ and ( n 2 ) $=4$

| (d) +8 | (d)+7 | (d) +6 | (d) +5 | (d) +4 | (n2) |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  | (d) +3 | (d) +2 | (d) +1 | (d) |
| 30FH | 1EH | 100H | OH | 1FFH | 10 H | 1FH | 7FFH | 2AH |
|  |  |  |  |  |  | $\rightarrow$ |  |  |
| (d) +8 | (d) +7 | (d) +6 | (d) +5 | (d) +4 | (d) +3 | (d) +2 | (d) +1 | (d) |
| OH | OH | OH | OH | 30FH | 1EH | 100 H | OH | 1FFH |

$$
\text { Filled with } \mathrm{OHs} \text {. }
$$

- The ( n 2 ) word(s) from the most significant bit is/are filled with $0(\mathrm{~s})$.
- If ( n 1 ) or ( n 2 ) is 0 , no processing is performed.
- In the case of ( n 1 ) $\leq(\mathrm{n} 2)$, ( n 1 ) words of data starting from the device specified by (d) become all 0 s .


## Operation error

There is no operation error.

## Shifting n－word data to the left by n word（s）

## SFTWL（P）

These instructions shift the n －word data starting from the specified device to the left by n word（s）．


## FBD／LD

| ■－－－］ |  |
| :---: | :---: |
| EN | ENO |
| n1 | d |
| n2 |  |

■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| SFTWL | - |
|  | - |
| SFTWLP | - |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （d） | Shift target device | - | Word | ANY16 |
| $(\mathrm{n} 1)$ | Number of words to be shifted | 0 to 65535 | 16－bit unsigned binary | ANY16 |
| $(\mathrm{n} 2)$ | Number of shifts | 0 to 65535 | 16－bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3EDI（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ n 1 ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （n2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- These instructions shift the ( n 1 ) word(s) of data starting from the specified device to the left by ( n 2 ) word(s).

Ex.
When ( n 1 ) $=9$ and ( n 2 ) $=4$


- The ( n 2 ) word(s) from the least significant bit is/are filled with $0(\mathrm{~s})$.
- If ( n 1 ) or ( n 2 ) is 0 , no processing is performed.
- In the case of ( n 1 ) $\leq(\mathrm{n} 2)$, ( n 1 ) words of data starting from the device specified by (d) become all 0 s.


## Operation error

There is no operation error.

## 6．6 Data Conversion Instructions

## Converting binary data to BCD 4－digit data

## BCD（P）

These instructions convert the specified 16－bit binary data to BCD 4－digit data．

| Ladder | ST |
| :---: | :---: |
|  | $\begin{aligned} & \mathrm{ENO}:=\mathrm{BCD}(\mathrm{EN}, \mathrm{~s}, \mathrm{~d}) ; \\ & \mathrm{ENO}:=\mathrm{BCDP}(\mathrm{EN}, \mathrm{~s}, \mathrm{~d}) ; \end{aligned}$ |

## FBD／LD



## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| BCD | - |
| BCDP | - |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Binary data or the device where the binary data is stored | 0 to 9999 | 16－bit signed binary | ANY16 |
| （d） | Device for storing the converted BCD data | - | BCD 4－digit | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロום | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UवIGロ，JロIロ， U3E미（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions convert the 16 －bit binary data（ 0 to 9999 ）in the device specified by（s）to BCD 4－digit data，and store the converted data in the device specified by（d）．


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3401 H | Data in the device specified by (s) is out of the range, 0 to 9999. |

## Converting binary data to BCD 8－digit data

## DBCD（P）

These instructions convert the specified 32－bit binary data to BCD 8－digit data．

| Ladder | ST |
| :---: | :---: |
| $\square-\square-\square$ （s） （d） | $\begin{aligned} & \mathrm{ENO}:=\mathrm{DBCD}(\mathrm{EN}, \mathrm{~s}, \mathrm{~d}) ; \\ & \mathrm{ENO}:=\mathrm{DBCDP}(\mathrm{EN}, \mathrm{~s}, \mathrm{~d}) ; \end{aligned}$ |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DBCD | - |
|  | $\boxed{ }$ |
| DBCDP | - |

Setting data
Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Binary data or the start device where the binary data is <br> stored | 0 to 99999999 | 32－bit signed binary | ANY32 |
| （d） | Start device for storing the converted BCD data | - | BCD 8－digit | ANY32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロID， U3EDI（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions convert the 32-bit binary data (0 to 99999999) in the device specified by (s) to BCD 8-digit data, and store the converted data in the device specified by (d).



## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3401 H | Data in the device specified by (s) is out of the range, 0 to 99999999. |

## Converting BCD 4－digit data to 16－bit binary data

## BIN（P）

These instructions convert the specified BCD 4－digit data to 16－bit binary data．

| Ladder | ST |
| :---: | :---: |
|  | $\begin{aligned} & \mathrm{ENO}:=\mathrm{BIN}(\mathrm{EN}, \mathrm{~s}, \mathrm{~d}) ; \\ & \mathrm{ENO}:=\mathrm{BINP}(\mathrm{EN}, \mathrm{~s}, \mathrm{~d}) ; \end{aligned}$ |

FBD／LD


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| BIN | - |
|  | $\boxed{ }$ |
| BINP | - |

## Setting data

## ■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | BCD data or the device where the BCD data is stored | 0 to 9999 | BCD 4－digit | ANY16 |
| （d） | Device for storing the converted binary data | - | 16－bit signed binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions convert the BCD 4－digit data（0 to 9999）in the device specified by（s）to 16－bit binary data，and store the converted data in the device specified by（d）．
（s）BCD 9999

$\}$ Converting to binary
（d）BIN 9999


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3401 H | A value other than 0 to 9 exists at any digit of the value in the device specified by (s). ${ }^{* 1}$ |

*1 Turning on SM754 can prevent this error from being detected.
If the specified value is out of the valid range, the $\operatorname{BIN}(P)$ instruction is not executed regardless of the status (on/off) of SM754.
The $\operatorname{BIN}(P)$ instruction does not execute the next operation until the command (execution condition) is turned off and on regardless of the presence of an error.

## Converting BCD 8－digit data to 32－bit binary data

## DBIN（P）

These instructions convert the specified BCD 8－digit data to 32－bit binary data．

| Ladder | ST |
| :---: | :---: |
| $\square-\square-\square$ （s） （d） | $\begin{aligned} & \text { ENO:=DBIN(EN,s,d); } \\ & \text { ENO:=DBINP(EN,s,d); } \end{aligned}$ |

FBD／LD


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DBIN | - |
|  | $\boxed{-}$ |
| DBINP | - |

Setting data

## ■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | BCD data or the start device where the BCD data is stored | 0 to 99999999 | BCD 8－digit | ANY32 |
| （d） | Start device for storing the converted binary data | - | 32－bit signed binary | ANY32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions convert the BCD 8-digit data (0 to 99999999) in the device specified by (s) to 32-bit binary data, and store the converted data in the device specified by (d).
(s) +1
(s)
(d) +1
(d)
(d) BIN 99999999



## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3401 H | A value other than 0 to 9 exists at any digit of the value in the device specified by (s). ${ }^{* 1}$ |

*1 Turning on SM754 can prevent this error from being detected.
If the specified value is out of the valid range, the $\operatorname{DBIN}(P)$ instruction is not executed regardless of the status (on/off) of SM754.
The $\operatorname{DBIN}(P)$ instruction does not execute the next operation until the command (execution condition) is turned off and on regardless of the presence of an error.

# Converting single－precision real number to 16－bit signed binary data 

## FLT2INT（P）

These instructions convert the specified single－precision real number to 16－bit signed binary data．


## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| FLT2INT | - |
|  | - |
| FLT2INTP |  |

Setting data
■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Single－precision real number or the start device where the <br> single－precision real number is stored | -32768 to 32767 | Single－precision real <br> number | ANYREAL＿32 |
| （d） | Device for storing the converted binary data | - | 16－bit signed binary | ANY16＿S |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDI（H）Gロ | Z | LT，LST， LC | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions convert the single-precision real number in the device specified by (s) to 16-bit signed binary data, and store the converted data in the device specified by (d).
- After conversion, the first digit after the decimal point of the single-precision real number is rounded off.
- When an input value is set using the engineering tool, a rounding error may occur. For the precautions on setting an input value using the engineering tool, refer to the following.
$\longmapsto$ Page 41 Precautions
The following program example converts, when M0 turns on, the single-precision real number stored in D0 and D1 to 16-bit signed binary data, and stores the converted data in D100.

| SM402 |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| M0 | (s) |  |  |  | (d) |
| $\mid+$ | FLT2INT | D0 | D100 |  |  |

(s)
D1, D0

Before conversion

| (s) | b31 $\cdots$ b16 |
| :---: | :---: |
| D0 | b15 $\cdots$ b0 |
| C49AH | $5000 H$ |
| $(-1234.5)$ |  |

After conversion
(d)

D100 | $\begin{array}{c}\text { FB2DH } \\ (-1235)\end{array}$ |
| :---: |

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3401 H | The single-precision real number in the device specified by (s) is out of the range, -32768 to 32767. |
| 3402 H | An unusual number is set to (s). <br> • The single-precision real number set to (s) is not within the following range: <br> $0,2^{-126} \leq\|(s)\|<2^{128}$ <br> • The value set to a device or label is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |

## Converting single－precision real number to 16－bit unsigned binary data

## FLT2UINT（P）

These instructions convert the specified single－precision real number to 16－bit unsigned binary data．


## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| FLT2UINT | - |
|  | - |
| FLT2UINTP |  |

Setting data
■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Single－precision real number or the start device where the <br> single－precision real number is stored | 0 to 65535 | Single－precision real <br> number | ANYREAL＿32 |
| （d） | Device for storing the converted binary data | - | 16－bit unsigned binary | ANY16＿U |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## EApplicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDI（H）G口 | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions convert the single-precision real number in the device specified by (s) to 16-bit unsigned binary data, and store the converted data in the device specified by (d).
- After conversion, the first digit after the decimal point of the single-precision real number is rounded off.
- When an input value is set using the engineering tool, a rounding error may occur. For the precautions on setting an input value using the engineering tool, refer to the following.
W Page 41 Precautions
The following program example converts, when M0 turns on, the single-precision real number stored in D0 and D1 to 16-bit unsigned binary data, and stores the converted data in D100.


After conversion
\(\begin{array}{cc}(d) \& \mathrm{b} 15 \cdots b0 <br>

\)|  D100  |
| :---: |${ }^{(1235)}\end{array}$

## Operation error

| Error code (SDO) | Description |
| :---: | :---: |
| 3401H | The single-precision real number in the device specified by (s) is out of the range, 0 to 65535. |
| 3402 H | An unusual number is set to (s). <br> - The single-precision real number set to (s) is not within the following range: $0,2^{-126} \leq\|(\mathrm{s})\|<2^{128}$ <br> - The value set to a device or label is -0 , a subnormal number, NaN (not a number), or $\pm \infty$. |

## Converting single－precision real number to 32－bit signed binary data

## FLT2DINT（P）

These instructions convert the specified single－precision real number to 32－bit signed binary data．


## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| FLT2DINT | - |
|  | - |
| FLT2DINTP |  |

Setting data
■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Single－precision real number or the start device where the <br> single－precision real number is stored | -2147483648 to 2147483647 | Single－precision real <br> number | ANYREAL＿32 |
| （d） | Start device for storing the converted binary data | - | 32－bit signed binary | ANY32＿S |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロID， U3EDI（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions convert the single-precision real number in the device specified by (s) to 32-bit signed binary data, and store the converted data in the device specified by (d).
- After conversion, the first digit after the decimal point of the single-precision real number is rounded off.
- When an input value is set using the engineering tool, a rounding error may occur. For the precautions on setting an input value using the engineering tool, refer to the following.
W Page 41 Precautions
The following program example converts, when M0 turns on, the single-precision real number stored in D0 and D1 to 32-bit signed binary data, and stores the converted data in D100 and D101.



## Operation error

| Error code (SD0) | Description |
| :---: | :---: |
| 3401H | The single-precision real number in the device specified by (s) is out of the range, -2147483648 to 2147483647 . |
| 3402H | An unusual number is set to (s). <br> - The single-precision real number set to (s) is not within the following range: <br> $0,2^{-126} \leq\|(\mathrm{s})\|<2^{128}$ <br> - The value set to a device or label is -0 , a subnormal number, NaN (not a number), or $\pm \infty$. |

## Converting single－precision real number to 32－bit unsigned binary data

## FLT2UDINT（P）

These instructions convert the specified single－precision real number to 32－bit unsigned binary data．

| Ladder |  |  | ST |
| :---: | :---: | :---: | :---: |
|  |  |  | Not supported |
|  | （s） | （d） |  |
| FBD／LD |  |  |  |
|  |  |  |  |
|  |  |  |  |
|  |  |  |  |

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| FLT2UDINT | - |
|  | $\boxed{~ F L T 2 U D I N T P ~}$ |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Single－precision real number or the start device where the <br> single－precision real number is stored | 0 to 4294967295 | Single－precision real <br> number | ANYREAL＿32 |
| （d） | Start device for storing the converted binary data | - | 32－bit unsigned binary | ANY32＿U |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロID， U3E미（H）Gㅁ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions convert the single－precision real number in the device specified by（s）to 32－bit unsigned binary data， and store the converted data in the device specified by（d）．
－After conversion，the first digit after the decimal point of the single－precision real number is rounded off．
－When an input value is set using the engineering tool，a rounding error may occur．For the precautions on setting an input value using the engineering tool，refer to the following．
$\longmapsto$ Page 41 Precautions
The following program example converts，when M0 turns on，the single－precision real number stored in D0 and D1 to 32－bit unsigned binary data，and stores the converted data in D100 and D101．

| SM402 |  |  |  |
| :---: | :---: | :---: | :---: |
| EMOVP | E123456．7 | D0 |  |

Before conversion
（s）

（d）
$\square$ D101，D100

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3401 H | The single-precision real number in the device specified by (s) is out of the range, 0 to 4294967295. |
| 3402 H | An unusual number is set to (s). <br>  <br>  <br>  <br> $0,2^{-126} \leq \leq(\mathrm{s}) \mid<2^{128}$ <br> • The value set to a device or label is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |

## Converting double－precision real number to 16－bit signed binary data

## DBL2INT（P）

These instructions convert the specified double－precision real number to 16－bit signed binary data．


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DBL2INT | - |
|  | - |
| DBL2INTP |  |

Setting data
Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Double－precision real number or the start device where the <br> double－precision real number is stored | -32768 to 32767 | Double－precision real <br> number | ANYREAL＿64 |
| （d） | Device for storing the converted binary data | - | 16－bit signed binary | ANY16＿S |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロID， U3E미（H）Gㅁ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （d） | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions convert the double－precision real number in the device specified by（s）to 16 －bit signed binary data，and store the converted data in the device specified by（d）．
－After conversion，the first digit after the decimal point of the double－precision real number is rounded off．
－When an input value is set using the engineering tool，a rounding error may occur．For the precautions on setting an input value using the engineering tool，refer to the following．
$\longmapsto$ Page 41 Precautions
The following program example converts，when M0 turns on，the double－precision real number stored in D0 to D3 to 16－bit signed binary data，and stores the converted data in D100

| SM402 |  |  |  |
| :---: | :---: | :---: | :---: |
| －1 | EDMOVP | E－5432．1 | D0 |
| M0 | （s） |  | （d） |
| 1 | DBL2INT | D0 | D100 |

（s）

| Before conversion |  |  |  | After conversion |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| b63 $\cdots$ b49 | b48…b32 | b31 $\cdots$ b16 | b15 $\cdots$ b0 | （d） | b15 $\cdots$ b0 |
| C0B5H | 3819 H | 9999H | 999AH | $\square \mathrm{D} 100$ | EAC8H |
| （－5432．1） |  |  |  |  | （－5432） |

## Operation error

| Error code (SDO) | Description |
| :---: | :---: |
| 3401H | The double-precision real number in the device specified by (s) is out of the range, -32768 to 32767 . |
| 3402H | An unusual number is set to (s). <br> - The double-precision real number set to (s) is not within the following range: <br> $0,2^{-1022} \leq\|(s)\|<2^{1024}$ <br> - The value set to a device or label is -0 , a subnormal number, NaN (not a number), or $\pm \infty$. |

## Converting double－precision real number to 16－bit unsigned binary data

## DBL2UINT（P）

These instructions convert the specified double－precision real number to 16－bit unsigned binary data．


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DBL2UINT | - |
|  | $\boxed{ }$ |
| DBL2UINTP |  |

Setting data
Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Double－precision real number or the start device where the <br> double－precision real number is stored | 0 to 65535 | Double－precision real <br> number | ANYREAL＿64 |
| （d） | Device for storing the converted binary data | - | 16－bit unsigned binary | ANY16＿U |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロID， U3E미（H）Gㅁ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （d） | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions convert the double－precision real number in the device specified by（s）to 16－bit unsigned binary data， and store the converted data in the device specified by（d）．
－After conversion，the first digit after the decimal point of the double－precision real number is rounded off．
－When an input value is set using the engineering tool，a rounding error may occur．For the precautions on setting an input value using the engineering tool，refer to the following．

## $\checkmark$ Page 41 Precautions

The following program example converts，when M0 turns on，the double－precision real number stored in D0 to D3 to 16－bit unsigned binary data，and stores the converted data in D100．

Before conversion
After conversion
（s）

| Before conversion |  |  |  | After conversion |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| b63 $\cdots$ b 49 | b48 $\cdots$ b 32 | b31 $\cdots$ b16 | b15 $\cdots$ b0 | $\square \mathrm{D} 100$ | b15 $\cdots$ b0 |
| 40B5H | 3819 H | 9999H | 999AH |  | 1538H |
| （5432．1） |  |  |  |  | （5432） |

## Operation error

| Error code (SDO) | Description |
| :---: | :---: |
| 3401H | The double-precision real number in the device specified by (s) is out of the range, 0 to 65535. |
| 3402H | An unusual number is set to (s). <br> - The double-precision real number set to $(s)$ is not within the following range: <br> $0,2^{-1022} \leq\|(\mathrm{s})\|<2^{1024}$ <br> - The value set to a device or label is -0 , a subnormal number, NaN (not a number), or $\pm \infty$. |

## Converting double－precision real number to 32－bit signed binary data

## DBL2DINT（P）

These instructions convert the specified double－precision real number to 32－bit signed binary data．


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DBL2DINT | - |
|  | $\boxed{ }$ |
| DBL2DINTP |  |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Double－precision real number or the start device where the <br> double－precision real number is stored | -2147483648 to 2147483647 | Double－precision real <br> number | ANYREAL＿64 |
| （d） | Start device for storing the converted binary data | - | 32－bit signed binary | ANY32＿S |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロID， U3E미（H）Gㅁ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （d） | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions convert the double－precision real number in the device specified by（s）to 32－bit signed binary data，and store the converted data in the device specified by（d）．
－After conversion，the first digit after the decimal point of the double－precision real number is rounded off．
－When an input value is set using the engineering tool，a rounding error may occur．For the precautions on setting an input value using the engineering tool，refer to the following．
$\longmapsto$ Page 41 Precautions
The following program example converts，when M0 turns on，the double－precision real number stored in D0 to D3 to 32－bit signed binary data，and stores the converted data in D100 and D101．

| SM402 | EDMOVP | E－765432．1 | D0 |
| :---: | :---: | :---: | :---: |
| $1+2$ | （s） | （d） |  |



|  | After conversion |  |
| :---: | :---: | :---: |
| （d） | b31 $\cdots$ b16 | b15 $\cdots$ b0 |
|  | FFF4H | 5208 H |
|  | $(-765432)$ |  |

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3401 H | The double-precision real number in the device specified by (s) is out of the range, -2147483648 to 2147483647. |
| 3402 H | An unusual number is set to (s). <br>  <br>  <br>  <br> - The double-precision real number set to (s) is not within the following range: <br> • The value set to a device or label is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |

## Converting double－precision real number to 32－bit unsigned binary data

## DBL2UDINT（P）

These instructions convert the specified double－precision real number to 32－bit unsigned binary data．


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DBL2UDINT | - |
|  | $\boxed{ }$ |
| DBL2UDINTP |  |

Setting data
Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Double－precision real number or the start device where the <br> double－precision real number is stored | 0 to 4294967295 | Double－precision real <br> number | ANYREAL＿64 |
| （d） | Start device for storing the converted binary data | - | 32－bit unsigned binary | ANY32＿U |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3Eपl（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （d） | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions convert the double－precision real number in the device specified by（s）to 32－bit unsigned binary data， and store the converted data in the device specified by（d）．
－After conversion，the first digit after the decimal point of the double－precision real number is rounded off．
－When an input value is set using the engineering tool，a rounding error may occur．For the precautions on setting an input value using the engineering tool，refer to the following．
$\longmapsto$ Page 41 Precautions
The following program example converts，when M0 turns on，the double－precision real number stored in D0 to D3 to 32－bit unsigned binary data，and stores the converted data in D100 and D101．



## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3401 H | The double-precision real number in the device specified by (s) is out of the range, 0 to 4294967295. |
| 3402 H | An unusual number is set to (s). <br>  <br>  <br>  <br> - The double-precision real number set to (s) is not within the following range: <br> • The value set to a device or label is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |

# Converting 16－bit signed binary data to 16－bit unsigned binary data 

## INT2UINT（P）

These instructions convert the specified 16 －bit signed binary data to 16 －bit unsigned binary data．


FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| INT2UINT | - |
|  | $\boxed{-}$ |
| INT2UINTP |  |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Binary data or the label where the binary data is stored | -32768 to 32767 | 16－bit signed binary | ANY16＿S |
| （d） | Label for storing the converted binary data | - | 16－bit unsigned binary | ANY16＿U |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ\ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDI（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

Point／
The INT2UINT（P）instruction is used in programming using labels．The purpose of using this instruction is to match the data type of the specified label with the data type that can be specified in the instruction operand． In programming using devices，use of the INT2UINT（P）instruction is not required．

## Processing details

- These instructions convert the 16-bit signed binary data (ANY16_S) in the label specified by (s) to 16-bit unsigned binary data (ANY16_U), and store the converted data in the label specified by (d).
- The following figure shows a program example using the INT2UINT(P) instruction.

Ex.
The +_U instruction requires ANY16_U to be specified in the operand, and therefore, before the +_U instruction is executed, the INT2UINT instruction is used to convert wLabel0 of ANY16_S to uLabel1 of ANY16_U.
The value in wLabel0 is stored in uLabel1 as is.


The data type of the value is converted to the one of the operand in the +_U instruction, and the operation starts.
bSwitchA: Bit
wLabel0: Word [signed]
uLabel0, uLabel1: Word [unsigned]/bit string [16 bits]

## Operation error

There is no operation error.

## Converting 16－bit signed binary data to 32－bit signed binary data

## INT2DINT（P）

These instructions convert the specified 16 －bit signed binary data to 32 －bit signed binary data．

| Ladder | ST |
| :---: | :---: |
|  | Not supported |
|  |  |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| INT2DINT | - |
|  | $\boxed{ }$ |
| INT2DINTP | - |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Binary data or the device where the binary data is stored | -32768 to 32767 | 16－bit signed binary | ANY16＿S |
| （d） | Start device for storing the converted binary data | - | 32－bit signed binary | ANY32＿S |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JपIप， U3E미（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions convert the 16－bit signed binary data in the device specified by（s）to 32－bit signed binary data，and store the converted data in the device specified by（d）．
The following program example converts，when MO turns on，the 16－bit signed binary data stored in D0 to 32－bit signed binary data，and stores the converted data in D100 and D101．


## Operation error

There is no operation error

# Converting 16－bit signed binary data to 32－bit unsigned binary data 

## INT2UDINT（P）

These instructions convert the specified 16－bit signed binary data to 32－bit unsigned binary data．


FBD／LD


■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| INT2UDINT | - |
|  | - |
| INT2UDINTP |  |

Setting data
Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Binary data or the device where the binary data is stored | -32768 to 32767 | 16－bit signed binary | ANY16＿S |
| （d） | Start device for storing the converted binary data | - | 32－bit unsigned binary | ANY32＿U |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3E미（H）G口 | Z | LT，LST， LC | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions convert the 16－bit signed binary data in the device specified by（s）to 32－bit unsigned binary data，and store the converted data in the device specified by（d）．
The following program example converts，when M0 turns on，the 16－bit signed binary data stored in D0 to 32－bit unsigned binary data，and stores the converted data in D100 and D101．


## Operation error

There is no operation error．

# Converting 16－bit unsigned binary data to 16－bit signed binary data 

## UINT2INT（P）

These instructions convert the specified 16 －bit unsigned binary data to 16 －bit signed binary data．


■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| UINT2INT | - |
|  | - |
| UINT2INTP |  |

Setting data
Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Binary data or the label where the binary data is stored | 0 to 65535 | 16－bit unsigned binary | ANY16＿U |
| （d） | Label for storing the converted binary data | - | 16－bit signed binary | ANY16＿S |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3E미（H）G口 | z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Point $\rho$

The UINT2INT（P）instruction is used in programming using labels．The purpose of using this instruction is to match the data type of the specified label with the data type that can be specified in the instruction operand． In programming using devices，use of the UINT2INT（P）instruction is not required．

## Processing details

- These instructions convert the 16-bit signed binary data (ANY16_U) in the label specified by (s) to 16-bit unsigned binary data (ANY16_S), and store the converted data in the label specified by (d).
- The following figure shows a program example using the UINT2INT(P) instruction.

Ex.
The + instruction requires ANY16_S to be specified in the operand, and therefore, before the + instruction is executed, the UINT2INT instruction is used to convert uLabel0 of ANY16_U to wLabel1 of ANY16_S.
The value in uLabel0 is stored in wLabel1 as is.

bSwitchA: Bit
wLabel0, wLabel1: Word [signed]
uLabel0: Word [unsigned]/bit string [16 bits]

## Operation error

There is no operation error.

# Converting 16－bit unsigned binary data to 32－bit signed binary data 

## UINT2DINT（P）

These instructions convert the specified 16－bit unsigned binary data to 32－bit signed binary data．

| Ladder |  | ST |
| :---: | :---: | :---: |
|  |  | Not supported |
| —－-7 （s） | （d） |  |
| FBD／LD |  |  |
|  |  |  |
|  |  |  |
|  |  |  |

■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| UINT2DINT | - |
|  | - |
| UINT2DINTP |  |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Binary data or the device where the binary data is stored | 0 to 65535 | 16－bit unsigned binary | ANY16＿U |
| （d） | Start device for storing the converted binary data | - | 32－bit signed binary | ANY32＿S |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3E미（H）G口 | Z | LT，LST， LC | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions convert the 16－bit unsigned binary data in the device specified by（s）to 32－bit signed binary data，and store the converted data in the device specified by（d）．
The following program example converts，when M0 turns on，the 16－bit unsigned binary data stored in D0 to 32－bit signed binary data，and stores the converted data in D100 and D101．


## Operation error

There is no operation error．

# Converting 16－bit unsigned binary data to 32－bit unsigned binary data 

## UINT2UDINT（P）

These instructions convert the specified 16－bit unsigned binary data to 32－bit unsigned binary data．

| Ladder | ST |
| :---: | :---: |
|  | Not supported |
| ■－—— （s） （d） |  |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| UINT2UDINT | - |
|  | - |
| UINT2UDINTP |  |

Setting data
Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Binary data or the device where the binary data is stored | 0 to 65535 | 16－bit unsigned binary | ANY16＿U |
| （d） | Start device for storing the converted binary data | - | 32－bit unsigned binary | ANY32＿U |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions convert the 16－bit unsigned binary data in the device specified by（s）to 32－bit unsigned binary data，and store the converted data in the device specified by（d）．
The following program example converts，when M0 turns on，the 16－bit unsigned binary data stored in D0 to 32－bit unsigned binary data，and stores the converted data in D100 and D101．


## Operation error

There is no operation error

## Converting 32－bit signed binary data to 16－bit signed binary data

## DINT2INT（P）

These instructions convert the specified 32－bit signed binary data to 16 －bit signed binary data．


FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DINT2INT | - |
|  | - |
| DINT2INTP | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Binary data or the start device where the binary data is <br> stored | -32768 to 32767 | 32－bit signed binary | ANY32＿S |
| （d） | Device for storing the converted binary data | - | 16－bit signed binary | ANY16＿S |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDI（H）Gロ | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions convert the 32－bit signed binary data in the device specified by（s）to 16－bit signed binary data，and store the converted data in the device specified by（d）．
The following program example converts，when M0 turns on，the 32－bit signed binary data stored in D0 and D1 to 16－bit signed binary data，and stores the converted data in D100．



## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3401 H | The 32-bit signed binary data in the device specified by (s) is out of the range, -32768 to 32767. |

## Converting 32－bit signed binary data to 16 －bit unsigned binary

 data
## DINT2UINT（P）

These instructions convert the specified 32－bit signed binary data to 16 －bit unsigned binary data．

－Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DINT2UINT | - |
|  | - |
| DINT2UINTP |  |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Binary data or the start device where the binary data is <br> stored | -2147483648 to 2147483647 | 32－bit signed binary | ANY32＿S |
| （d） | Device for storing the converted binary data | - | 16－bit unsigned binary | ANY16＿U |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions convert the 32－bit signed binary data in the device specified by（s）to 16 －bit unsigned binary data，and store the converted data in the device specified by（d）．

The following program example converts，when M0 turns on，the 32－bit signed binary data stored in D0 and D1 to 16－bit unsigned binary data，and stores the converted data in D100．

## Operation error

There is no operation error.

Converting 32－bit signed binary data to 32－bit unsigned binary data

## DINT2UDINT（P）

These instructions convert the specified 32－bit signed binary data to 32－bit unsigned binary data．

－Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DINT2UDINT | - |
|  | - |
| DINT2UDINTP |  |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Binary data or the label where the binary data is stored | -2147483648 to 2147483647 | 32－bit signed binary | ANY32＿S |
| （d） | Label for storing the converted binary data | - | 32－bit unsigned binary | ANY32＿U |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3Eपl（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Point $\rho$

The DINT2UDINT（P）instruction is used in programming using labels．The purpose of using this instruction is to match the data type of the specified label with the data type that can be specified in the instruction operand． In programming using devices，use of the DINT2UDINT（P）instruction is not required．

## Processing details

- These instructions convert the 32-bit signed binary data (ANY32_S) in the label specified by (s) to 32-bit unsigned binary data (ANY32_U), and store the converted data in the label specified by (d).
- The following figure shows a program example using the DINT2UDINT(P) instruction.

Ex.
The $\mathrm{D}+\_\mathrm{U}$ instruction requires $\mathrm{ANY} 32 \_\mathrm{U}$ to be specified in the operand, and therefore, before the $\mathrm{D}+\mathrm{U}$ instruction is executed, the DINT2UDINT instruction is used to convert dLabel0 of ANY32_S to udLabel1 of ANY32_U.
The value in dLabel0 is stored in udLabel1 as is.


The data type of the value is converted to the one of the operand in the $\mathrm{D}+$ _U instruction, and the operation starts.
bSwitchA: Bit
dLabel0: Double word [signed]
udLabel0, udLabel1: Double word [unsigned]/bit string [32 bits]

## Operation error

There is no operation error.

Converting 32－bit unsigned binary data to 16－bit signed binary data

## UDINT2INT（P）

These instructions convert the specified 32－bit unsigned binary data to 16－bit signed binary data．

| Ladder | ST |
| :---: | :---: |
|  | Not supported |
| - －$\square$ （s） （d） |  |

## FBD／LD



Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| UDINT2INT | - |
|  | - |
| UDINT2INTP |  |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Binary data or the start device where the binary data is <br> stored | 0 to 4294967295 | 32－bit unsigned binary | ANY32＿U |
| （d） | Device for storing the converted binary data | - | 16－bit signed binary | ANY16＿S |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J밈， U3EDI（H）GD | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions convert the 32－bit unsigned binary data in the device specified by（s）to 16 －bit signed binary data，and store the converted data in the device specified by（d）．

The following program example converts，when M0 turns on，the 32－bit unsigned binary data stored in D0 and D1 to 16－bit signed binary data，and stores the converted data in D100．


## Operation error

There is no operation error.

Converting 32－bit unsigned binary data to 16－bit unsigned binary data

## UDINT2UINT（P）

These instructions convert the specified 32－bit unsigned binary data to 16－bit unsigned binary data．


FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| UDINT2UINT | - |
|  | $\boxed{ }$ |
| UDINT2UINTP |  |

## Setting data

DDescription，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Binary data or the start device where the binary data is <br> stored | 0 to 4294967295 | 32－bit unsigned binary | ANY32＿U |
| （d） | Device for storing the converted binary data | - | 16－bit unsigned binary | ANY16＿U |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3EDI（H）G口 | Z | LT，LST， LC | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions convert the 32－bit unsigned binary data in the device specified by（s）to 16－bit unsigned binary data，and store the converted data in the device specified by（d）．
The following program example converts，when M0 turns on，the 32－bit unsigned binary data stored in D0 and D1 to 16－bit unsigned binary data，and stores the converted data in D100．


## Operation error

There is no operation error.

Converting 32－bit unsigned binary data to 32－bit signed binary data

## UDINT2DINT（P）

These instructions convert the specified 32－bit unsigned binary data to 32－bit signed binary data．

| Ladder | ST |
| :---: | :---: |
|  | Not supported |
| $-\square-\square$ （s） （d） |  |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| UDINT2DINT | - |
|  | $\boxed{ }$ |
| UDINT2DINTP |  |

## Setting data

DDescription，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Binary data or the label where the binary data is stored | 0 to 4294967295 | 32－bit unsigned binary | ANY32＿U |
| （d） | Label for storing the converted binary data | - | 32－bit signed binary | ANY32＿S |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）Gロ | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

The UDINT2DINT（ P ）instruction is used in programming using labels．The purpose of using this instruction is to match the data type of the specified label with the data type that can be specified in the instruction operand． In programming using devices，use of the UDINT2DINT（P）instruction is not required．

## Processing details

- These instructions convert the 32-bit signed binary data (ANY32_U) in the label specified by (s) to 32-bit unsigned binary data (ANY32_S), and store the converted data in the label specified by (d).
- The following figure shows a program example using the UDINT2DINT(P) instruction.


## Ex.

The $D+$ instruction requires ANY32_S to be specified in the operand, and therefore, before the $D+$ instruction is executed, the UDINT2DINT instruction is used to convert udLabel0 of ANY32_U to dLabel1 of ANY32_S.
The value in udLabel0 is stored in dLabel1 as is.

bSwitchA: Bit
dLabel0, dLabel1: Double word [signed]
udLabel0: Double word [unsigned]/bit string [32 bits]

## Operation error

There is no operation error.

## Converting 16－bit binary data to Gray code data

## GRY（P）（＿U）

These instructions convert the specified 16－bit binary data to 16 －bit binary Gray code data．

| Ladder | ST |  |
| :---: | :---: | :---: |
|  | $\begin{aligned} & \text { ENO:=GRY(EN,s,d); } \\ & \text { ENO:=GRYP(EN,s,d); } \end{aligned}$ | $\begin{aligned} & \text { ENO:=GRY_U(EN,s,d); } \\ & \text { ENO:=GRYP_U(EN,s,d); } \end{aligned}$ |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| GRY | - |
| GRY＿U | - |
| GRYP | - |
| GRYP＿U |  |

## Setting data

Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- | :--- |
| （s） | GRY（P） | Binary data or the device where the binary data |  |  |  |
|  | GRY（P）＿U |  | 0 to 32767 | 16－bit signed binary | ANY16＿S |
| （d） | GRY（P） | Device for storing the converted Gray code data | - | 16－bit unsigned binary | ANY16＿U |
|  | GRY（P）＿U |  |  | 16－bit signed binary | ANY16＿S |
| EN |  | Execution condition | - | 16－bit unsigned binary | ANY16＿U |
| ENO | Execution result | - | Bit | BOOL |  |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDl（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions convert the 16－bit binary data in the device specified by（s）to 16－bit binary Gray code data，and store the converted data in the device specified by（d）．
（s）BIN

（d）Gray code

|  | 15 |  |  |  |  |  |  |  |  |  |  |  |  |  |  | b0 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 1234 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 1 | 1 |

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3401 H | When the GRY(P) instruction is used, the value in the device specified by (s) is out of the range, 0 to 32767. |

## Converting 32－bit binary data to Gray code data

## DGRY（P）（＿U）

These instructions convert the specified 32－bit binary data to 32－bit binary Gray code data．

| Ladder | ST |  |
| :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{ENO}:=\mathrm{DGRY}(\mathrm{EN}, \mathrm{~s}, \mathrm{~d}) ; \\ & \text { ENO:=DGRYP(EN,s,d); } \end{aligned}$ | $\begin{aligned} & \text { ENO:=DGRY_U(EN,s,d); } \\ & \text { ENO:=DGRYP_U(EN,s,d); } \end{aligned}$ |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DGRY | - |
| DGRY＿U | - |
| DGRYP | - |
| DGRYP＿U |  |

## Setting data

Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- | :--- |
| （s） | DGRY $(P)$ | Binary data or the start device where the binary |  |  |  |
|  | data is stored |  |  |  |  |$)$

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDl（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions convert the 32－bit binary data in the device specified by（s）to 32－bit binary Gray code data，and store the converted data in the device specified by（d）．
（s）+1 （Upper 16 bits）
（s）（Lower 16 bits）
（s）BIN

（d）+1
（d）
（d）Gray code


## Operation error

Error code (SDO) $\quad$ Description

3401H
When the $\operatorname{DGRY}(P)$ instruction is used, the value in the device specified by $(s)$ is out of the range, 0 to 2147483647 .

## Converting 16－bit binary Gray code data to 16－bit binary data

## GBIN（P）（＿U）

These instructions convert the specified 16－bit binary Gray code data to 16 －bit binary data．

| Ladder | ST |  |
| :---: | :---: | :---: |
|  | $\begin{aligned} & \text { ENO:=GBIN(EN,s,d); } \\ & \text { ENO:=GBINP(EN,s,d); } \end{aligned}$ | $\begin{aligned} & \text { ENO:=GBIN_U(EN,s,d); } \\ & \text { ENO:=GBINP_U(EN,s,d); } \end{aligned}$ |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| GBIN | - |
| GBIN＿U | - |
| GBINP | - |
| GBINP＿U |  |

## Setting data

■Description，range，data type

\left.| Operand |  | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- | :--- |
| （s） | GBIN（P） | Gray code data or the device where the Gray |  |  |  |
|  | code data is stored |  |  |  |  |$\right)$

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3E미（H）Gロ | z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions convert the 16－bit binary Gray code data in the device specified by（s）to 16 －bit binary data，and store the converted data in the device specified by（d）．
（s）Gray code

（d）BIN


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3401 H | When the GBIN(P) instruction is used, the value in the device specified by (s) is out of the range, 0 to 32767. |

Converting 32－bit binary Gray code data to 32－bit binary data

## DGBIN（P）（＿U）

These instructions convert the specified 32－bit binary Gray code data to 32－bit binary data．

| Ladder | ST |  |
| :---: | :---: | :---: |
| $-\square-\square$ （s） （d） | $\begin{aligned} & \mathrm{ENO}:=\mathrm{DGBIN}(\mathrm{EN}, \mathrm{~s}, \mathrm{~d}) ; \\ & \mathrm{ENO}:=\mathrm{DGBINP}(\mathrm{EN}, \mathrm{~s}, \mathrm{~d}) ; \end{aligned}$ | $\begin{aligned} & \text { ENO:=DGBIN_U(EN,s,d); } \\ & \text { ENO:=DGBINP_U(EN,s,d); } \end{aligned}$ |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DGBIN | - |
| DGBIN＿U | - |
| DGBINP | - |
| DGBINP＿U |  |

## Setting data

■Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- | :--- |
| （s） | DGBIN（P） | Gray code data or the start device where the | 0 to 2147483647 | 32－bit signed binary | ANY32＿S |
|  | DGBIN（P）＿U | Gray code data is stored | 0 to 4294967295 | 32－bit unsigned binary | ANY32＿U |
| （d） | DGBIN（P） | Start device for storing the converted binary data | - | 32－bit signed binary | ANY32＿S |
|  | DGBIN（P）＿U |  |  | 32－bit unsigned binary | ANY32＿U |
| EN | Execution condition | - | Bit | BOOL |  |
| ENO | Execution result | - | Bit | BOOL |  |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3E미（H）Gロ | z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions convert the 32－bit binary Gray code data in the device specified by（s）to 32－bit binary data，and store the converted data in the device specified by（d）．
（s）＋1（Upper 16 bits）
（s）（Lower 16 bits）

（d）+1
（d）
（d）BIN


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3401 H | When the DGBIN $(\mathrm{P})$ instruction is used, the value in the device specified by (s) is out of the range, 0 to 2147483647. |

## Converting 16－bit binary data block to BCD 4－digit data block

## BKBCD（P）

These instructions convert the n points of binary data（ 0 to 9999 ）starting from the specified device to BCD data．

| Ladder | ST |
| :---: | :---: |
| ■－二－ | $\begin{aligned} & \mathrm{ENO}:=\mathrm{BKBCD}(\mathrm{EN}, \mathrm{~s}, \mathrm{n}, \mathrm{~d}) ; \\ & \mathrm{ENO}:=\mathrm{BKBCDP}(\mathrm{EN}, \mathrm{~s}, \mathrm{n}, \mathrm{~d}) ; \end{aligned}$ |

FBD／LD


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| BKBCD | - |
|  | $\boxed{ }$ |
| BKBCDP | - |

Setting data
Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Start device where the binary data is stored | - | 16－bit signed binary | ANY16 |
| （d） | Start device for storing the converted BCD data | - | BCD 4－digit | ANY16 |
| （n） | Number of variables | 0 to 65535 | 16－bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3Eपl（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- These instructions convert the ( n ) points of 16 -bit binary data ( 0 to 9999 ) starting from the device specified by ( s ) to BCD data, and store the converted data in the device specified by (d) and later.



## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2821 H | The device ranges specified by (s) and (d) are overlapping. |
| 3401 H | The (n) points of data starting from the device specified by (s) is out of the range, 0 to 9999. |

## Converting BCD 4－digit block data to 16－bit binary block data

## BKBIN（P）

These instructions convert the n points of BCD data（0 to 9999）starting from the specified device to binary data．


FBD／LD

－Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| BKBIN | - |
|  | $\boxed{ }$ |
| BKBINP | - |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Start device where the BCD data is stored | - | BCD 4－digit | ANY16 |
| （d） | Start device for storing the converted binary data | - | 16－bit signed binary | ANY16 |
| （n） | Number of variables | 0 to 65535 | 16－bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J밈， U3EDI（H）GD | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- These instructions convert the ( n ) points of BCD data (0 to 9999 ) starting from the device specified by ( s ) to 16-bit binary data, and store the converted data in the device specified by (d) and later.

|  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| (s) | BCD 1234 | 0101011 | 010110 | 010111 | 01100 |
| (s) +1 | BCD 5678 | 01101 | 011110 | 0:1111 | $1{ }^{1}$ |
| (s)+2 | BCD 1545 | 01011 | 011011 | 01100 | 01101 |
| ! | ! |  |  | : |  |
| (s) $+(\mathrm{n})-2$ | BCD 4321 | 01100 | 001111 | 010110 | 01001 |
| (s) $+(\mathrm{n})-1$ | BCD 5555 | 01101 | 011011 | 0:110:1 | 01101 |


|  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| (d) | BIN 1234 | 0101010 | 0110 | 111011 | 01010 |
| (d) +1 | BIN 5678 | 010011 | 011110 | 010110 | 11110 |
| (d)+2 | BIN 1545 | 01010 | 011110 | 010 | , |
| : | ! |  |  |  |  |
| (d) $+(\mathrm{n})-2$ | BIN 4321 |  | 0101010 | 111110 | 0101 |
| (d) $+(\mathrm{n})-1$ | BIN 5555 | 01010:1 | 011011 | 10011 | 0:111 |

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2821 H | The device ranges specified by (s) and (d) are overlapping. |
| 3401 H | The $(\mathrm{n})$ points of data starting from the device specified by $(\mathrm{s})$ is out of the range, 0 to 9999. |

## Converting decimal ASCII data to 16－bit binary data

## DABIN（P）（＿U）

These instructions convert decimal ASCII data to 16－bit binary data．

| Ladder | ST |  |
| :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{ENO}:=\mathrm{DABIN}(\mathrm{EN}, \mathrm{~s}, \mathrm{~d}) ; \\ & \mathrm{ENO}:=\mathrm{DABINP}(\mathrm{EN}, \mathrm{~s}, \mathrm{~d}) ; \end{aligned}$ | $\begin{aligned} & \text { ENO:=DABIN_U(EN,s,d); } \\ & \text { ENO:=DABINP_U(EN,s,d); } \end{aligned}$ |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DABIN | - |
| DABIN＿U | - |
| DABINP | - |
| DABINP＿U |  |

## Setting data

Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- | :--- |
| （s） |  | ASCII data or the start device where the ASCII <br> data is stored | - | String | ANYSTRING＿SINGL <br> E |
| （d） | DABIN（P） | Device for storing the converted binary data | - | 16－bit signed binary | ANY16＿S |
|  | DABIN（P）＿U |  | - | 16－bit unsigned binary | ANY16＿U |
| EN | Execution condition | Bit | BOOL |  |  |
| ENO | Execution result | - | Bit | BOOL |  |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions convert the decimal ASCII data in the device specified by (s) and later to 16-bit binary data, and store the converted data in the device specified by (d).
(s)
 $\longrightarrow$
(d)

16-bit binary data


## Ex.

When (s)=-25108 (signed)

|  | ... | b8b7 | ... | b0 |
| :---: | :---: | :---: | :---: | :---: |
| (s) | 32H (2) | , | 2DH (-) |  |
| (s) +1 | 31 H (1) |  | 35H (5) |  |
| (s)+2 | 38H (8) |  | 30H (0) |  |

$\qquad$
(d)
b15


- The ASCII data in the device specified by (s) to (s) +2 is -32768 to 32767 when signed data is specified, or 0 to 65535 when unsigned data is specified.
- As sign data, "20H" is stored if the ASCII data is positive, and "2DH" is stored if the data is negative. (If a value other than " 20 H " and " 2 DH " is set, the data will be processed as positive data.)
- A value from " 30 H " to " 39 H " can be set in each place of ASCII code.
- If a value " 20 H " or " 00 H " is set, the value will be processed as " 30 H ".


## Operation error

| Error code (SD0) | Description |
| :--- | :--- |
| 3401 H | An out-of-range value is input to $(\mathrm{s})$ to $(\mathrm{s})+2$. <br>  <br>  <br>  <br>  <br>  <br>  <br> - ASCII code: other than 30 H to $39 \mathrm{H}, 2 \mathrm{DH}$, and 00 H <br> - When the DABIN(P) instruction is used, ASCII data is out of the range from -32768 to 32767. |

## Converting decimal ASCII data to 32－bit binary data

## DDABIN（P）（＿U）

These instructions convert decimal ASCII data to 32－bit binary data．

| Ladder | ST |  |
| :---: | :---: | :---: |
| $-\square-\square$ （s） （d） | $\begin{aligned} & \text { ENO:=DDABIN(EN,s,d); } \\ & \text { ENO:=DDABINP(EN,s,d); } \end{aligned}$ | $\begin{aligned} & \text { ENO:=DDABIN_U(EN,s,d); } \\ & \text { ENO:=DDABINP_U(EN,s,d); } \end{aligned}$ |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DDABIN | - |
| DDABIN＿U | - |
| DDABINP | - |
| DDABINP＿U |  |

## Setting data

■Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :---: | :---: | :---: | :---: | :---: | :---: |
| （s） |  | ASCII data to be converted to binary data or the start device where the ASCII data is stored | － | String | ANYSTRING＿SINGLE |
| （d） | DDABIN（P） | Start device for storing the conversion result | － | 32－bit signed binary | ANY32＿S |
|  | DDABIN（P）＿U |  |  | 32－bit unsigned binary | ANY32＿U |
| EN |  | Execution condition | － | Bit | BOOL |
| ENO |  | Execution result | － | Bit | BOOL |

## －Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions convert the decimal ASCII data in the device areas specified by (s) and later to 32-bit binary data, and store the converted data in the device specified by (d).


Ex.
When the ASCII data, -1234543210 (signed), is specified by (s)


- The ASCII data in the device specified by (s) to (s)+5 is -2147483648 to 2147483647 when signed data is specified, or 0 to 4294967295 when unsigned data is specified. Any data stored in the upper bytes in the device specified by (s)+5 is ignored.
- As sign data, " 20 H " is stored if the ASCII data is positive, and " 2 DH " is stored if the data is negative. (If a value other than " 20 H " and " 2 DH " is set, the data will be processed as positive data.)
- A value from " 30 H " to " 39 H " can be set in each place of ASCII code.
- If a value " 20 H " or " 00 H " is set, the value will be processed as " 30 H ".


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3401 H | Invalid data that cannot be converted is specified in (s) to (s) +5. |
|  | - A value in each place of ASCII code is other than " 30 H " to " $39 \mathrm{H} "$, "20H", and "00H". |
|  | - When the DDABIN(P) instruction is used, ASCII data is out of the range from -2147483648 to 2147483647. |
|  | - When the DDABIN(P)_U instruction is used, ASCII data is out of the range from 0 to 4294967295. |

## Converting hexadecimal ASCII data to 16－bit binary data

## HABIN（P）

These instructions convert hexadecimal ASCII data to 16－bit binary data．

| Ladder | ST |
| :---: | :---: |
| $\begin{array}{\|l\|l\|l\|} \hline-\square-\square & \text { (s) } & \text { (d) } \\ \hline \end{array}$ | $\begin{aligned} & \mathrm{ENO}:=\mathrm{HABIN}(\mathrm{EN}, \mathrm{~s}, \mathrm{~d}) ; \\ & \mathrm{ENO}:=\mathrm{HABINP}(\mathrm{EN}, \mathrm{~s}, \mathrm{~d}) ; \end{aligned}$ |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| HABIN | - |
|  | $\boxed{ }$ |
| HABINP | - |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | ASCII data to be converted to binary data or the start <br> device where the ASCII data is stored | - | String | ANYSTRING＿SINGLE |
| （d） | Device for storing the conversion result | - | 16－bit signed binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDl（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions convert the hexadecimal ASCII data stored in the device areas specified by（s）and later to 16－bit binary data，and store the converted data in the device specified by（d）．

（s）$\quad$|  | ASCII code（3rd digit） | ASCII code（4th digit） |
| :--- | :--- | :--- | $\square$

（d）

16－bit binary data

## Ex．

When 5A8DH is specified in（s）
（s）

| b15 $\quad \cdots$ | b8b7 | $\cdots$ | b0 |
| :---: | :---: | :---: | :---: |
|  | $41 \mathrm{H}(A)$ |  | $35 \mathrm{H}(5)$ |
|  | $44 \mathrm{H}(\mathrm{D})$ |  | $38 \mathrm{H}(8)$ |

（d）
d） $\qquad$
－The ASCII data in the device specified by（s）to（s）+1 is within the range from 0000 H to FFFFH ．
－A value from＂ 30 H ＂to＂ 39 H ＂and＂ 41 H to 46 H ＂can be set in each place of ASCII code．

## Operation error

Error code (SDO) Description
$3401 \mathrm{H} \quad$ Invalid data that cannot be converted is specified in (s) to (s)+1.

- A value in each place of ASCII code is other than " 30 H " to " 39 H " and " 41 H " to " 46 H ".


## Converting hexadecimal ASCII data to 32－bit binary data

## DHABIN（P）

These instructions convert hexadecimal ASCII data to 32－bit binary data．

| Ladder | ST |
| :---: | :---: |
| $\begin{array}{\|l\|l\|l\|} \hline-\square-\square & \text { (s) } & \text { (d) } \\ \hline \end{array}$ | $\begin{aligned} & \text { ENO:=DHABIN(EN,s,d); } \\ & \text { ENO:=DHABINP(EN,s,d); } \end{aligned}$ |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DHABIN | - |
|  | $\boxed{ }$ |
| DHABINP | - |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | ASCII data to be converted to binary data or the start <br> device where the ASCII data is stored | - | String | ANYSTRING＿SINGLE |
| （d） | Start device for storing the conversion result | - | 32－bit signed binary | ANY32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3E미（H）Gㅁ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions convert the hexadecimal ASCII data stored in the device specified by (s) and later to 32-bit binary data, and store the converted data in the device specified by (d).


Ex.
When 5CB807E1H is specified in (s)

|  | ... | b8b7 | ... | b0 |
| :---: | :---: | :---: | :---: | :---: |
| (s) | 43H (C) |  | 35H (5) |  |
| (s)+1 | 38H (8) | , | 42H (B) |  |
| (s)+2 | 37H (7) |  | 30 H (0) |  |
| (s)+3 | 31H (1) | ' | 45H (E) |  |

- The ASCII data in the device specified by (s) to (s)+3 is within the range from 00000000 H to FFFFFFFFFH.
- A value from " 30 H " to " 39 H " and " 41 H to 46 H " can be set in each place of ASCII code.


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3401 H | Invalid data that cannot be converted is specified in $(\mathrm{s})$ to $(\mathrm{s})+3$. <br> $\quad$ A value in each place of ASCII code is other than " 30 H " to " 39 H " and " 41 H " to " 46 H ". |

## Converting decimal ASCII data to BCD 4－digit data

## DABCD（P）

These instructions convert decimal ASCII data to BCD 4－digit data．

| Ladder | ST |
| :---: | :---: |
| $\square-\square-\square$ （s） （d） | $\begin{aligned} & \mathrm{ENO}:=\mathrm{DABCD}(\mathrm{EN}, \mathrm{~s}, \mathrm{~d}) ; \\ & \mathrm{ENO}:=\mathrm{DABCDP}(\mathrm{EN}, \mathrm{~s}, \mathrm{~d}) \end{aligned}$ |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DABCD | - |
|  | $\boxed{ }$ |
| DABCDP | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | ASCII data to be converted to BCD data or the start device <br> where the ASCII data is stored | - | String | ANYSTRING＿SINGLE |
| （d） | Device for storing the conversion result | - | BCD 4－digit | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3E미（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions convert the decimal ASCII data stored in the device areas specified by (s) and later to BCD 4-digit data, and store the converted data in the device specified by (d).



## Ex.

When 8765 is specified in (s)
(s)

$\qquad$
(d)


- The ASCII data in the device specified by (s) to (s)+1 is within the range from 0 to 9999 .
- A value from " 30 H " to " 39 H " can be set in each place of ASCII code.
- If a value " 20 H " or " 00 H " is set, the value will be processed as " 30 H ".


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3401 H | An out-of-range value was input to (s). <br>  <br> • A character other than 0 to 9 exists in the data. |

## Converting decimal ASCII data to BCD 8－digit data

## DDABCD（P）

These instructions convert decimal ASCII data to BCD 8－digit data．

| Ladder | ST |
| :---: | :---: |
|  | $\begin{aligned} & \text { ENO:=DDABCD(EN,s,d); } \\ & \text { ENO:=DDABCDP(EN,s,d); } \end{aligned}$ |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DDABCD | - |
|  | $\boxed{ }$ |
| DDABCDP | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | ASCII data to be converted to BCD data or the start device <br> where the ASCII data is stored | - | String | ANYSTRING＿SINGLE |
| （d） | Start device for storing the conversion result | - | BCD 8－digit | ANY32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J밈， U3EDI（H）Gロ | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions convert the decimal ASCII data stored in the device areas specified by (s) and later to BCD 8-digit data, and store the converted data in the device number specified by (d).
(s)
b15

| ASCII code (millions place) | b8 b7 |
| :--- | :--- |
| ASCII code <br> (ten-thousands place) | (ten-millions place) |
| ASCII code (hundreds place) | ASCII code |
| ASCII code (ones place) | ASCI code (thousands place) |
| ASode (tens place) |  |

(d) +1


Ex.
When 87654321 is specified in (s)
(s)

| b15 | b8 b7 | $\cdots$ |
| :---: | :---: | :---: |
| $37 \mathrm{H}(7)$ | b0 |  |
| $35 \mathrm{H}(5)$ | $38 \mathrm{H}(8)$ |  |
| $33 \mathrm{H}(3)$ | $36 \mathrm{H}(6)$ |  |
| $31 \mathrm{H}(1)$ | $34 \mathrm{H}(4)$ |  |


(d) +1

(d)

- The ASCII data in the device specified by (s) to ( s ) +3 is within the range from 0 to 99999999 .
- A value from "30H" to " 39 H " can be set in each place of ASCII code.
- If a value " 20 H " or " 00 H " is set, the value will be processed as " 30 H ".


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3401 H | An out-of-range value was input to (s). <br> • A character other than 0 to 9 exists in the data. |

## Converting decimal string data to 16－bit binary data

## VAL（P）（＿U）

These instructions convert character strings to 16－bit binary data．

| Ladder | ST |  |
| :---: | :---: | :---: |
| ■－二－$-\square$ （s） （d1） （d2） | $\begin{aligned} & \text { ENO:=VAL(EN,s,d1,d2); } \\ & \text { ENO:=VALP(EN,s,d1,d2); } \end{aligned}$ | $\begin{aligned} & \text { ENO:=VAL_U(EN,s,d1,d2); } \\ & \text { ENO:=VALP_U(EN,s,d1,d2); } \end{aligned}$ |

## FBD／LD


－Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| VAL | - |
| VAL＿U | - |
| VALP | - |
| VALP＿U |  |

## Setting data

Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- | :--- |
| （s） | String data to be converted to binary data or the <br> start device where the string data is stored | - | String | ANYSTRING＿SINGLE |  |
| （d1） | VAL（P） | Start device for storing the number of binary <br> digits after conversion | - | 16 －bit signed binary | ANY16＿S＿ARRAY <br> （Number of elements：2） |
|  | VAL（P）＿U |  | - | 16 －bit unsigned binary | ANY16＿U＿ARRAY <br> （Number of elements：2） |
| （d2） | VAL（P） | Device for storing the binary data after <br> conversion | VAL（P）＿U |  | 16－bit signed binary |
| EN | Execution condition | - | ANY16＿S |  |  |
| ENO | Execution result | - | Bit | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDl（H）G口 | Z | LT，LST， LC | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － |
| （d1） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions convert the character strings stored in the device numbers specified by (s) and later to 16-bit binary data, and store the number of digits in (d1) and converted binary data in the device specified by (d2). For conversion of character strings to binary data, the data from the device number specified by (s) to the device number containing " 00 H " is processed as character strings.



## Ex.

When character string "-123.45" (signed) is stored in the device specified in (s) or later
(s)


(d1)

| 7 |
| :--- |
| 2 |

- The total number of characters of the character string stored in the device specified by (s) is 2 to 8.
- Of the character string stored in the device specified by (s), the number of characters in the decimal part is 0 to 5 . Note, however, that the number must not exceed the total number of digits minus 3 .
- A numerical character string that can be converted to binary data ranges from -32768 to 32767 when a signed value is specified ignoring the decimal point or from 0 to 65535 when an unsigned value is specified. Numerical character strings excluding signs and decimal points can be specified only within the range from 30 H to 39 H . (A value ignoring the decimal point..."-12345.6" for example becomes "-123456".)
- For the sign, " 20 H " can be set to indicate a positive numerical value, or "2DH" can be set to indicate a negative numerical value.
- "2EH" is set for the decimal point.
- The total number of digits stored in the device specified by (d1) includes all characters (including signs and decimal points) that represent a numerical value. The number of digits in the decimal part to be stored in the device specified by (d1)+1 represents the decimal part after $2 \mathrm{EH}($.$) For the 16-bit binary data to be stored in the device specified by (d2), the character$ string is converted to binary data by ignoring the decimal point and stored.
- If " 20 H " (space) or " 30 H " (0) exists between the sign and the first numerical value other than 0 in the character string in the device specified by (s), the instruction performs conversion to binary data by ignoring "20H" and "30H".


## Ex.

When " 20 H " exists between the sign and the first numerical value other than 0 (when a signed value is specified)


## Ex.

When " 30 H " exists between the sign and the first numerical value other than 0


## Operation error

| Error code (SDO) | Description |
| :---: | :---: |
| 2820 H | 00 H is not set between the device number specified by (s) and the last device number of the relevant device. |
| 3401H | An out-of-range value was input to (s). <br> - The number of characters is not between 2 and 8 . <br> - The number of characters in the decimal part is not between 0 and 5 . <br> - The relationship between the total number of characters and the number of characters in the decimal part is not in the following range. <br> Total number of characters $-3 \geq$ number of characters in the decimal part <br> - When the VAL $(P)$ instruction is used, an ASCII code other than " 20 H " and "2DH" is set as a sign. <br> - When the VAL(P)_U instruction is used, an ASCII code other than " 20 H " is set as a sign. <br> - An ASCII code other than " 30 H " to " 39 H " and " 2 EH " (decimal point) is set as a digit of individual numbers. <br> - More than one decimal point are set. <br> The converted binary value exceeds the range in which each instruction can implement conversion. |
| 3405H | The number of characters of the character string in the device specified by (s) exceeds 16383. |

## Converting decimal string data to 32－bit binary data

## DVAL（P）（＿U）

These instructions convert character strings to 32－bit binary data．


FBD／LD

－Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DVAL | - |
| DVAL＿U | - |
| DVALP | - |
| DVALP＿U |  |

## Setting data

Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- | :--- |
| （s） | String data to be converted to binary data or the <br> start device where the string data is stored | - | String | ANYSTRING＿SINGLE |  |
| （d1） | DVAL（P） | Start device for storing the number of binary digits <br> after conversion | - | 16－bit signed binary | ANY16＿S＿ARRAY <br> （Number of elements：2） |
|  | DVAL（P）＿U |  |  | 16－bit unsigned binary | ANY16＿U＿ARRAY <br> （Number of elements：2） |
|  | DVAL（P） | Start device for storing the converted binary data | - | 32－bit signed binary | ANY32＿S |
|  | DVAL（P）＿U |  | - | 32－bit unsigned binary | ANY32＿U |
| EN | Execution condition | Execution result | - | Bit | BOOL |
| ENO |  |  | Bit | BOOL |  |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，Jㅁㅁ， U3EDl（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － |
| （d1） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

- Theses instructions convert the character strings stored in the device numbers specified by (s) and later to 32-bit binary data, and store the number of digits in (d1) and converted binary data in the device specified by (d2). For conversion of character strings to binary data, the data from the device number specified by (s) to the device number containing " 00 H " is processed as character strings.



## Ex.

When character string "-12345.678" (signed) is stored in the device specified in (s) or later


- The total number of characters of the character string stored in the device specified by $(\mathrm{s})$ is 2 to 13 .
- Of the character string stored in the device specified by (s), the number of characters in the decimal part is 0 to 10 . Note, however, that the number must not exceed the total number of digits minus 3 .
- The range of numerical character strings that can be converted to binary is as follows. Numerical character strings excluding signs and decimal points can be specified only within the range from 30 H to 39 H . (A value ignoring the decimal point..."-12345.6" for example becomes "-123456".)
- When a signed value ignoring the decimal point is specified: -2147483648 to 2147483647
-When an unsigned value ignoring the decimal point is specified: 0 to 4294967295
- For the sign, " 20 H " can be set to indicate a positive numerical value, or " 2 DH " can be set to indicate a negative numerical value.
- "2EH" is set for the decimal point.
- The total number of digits stored in the device specified by (d1) includes all characters (including signs and decimal points) that represent a numerical value. The number of digits in the decimal part to be stored in the device specified by (d1)+1 represents the decimal part after 2 EH (.) For the 32-bit binary data to be stored in the device specified by (d2), the character string is converted to binary data by ignoring the decimal point and stored.
- If " 20 H " (space) or " 30 H " ( 0 ) exists between the sign and the first numerical value other than 0 in the character string in the device specified by (s), the instruction performs conversion to binary data by ignoring " 20 H " and " 30 H ".


## Ex.

When " 20 H " exists between the sign and the first numerical value other than 0 (when a signed value is specified)


## Ex.

When " 30 H " exists between the sign and the first numerical value other than 0


| Error code (SD0) | Description |
| :---: | :---: |
| 2820 H | 00 H is not set between the device number specified by (s) and the last device number of the relevant device. |
| 3401H | An out-of-range value was input to (s). <br> - The number of characters of the character string is not between 2 and 13. <br> - The number of characters in the decimal part of the character string is not between 0 and 10. <br> - The relationship between the total number of characters and the number of characters in the decimal part is not in the following range. <br> Total number of characters $-3 \geq$ number of characters in the decimal part <br> - When the DVAL(P) instruction is used, an ASCII code other than " 20 H " and " 2 DH " is set as a sign. <br> - When the DVAL(P)_U instruction is used, an ASCII code other than " 20 H " is set as a sign. <br> - An ASCII code other than " 30 H " to " 39 H " and " 2 EH " (decimal point) is set as a digit of individual numbers. <br> - More than one decimal point are set. |
|  | The converted binary value exceeds the range in which each instruction can implement conversion. |
| 3405H | The number of characters of the character string in the device specified by (s) exceeds 16383. |

## Converting hexadecimal ASCII to hexadecimal binary data

## ASC2INT（P）

These instructions convert hexadecimal ASCII data to binary data．


FBD／LD


■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| ASC2INT | - |
|  | - |
| ASC2INTP |  |

Setting data
■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Start device where the string data to be converted to binary <br> data | - | String | ANYSTRING＿SINGLE |
| （d） | Start device for storing the converted binary data | - | 16－bit signed binary | ANY16 |
| （n） | Number of characters to be stored | 0 to 16383 | 16－bit signed binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J밈， U3EDI（H）GD | z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- These instructions convert the hexadecimal ASCII data stored in the device by the number of characters specified by ( n ) after the device number specified by (s) and later to binary data, and store the converted data in the device number specified by (d) and later.

- Setting the number of characters for $(\mathrm{n})$ automatically determines the range of the character string in the device specified by (s) and the device range in which the binary data in the device specified by (d) is stored.
- Processing is performed normally even if the device range in which the ASCII data to be converted and the device range for storing the converted binary data overlap.

- If the number of characters in the device specified by $(n)$ is not a multiple of $4, ~ " O H$ " is automatically stored after the specified number of the last device number among device numbers for storing the converted binary data.


## Ex.

When the number of characters in ( n ) is 9


- If the number of characters in the device specified by $(\mathrm{n})$ is 0 , no processing is performed.
- The ASCII code that can be specified by (s) must be in the range from " 30 H " to " 39 H " or " 41 H to 46 H ".


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3401 H | A character other than hexadecimal numerical character string (an ASCII code other than " 30 H " to " 39 H " and " 41 H " to " $46 \mathrm{H} "$ ") is set in the <br> device specified by (s). |
| 3405 H | Out-of-range data is set in the device specified by ( n ). <br> - The specified number of characters is not between 0 and 16383. |

## Converting single－precision real number to BCD format data

## EMOD（P）

These instructions convert single－precision real number data to the BCD floating point format data．

| Ladder | ST |
| :---: | :---: |
| ■－—－$\square$ （s1） （s2） （d） | $\begin{aligned} & \text { ENO:=EMOD(EN,s1,s2,d); } \\ & \text { ENO:=EMODP(EN,s1,s2,d); } \end{aligned}$ |

FBD／LD


■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| EMOD | - |
|  | - |
| EMODP | - |

Setting data
Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Single－precision real number data or the start device <br> where the single－precision real number data is stored | $0,2^{-126 \leq\|(s 1)\|<2^{128}}$ | Single－precision real <br> number | ANYREAL＿32 |
| （s2） | Decimal part digit data | 0 to 7 | 16 －bit signed binary | ANY16 |
| （d） | Start device for storing the BCD format data | - | 16 －bit signed binary | ANY16＿ARRAY <br> （Number of elements： 5$)$ |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathbf{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathbf{S M}, \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，Jㅁㅁ， U3EDl（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （s2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions convert the single-precision real number data stored in the device specified by ( $s$ 1) to the BCD floating point format based on the number of decimal part digits stored in the device specified by (s2), and store the converted data in the device number specified by (d) and later.

(s2) $\begin{aligned} & \text { Number of digits } \\ & \text { in the decimal }\end{aligned}$


Single-precision real number
(s2) $\square$

3254270 H

- The number of decimal part digits of the single-precision real number data in the device specified by (s1) is stored in the device specified by (S2). The example in the above figure shows the following.
3.25427

伏
$(s 2)=3$

- Six-digit BCD data, determined by rounding off the seventh digit, is stored in (d)+1 and (d)+2.

(s2)


- A value of 0 to 7 can be set for the number of decimal part digits in the device specified by (s2).
- When an input value is set using the engineering tool, a rounding error may occur. Refer to the following for the precautions on setting input values using the engineering tool.
$\longmapsto$ Page 41 Precautions


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3401 H | The number of decimal part digits in the device specified by (s2) is out of the range from 0 to 7. |
| 3402 H | The value set to a device or label in (s1) is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |

## Two＇s complement of 16－bit binary data（sign inversion）

## NEG（P）

Invert the sign of 16－bit binary device．

| Ladder | ST |
| :---: | :---: |
|  | $\begin{aligned} & \text { ENO:=NEG(EN,d); } \\ & \text { ENO:=NEGP(EN,d); } \end{aligned}$ |

FBD／LD


## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| NEG | - |
|  | $\boxed{ }$ |
| NEGP | - |

Setting data

## ■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （d） | Device where the data subjected to two＇s complement is <br> stored | - | 16－bit signed binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions invert the sign of the 16－bit binary data in the device specified by（d）and store the inverted data in the device specified by（d）．
－The instructions are used to invert positive and negative signs．

Before execution $\qquad$ $-21846$

Sign conversion


After execution


## Operation error

There is no operation error．

## Two＇s complement of 32－bit binary data（sign inversion）

## DNEG（P）

These instructions invert the sign of 32－bit binary device．

| Ladder | ST |  |
| :--- | :--- | :--- |
| $-\square-\square$ （d） <br>   |  |  |

FBD／LD


## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DNEG | - |
|  | $\boxed{Z}$ |
| DNEGP | - |

Setting data
■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （d） | Start device where the data subjected to two＇s complement is <br> stored | - | 32－bit signed binary | ANY32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions invert the sign of the 32－bit binary data in the device specified by（d）and store the inverted data in the device specified by（d）．
－The instructions are used to invert positive and negative signs．


Sign conversion


After execution


## Operation error

There is no operation error

## Decoding 8－bit data to 256－bit data

## DECO（P）

These instructions decode the lower（ $n$ ）bits of the specified device．


FBD／LD


■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DECO | - |
|  | $\boxed{~ D E C O P ~}$ |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Decode data or the start number of the device where the <br> decode data is stored | - | Bit／16－bit signed binary | ANY＿ELEMENTARY |
| （d） | Device for storing the decoded data | - | Bit／Word | ANY＿ELEMENTARY |
| （n） | Effective bit length | 1 to 8 | 16 －bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDI（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

－These instructions turn on the bit，corresponding to the binary value specified by the lower（ n ）bits in the device specified by （s），in the device specified by（d）
（s）

－Specify a value 1 to 8 for（ n ）．
－When（ n ）$=0$ ，no processing is performed and the values in the device specified by（d）remain unchanged．
－A bit device is treated as 1 bit，and a word device is treated as 16 bits．

## Operation error

| Error code (SD0) | Description |
| :--- | :--- |
| 3401 H | The value specified by $(\mathrm{n})$ is out of the range, 0 to 8. |

## Encoding 256－bit data to 8－bit data

## ENCO（P）

These instructions encode the bit data of＇n＇th power of 2.


FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| ENCO | - |
|  | $\boxed{Y}$ |
| ENCOP | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| $(\mathrm{s})$ | Device where the encode data is stored | - | Bit／Word | ANY＿ELEMENTARY |
| $(\mathrm{d})$ | Start number of the device for storing the encoded data | - | Bit／16－bit signed binary | ANY＿ELEMENTARY |
| $(\mathrm{n})$ | Effective bit length | 1 to 8 | 16－bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

－These instructions store the binary value，corresponding to the bit which is set to 1 in the $2^{(n)}$ bit data in the device specified by（ s ），in the device specified by（d）．

－Specify a value 1 to 8 for（ n ）．
－When $(\mathrm{n})=0$ ，no processing is performed and the values in the device specified by（d）remain unchanged．
－A bit device is treated as 1 bit，and a word device is treated as 16 bits．
－When two or more bits are 1 ，the upper bit position is used for processing．

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3401 H | The value specified by $(\mathrm{n})$ is out of the range, 0 to 8. |
|  | The bits in the $2^{(\mathrm{n})}$ bit data in the device specified by (s) are all 0 s. |

## Decoding data to seven－segment display data

## SEG（P）

These instructions decode the data consisting of 0 to $F$ specified by the lower 4 bits of the device to seven－segment display data．


FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| SEG | - |
| SEGP | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Decode data or the device where the decode data is stored | -32768 to 32767 | 16－bit signed binary | ANY16 |
| （d） | Device for storing the decoded data | - | 16－bit signed binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3E미（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions decode the data consisting of 0 to $F$ specified by the lower 4 bits in the device specified by (s) to sevensegment display data, and store the decoded data in the device specified by (d).
- In the case of a bit device, (d) indicates the start device for storing 7-segment display data. In the case of a word device, it indicates the device number for storing the data.


## Ex.

Bit device


The data in Y 48 to Y 4 F does not change until the next data is output.

## Ex.

Word device


- The following is the truth table for the seven-segment display.

| (s) |  | Seven-segment display | (d) |  |  |  |  |  |  |  | Display data |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Hexadecimal | Bit pattern |  | b7 | b6 | b5 | b4 | b3 | b2 | b1 | b0 |  |
| 0 | 0000 |  | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | I_1 |
| 1 | 0001 |  | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 1 |
| 2 | 0010 |  | 0 | 1 | 0 | 1 | 1 | 0 | 1 | 1 | ミ |
| 3 | 0011 |  | 0 | 1 | 0 | 0 | 1 | 1 | 1 | 1 | - |
| 4 | 0100 |  | 0 | 1 | 1 | 0 | 0 | 1 | 1 | 0 | 1 |
| 5 | 0101 |  | 0 | 1 | 1 | 0 | 1 | 1 | 0 | 1 | ■ |
| 6 | 0110 |  | 0 | 1 | 1 | 1 | 1 | 1 | 0 | 1 | E |
| 7 | 0111 |  | 0 | 0 | 1 | 0 | 0 | 1 | 1 | 1 | 1 |
| 8 | 1000 |  | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | E |
| 9 | 1001 |  | 0 | 1 | 1 | 0 | 1 | 1 | 1 | 1 | - |
| A | 1010 |  | 0 | 1 | 1 | 1 | 0 | 1 | 1 | 1 | F1 |
| B | 1011 |  | 0 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | İ |
| C | 1100 |  | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | $\begin{aligned} & \mathbf{1} \\ & \mathbf{1} \end{aligned}$ |
| D | 1101 |  | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | - ا |
| E | 1110 |  | 0 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | E |
| F | 1111 |  | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 1 | E |

## Operation error

There is no operation error.

## Separating data in units of 4 bits

## DIS（P）

These instructions store the lower（ n ）nibble（s）of 16－bit binary data in another device range specified．


FBD／LD


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DIS | - |
|  | $\boxed{ }$ |
| DISP | $\boxed{ }$ |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Device where the separation target data is stored | - | 16－bit signed binary | ANY16 |
| （d） | Start device for storing the separated data | - | 16－bit signed binary | ANY16 |
| （n） | Number of separation units | 1 to 4 | 16－bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3E미（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- These instructions separate the lower (n) nibble(s) (4 bits/nibble) from the 16-bit binary data in the device specified by (s), and store each of the separated data in the lower 4 bits of the ( $n$ ) points of data in the device specified by (d).

- The upper 12 bits of the $(\mathrm{n})$ points of data in the device specified by (d) are filled with 0 s.
- Specify a value 1 to 4 for ( n ).
- When $(\mathrm{n})=0$, no processing is performed and the $(\mathrm{n})$ points of data starting from the device specified by (d) remain unchanged.
Operation error

| Error code (SD0) | Description |
| :--- | :--- |
| 3401 H | The value specified by $(\mathrm{n})$ is out of the range, 0 to 4. |

## Combining data in units of 4 bits

## UNI（P）

These instructions store the lower 4 bits of the（ n ）points of 16－bit binary data in another 16－bit device．


FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| UNI | - |
| UNIP | $\boxed{ }$ |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| $(\mathrm{s})$ | Start device where the combination target data is stored | - | 16－bit signed binary | ANY16 |
| $(\mathrm{d})$ | Device for storing the combined data | - | 16－bit signed binary | ANY16 |
| $(\mathrm{n})$ | Number of combination units | 1 to 4 | 16－bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDI（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |
| （ $n$ ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

－These instructions store the lower 4 bits of the（ n ）points of 16－bit binary data in the device specified by（s）in the 16 －bit device specified by（d）．

－The upper bits（bits in the（4－n）nibble（s））of data in the device specified by（d）are filled with 0s．
－Specify a value 1 to 4 for（ n ）．
－When $(\mathrm{n})=0$ ，no processing is performed and the data in the device specified by（d）remain unchanged．

## Operation error

| Error code (SD0) | Description |
| :--- | :--- |
| 3401 H | The value specified by $(\mathrm{n})$ is out of the range, 0 to 4. |

## Separating data in units of bits

## NDIS（P）

These instructions separate the data in units of bits．（The number of bits can be specified as desired．）


FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| NDIS | - |
|  | $\boxed{ }$ |
| NDISP | $\boxed{ }$ |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Start device where the separation target data is stored | - | 16－bit signed binary | ANY16 |
| （d） | Start device for storing the separated data | - | 16－bit signed binary | ANY16 |
| （s2） | Start device for storing the separation unit | - | 16－bit signed binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDl（H）Gロ | z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions separate the bits of data in the device specified by ( s 1 ) and later in units of bits specified by ( s 2 ), and store the separated data in the device range specified by (d) and later.

- Specify the value 1 to 16 for (s2).
- The device areas from the one specified by ( s 2 ) to the one storing " 0 " are processed.
- Specify the devices so that the range of the device where the separation target data is stored ((s1) and later) and the range of the device for storing the separated data ((d) and later) do not overlap. If they overlap, a correct operation result may not be obtained.
- Do not overlap the device numbers that are specified by ( s 1 ), ( s 2 ), and (d).


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2821 H | The device ranges specified by (s1) and (s2) are overlapping. |
|  | The device ranges specified by (s1) and (d) are overlapping. |
|  | The device ranges specified by (s2) and (d) are overlapping. |
| 3401 H | An out-of-range value was input to (s2). <br> • The value specified is out of the range, 1 to 16. <br> - There is no 0 in the label or device area (between the specified device number and the last device number). |

## Combining data in units of bits

## NUNI（P）

These instructions combine the data in units of bits．（The number of bits can be specified as desired．）

| Ladder | ST |
| :---: | :---: |
| $\square-\overline{-} \square$ （s1） （d） （s2） | $\begin{aligned} & \text { ENO:=NUNI(EN,s1,s2,d); } \\ & \text { ENO:=NUNIP(EN,s1,s2,d); } \end{aligned}$ |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| NUNI | - |
|  | $\boxed{ }$ |
| NUNIP | - |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Start device where the combination target data is stored | - | 16－bit signed binary | ANY16 |
| （d） | Start device for storing the combined data | - | 16－bit signed binary | ANY16 |
| （s2） | Start device for storing the combination unit | - | 16－bit signed binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDl（H）Gロ | z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions combine the bits of data in the device specified by ( s 1 ) and later in units of bits specified by ( s 2 ), and store the combined data in the device specified by (d) and later.

- Specify the value 1 to 16 for (s2).
- The device areas from the one specified by ( s 2 ) to the one storing " 0 " are processed.
- Specify the devices so that the range of the device where the combination target data is stored ((s1) and later) and the range of the device for storing the combined data ((d) and later) do not overlap. If they overlap, a correct operation result may not be obtained.
- Do not overlap the device numbers that are specified by ( s 1 ), ( s 2 ), and (d).



## Separating data in units of bytes

## WTOB（P）

These instructions separate 16－bit binary data into（ n ）bytes．


FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| WTOB | - |
|  | $\boxed{~ W T O B P ~}$ |
|  | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Start device where the separation target data is stored | - | 16－bit signed binary | ANY16 |
| （d） | Start device for storing the separated data | - | 16－bit signed binary | ANY16 |
| （n） | Number of data bytes to be separated | 0 to 65535 | 16－bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）GD | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ $n$ ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- These instructions separate the 16 -bit binary data in the device specified by ( s ) and later into ( n ) bytes, and store the separated data in the device specified by (d) and later.

*1 Values after the decimal point are rounded up.


## Ex.

When ( n ) is 5 , the data in the device specified by ( s ) (upper 8 bits) to ( s ) +2 (lower 8 bits) are stored in the device specified by (d) to (d) +4 .


- Setting the number of bytes for $(\mathrm{n})$ automatically determines the range of 16 -bit binary data specified by ( s ) and the range of the device specified by (d) for storing the separated data.
- If $(\mathrm{n})$ is 0 , no processing is performed.
- The upper 8 bits of the device specified by (d) are automatically filled with 00 Hs .


## Ex.

When the byte data in D12 to D14 are stored in the lower 8 bits of D11 to D16


- Even when the ranges of the device where the separation target data is stored and the device for storing the separated data overlap, the processing is performed normally.

| Range of the device where the separation target data is stored | Range of the device for storing the separated data |
| :--- | :--- |
| (s) to $(\mathrm{s})+\left(\frac{(\mathrm{n})}{2}-1\right)$ | (d)+0 to (d)+(n)-1 |

## Operation error

There is no operation error.

## Combining data in units of bytes

## BTOW（P）

These instructions combine the lower 8 bits of 16－bit binary data in units of words．


FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| BTOW | - |
|  | $\boxed{ }$ |
| BTOWP | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Start device where the combination target data is stored | - | 16－bit signed binary | ANY16 |
| （d） | Start device for storing the combined data | - | 16－bit signed binary | ANY16 |
| （n） | Number of data bytes to be combined | 0 to 65535 | 16－bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- These instructions combine the $(\mathrm{n})$ bytes of lower 8 bits of 16 -bit binary data in the device specified by (s) and later in units of words, and store the combined data in the device specified by (d) and later.
- The $(\mathrm{n})$ bytes of upper 8 bits of 16 -bit binary data in the device specified by ( s ) and later are ignored. When ( n ) is an odd number, the upper 8 bits of the device where 'n'th-byte data is stored are filled with 0s.

*1 Values after the decimal point are rounded up.


## Ex.

When ( n ) is 5 , the lower 8 bits of the data in the device specified by ( s ) to ( s ) +4 are combined and stored in the device specified by (d) to (d)+2.


- Setting the number of bytes for ( n ) automatically determines the range of byte data in the device specified by ( s ) and the range of the device specified by (d) for storing the combined data.
- If ( n ) is 0 , no processing is performed.
- The upper 8 bits in the device specified by (s) and later are ignored, and only the lower 8 bits are processed.
- Even when the ranges of the device where the combination target data is stored and the device for storing the combined data overlap, the processing is performed normally.

| Range of the device where the combination target data is stored | Range of the device for storing the combined data |
| :--- | :--- |
| (s) +0 to $(\mathrm{s})+(\mathrm{n})-1$ | (d) to (d) $+\left(\frac{(\mathrm{n})}{2}-1\right)$ |

## Ex.

When the lower 8 bits of D11 to D16 are stored in D12 to D14

|  | ... | b8 b7 | ... | b0 |  | ... | b8 b7 | ... | b0 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| D11 | OOH |  | 31 H |  | D11 | OOH | ' | 31 H |  |
| D12 | 00H |  | 32 H |  | $\longrightarrow$ D12 | 32H |  | 31 H |  |
| D13 | OOH |  | 33 H |  | $\longrightarrow$ D13 | 34 H | ' | 33 H |  |
| D14 | 00H |  | 34 H |  | $\rightarrow$ D14 | 36H |  | 35 H |  |
| D15 | 00H |  | 35 H |  | D15 | 00H |  | 35 H |  |
| D16 | 00H |  | 36H |  | D16 | OOH |  | 36 H |  |

## Operation error

There is no operation error.

## 6．7 Data Transfer Instructions

## Transferring 16－bit binary data

## MOV（P）

These instructions transfer the 16－bit binary data in the device specified．

| Ladder | ST |
| :---: | :---: |
|  | $\begin{aligned} & \text { ENO:=MOV(EN,s,d); } \\ & \text { ENO:=MOVP(EN,s,d); } \end{aligned}$ |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| MOV | - |
|  | $\boxed{ }$ |
| MOVP | - |

Setting data
Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Transfer source data or the number of the device where the <br> transfer source data is stored | -32768 to 32767 | 16－bit signed binary | ANY16 |
| （d） | Transfer destination device number | - | 16－bit signed binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDl（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions transfer the 16－bit binary data in the device specified by（s）to the device specified by（d）．
（s）

（d）


## Operation error

There is no operation error．

## Transferring 32-bit binary data

## DMOV(P)

These instructions transfer the 32-bit binary data in the device specified.

| Ladder | ST |
| :---: | :---: |
|  | $\begin{aligned} & \mathrm{ENO}:=\mathrm{DMOV}(\mathrm{EN}, \mathrm{~s}, \mathrm{~d}) ; \\ & \mathrm{ENO}:=\mathrm{DMOVP}(\mathrm{EN}, \mathrm{~s}, \mathrm{~d}) \end{aligned}$ |

FBD/LD


## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DMOV | - |
|  | $\boxed{ }$ |
| DMOVP | - |

Setting data
■Description, range, data type

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (s) | Transfer source data or the number of the device where the <br> transfer source data is stored | -2147483648 to 2147483647 | 32-bit signed binary | ANY32 |
| (d) | Transfer destination device number | - | 32-bit signed binary | ANY32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jप\ロ | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | UपIGロ, J밈, U3EDl(H)GD | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (s) | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - | - |
| (d) | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - | - | - |

## Processing details

- These instructions transfer the 32-bit binary data in the device specified by (s) to the device specified by (d).
(s) +1
(s)
(s)

(d) +1
Transfer
(d)
(d)

| b15 |  | b0 b15 |  |  |  |  |  |  |  |  |  |
| :---: | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| 1 | 0 | 1 | 1 | $\square$ | 0 | 1 | 0 | 0 | 0 | 1 | 1 |

## Operation error

There is no operation error.

## Inverting and transferring 16－bit binary data

## CML（P）

These instructions invert the specified 16 －bit binary data bit by bit，and transfer the inverted data．

| Ladder | ST |
| :---: | :---: |
|  | $\begin{aligned} & \text { ENO:=CML(EN,s,d); } \\ & \text { ENO:=CMLP(EN,s,d); } \end{aligned}$ |

FBD／LD


## －Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| CML | - |
|  | $\boxed{ }$ |
| CMLP | - |

Setting data
■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Inversion target data or the number of the device where the <br> inversion target data is stored | -32768 to 32767 | 16－bit signed binary | ANY16 |
| （d） | Number of the device for storing the inverted data | - | 16－bit signed binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3EDI（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions invert the 16－bit binary data in the device specified by（s）bit by bit，and transfer the inverted data to the device specified by（d）．
（s）

（d）


## Operation error

There is no operation error．

## Inverting and transferring 32－bit binary data

## DCML（P）

These instructions invert the specified 32－bit binary data bit by bit，and transfer the inverted data．

| Ladder | ST |
| :---: | :---: |
|  | $\begin{aligned} & \text { ENO:=DCML(EN,s,d); } \\ & \text { ENO:=DCMLP(EN,s,d); } \end{aligned}$ |

FBD／LD


■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DCML | - |
|  | $\boxed{Z}$ |
| DCMLP | $\boxed{ }$ |

Setting data
■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Inversion target data or the number of the device where the <br> inversion target data is stored | -2147483648 to 2147483647 | 32－bit signed binary | ANY32 |
| （d） | Number of the device for storing the inverted data | - | 32－bit signed binary | ANY32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3EDI（H）G口 | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions invert the 32－bit binary data in the device specified by（s）bit by bit，and transfer the inverted data to the device specified by（d）．
（s）+1
（s）
（s）

| b15 |  | b0 b15 |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| 1 | 0 | 1 | 1 |  |

（d）+1
$\}$ Inversion

| b15 |  |  |  |  |  |  |  |  |  | $\ldots$ |  |  |  |  | b0 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 0 | 1 | 0 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 1 | 1 | 0 | 1 |

## Operation error

There is no operation error

## Inverting and transferring 1-bit data

## CMLB(P)

These instructions invert the specified bit data, and transfer the inverted data.

| Ladder |  | ST |
| :--- | :--- | :--- |
| $-\square$ (s) (d) <br>   ENO:=CMLB(EN,s,d); |  |  |

FBD/LD


## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| CMLB | - |
|  | $\boxed{ }$ |
| CMLBP | - |

Setting data
■Description, range, data type

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (s) | Inversion target data or the number of the device where the <br> inversion target data is stored | - | Bit | ANY_BOOL |
| (d) | Transfer destination device number | - | Bit | ANY_BOOL |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | U밈, J밈, U3EDI(H)GD | z | LT, LST, LC | LZ |  | K, H | E | \$ |  |
| (s) | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - | $\bigcirc$ | - | $\bigcirc$ | - | - | - | - |
| (d) | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - | $\bigcirc$ | - | $\bigcirc$ | - | - | - | - |

## Processing details

- These instructions invert the bit data in the device specified by (s), and transfer the inverted data to the device specified by (d).
(d)
(s)

$\square$


The bit is inverted and transferred.


Operation error
There is no operation error.

## Transferring 16－bit binary data block（16 bits）

## BMOV（P）

These instructions batch－transfer the（n）points（0 to 65535）of 16－bit binary data starting from the device specified．

| Ladder | ST |
| :---: | :---: |
| ■－－$-\square$ （s） （d） （n） | $\begin{aligned} & \text { ENO:=BMOV(EN,s,n,d); } \\ & \text { ENO:=BMOVP(EN,s,n,d); } \end{aligned}$ |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| BMOV | - |
|  | $\boxed{ }$ |
| BMOVP | - |

Setting data
Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Start device where the transfer target data is stored | - | 16－bit signed binary | ANY16 |
| （d） | Transfer destination start device | - | 16－bit signed binary | ANY16 |
| （n） | Number of transfer data points | 0 to 65535 | 16－bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | J밈 | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3EDI（H）G口 | z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- These instructions batch-transfer the ( n ) points of 16-bit binary data starting from the device specified by ( s ) to the device specified by (d).

- Data can be transferred even when the transfer source device and destination device overlap. A transfer to smaller device numbers begins from the device specified by (s), and a transfer to larger device numbers begins from the device specified by ( s ) $+(\mathrm{n})-1$.
- When (s) is a word device and (d) is a bit device, the number of digit-specified bits in the word device is transferred.


## Ex.

When K1Y30 is specified in (d), the lower 4 bits of the word device specified by (s) are transferred.


- When both (s) and (d) are bit devices, set the same number of digits for both devices.
- To use the link direct device, module access device, or CPU buffer memory access device for (s) or (d), specify it only for one of the devices. Note that the CPU buffer memory access device (U3EO\Gロ) of the host CPU module in which index modification is not specified in the I/O No. specification can be specified in both (s) and (d).


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3420 H | The link direct device, module access device, or CPU buffer memory access device is specified in both (s) and (d). <br> However, this is not true when the CPU buffer memory access device (U3EO\GI) of the host CPU module in which index modification is <br> not specified is specified. |

## Transferring 16－bit binary data block（32 bits）

## BMOVL（P）

These instructions batch－transfer the（ n ）points（ 1 to 4294967295 ）of 16－bit binary data starting from the device specified．


FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| BMOVL | - |
|  | $\boxed{~ B M O V L P ~}$ |
|  | - |

Setting data
Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Start device where the transfer target data is stored | - | 16－bit signed binary | ANY16 |
| （d） | Transfer destination start device | - | 16－bit signed binary | ANY16 |
| （n） | Number of transfer data points | 0 to 4294967295 | 32－bit unsigned binary | ANY32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3EDl（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- These instructions batch-transfer the ( n ) points of 16-bit binary data starting from the device specified by ( s ) to the device specified by (d).

- Data can be transferred even when the transfer source device and destination device overlap. A transfer to smaller device numbers begins from the device specified by (s), and a transfer to larger device numbers begins from the device specified by ( s ) $+(\mathrm{n})-1$.
- When (s) is a word device and (d) is a bit device, the number of digit-specified bits in the word device is transferred.


## Ex.

When K1Y30 is specified in (d), the lower 4 bits of the word device specified by (s) are transferred.


- When both (s) and (d) are bit devices, set the same number of digits for both devices.
- To use the link direct device, module access device, or CPU buffer memory access device for (s) or (d), specify it only for one of the devices. Note that the CPU buffer memory access device (U3EO\GD) of the host CPU module in which index modification is not specified in the I/O No. specification can be specified in both (s) and (d).


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3420 H | The link direct device, module access device, or CPU buffer memory access device is specified in both (s) and (d). <br> However, this is not true when the CPU buffer memory access device (U3EO\Gロ) of the host CPU module in which index modification is <br> not specified is specified. |

## Transferring the same 16－bit binary data block（16 bits）

## FMOV（P）

These instructions transfer 16－bit binary data to the $(\mathrm{n})$ points（ 0 to 65535 ）starting from the device specified．


FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| FMOV | - |
|  | $\boxed{ }$ |
| FMOVP | - |

Setting data
Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Transfer target data or the device where the transfer target <br> data is stored | -32768 to 32767 | 16－bit signed binary | ANY16 |
| （d） | Transfer destination start device | - | 16－bit signed binary | ANY16 |
| （n） | Number of transfer data points | 0 to 65535 | 16－bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3Eपl（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- These instructions transfer the 16-bit binary data in the device specified by ( s ) to the ( n ) points of the device specified by (d).

- When (s) is a word device and (d) is a bit device, the number of digit-specified bits in the word device is transferred.


## Ex.

When K1Y30 is specified in (d), the lower 4 bits of the word device specified by (s) are transferred.
(s) D100


- When both (s) and (d) are bit devices, set the same number of digits for both devices.


## Operation error

There is no operation error.

## Transferring the same 16－bit binary data block（32 bits）

## FMOVL（P）

These instructions transfer 16－bit binary data to the（ n ）points（1 to 4294967295 ）starting from the device specified．


FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| FMOVL | - |
|  | $\boxed{ }$ |
| FMOVLP | - |

Setting data
Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Transfer target data or the device where the transfer target <br> data is stored | -32768 to 32767 | 16－bit signed binary | ANY16 |
| （d） | Transfer destination start device | - | 16－bit signed binary | ANY16 |
| （n） | Number of transfer data points | 0 to 4294967295 | 32－bit unsigned binary | ANY32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- These instructions transfer the 16-bit binary data in the device specified by ( s ) to the ( n ) points of the device specified by (d).

- When (s) is a word device and (d) is a bit device, the number of digit-specified bits in the word device is transferred.


## Ex.

When K1Y30 is specified in (d), the lower 4 bits of the word device specified by (s) are transferred.
(s) D 100


- When both (s) and (d) are bit devices, set the same number of digits for both devices.


## Operation error

There is no operation error.

## Transferring the same 32－bit binary data block（16 bits）

## DFMOV（P）

These instructions transfer 32－bit binary data to the（n）points（1 to 65535）starting from the device specified．


FBD／LD


■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DFMOV | - |
|  | - |
| DFMOVP | - |

Setting data
Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Transfer target data or the start device where the transfer <br> target data is stored | -2147483648 to 2147483647 | 32－bit signed binary | ANY32 |
| （d） | Transfer destination start device | - | 32－bit signed binary | ANY32 |
| （n） | Number of transfer data points | 0 to 65535 | 16－bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## －Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J밈， U3EDI（H）GD | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- These instructions transfer the 32-bit binary data in the device specified by ( s ) to the ( n ) points of the device specified by (d).

- When the number of digits is specified for the data in the device specified by (s), only the data corresponding to the specified digits are transferred. When K5Y0 is specified in (s), the lower 20 bits (five digits) of the word device specified by (s) are transferred.

- When the number of digits is specified for the data in the device specified by (d), the data corresponding to the specified digits are transferred. When K5Y0 is specified in (d), the lower 20 bits (five digits) of the word device specified by (s) are transferred. When the number of digits is specified for both data in the devices specified by (s) and (d), the data corresponding to the digits specified in (d) are transferred.

- If $(\mathrm{n})$ is 0 , no processing is performed.


## Operation error

There is no operation error.

## Transferring the same 32－bit binary data block（32 bits）

## DFMOVL（P）

These instructions transfer 32－bit binary data to the（ n ）points（1 to 4294967295）starting from the device specified．


FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DFMOVL | $\boxed{ }$ |
| DFMOVLP | $\boxed{ }$ |

Setting data
Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Transfer target data or the start device where the transfer <br> target data is stored | -2147483648 to 2147483647 | 32－bit signed binary | ANY32 |
| （d） | Transfer destination start device | - | 32－bit signed binary | ANY32 |
| （n） | Number of transfer data points | 0 to 4294967295 | 32－bit unsigned binary | ANY32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3Eपl（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- These instructions transfer the 32-bit binary data in the device specified by ( s ) to the ( n ) points of the device specified by (d).

- When the number of digits is specified for the data in the device specified by (s), only the data corresponding to the specified digits are transferred. When K5Y0 is specified in (s), the lower 20 bits (five digits) of the word device specified by (s) are transferred.

- When the number of digits is specified for the data in the device specified by (d), the data corresponding to the specified digits are transferred. When K5Y0 is specified in (d), the lower 20 bits (five digits) of the word device specified by (s) are transferred. When the number of digits is specified for both data in the devices specified by (s) and (d), the data corresponding to the digits specified in (d) are transferred.

- If $(\mathrm{n})$ is 0 , no processing is performed.


## Operation error

There is no operation error.

## Exchanging 16－bit binary data

## XCH（P）

These instructions exchange the 16－bit binary data specified．

| Ladder | ST |
| :---: | :---: |
| $\begin{array}{\|l\|l\|l\|} \hline-\square-\square & \text { (d1) } & \text { (d2) } \\ \hline \end{array}$ | $\begin{aligned} & \text { ENO:=XCH(EN,d1,d2); } \\ & \text { ENO:=XCHP(EN,d1,d2); } \end{aligned}$ |

## FBD／LD


－Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| XCH | - |
|  | - |
| XCHP |  |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （d1） | Device where the exchange target data is stored | - | 16－bit signed binary | ANY16 |
| （d2） | Device where the exchange target data is stored | - | 16－bit signed binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDl（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （d1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |
| （d2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instruction exchange the 16－bit binary data between the devices specified by（d1）and（d2）．


## Operation error

There is no operation error．

## Exchanging 32－bit binary data

## DXCH（P）

These instructions exchange the 32－bit binary data specified．

| Ladder | ST |
| :---: | :---: |
|  | ENO：＝DXCH（EN，d1，d2）； |
| $--\square$ （d1） （d2） | ENO：＝DXCHP（EN，d1，d2）； |

FBD／LD

－Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DXCH | - |
|  | $\boxed{~ D X C H P ~}$ |
|  | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （d1） | Start device where the exchange target data is stored | - | 32－bit signed binary | ANY32 |
| （d2） | Start device where the exchange target data is stored | - | 32－bit signed binary | ANY32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J미， U3EDI（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （d1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |
| （d2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

－These instruction exchange the 32－bit binary data between the device ranges specified by（d1）and（d2）．

| （d1）${ }^{\text {＋1 }}$ |  | （d1） |
| :---: | :---: | :---: |
| b31 | b16b15 | b0 |
| 1／1／1！1 |  | 010100 |


| （d2）＋1 |  | （d2） |  |
| :---: | :---: | :---: | :---: |
| b31 | b16 | b15 | ．．b0 |
| 0101010 | 11111 | $1\|1\| 1 \mid$ | $1) 111111$ |


（d1）+1
（d1）




## Operation error

There is no operation error．

## Exchanging 16-bit binary block data

## BXCH(P)

These instructions exchange the $(\mathrm{n})$ points of 16-bit binary data starting from the devices specified.


FBD/LD


■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| BXCH | - |
| BXCHP | - |

## Setting data

-Description, range, data type

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (d1) | Start device where the exchange target data is stored | - | Word | ANY16 |
| (d2) | Start device where the exchange target data is stored | - | Word | ANY16 |
| (n) | Number of exchange data points | 0 to 65535 | 16-bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | J밈 | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | UपIGロ, JपIप, U3E미(H)Gㅁ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (d1) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (d2) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| ( n ) | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - | $\bigcirc$ | $\bigcirc$ | - | - | - |

## Processing details

- These instructions exchange the ( n ) points of 16-bit binary data starting from the device specified by (d1) with the ( n ) points of 16-bit binary data starting from the device specified by (d2).
(d1)

| b15 | b8b | b7 | .. b0 |
| :---: | :---: | :---: | :---: |
| 010100 | 11111110 | 010100 | 1/1/1!1 |
| 111111 | 1111110 | 01000 | 01000 |
| 000010 | 0000 | 1!1!11 | 111111 |




(d2)




(d1)








## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2821 H | The device ranges specified by (d1) and (d2) are overlapping. |

## Exchanging the upper and lower bytes of 16－bit binary data

## SWAP（P）

These instructions exchange upper and lower 8－bit data in the specified device．

| Ladder | ST |
| :---: | :---: |
|  | $\begin{aligned} & \text { ENO:=SWAP(EN,d); } \\ & \text { ENO:=SWAPP(EN,d); } \end{aligned}$ |

FBD／LD


■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| SWAP | - |
|  | $\boxed{ }$ |
| SWAPP | - |

Setting data
■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （d） | Device where the exchange target data is stored | - | 16－bit signed binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## －Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J밈， U3EDI（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions exchange upper and lower 8－bit data in the specified device．






## Operation error

There is no operation error．

## Transferring 1－bit data

## MOVB（P）

These instructions transfer the specified 1－bit data．

| Ladder |  | ST |
| :--- | :--- | :--- |
| $\|$$-\square$ （s） （d） <br>   ENO：＝MOVB（EN，s，d）； <br>  ENO：＝MOVBP（EN，s，d）；  |  |  |

FBD／LD


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| MOVB | - |
|  | $\boxed{Z}$ |
| MOVBP | - |

Setting data
■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Number of the device where the transfer target data is <br> stored | - | Bit | ANY＿BOOL |
| （d） | Transfer destination device number | - | Bit | ANY＿BOOL |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3E미（H）Gロ | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions transfer the bit data in the device specified by（s）to the device specified by（d）．


## Operation error

There is no operation error．

## Transferring n－bit data

## BLKMOVB（P）

These instructions batch－transfer the（ n ）points of bit data．


FBD／LD


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| BLKMOVB | - |
|  | $\boxed{ }$ |
| BLKMOVBP | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Transfer source block data | - | Bit | ANY＿BOOL |
| （d） | Transfer destination block data | - | Bit | ANY＿BOOL |
| （n） | Number of transfer data points | 0 to 65535 | 16－bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J미， U3EDI（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { I } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- These instructions batch-transfer the ( n ) points of bit data starting from the device specified by $(\mathrm{s})$ to the $(\mathrm{n})$ points of bits starting from the device specified by (d).
- Data can be transferred even when the transfer source device and destination device overlap.



## Operation error

There is no operation error.

## APPLICATION INSTRUCTIONS

## 7．1 Rotation Instructions

## Rotating 16－bit binary data to the right

## ROR（P），RCR（P）

－ROR（P）：These instructions rotate the 16－bit binary data to the right by（ n ）bit（ s ），excluding the carry flag．
－RCR（P）：These instructions rotate the 16 －bit binary data to the right by $(n)$ bit（ $s$ ），including the carry flag．

| Ladder |  | ST＊${ }^{\text {＊}}$ |
| :---: | :---: | :---: |
|  | （n） | $\begin{aligned} & \text { ENO:=RORP(EN,n,d); } \\ & \text { ENO:=RCR(EN,n,d); } \\ & \text { ENO:=RCRP(EN,n,d); } \end{aligned}$ |
| FBD／LD＊1 |  |  |
|  |  |  |

＊1 The ROR instruction does not support the structured text language and FBD／LD language．Use the standard function，ROR． W Page 1609 ROR（＿E）

## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| ROR | - |
| RCR | - |
| RORP | - |
| RCRP |  |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （d） | Rotation target device | - | 16－bit signed binary | ANY16 |
| （n） | Number of rotations | 0 to 15 | 16－bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

-ROR(P)

- These instructions rotate the 16-bit binary data in the device specified by (d) to the right by ( n ) bit( s ), excluding the carry flag (SM700). The carry flag (SM700) is on or off depending on the status prior to the execution of the ROR(P) instruction.


Carry flag (SM700)


- When (d) is a bit device, bits are rotated to the right within the device range specified by digit specification. The number of bits actually to be rotated is the remainder of (n) $\div$ (specified number of bits). For example, when ( $n$ ) is 15 and the specified number of bits is 12,3 bits are rotated because 15 divided by 12 equals 1 with a remainder of 3 .
- Specify any value between 0 and 15 for ( n ). If a value 16 or bigger is specified, bits are rotated by the remainder value of $\mathrm{n} \div 16$. For example, when $(\mathrm{n})$ is 18,2 bits are rotated because 18 divided by 16 equals 1 with a remainder of 2 .


## (RCR(P)

- These instructions rotate the 16-bit binary data in the device specified by (d) to the right by ( $n$ ) bit(s), including the carry flag (SM700). The carry flag (SM700) is on or off depending on the status prior to the execution of the $\operatorname{RCR}(\mathrm{P})$ instruction.

- When (d) is a bit device, bits are rotated to the right within the device range specified by digit specification. The number of bits actually to be rotated is the remainder of $(n) \div($ specified number of bits). For example, when $(n)$ is 15 and the specified number of bits is 12,3 bits are rotated because 15 divided by 12 equals 1 with a remainder of 3 .
- Specify any value between 0 and 15 for ( n ). If a value 16 or bigger is specified, bits are rotated by the remainder value of $n \div 16$. For example, when $(n)$ is 18,2 bits are rotated because 18 divided by 16 equals 1 with a remainder of 2 .


## Operation error

There is no operation error.

## Rotating 16－bit binary data to the left

## ROL（P），RCL（P）

－ROL（P）：These instructions rotate the 16－bit binary data to the left by（ $n$ ）bit（ $s$ ），excluding the carry flag．
－RCL（P）：These instructions rotate the 16－bit binary data to the left by（n）bit（s），including the carry flag．

＊1 The ROL instruction does not support the structured text language and FBD／LD language．Use the standard function，ROL． $\mapsto$ Page 1607 ROL（＿E）

Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| ROL | - |
| RCL | - |
| ROLP | - |
| RCLP |  |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （d） | Rotation target device | - | 16－bit signed binary | ANY16 |
| $(\mathrm{n})$ | Number of rotations | 0 to 15 | 16－bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）GD | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |
| （ $n$ ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

-ROL(P)

- These instructions rotate the 16-bit binary data in the device specified by (d) to the left by ( n ) bit( s ), excluding the carry flag (SM700). The carry flag (SM700) is on or off depending on the status prior to the execution of the ROL(P) instruction.

- When (d) is a bit device, bits are rotated to the left within the device range specified by digit specification. The number of bits actually to be rotated is the remainder of $(\mathrm{n}) \div($ specified number of bits). For example, when $(\mathrm{n})$ is 15 and the specified number of bits is 12,3 bits are rotated because 15 divided by 12 equals 1 with a remainder of 3 .
- Specify any value between 0 and 15 for ( n ). If a value 16 or bigger is specified, bits are rotated by the remainder value of $n \div 16$. For example, when $(n)$ is 18,2 bits are rotated to the left because 18 divided by 16 equals 1 with a remainder of 2 .


## -RCL(P)

- These instructions rotate the 16-bit binary data in the device specified by ( d ) to the left by $(\mathrm{n})$ bit( s ), including the carry flag (SM700). The carry flag (SM700) is on or off depending on the status prior to the execution of the $\operatorname{RCL}(\mathrm{P})$ instruction.

- When (d) is a bit device, bits are rotated to the left within the device range specified by digit specification. The number of bits actually to be rotated is the remainder of ( $n$ ) $\div($ specified number of bits). For example, when $(n)$ is 15 and the specified number of bits is 12,3 bits are rotated because 15 divided by 12 equals 1 with a remainder of 3 .
- Specify any value between 0 and 15 for ( n ). If a value 16 or bigger is specified, bits are rotated by the remainder value of $n \div 16$. For example, when $(n)$ is 18,2 bits are rotated to the left because 18 divided by 16 equals 1 with a remainder of 2 .


## Operation error

There is no operation error.

## Rotating 32－bit binary data to the right

## DROR（P），DRCR（P）

－DROR（P）：These instructions rotate the 32－bit binary data to the right by（n）bit（s），excluding the carry flag．
－ $\operatorname{DRCR}(P)$ ：These instructions rotate the 32－bit binary data to the right by $(n)$ bit（s），including the carry flag．

＊1 The DROR instruction does not support the structured text language and FBD／LD language．Use the standard function，ROR． W Page 1609 ROR（＿E）

■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DROR | - |
| DRCR | - |
| DRORP | - |
| DRCRP |  |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| $(\mathrm{d})$ | Start device where the rotation target data is stored | - | 32－bit signed binary | ANY32 |
| $(\mathrm{n})$ | Number of rotations | 0 to 31 | 16－bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロID， U3EDI（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |
| （ $n$ ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

-DROR(P)

- These instructions rotate the 32-bit binary data in the device specified by (d) to the right by ( n ) bit( s ), excluding the carry flag (SM700). The carry flag (SM700) is on or off depending on the status prior to the execution of the DROR(P) instruction.

- When (d) is a bit device, bits are rotated to the right within the device range specified by digit specification. The number of bits actually to be rotated is the remainder of (n)*(specified number of bits). For example, when ( $n$ ) is 31 and the specified number of bits is 24,7 bits are rotated because 31 divided by 24 equals 1 with a remainder of 7 .
- Specify any value between 0 and 31 for ( n ). If a value 32 or bigger is specified, bits are rotated by the remainder value of $\mathrm{n} \div 32$. For example, when $(\mathrm{n})$ is 34,2 bits are rotated because 34 divided by 32 equals 1 with a remainder of 2 .


## DRCR(P)

- These instructions rotate the 32-bit binary data in the device specified by (d) to the right by ( n ) bit(s), including the carry flag (SM700). The carry flag (SM700) is on or off depending on the status prior to the execution of the $\operatorname{DRCR}(\mathrm{P})$ instruction.

- When (d) is a bit device, bits are rotated to the right within the device range specified by digit specification. The number of bits actually to be rotated is the remainder of (n)*(specified number of bits). For example, when ( $n$ ) is 31 and the specified number of bits is 24,7 bits are rotated because 31 divided by 24 equals 1 with a remainder of 7 .
- Specify any value between 0 and 31 for ( n ). If a value 32 or bigger is specified, bits are rotated by the remainder value of $n \div 32$. For example, when $(n)$ is 34,2 bits are rotated because 34 divided by 32 equals 1 with a remainder of 2 .


## Operation error

There is no operation error.

## Rotating 32－bit binary data to the left

## DROL（P），DRCL（P）

－ $\operatorname{DROL}(P)$ ：These instructions rotate the 32－bit binary data to the left by（ $n$ ）bit（s），excluding the carry flag．
－DRCL（P）：These instructions rotate the 32－bit binary data to the left by（ $n$ ）bit（s），including the carry flag．

＊1 The DROL instruction does not support the structured text language and FBD／LD language．Use the standard function，ROL． $\longmapsto$ Page 1607 ROL（＿E）

## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DROL | - |
| DRCL | - |
| DROLP | - |
| DRCLP |  |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （d） | Start device where the rotation target data is stored | - | 32－bit signed binary | ANY32 |
| （n） | Number of rotations | 0 to 31 | 16－bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロID， U3EDI（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |
| （ $n$ ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

■DROL(P)

- These instructions rotate the 32-bit binary data in the device specified by (d) to the left by ( n ) bit( s ), excluding the carry flag (SM700). The carry flag (SM700) is on or off depending on the status prior to the execution of the $\operatorname{DROL}(\mathrm{P})$ instruction.

- When (d) is a bit device, bits are rotated to the left within the device range specified by digit specification. The number of bits actually to be rotated is the remainder of ( $n$ ) $\div$ (specified number of bits). For example, when $(\mathrm{n})$ is 31 and the specified number of bits is 24,7 bits are rotated because 31 divided by 24 equals 1 with a remainder of 7 .
- Specify any value between 0 and 31 for ( n ). If a value 32 or bigger is specified, bits are rotated by the remainder value of $n \div 32$. For example, when $(n)$ is 34,2 bits are rotated to the left because 34 divided by 32 equals 1 with a remainder of 2 .


## -DRCL(P)

- These instructions rotate the 32-bit binary data in the device specified by ( d ) to the left by $(\mathrm{n})$ bit( s ), including the carry flag (SM700). The carry flag (SM700) is on or off depending on the status prior to the execution of the $\operatorname{DRCL}(\mathrm{P})$ instruction.

- When (d) is a bit device, bits are rotated to the left within the device range specified by digit specification. The number of bits actually to be rotated is the remainder of (n) $\div$ (specified number of bits). For example, when ( $n$ ) is 31 and the specified number of bits is 24,7 bits are rotated because 31 divided by 24 equals 1 with a remainder of 7 .
- Specify any value between 0 and 31 for ( n ). If a value 32 or bigger is specified, bits are rotated by the remainder value of $n \div 32$. For example, when $(n)$ is 34,2 bits are rotated to the left because 34 divided by 32 equals 1 with a remainder of 2 .


## Operation error

There is no operation error.

## 7．2 Program Branch Instructions

## Pointer branch

## CJ，SCJ，JMP

－CJ：This instruction executes the program specified by the pointer number within the same program file．
－SCJ：This instruction executes the program specified by the pointer number within the same program file starting with the next scan．
－JMP：This instruction unconditionally executes the program specified by the pointer number within the same program file．


## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| $(\mathrm{P})$ | Pointer number of the jump destination | - | Device name | POINTER |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （P） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM} \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J미， U3EDI（H）Gㅁ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （P） | － | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ |

## Processing details

## CJ

－This instruction executes the program specified by the pointer number within the same program file when the execution command is on．
－When the execution command is off，the program in the next step is executed．


CJ instruction

-SCJ

- This instruction executes the program specified by the pointer number within the same program file starting with the scan immediately after the execution command turns on.
- When the execution command is off or turned off, the program in the next step is executed.


JMP

- This instruction unconditionally executes the program specified by the pointer number within the same program file.


## Precautions

- If the timer with its coil on is skipped by these instructions, time cannot be measured correctly.
- If the OUT instruction is skipped by these instructions, the scan time will be shortened.
- If these instructions specify and jump to the program with a bigger step number, the scan time will be shortened.
- These instructions can specify and jump to the program with a smaller step number. In this case, consider a method to exit a loop so that the watchdog timer does not time out.

(1) While $X 3$ is on, the loop is repeated.
(2) To exit the loop, turn on X7.
- The value in the device skipped with these instructions remains unchanged.


When XB turns on, the program jumps to the label, P19.
Y43 and Y49 remain unchanged even if $X B$ and $X C$ turn on/off during the execution of the CJ instruction.

- A label (Pロ) occupies one step.

(1) A label occupies one step.
- Only the pointer numbers within the same program file can be specified.
- If the program jumps to the pointer number within the skip range, the programs of the jump destination pointer number and later are executed.


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| }{} | The pointer number specified by $(\mathrm{P})$ is not set before the END instruction. |
|  | The pointer number specified by $(\mathrm{P})$ is not used as a label in the same program. |
|  | The pointer number specified by $(\mathrm{P})$ is a global pointer in another program. |

## Jumping to END

## GOEND

This instruction jumps the program to the FEND or END instruction within the same program file.

| Ladder | ST |  |
| :--- | :--- | :--- |
|  |  | Not supported |
| FBD/LD |  |  |
| Not supported |  |  |
| EXXecution condition |  |  |
| Instruction |  |  |
| GOEND |  |  |

## Processing details

- This instruction jumps the program to the FEND or END instruction within the same program file.


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3340 H | After execution of the FOR instruction, the GOEND instruction is executed before the NEXT instruction. |
| 3381 H | After execution of the CALL(P) or ECALL(P) instruction, the GOEND instruction is executed before the RET instruction. |
| $33 A 1 \mathrm{H}$ | The GOEND instruction is executed before the IRET instruction in the interrupt program specified by the interrupt pointer (I). |

### 7.3 Program Execution Control Instructions

## Disabling/enabling interrupt programs

## DI, EI

- DI: This instruction disables execution of interrupt programs.
- El: This instruction clears the interrupt disabled state.

| Ladder | ST |
| :---: | :---: |
|  | $\begin{aligned} & \text { ENO:=DI(EN); } \\ & \text { ENO:=EI(EN); } \end{aligned}$ |
| FBD/LD |  |



## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DI | Every scan |
| EI |  |

## Processing details

DI

- This instruction disables execution of interrupt programs until the El instruction is executed even though they are requested.
- When the system is powered on or the CPU module is reset, the system is in the state where the DI instruction has been executed.
- For the operation of the DI (Disabling interrupt programs) instruction used with the DI (Disabling interrupt programs with specified priority or lower) instruction, refer to the following.
$\longmapsto$ Page 479 DI
- The DI (Disabling interrupt programs) instruction cannot be executed in interrupt programs. If executed, no processing is performed.


## ■EI

- This instruction clears the interrupt disabled state that has been set by the DI (Disabling interrupt programs) instruction, and enables execution of interrupt programs with the interrupt pointer numbers permitted by the IMASK instruction. When the IMASK instruction is not executed, I32 to I43 are disabled.
- For the operation of the EI instruction used with the DI (Disabling interrupt programs with specified priority or lower) instruction, refer to the following.
$\mapsto$ Page 479 DI
- The operation of the El instruction in interrupt programs differs depending on the execution status of the DI (Disabling interrupt programs with specified priority or lower) instruction before the El instruction. The El instruction in the interrupt program after the execution of the DI (Disabling interrupt programs with specified priority or lower) instruction can be executed.



## Point?

- An interrupt pointer occupies one step. (At the program (1) below, for example, I10 is step 50, X1C is step 51 , and Y 10 is step 52.)

- If the El and DI instructions are provided in the master control, these instructions are executed regardless of the execution status of the MC instruction.


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3362 H | More than 16 DI (Disabling interrupt programs) instructions and DI (Disabling interrupt programs with specified priority or lower) <br> instructions are nested. |

## Disabling interrupt programs with specified priority or lower

## DI

This instruction disables execution of interrupt programs with the specified priority or lower．

| Ladder | ST |
| :--- | :--- | :--- |
|  | ENO：＝DI＿1（EN，s）； |
| $\square-\square$ |  |
|  |  |

FBD／LD

（ $\square$ is replaced by DI＿1．）
Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DI | Every scan |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Interrupt disable priority | 1 to 8 | 16－bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロום | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

－This instruction disables execution of interrupt programs with the interrupt pointer numbers specified by（s）or lower．

Interrupt priority setting

| I No． | Priority |
| :--- | :--- |
| 150 | 5 |
| 151 | 6 |


（1）Interrupt disabled section for priority 6 to 8 （Interrupt enabled section for priority 1 to 5 ）
（2）The interrupt program can be executed because its priority is 5.
（3）The interrupt program cannot be executed because its priority is 6

- The execution of the El instruction enables the interrupt that has been disabled by a single DI (Disabling interrupt programs with specified priority or lower) instruction. Note that if interrupts have been disabled only by the DI (Disabling interrupt programs) instruction, executing the El instruction only once enables interrupts in all priorities.

(1) Interrupt enabled section for all priorities
(2) Interrupt disabled section for priority 6 to 8 (Interrupt enabled section for priority 1 to 5 )
(3) Interrupt disabled section for priority 5 to 8 (Interrupt enabled section for priority 1 to 4 )
(4) Interrupt disabled section for priority 6 to 8 (Interrupt enabled section for priority 1 to 5 )
(5) Interrupt enabled section for all priorities
- When multiple DI (Disabling interrupt programs with specified priority or lower) instructions are executed and the specified interrupt disable priority is lower than the priority of the currently disabled interrupt, the priority of the currently disabled interrupt remains unchanged.
- Up to 16 DI instructions can be nested.
- The priority of the interrupt pointer ${ }^{* 1}$ can be set in parameter. (L] MELSEC iQ-R CPU Module User's Manual (Application))
*1 10 to I15, I50 to I1023
- The interrupt disabled priority currently set can be checked in SD758.
- If the DI (Disabling interrupt programs with specified priority or lower) instruction is executed in the interrupt program and the interrupt disabled priority is changed, the value in SD758 also changes.
- When the DI (Disabling interrupt programs) instruction, DI (Disabling interrupt programs with specified priority or lower) instruction, and El instruction are executed, the interrupt disabled sections will be as follows.
- When another DI (Disabling interrupt programs with specified priority or lower) instruction with a wider priority range is executed during execution of the DI (Disabling interrupt programs with specified priority or lower) instruction

(1) Interrupt enabled section for all priorities
(2) Interrupt disabled section for priority 3 to 8 (Interrupt enabled section for priority 1 and 2)
(3) Interrupt disabled section for priority 2 to 8 (Interrupt enabled section for priority 1 )
- When another DI (Disabling interrupt programs with specified priority or lower) instruction with a narrower priority range is executed during execution of the DI (Disabling interrupt programs with specified priority or lower) instruction

(3)
(1) Interrupt enabled section for all priorities
(2) Interrupt disabled section for priority 2 to 8 (Interrupt enabled section for priority 1)
(3) The priority of the disabled interrupt remains unchanged because the interrupt with priority 2 or lower is already disabled.
- When the DI (Disabling interrupt programs with specified priority or lower) instruction is executed in the interrupt program

(1) Interrupt enabled section for all priorities
(2) Interrupt disabled section for priority 3 to 8 (Interrupt enabled section for priority 1 and 2 )
(3) Interrupt disabled section for priority 2 to 8 (Interrupt enabled section for priority 1)
- When only the DI (Disabling interrupt programs) instruction is executed

(3)
(1) Interrupt enabled section for all priorities
(2) Interrupt disabled section for priority 1 to 8 (Interrupt disabled section for all priorities)
(3) Executing the El instruction only once enables interrupts with all priorities.
- When the DI (Disabling interrupt programs with specified priority or lower) instruction and the DI (Disabling interrupt programs) instruction are executed in this order

(1) Interrupt enabled section for all priorities
(2) Interrupt disabled section for priority 2 to 8 (Interrupt enabled section for priority 1)
(3) Interrupt disabled section for priority 1 to 8 (Interrupt disabled section for all priorities)
- When the DI (Disabling interrupt programs) instruction and the DI (Disabling interrupt programs with specified priority or lower) instruction are executed in this order

(1) Interrupt enabled section for all priorities
(2) Interrupt disabled section for priority 1 to 8 (Interrupt disabled section for all priorities)


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3362 H | More than 16 DI (Disabling interrupt programs) instructions and DI (Disabling interrupt programs with specified priority or lower) <br> instructions are nested. |
| 3405 H | The priority specified by (s) is out of the range, 1 to 8. |

## Interrupt program mask

IMASK
This instruction enables or disables the execution of the interrupt program with the specified interrupt pointer number.

| Ladder | ST |  |
| :--- | :--- | :--- |
|  | ENO:=IMASK(EN,s); |  |
| $\square-\square-\square$ | (s) |  |
|  |  |  |

FBD/LD


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| IMASK | Every scan |

## Setting data

■Description, range, data type

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (s) | Interrupt mask data or the start device where the interrupt <br> mask data is stored | - | 16-bit signed binary | ANY16_ARRAY <br> (Number of elements: <br> $16)$ |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathbf{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | J미미 | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | UपIGロ, Jपاप, U3EDI(H)GD | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (s) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |

## Processing details

- This instruction enables or disables the execution of the interrupt program with the interrupt pointer number specified by the 16 points of bit pattern starting from the device specified by (s).
- 1 (on): Enables the execution of the interrupt program.
- 0 (off): Disables the execution of the interrupt program.
- The interrupt pointer numbers correspond to individual bits as shown below.

(s)+1





$(s)+\left.7 \quad\left|127^{\prime}\right| 126^{\prime}\left|125^{\prime}\right| 124^{\prime}\left|123^{\prime}\right| 122^{\prime}\left|121^{\prime}\right| 120^{\prime}\left|119^{\prime}\right| 118^{\prime}\left|117^{\prime}\right| 116^{\prime}\left|115^{\prime}\right| 114\right|^{\prime}\left|113^{\prime}\right| 112$








- When the system is powered on or the CPU module is reset, the execution of interrupt programs IO to I31 and I44 to I1023 is enabled and the execution of interrupt programs I 32 to 143 is disabled.
- The states of the device range (s) to (s)+15 are stored in SD1400 to SD1463.


## Point $\rho$

The IMASK instruction can enable or disable the execution of interrupt pointers 10 to $I 255$ altogether.
To enable or disable the execution of interrupt pointers I256 to I1023, substitute the SIMASK instruction for the IMASK instruction in the program.
For details on the SIMASK instruction, refer to the following.
W Page 485 SIMASK

## Operation error

There is no operation error.

## Disabling／enabling the specified interrupt pointer

## SIMASK

This instruction enables or disables the execution of the interrupt program with the specified interrupt pointer number．

| Ladder | ST |
| :---: | :---: |
|  | ENO：＝SIMASK（EN，I，s）； |
| $\begin{array}{\|l\|l\|l\|} \hline \square-\square-\square & \text { (I) } & \text { (s) } \\ \hline \end{array}$ |  |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| SIMASK | Every scan |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （I） | Interrupt pointer number to be interrupt－enabled or－ <br> disabled | 10 to I1023 | Device name | POINTER＊1 $^{(\text {Execution status of the specified interrupt pointer }}$ |
| （s） | 0：Disabled <br> $1:$ Enabled | 16－bit unsigned binary | ANY16 |  |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

＊1 Only labels assigned to the device（I）can be used．
■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （I） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDl（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （I） | － | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ |
| （s） | － | － | $\bigcirc$ | － | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

－This instruction，according to the data specified by（s），enables or disables the execution of the interrupt program with the interrupt pointer number specified by（I）．
－When（s）is 1：The execution of the interrupt program is enabled．
－When（s）is 0 ：The execution of the interrupt program is disabled．
－When the system is powered on or the CPU module is reset，the execution of interrupt programs IO to I31 and I44 to I1023 is enabled and the execution of interrupt programs I 32 to I 43 is disabled．
－The execution status of the interrupt pointers are stored in SD1400 to SD1463．

The device（I）can be index modified．Using the SIMASK instruction specifying the index－modified device can enable or disable the execution of interrupt pointers IO to I1023．

## Operation error

| Error code (SD0) | Description |
| :--- | :--- |
| 3405 H | The interrupt pointer number specified by $(\mathrm{I})$ is out of the valid range. |
|  | The value in the device specified by $(\mathrm{s})$ is neither 0 (disabled) nor 1 (enabled). |

## Returning from the interrupt program

## IRET

This instruction indicates the end of the processing of an interrupt program.

| Ladder | ST |  |
| :--- | :--- | :--- |
|  |  | Not supported |
| FBD/LD |  |  |
| Not supported |  |  |
| EEXeCution Condition |  |  |
| Instruction | Execution condition |  |
| IRET | Every scan |  |

## Processing details

- This instruction indicates the end of the processing of the interrupt program specified by the interrupt pointer (I).
- The instruction returns control to the sequence program after execution.


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| $33 A 0 H$ | There is no pointer corresponding to the interrupt number. |
| 33A1H | After an interrupt occurs, the END, FEND, GOEND, or STOP instruction is executed before the IRET instruction. |
| 33A2H | The IRET instruction is executed before the interrupt program specified by the interrupt pointer (I) is executed. |
| 33A3H | The IRET instruction is executed in a fixed scan execution type program. |

## Resetting the watchdog timer

## WDT(P)

These instructions reset the watchdog timer.

| Ladder | ST |
| :--- | :--- |
|  | $-\square-\square$ ENO:=WDT(EN); <br>  ENO:=WDTP(EN); |

FBD/LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| WDT | - |
|  | $\boxed{ }$ |
| WDTP | - |

## Processing details

- These instructions reset the watchdog timer in a program.
- These instructions are used when the scan time exceeds the value set for the watchdog timer depending on the condition. If the scan time exceeds the value set for the watchdog timer every scan, change the watchdog timer value in parameter using the engineering tool.
- The time values required for $t 1$ from step 0 to the WDT $(P)$ instruction and $t 2$ from the WDT $(P)$ instruction to the END (FEND) instruction must not exceed the value set for the watchdog timer.

- The instruction can be used twice or more in a single scan, but it takes time to turn off the output when an error occurs.
- Executing the instruction does not clear the scan time value stored in the special register. For this reason, the scan time value in the special register may be greater than the value set for the watchdog timer in parameter.


## Operation error

There is no operation error.

## 7．4 Structure Creation Instructions

## Performing the FOR to NEXT instruction loop

## FOR，NEXT

These instructions execute the processing between the FOR instruction and the NEXT instruction（n）times．


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| FOR | Every scan |
| NEXT |  |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| $(\mathrm{n})$ | Number of FOR to NEXT instruction loops | 1 to 32767 | 16－bit signed binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3E미（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- After the FOR to NEXT instruction loop is executed unconditionally ( $n$ ) times, the step following the NEXT instruction is executed.
- Specify a value 1 to 32767 for ( n ). If a value -32768 to 0 is specified, the processing is performed regarding ( n ) as 1 .
- To not to execute the FOR to NEXT instruction loop, use the CJ or SCJ instruction to jump the instruction loop.
- Up to 16 FOR instructions can be nested.



## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3340 H | After execution of the FOR instruction, the END, FEND, or GOEND instruction is executed before the NEXT instruction. |
|  | The STOP instruction is provided within the FOR to NEXT instruction loop. |
| 3341 H | The NEXT instruction is executed before the FOR instruction. |
| 3361 H | More than 16 FOR instructions are nested. (The 17th instruction is executed.) |

## Point $P$

- To terminate the FOR to NEXT instruction loop being executed, use the BREAK instruction.
$\checkmark$ Page 491 BREAK (P)
- To perform pulse operations of an index-modified program within the FOR to NEXT instruction loop, use the EGP or EGF instruction. Note, however, that no rising edge or falling edge instruction can be used on the operation output side.

- The JMP instruction cannot be used to branch into the FOR to NEXT instruction loop from the outside.
- To create an easy-to-understand program, use a pair of instructions, the FOR and NEXT instructions, within a single program block.


## Forcibly terminating the FOR to NEXT instruction loop

## BREAK（P）

These instructions forcibly terminate the loop processing between the FOR and NEXT instructions，and pass the control to the specified pointer．


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| BREAK | - |
| BREAKP | $\boxed{ }$ |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （d） | Number of the device for storing the remaining number of <br> loops | - | 16－bit signed binary | ANY16 |
| （P） | Branch destination pointer number at the time of forced <br> termination of loop processing | - | Device name | POINTER |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （P） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J밈， U3EDl（H）GD | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |
| （P） | － | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ |

## Processing details

- These instructions forcibly terminate the loop processing between the FOR and NEXT instructions, and pass the control to the pointer specified by $(P)$. Only a pointer in the same program file can be specified for $(P)$. An operation error occurs if a pointer in another program file is specified.


If no BREAK instruction is executed, the program performs loop processing as many times as specified by the FOR instruction.

- The remaining number of FOR to NEXT instruction loops at the time of forced termination is stored in (d). The remaining number of loops includes the processing when the BREAK $(P)$ instruction is executed.
- The $\operatorname{BREAK}(P)$ instruction can be used only within the FOR to NEXT instruction loop.
- The BREAK $(P)$ instruction is valid only for one level of nesting. To forcibly terminate multiple levels of nesting, execute as many BREAK $(P)$ instructions as nesting levels.


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3342 H | The instruction is used outside the FOR to NEXT instruction loop. |
| 3380 H | The jump destination corresponding to the pointer specified by (P) does not exist. |
|  | A pointer in another program is specified in $(\mathrm{P})$. |

## Calling a subroutine program

## CALL（P）

These instructions execute the subroutine program of the specified pointer．

| Ladder | ST |
| :---: | :---: |
|  | Not supported |
| $-\square$ $(P)$ |  |
| $-\square$ $(P)$ $(\mathrm{s} 1) \ldots(\mathrm{s} 5)$ |  |

## FBD／LD



## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| CALL | - |
|  | $\boxed{ }$ |
| CALLP | $\boxed{ }$ |

## Setting data

## Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| $(\mathrm{P})$ | Start pointer number of subroutine program | - | Device name | POINTER |
| （s1）to（s5）${ }^{* 1}$ | Device number to be passed to the subroutine program as <br> an argument | -2147483648 to 2147483647 | Bit／16－bit signed <br> binary／32－bit signed <br> binary | ANY |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

＊1 Cannot be specified in FBD／LD．

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （P） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDl（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （P） | － | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ |
| （s1）to（s5） | O＊1 | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |

＊1 Devices other than F can be used．

## Processing details

- These instructions execute the subroutine program of the pointer specified by $(P)$. The instructions can execute the subroutine program specified by a pointer in the same program file or the subroutine program specified by a global pointer.

- When the function device (FX, FY, or FD) is used in the subroutine program, specify the device corresponding to the function device in (s1) to (s5). The following figure shows the data of the device areas specified by (s1) to (s5).

- Before execution of the subroutine program, bit data is transferred to FX and word data is transferred to FD.
- After execution of the subroutine program, the data in FY and FD are transferred to the corresponding devices.
- Function devices FX and FY are processed in units of bits. Function device FD is processed in units of 4 words. The size of data to be processed varies depending on the type of the device specified by an argument. The device specified as a function device should be secured for the data size. An error occurs if an appropriate data area cannot be secured.
- The following table lists the data sizes of each function device.

| Function device | Device to be used | Data size |
| :--- | :--- | :--- |
| FX, FY | Bit device | 1 point |
|  | Bit-specified word device | 1 bit |
| FD | Digit-specified bit device ${ }^{* 2}$ | 4 words $^{* 3}$ |
|  | Word device | 4 words $^{* 3}$ |

*2 An error does not occur even if the device number specified by ( $\mathbf{s} 1$ ) to ( s 5 ) is not a multiple of 16.
*3 The data size varies depending on the instruction used.

## Ex.

Data in the specified devices

(1) MO occupied (The data is transferred to FXO.)
(2) D0 to D3 occupied (The data is transferred to FD1.)
(3) D30 to D33 occupied (The data is transferred to FD2.)

- The CALL(P) instruction can use (s1) to (s5).
- The number of function devices used in the subroutine program must be identical to the number of arguments of the CALL(P) instruction. Also, the types of function devices and CALL(P) instruction arguments must be the same.
- Set the device numbers in the argument of the $\operatorname{CALL}(P)$ instruction so that they do not overlap. If they overlap, normal operation cannot be performed.
- If the timer or counter is specified as a device in the argument of the $\operatorname{CALL}(\mathrm{P})$ instruction, only the current value is transmitted/received.
- Do not use any device used in the argument of the $\operatorname{CALL}(P)$ instruction in the subroutine program. If used, normal operation cannot be performed.


## Ex.

Wrong operation: While D0 is specified for FD0 in the subroutine program, D1 is used in the subroutine program.


(1) The operation result of the subroutine program is stored.
(2) These values are replaced with the function device values.
(3) The value of D1 is not replaced with the function device value.

Ex.
Correct operation: While D0 is specified for FD0 in the subroutine program, D4 is used in the subroutine program.

(1) The operation result of the subroutine program is stored.
(2) These values are replaced with the function device values.

- Up to $16 \operatorname{CALL}(P)$ instructions can be nested. Note that the $16-l e v e l$ nesting is the total of the $\operatorname{CALL}(\mathrm{P}), \operatorname{FACLL}(\mathrm{P})$, ECALL(P), EFCALL(P), and XCALL instructions.

- Devices which are turned on in the subroutine program hold the on state even when the subroutine program is not executed. The on state can be changed to off by executing the FCALL(P) instruction.


## Precautions

- An FBD/LD program cannot be created as a subroutine program.
- FBD/LD does not support the execution of a subroutine program with an argument.


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2820 H | The device specified in an argument from (s1) to (s5) cannot be secured for the data size. |
| 3360 H | More than 16 CALL(P) instructions are nested. (The 17th instruction is executed.) |
| 3380 H | The subroutine program corresponding to the pointer specified by (P) does not exist. |
| 3381 H | After execution of the CALL(P) instruction, the END, FEND, GOEND, or STOP instruction is executed before the RET instruction. |
| 3382 H | The RET instruction is executed before the CALL(P) instruction. |

## Returning from the subroutine program called

## RET

This instruction indicates the end of a subroutine program.

| Ladder | ST |  |
| :--- | :--- | :--- |
|  |  | Not supported |
| FBD/LD |  |  |
| Not supported |  |  |
| EXXecution condition |  |  |
| Instruction | Execution condition |  |
| RET | Every scan |  |

## Processing details

- This instruction indicates the end of a subroutine program.
- When the instruction is executed, the program returns to the next step where the $\operatorname{CALL}(\mathrm{P}), \operatorname{FCALL}(\mathrm{P}), \operatorname{ECALL}(\mathrm{P})$, $\operatorname{EFCALL}(\mathrm{P})$, or XCALL instruction that called the subroutine program is executed.



## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3381 H | After execution of the CALL(P), FCALL(P), ECALL(P), EFCALL(P), or XCALL instruction, the END, FEND, GOEND, or STOP instruction <br> is executed before the RET instruction. |
| 3382 H | The RET instruction is executed before the CALL(P), FCALL(P), ECALL(P), EFCALL(P), or XCALL instruction. |

Calling a subroutine program and turning the output off

## FCALL（P）

These instructions perform non－execution processing of the subroutine program of the specified pointer．


## FBD／LD



## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| FCALL | $\square$ |
| FCALLP | $\square$ |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| $(\mathrm{P})$ | Start pointer number of subroutine program | - | Device name | POINTER |
| $(\mathrm{s} 1) \cdots(\mathrm{s} 5)^{* 1}$ | Device number to be passed to the subroutine program as <br> an argument | -2147483648 to 2147483647 | Bit／16－bit signed <br> binary／32－bit signed <br> binary | ANY |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

＊1 Cannot be specified in FBD／LD．

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （P） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3E미（H）Gㅁ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （P） | － | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ |
| （s1）$\cdots$（s5） | O＊1 | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |

＊1 Devices other than F can be used．

## Processing details

- When the FCALL $(P)$ instruction is executed, this instruction executes non-execution processing ${ }^{* 2}$ of the subroutine program of the pointer $(P)$. The $F C A L L(P)$ instruction can disable the execution of the subroutine program specified by a pointer in the same program file or the subroutine program specified by a global pointer.
*2 Non-execution processing is the same as the processing performed by each coil instruction with the condition contact set to off.

(1) Non-execution processing is performed when the command of the FCALL(P) instruction changes from on to off.
- The operation results of individual coil instructions after the end of non-execution processing are as follows regardless of on/off of the condition contact.

| Device used for operation | Operation result (device status) |
| :--- | :--- |
| High-speed timer, low-speed timer | Set to 0 |
| High-speed retentive timer, low-speed retentive timer, counter | Maintains the current status. |
| Device in OUT instruction | Forcibly turned off. |
| Device in the SET, RST, SFT(P), or basic/application instruction | Maintains the current status. |
| PLS or pulse instruction (ロP) | Performs the same processing as when the condition contact <br> is off. |

- The FCALL(P) instruction is used in combination with the CALL(P) instruction. If the FCALL(P) instruction is not used in combination with the $\operatorname{CALL}(P)$ instruction, non-execution processing of the subroutine program is not performed even if the execution command is turned off, and therefore the output status of each coil instruction is retained.
- When the execution command is turned off, non-execution processing of the subroutine program is performed, enabling the OUT instruction and PLS instruction (including pulse conversion instructions) to be forcibly turned off.


## Ex.

When the $\operatorname{FCALL}(P)$ instruction is used

en the FCALL(P) instruction is not used


- When the subroutine program uses function devices (FX, FY, FD), specify the devices corresponding to the function devices in ( s 1 ) to ( s 5 ). The following figure the contents of the devices specified by ( s 1 ) to ( s 5 ).

- Before execution of the subroutine program, bit data is transferred to FX and word data is transferred to FD.
- After execution of the subroutine program, the contents of FY and FD are transferred to the corresponding devices.
- Function devices FX and FY are processed in units of bits. Function device FD is processed in units of 4 words. The size of data to be processed varies depending on the type of the device specified by an argument. The device specified as a function device should be secured for the data size. An error occurs if it cannot be secured for the data size.
- The following table lists the data sizes of individual types of function devices.

| Function device | Device | Data size |
| :--- | :--- | :--- |
|  | Bit device | 1 point |
|  | When a bit-specified word device is used | 1 bit |
| FD | When the bit device digit is specified ${ }^{* 3}$ | 4 words $^{* 4}$ |
|  | Word device | 4 words |

*3 An error does not occur even if the device number specified by ( s 1 ) to ( s 5 ) is not a multiple of 16 in bit device digit specification mode.
*4 The upper two words of FD are 0.

## Ex.

Content of specified device

(1) MO occupied (transferred to FXO)
(2) D0 to D3 occupied (transferred to FD1)
(3) D30 to D33 occupied (transferred to FD2)

- The FCALL(P) instruction can use (s1) to (s5).
- Up to 16 FCALL(P) instructions can be nested. Note that the 16 -level nesting is the total of the $\operatorname{CALL}(\mathrm{P}), \operatorname{FCALL}(\mathrm{P})$, ECALL(P), EFCALL(P), and XCALL instructions.



## Precautions

- An FBD/LD program cannot be created as a subroutine program.
- FBD/LD does not support the execution of a subroutine program with an argument.


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2820 H | The device specified in an argument from (s1) to (s5) cannot be secured for the data size. |
| 3360 H | More than 16 FCALL(P) instructions are nested. (The 17th instruction is executed.) |
| 3380 H | The subroutine program corresponding to the pointer specified by the FCALL(P) instruction does not exist. |
| 3381 H | After execution of the FCALL(P) instruction, the END, FEND, GOEND, or STOP instruction is executed before the RET instruction. |
| 3382 H | The RET instruction is executed before the FCALL(P) instruction. |

Calling a subroutine program in the specified program file

## ECALL（P）

These instructions execute the subroutine program corresponding the specified pointer of the specified program file name．


FBD／LD


## ■xecution condition

| Instruction | Execution condition |
| :--- | :--- |
| ECALL | - |
|  | $\boxed{ }$ |
| ECALLP | $\boxed{ }$ |

Setting data
■Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （File name） | Program file name to be called | - | Unicode string | ANYSTRING＿DOUB <br> LE |
| （P） | Start pointer number of subroutine program | - | Device name | POINTER |
| （s1）…（s5）${ }^{* 1}$ | Device number to be passed to the subroutine program as <br> an argument | -2147483648 to 2147483647 | Bit／16－bit signed <br> binary／32－bit signed <br> binary | ANY |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

＊1 Cannot be specified in FBD／LD．

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （P） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロID， U3EDl（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （File name） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － |
| （P） | － | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ |
| （s1）$\cdots$（s5） | O＊1 | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |

＊1 Any value other than F can be used．

## Processing details

- When the ECALL $(P)$ instruction is executed, these instructions execute the subroutine program corresponding to the pointer specified by $(P)$ of the specified program file name. The ECALL $(P)$ instructions can also call the subroutine program using the local pointer of another program file.
[File name: MAIN] Main routine program

- Only a program file stored in program memory can be specified for the file name.
- Extension ".PRG" need not be specified for the file name. (Only .PRG files can be processed by these instructions.)
- When the subroutine program uses function devices (FX, FY, FD), specify the devices corresponding to the function devices in (s1) to (s5). The following figure the contents of the devices specified by (s1) to (s5).

- Before execution of the subroutine program, bit data is transferred to FX and word data is transferred to FD.
- After execution of the subroutine program, the contents of FY and FD are transferred to the corresponding devices.
- Function devices FX and FY are processed in units of bits. Function device FD is processed in units of 4 words. The size of data to be processed varies depending on the type of the device specified by an argument. The device specified as a function device should be secured for the data size. An error occurs if it cannot be secured for the data size.
- The following table lists the data sizes of individual types of function devices.

| Function device | Device | Data size |
| :--- | :--- | :--- |
| FX, FY | Bit device | 1 point |
|  | When a bit-specified word device is used | 1 bit |
| FD | When the bit device digit is specified ${ }^{* 2}$ | 4 words $^{* 3}$ |
|  | Word device | 4 words $^{* 3}$ |

*2 An error does not occur even if the device number specified by ( s 1 ) to ( s 5 ) is not a multiple of 16 in bit device digit specification mode.
*3 The data size varies depending on the instruction used.

## Ex.

Content of specified device

(1) MO occupied (transferred to FXO)
(2) D0 to D3 occupied (transferred to FD1)
(3) D30 to D33 occupied (transferred to FD2)

- The ECALL(P) instruction can use (s1) to (s5).
- Any device used in the argument of the $\operatorname{ECALL}(P)$ instruction must not used in the subroutine program. Otherwise, normal operation cannot be performed.


## Ex.

Wrong operation: While D0 is specified for FD0 in the subroutine program, D1 is used in the subroutine program.

(1) The execution result of the subroutine program is stored.
(2) These values are replaced by the function device values.
(3) The value of D1 is not replaced by the function device value.

Ex.
Correct operation: While D0 is specified for FD0 in the subroutine program, D4 is used in the subroutine program.

(1) The execution result of the subroutine program is stored.
(2) These values are replaced by the function device values.

- The device numbers specified by the ECALL(P) instruction arguments must not be overlapping. If they are overlapping, normal operation cannot be performed.
- Up to $16 \operatorname{ECALL}(P)$ instructions can be nested. Note that the 16 -level nesting is the total of the $\operatorname{CALL}(\mathrm{P}), \operatorname{FCALL}(\mathrm{P})$, ECALL $(P)$, EFCALL(P), and XCALL instructions.

- Devices which are turned on in the subroutine program are retained even while the subroutine program is not executed. Devices which are turned on during execution of the subroutine program can be turned off by the EFCALL $(\mathrm{P})$ instruction.


## Precautions

- An FBD/LD program cannot be created as a subroutine program.
- FBD/LD does not support the execution of a subroutine program with an argument.


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2820 H | The device specified in an argument from (s1) to (s5) cannot be secured for the data size. |
| 2840 H | The file specified by (file name) does not exist. |
| 2841 H | The file specified by (file name) cannot be executed. |
| 3360 H | More than 16 ECALL(P) instructions are nested. (The 17th instruction is executed.) |
| 3380 H | The subroutine program corresponding to the pointer specified by (P) does not exist. |
| 3381 H | After execution of the ECALL(P) instruction, the END, FEND, GOEND, or STOP instruction is executed before the RET instruction. |
| 3382 H | The RET instruction is executed before the ECALL(P) instruction. |

Calling a subroutine program in the specified program file and turning the output off

## EFCALL（P）

These instructions perform non－execution processing of the subroutine program corresponding the specified pointer of the specified program file name．



Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| EFCALL | $\square$ |
| EFCALLP | $\square$ |

## Setting data

## Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （File name） | Program file name subject to non－execution processing | - | Unicode string | ANYSTRING＿DOUB <br> LE |
| （P） | Start pointer number of subroutine program | - | Device name | POINTER |
| （s1）to（s5）${ }^{* 1}$ | Device number to be passed to the subroutine program as <br> an argument | -2147483648 to 2147483647 | Bit／16－bit signed <br> binary／32－bit signed <br> binary | ANY |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

＊1 Cannot be specified in FBD／LD．

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （P） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JपIロ， U3EDI（H）GD | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （File name） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － |
| （P） | － | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ |
| （s1）．．．s5） | $0^{* 1}$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |

＊1 Any value other than F can be used．

## Processing details

- When the EFCALL(P) instruction is executed, these instructions perform non-execution processing ${ }^{*}$ 。of the subroutine program of the pointer specified by $(P)$. The EFCALL( P ) instructions can also call the subroutine program using the local pointer of another program file.
*2 Non-execution processing is the same as the processing performed by each coil instruction with the condition contact set to off.

- The operation results of individual coil instructions after the end of non-execution processing are as follows regardless of on/off of the condition contact.

| Device used for operation | Operation result (device status) |
| :--- | :--- |
| High-speed timer, low-speed timer | Set to 0 |
| High-speed retentive timer, low-speed retentive timer, counter | Maintains the current status. |
| Device in OUT instruction | Forcibly turned off. |
| Device in the SET, RST, SFT(P), or basic/application instruction | Maintains the current status. |
| PLS or pulse instruction (DP) | Performs the same processing as when the condition contact <br> is off. |

- The EFCALL(P) instruction is used in combination with the ECALL(P) instruction. If the EFCALL(P) instruction is not used in combination with the ECALL $(P)$ instruction, non-execution processing of the subroutine program is not performed even if the execution command is turned off, and therefore the output status of each coil instruction is retained.
- When the execution command is turned off, non-execution processing of the subroutine program is performed, enabling the OUT instruction and PLS instruction (including pulse conversion instructions) to be forcibly turned off.


## Ex.

When the EFCALL (P) instruction is used


Ex.
When the EFCALL ( P ) instruction is not used


- Only a program file stored in program memory can be specified for the file name.
- Extension ".PRG" need not be specified for the file name. (Only .PRG files can be processed by these instructions.)
- When the subroutine program uses function devices (FX, FY, FD), specify the devices corresponding to the function devices in (s1) to (s5). The following figure the contents of the devices specified by (s1) to (s5).

- Before execution of the subroutine program, bit data is transferred to FX and word data is transferred to FD.
- After execution of the subroutine program, the contents of FY and FD are transferred to the corresponding devices.
- Function devices FX and FY are processed in units of bits. Function device FD is processed in units of 4 words. The size of data to be processed varies depending on the type of the device specified by an argument. The device specified as a function device should be secured for the data size. An error occurs if it cannot be secured for the data size.
- The following table lists the data sizes of individual types of function devices.

| Function device | Device | Data size |
| :--- | :--- | :--- |
|  | Bit device | 1 point |
|  | When a bit-specified word device is used | 1 bit |
| FD | When the bit device digit is specified ${ }^{* 3}$ | 4 words $^{* 4}$ |
|  | Word device | 4 words |

*3 An error does not occur even if the device number specified by ( s 1 ) to ( s 5 ) is not a multiple of 16 in bit device digit specification mode.
*4 The upper two words of FD are 0.

## Ex.

Content of specified device

(1) M0 occupied (transferred to FX0)
(2) D0 to D3 occupied (transferred to FD1)
(3) D30 to D33 occupied (transferred to FD2)

- The EFCALL(P) instruction can use (s1) to (s5).
- The number of function devices used in the subroutine program must be identical to the number of arguments of the EFCALL(P) instruction. Also, the types of function devices and EFCALL(P) instruction arguments must be the same.
- Up to $16 \operatorname{EFCALL}(P)$ instructions can be nested. Note that the 16 -level nesting is the total of the CALL(P), FCALL(P), ECALL(P), EFCALL(P), and XCALL instructions.



## Precautions

- An FBD/LD program cannot be created as a subroutine program.
- FBD/LD does not support the execution of a subroutine program with an argument.


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2820 H | The device specified in an argument from (s1) to (s5) cannot be secured for the data size. |
| 2840 H | The file specified by (file name) does not exist. |
| 2841 H | The file specified by (file name) cannot be executed. |
| 3360 H | More than 16 EFCALL(P) instructions are nested. (The 17th instruction is executed.) |
| 3380 H | The subroutine program corresponding to the pointer specified by (P) does not exist. |
| 3381 H | After execution of the EFCALL(P) instruction, the END, FEND, GOEND, or STOP instruction is executed before the RET instruction. |
| 3382 H | The RET instruction is executed before the EFCALL(P) instruction. |

## Calling a subroutine program

## XCALL

This instruction performs execution or non－execution processing of a subroutine program．When the condition is satisfied，the instruction triggers CALL for the subroutine．When the condition is broken，it triggers FCALL．


## FBD／LD



## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| XCALL | $-\square$ |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| $(\mathrm{P})$ | Start pointer number of subroutine program | - | Device name | POINTER |
| $(\mathrm{s} 1) \cdots(\mathrm{s} 5)^{* 1}$ | Device number to be passed to the subroutine program as <br> an argument | -2147483648 to 2147483647 | Bit／16－bit signed <br> binary／32－bit signed <br> binary | ANY |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

＊1 Cannot be specified in FBD／LD．

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （P） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，Jपاロ， U3EDI（H）GD | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （P） | － | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ |
| （s1）．．．s5） | O＊1 | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |

＊1 Any value other than F can be used．

## Processing details

- This instruction performs execution or non-execution processing of a subroutine program.
- For execution of the subroutine program, it operates each coil instruction according to the on/off status of condition contacts.
- For non-execution processing of the subroutine program, it operates each coil instruction in the same way as when condition contacts are off.
- The operation results of individual coil instructions after the end of non-execution processing are as follows regardless of on/off of the condition contact.

| Device used for operation | Operation result (device status) |
| :--- | :--- |
| High-speed timer, low-speed timer | Set to 0 |
| High-speed retentive timer, low-speed retentive timer, counter | Maintains the current status. |
| Device in OUT instruction | Forcibly turned off. |
| Device in the SET, RST, SFT(P), or basic/application instruction | Maintains the current status. |
| PLS or pulse instruction (DP) | Performs the same processing as when the condition contact is <br> off. |

- The following figure shows the operation of the XCALL instruction.

(1) On the rising edge of X 0 (off $\rightarrow o n$ ): Executes the subroutine program specified by P1.
(2) During XO is on: Executes the subroutine program specified by P1. ("During X0 is on" does not include the rising edge of XO .)
(3) On the falling edge of $X 0$ (on $\rightarrow$ off): Performs non-execution processing of a subroutine program specified by P1.
- When the subroutine program uses function devices (FX, FY, FD), specify the devices corresponding to the function devices in ( s 1 ) to ( s 5 ). The following figure the contents of the devices specified by ( s 1 ) to ( s 5 ).

- Before execution of the subroutine program, bit data is transferred to FX and word data is transferred to FD.
- After execution of the subroutine program, the contents of FY and FD are transferred to the corresponding devices.
- Function devices FX and FY are processed in units of bits. Function device FD is processed in units of 4 words. The size of data to be processed varies depending on the type of the device specified by an argument. The device specified as a function device should be secured for the data size. An error occurs if it cannot be secured for the data size.
- The following table lists the data sizes of individual types of function devices.

| Function device | Device | Data size |
| :--- | :--- | :--- |
|  | Bit device | 1 point |
|  | When a bit is specified for a word device | 1 bit |
| FD | When the bit device digit is specified ${ }^{* 2}$ | 4 words $^{* 3}$ |
|  | Word device | 4 words $^{* 3}$ |

*2 An error does not occur even if the device number specified by ( s 1 ) to ( s 5 ) is not a multiple of 16 in bit device digit specification mode.
*3 The data size varies depending on the instruction used.

## Ex.

Content of specified device

(1) M0 occupied (transferred to FXO)
(2) D0 to D3 occupied (transferred to FD1)
(3) D30 to D33 occupied (transferred to FD2)

- The XCALL instruction can use ( s 1 ) to ( s 5 ).
- The number of function devices used in the subroutine program must be identical to the number of arguments of the XCALL instruction. Also, the types of function devices and XCALL instruction arguments must be the same.
- The device numbers specified by the XCALL instruction arguments must not be overlapping. If they are overlapping, normal operation cannot be performed.
- Up to 16 XCALL instructions can be nested. Note that the 16 -level nesting is the total of the $\operatorname{CALL}(\mathrm{P}), \operatorname{FCALL}(\mathrm{P})$, ECALL(P), EFCALL(P), and XCALL instructions.

- Any device used in the argument of the XCALL instruction must not used in the subroutine program. Otherwise, normal operation cannot be performed.


## Ex.

Wrong operation: While D0 is specified for FD0 in the subroutine program, D1 is used in the subroutine program.

(1) The execution result of the subroutine program is stored.
(2) These values are replaced by the function device values.
(3) The value of D1 is not replaced by the function device value.

Ex.
Correct operation: While D0 is specified for FD0 in the subroutine program, D4 is used in the subroutine program.

(1) The execution result of the subroutine program is stored.
(2) These values are replaced by the function device values.

## Precautions

- An FBD/LD program cannot be created as a subroutine program.
- FBD/LD does not support the execution of a subroutine program with an argument.


## Operation error

| Error code (SD0) | Description |
| :--- | :--- |
| 2820 H | The device specified in an argument from (s1) to (s5) cannot be secured for the data size. |
| 3360 H | More than 16 XCALL instructions are nested. (The 17th instruction is executed.) |
| 3380 H | The subroutine program corresponding to the pointer specified by (P) does not exist. |
| 3381 H | After execution of the XCALL instruction, the END, FEND, GOEND, or STOP instruction is executed before the RET instruction. |
| 3382 H | The RET instruction is executed before the XCALL instruction. |

### 7.5 Data Table Operation Instructions

## Reading the oldest data from the data table

## FIFR(P)

These instructions store the data first stored in the table in the specified device.

| Ladder | ST |
| :---: | :---: |
| $-\square-\square$ (s) (d) | $\begin{aligned} & \text { ENO:=FIFR(EN,s,d); } \\ & \text { ENO:=FIFRP(EN,s,d); } \end{aligned}$ |

FBD/LD


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| FIFR | - |
|  | $\boxed{ }$ |
| FIFRP | - |

Setting data
Description, range, data type

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| $(\mathrm{s})$ | Device for storing the data read from the table | - | 16-bit signed binary | ANY16 |
| $(\mathrm{d})$ | Start device of table | - | Word | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | UपIGㅁ, J밈, U3ED(H)GD | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (s) | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - | $\bigcirc$ | - | - | - | - |
| (d) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |

## Processing details

- These instructions store the first-in data in the device specified by (d)+1 in the table specified by (d) in the device specified by (s). After execution of the FIFR(P) instruction, the data in the data table is moved forward one by one.
(d)

| 3 |
| :---: |
| 12332 |
| 4321 |
| 0 |
| 0 |

- An interlock mechanism should be used to prevent the FIFR(P) instruction from being executed when the value stored in the device specified by (d) is 0 .
- A number from 0 to 65535 is stored in the number of data (d).


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3405 H | The FIFR(P) instruction is executed when the value in the device specified by (d) is 0. |

## Reading the newest data from the data table

## FPOP（P）

These instructions store the data last stored in the table in the specified device．

| Ladder | ST |
| :---: | :---: |
| $-\square-\square$ （s） （d） | $\begin{aligned} & \text { ENO:=FPOP(EN,s,d); } \\ & \text { ENO:=FPOPP(EN,s,d); } \end{aligned}$ |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| FPOP | - |
|  | $\boxed{~ F P O P P ~}$ |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| $(\mathrm{s})$ | Device for storing the data read from the table | - | 16－bit signed binary | ANY16 |
| （d） | Start device of table | - | Word | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3E미（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions store the data last stored in the table in the device specified by（d）in the device specified by（s）．After execution of the $\operatorname{FPOP}(P)$ instruction，the device in which the data read by the instruction has been stored is cleared to 0 ．

－An interlock mechanism should be used to prevent the $\operatorname{FPOP}(\mathrm{P})$ instruction from being executed when the value stored in the device specified by（d）is 0 ．
－A number from 0 to 65535 is stored in the number of data（d）．

## Operation error

| Error code (SD0) | Description |
| :--- | :--- |
| 3405 H | The FPOP(P) instruction is executed when the value in the device specified by (d) is 0. |

## Writing data to the data table

## FIFW（P）

These instructions transfer 16－bit binary data to the specified data table．

| Ladder | ST |
| :---: | :---: |
| $\square-\square-\square$ （s） （d） | $\begin{aligned} & \text { ENO:=FIFW(EN,s,d); } \\ & \text { ENO:=FIFWP(EN,s,d); } \end{aligned}$ |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| FIFW | - |
| FIFWP | $\boxed{ }$ |

## Setting data

Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Data to be written to the table，or the device number where <br> the write data is stored | - | 16－bit signed binary | ANY16 |
| （d） | Start device of table | - | Word | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## －Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3E미（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions store the 16-bit binary data in the device specified by (s) in the data table in the device specified by (d). The number of data stored in the table is stored in (d), and the data in the device specified by (s) is stored in order in the device specified by (d)+1 and later.
(d)


(s) 4321
- When the FIFW(P) is executed for the first time, the value in the device specified by (d) must be cleared.
- A care must be taken for the data table range because data is stored sequentially in the device specified by ( d ) +1 and later.
- A number from 0 to 65535 is stored in the number of data (d).


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3405 H | The FIFW $(\mathrm{P})$ instruction is executed when the value in the device specified by (d) is FFFFH. |

## Inserting data to the data table

## FINS（P）

These instructions insert 16－bit binary data to the（n）th position in the specified data table．


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| FINS | - |
|  | $\boxed{Y}$ |
| FINSP | - |

## Setting data

Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| $(\mathrm{s})$ | Device where the insertion data is stored | - | 16－bit signed binary | ANY16 |
| $(\mathrm{d})$ | Start device of table | - | Word | ANY16 |
| $(\mathrm{n})$ | Insertion position in the table | 0 to 65535 | 16－bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）GD | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- These instructions store the 16 -bit binary data in the device specified by (s) in the (n)th position in the data table in the device specified by (d). After execution of the FINS(P) instruction, the data after the ( n )th position in the data table is moved down one by one.

(s) 4444
When $(\mathrm{n})=2$, data is inserted to the device specified by $(\mathrm{d})+2$.
- A number from 0 to 65535 is stored in the number of data (d).


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3405 H | The FINS(P) instruction is executed when the value specified by $(\mathrm{n})$ is 0. |
|  | The FINS(P) instruction is executed when the value in the device specified by $(\mathrm{d})$ is FFFFH. |

## Deleting data from data table

## FDEL（P）

These instructions delete the data at the（ n ）th position in the data table．


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| FDEL | - |
|  | $\boxed{Y}$ |
| FDELP | - |

## Setting data

Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| $(\mathrm{s})$ | Device where the data to be deleted is stored | - | 16－bit signed binary | ANY16 |
| $(\mathrm{d})$ | Start device of table | - | Word | ANY16 |
| $(\mathrm{n})$ | Deletion position in the table | 0 to 65535 | 16－bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）GD | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- These instructions delete the ( n )th data in the data table in the device specified by (d) and store it in the device specified by (s). After execution of the FDEL $(P)$ instruction, the data at the $(n)+1$ th position and later in the data table is moved forward one by one.

- A number from 0 to 65535 is stored in the number of data (d).
Operation error

| Error code (SD0) | Description |
| :--- | :--- |
| 3405 H | The $\operatorname{FDEL}(\mathrm{P})$ instruction is executed when the value specified by $(\mathrm{n})$ is 0. |
|  | The $\operatorname{FDEL}(\mathrm{P})$ instruction is executed when the value in the device specified by (d) is 0. |

### 7.6 Reading/Writing Data Instructions

## Data memory read/write

The data write instruction is an instruction to write arbitrary device data to data memory.
Writing the fixed values used for operation and operation results to data memory can prevent data loss when the battery is low.

The data that has been written to data memory can be read at any time using the data memory read instruction.

## Execution method

Use the SP.DEVST instruction to write device data to data memory.
Use the $S(P)$.DEVLD instruction to read device data from data memory to any specified device.

## Setting method

When the SP.DEVST and S(P).DEVLD instructions are used, a device data storage file must be set up in advance.
[CPU parameter] $\Rightarrow$ "File setting" $\Rightarrow$ "Device data storage file"


If data memory does not have enough free space for creating a device data storage file, an error (error code: 21A1H) occurs. When the CPU module is switched from STOP to RUN, it is checked to see whether the actual device data storage file matches the parameter setting. If they do not match, an error (error code: 21 AOH ) occurs.

## Read/write of the specified file

When the SP.FREAD or SP.FWRITE instruction is terminated with an error, an error code is stored in the completion status. The following table lists the error codes stored in the completion status.

| Error code | Error definition and cause | Action |
| :--- | :--- | :--- |
| 8000 H | - SM606 (SD memory card forced disable instruction) is on. | - If SM606 is on, turn it off to cancel the SD memory card forced <br> disable status. |
|  | • No SD memory card is inserted. | • Insert an SD memory card. |

## Reading 16-bit data from the data memory

## S(P).DEVLD

These instructions read data from the device data storage file in data memory.

| Ladder | ST |
| :---: | :---: |
| ■-二- $]$ (s) (d) (n) | $\begin{aligned} & \text { ENO:=S_DEVLD(EN,s,n,d); } \\ & \text { ENO:=SP_DEVLD(EN,s,n,d); } \end{aligned}$ |

FBD/LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S.DEVLD | - |
|  | $\boxed{Z}$ |
| SP.DEVLD | - |

## Setting data

Descriptions, ranges, and data types

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (s) | Read offset of device data storage file (specified in units of <br> 16-bit words) | 0 to 524287 | 32-bit unsigned binary | ANY32 |
| (d) | Device for storing the data that has been read | - | Word | ANY16 |
| (n) | Number of read points | 1 to 65535 | 16-bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | J미민 | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | U밈, J밈, U3EDI(H)G口 | Z | LT, LST, LC | LZ |  | K, H | E | \$ |  |
| (s) | - | - | $\bigcirc$ | - | - | $\bigcirc$ | - | $\bigcirc$ | $\bigcirc$ | - | - | - |
| (d) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| ( n ) | $\bigcirc$ | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | $\bigcirc$ | - | - | - |

## Processing details

- These instructions read device data by the number of points specified by ( n ) from the read offset specified by ( s ) in the device data storage file in data memory, and store it in the device specified by (d). (s) indicates the offset from the start of the device data storage file and can be specified by word offsets (incremented by 1 every 16 bits).

- When the $S(P)$.DEVLD instruction is used, a device data storage file must be set up in advance. (以 Page 526 Setting method)


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2840 H | The device data storage file is not set in parameter. |
| 3405 H | The value specified by $(\mathrm{n})$ is 0. |

## Writing 16－bit data to the data memory

## SP．DEVST

This instruction writes the specified number of points of data to the device data storage file in data memory．


FBD／LD


■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| SP．DEVST | - |
|  | - |

## Setting data

Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Write offset of device data storage file（specified in units of <br> 16－bit words） | 0 to 524287 | 32－bit unsigned binary | ANY32 |
| （s2） | Start device to which data is to be written | - | Word | ANY16 |
| （n） | Number of write points | 1 to 65535 | 16－bit unsigned binary | ANY16 |
| （d） | （d）：Completion device，（d）+1 ：Error completion device | - | Bit | ANYBIT＿ARRAY <br> （Number of elements： <br> 2） |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions retrieves the specified number of points of data specified by ( n ) from the device specified by ( s 2 ) and write it to the write offset in the device specified by (s1) in the device data storage file in data memory. (s1) indicates the offset from the start of the device data storage file and can be specified by word offsets (incremented by 1 every 16 bits).

- The completion device specified by (d) automatically turns on upon execution of the END instruction following the detection of processing completion of the SP.DEVST instruction and turns off upon execution of the END instruction in the next scan, so it is used as the execution complete flag of the SP.DEVST instruction.
- If the SP.DEVST instruction completes with an error, the error completion device specified by (d)+1 turns on or off at the same time as the completion device specified by (d). Therefore, the device is used as the error completion flag of the SP.DEVST instruction.
- SM753 (file accessed) turns on while the SP.DEVST instruction is executed. If SM753 has already been on, the SP.DEVST instruction cannot be executed. (If executed, no processing is performed.)
- If an error is detected during execution of the SP.DEVST instruction, the completion device (d), error completion device (d) +1 , and SM753 do not turn on.
- When the $S(P)$.DEVST instruction is used, a device data storage file must be set up in advance. ( $\mathfrak{F}$ Page 526 Setting method)


## Precautions

- The value written to data memory is the one at execution of the SP.DEVST instruction.
- Execution of the SP.DEVST instruction increases SD634 and SD635. If the data memory write count index exceeds 100,000, an error occurs with error code 1080H.
- To prevent the data memory write count from being increased by careless instruction execution, SD771 can be set to limit the write count per day. If the specified write count (default: 36) is exceeded, an error occurs with error code 3421H. The number of executions of the instruction to write to data memory per day is initialized to 0 at the following timing.
- When power off $\rightarrow$ on, or when reset $\rightarrow$ reset canceled
- The date (year, month, day) in clock data is changed by time advancement.
- CPU module internal clock data (year, month, day) is changed by the clock data change function.
- Data is written to the device data storage file when the END instruction is executed. Data is written to the device data storage file when the END instruction is executed immediately after the SP.DEVST instruction is executed. Thus, depending on the number of write points, writing to the device data storage file may involve multiple scans. Check the completion device to see whether the writing is completed.


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2840 H | The device data storage file is not set in parameter. |
| 3405 H | The value specified by ( n ) is 0. |
| 3421 H | When the SP.DEVST instruction is executed, the write count of the day exceeds the value specified in SD771. |
|  | When the SP.DEVST instruction is executed, a value out of the range (1 to 32767 ) is set in SD771. |

## Reading 16－bit data from the specified file

## SP．FREAD

This instruction reads device data from the specified file on the SD memory card．


FBD／LD

| ■－二－】 |  |
| :---: | :---: |
| EN | ENO |
| U | d1 |
| s1 | d2 |
| s2 | d3 |

■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| SP．FREAD | - |

## Setting data

## Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （U） | Dummy | - | Device name | ANY16 |
| （s1） | Drive specification | 2 （fixed）${ }^{* 1}$ | ANY16 |  |
| （d1） | Start device where the control data is stored | Refer to the control data． | Word | ANY16＿ARRAY <br> （Number of elements： <br> 8） |
| （s2） | Start device where the file name is stored | - | Unicode string | ANYSTRING＿DOUB <br> LE |
| （d2） | Start device for storing the data that has been read | - | Word | ANY16 |
| （d3） | Bit device that turns on upon completion of the processing <br> （In the case of an error completion，the device specified by <br> （d3）＋1 also turns on．） | - | Bit | ANYBIT＿ARRAY <br> （Number of elements： <br> 2） |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

＊1 Only drive 2 （for the SD memory card）can be set．
■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDI（H）GD | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （U） | － | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ |
| （s1） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － |
| （d2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d3） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | － | － | － | － | － |

Control data

| Operand: (d1) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +0 | Execution/completion type | Specify the execution type. <br> - 0000H: Reading binary data <br> - 0100H: Reading data after converted to CSV format | $\begin{aligned} & 0000 \mathrm{H} \\ & 0100 \mathrm{H} \end{aligned}$ | User |
| +1 | Completion status | The completion status is stored upon completion of the instruction. <br> - 0000H: Completed successfully <br> - Other than $0000 \mathrm{H}:$ Completed with an error (error code) | - | System |
| +2 | Number of read-target data | Specify the number of read-target data (in units of words). (in units of words) Even when "Byte" is specified by (d1)+7, specify the number in units of words. | 1 to 65535 | User |
| +3 | Not used | - | - | - |
| $\begin{aligned} & +4 \\ & +5 \end{aligned}$ | File position | -When "Reading binary data" is specified by ( d 1 ) +0 <br> - 00000000H: From the beginning of the file <br> - 00000001H to FFFFFFFFEH: From the specified position (The data unit is determined by (d1)+7.) <br> - FFFFFFFFFH: Cannot be specified. <br> -When "Reading data after converted to CSV format" is specified by (d1)+0 <br> - 00000000H: From the beginning of the file <br> - 00000001H to FFFFFFFFEH: From the specified row <br> - FFFFFFFFH: From the position where the previous reading ends | $00000000 \mathrm{H} \text { to }$ <br> FFFFFFFFH | User |
| +6 | Number of columns | When "Reading binary data" is specified by ( d 1 ) +0 , set 0 . <br> When "Reading data after converted to CSV format" is specified by ( d 1 ) +0 , set the number of read-target columns. <br> - 0: No column (Regarded as one row.) <br> - Other than 0 : Number of specified columns | 0000H to FFFFH (0 to 65535) | User |
| +7 | Data type specification | $\begin{aligned} & \text { 0: Word } \\ & \text { 1: Byte } \end{aligned}$ | 0, 1 | User |

File name

| Operand: (s2) |  | Description | Setting range | Set by |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Item | File name character <br> string | Specify the character string of the file name. <br> When omitting an extension, omit the ". (period)" as well. <br> Specify the name within 60 characters (a period and extension excluded). <br> If 61 or more characters are used, the extension is ignored and replaced to ".BIN" or <br> ".CSV". | Unicode string | User |
| +0 to |  |  |  |  |  |

Read data

| Operand: (d2) |  |  |  |  |  |  | Setting range | Set by |
| :--- | :--- | :--- | :--- | :--- | :---: | :---: | :---: | :---: |
| Device | Item | Description | - | System |  |  |  |  |
| +0 | Number of data actually <br> read | The number of data actually read is set. <br> The data unit is determined by (d1)+7. | - | System |  |  |  |  |
| +1 to $+\square$ | Read data | The read data is stored. | - |  |  |  |  |  |

## Processing details

- This instruction reads data from the specified file. Set the execution/completion type in the control data to decide whether to read binary data without conversion or to read data after converted to the CSV format. (The read target is the data in the SD memory card only.)
- The bit device (d3) is used as the execution completion flag of the instruction. The bit automatically turns on upon execution of the END instruction after the instruction processing completion is detected, and the bit turns off upon execution of the END instruction in the next scan.
- If the instruction completes with an error, the bit device (d3)+1 turns on in synchronization with (d3). Therefore, the bit device (d3)+1 can be used as the error completion flag.
- SM753 turns on while the SP.FREAD instruction is being executed.
- While SM753 is on, the instruction cannot be executed. (If executed, no processing is performed.)
- If an error is detected during the execution of the instruction, Processing Complete (d3), Error Completion (d3)+1, and SM753 do not turn on.
- Specify data in (d1)+2, (d1)+4, (d1)+5, and (d2) in units of words.


## ■When reading binary data

- If the extension of the target file is omitted, the extension will be ".BIN".
- If the specified file does not exist, an error occurs.
- If the position is specified exceeding the existing file size, 0 point of data is read and the processing completes successfully. The following shows how the binary data are read.



## When reading data after converted to CSV format

- Elements in the CSV format file (cells in Excel) are read by each row. Numerical values and character strings are converted to binary data, and stored in the device.
- If the extension of the target file is omitted, the extension will be ".CSV".
- If the specified file does not exist, an error occurs.
- The number of data specified by (d1)+2 is read from the beginning of the file. If the last data in the file is read before reaching to the number specified, only the available number of data is read.
- If numerical values out of the range or elements other than numerical values exist in the CSV format file, they are converted to OH .
- Numerical values in the CSV format file are read and converted as follows.

| Numerical value in a CSV format file |  | $\mathbf{- 3 2 7 6 8}$ to $\mathbf{- 1}$ | $\mathbf{0}$ to $\mathbf{3 2 7 6 7}$ | $\mathbf{3 2 7 6 8}$ to $\mathbf{6 5 5 3 5}$ |
| :--- | :--- | :--- | :--- | :--- |
| Word device | Unsigned | 32768 to 65535 | 0 to 32767 | 32768 to 65535 |
|  | Signed | -32768 to -1 | 0 to 32767 | -32768 to -1 |

- When the number of columns is set to 0 , the data are read by ignoring the rows in the CSV format file.


## Ex.

When the number of columns is set to 0
[Data created in Excel]

[Data to be loaded to the device]

[Control data]

|  |  | Execution/completion type |
| :---: | :---: | :---: |
| D10 | 0100H |  |
| D11 | - | Not used |
| D12 | K9 | Number of read-target data |
| D13 | - | Not used |
| D14 | K0 |  |
| D15 | K0 | File position |
| D16 | K0 | Number of columns |
| D17 | K0 | Data type |

[Loaded data]

(1) Number of data actually read
(2) Since "Main/sub item" is not a numerical value, the conversion data, 0 , is stored.
(3) Since " " is not a numerical value, the conversion data, 0 , is stored.
(4) Since "Measured value" is not a numerical value, the conversion data, 0 , is stored.
(5) Since "Length" is not a numerical value, the conversion data, 0 , is stored.
(6) Since "1" is a numerical value, it is converted to a binary value.
(7) Since " 3 " is a numerical value, it is converted to a binary value.
(8) Since "Temperature" is not a numerical value, the conversion data, 0 , is stored.
(9) Since "-21" is a numerical value, it is converted to a binary value.
(10)Since " " is not a numerical value, the conversion data, 0 , is stored.

- When the number of columns differs in each row, the data are also read by ignoring the rows.

Ex.
When the number of columns differs in each row

## [Data saved in CSV format]


[Control data]

[Loaded data]

(1) Number of data actually read
(2) Since "Main/sub item" is not a numerical value, the conversion data, 0 , is stored.
(3) Since " " is not a numerical value, the conversion data, 0 , is stored.
(4) Since "Measured value" is not a numerical value, the conversion data, 0 , is stored.
(5) Since "Excess" is not a numerical value, the conversion data, 0 , is stored.
(6) Since "Length" is not a numerical value, the conversion data, 0 , is stored.
(7) Since "Temperature" is not a numerical value, the conversion data, 0 , is stored.
(8) Since " -21 " is a numerical value, it is converted to a binary value.

## Point ${ }^{\rho}$

This type of file in which the number of columns vary with individual rows cannot be created by Excel. It is created when the CSV file is modified by a user.

- When the specified number of columns is set to a value other than 0 , a CSV format file is read as the table with the specified number of columns. The elements outside the specified number of columns are ignored.


## Ex.

When "Reading data after converted to CSV format" is specified and the number of columns is set to 2

## [Data created in Excel]


[Data to be loaded to the device]

[Control data]

[Loaded data]

(1) Number of data actually read
(2) Since "Main/sub item" is not a numerical value, the conversion data, 0 , is stored.
(3) Since " " is not a numerical value, the conversion data, 0 , is stored.
(4) Since "Length" is not a numerical value, the conversion data, 0 , is stored.
(5) Since " 1 " is a numerical value, it is converted to a binary value.
(6) Since "Temperature" is not a numerical value, the conversion data, 0 , is stored.
(7) Since " -21 " is a numerical value, it is converted to a binary value.

- When the number of columns differs in each row, the elements outside the specified number of columns are ignored and 0 is added to the cells where no element exists.


## Ex.

When the number of columns differs in each row

[Control data]

[Loaded data]

(1) Number of data actually read
(2) Since "Main/sub item" is not a numerical value, the conversion data, 0 , is stored.
(3) Since " " is not a numerical value, the conversion data, 0 , is stored.
(4) Since "Length" is not a numerical value, the conversion data, 0 , is stored.
(5) Since "No data" means that no element exists, the conversion data, 0 , is added.
(6) Since "Temperature" is not a numerical value, the conversion data, 0 , is stored.
(7) Since " -21 " is a numerical value, it is converted to a binary value.

- When "Reading data after converted to CSV format" is specified, data can be divided and read.
[Specify row to start reading]
- Execution/End type: Reading data after converted to CSV format
- Number of columns specification: 4H
- Data type specification: Words
- File position: 2H
- Read start device: DO
- Number of data actually read: 6H

[Reading data from the position where the previous reading ends]
- Execution/End type: Reading data after converted to CSV format
- Number of columns specification: 4H
- Data type specification: Words
- File position: FFFFFFFFFH (continuing from the position where the previous reading ends)
- Read start device: D7
- Number of data actually read: 5 H

- When reading data from the position where the previous reading ends, specify the same values for "Execution/End type", "Number of columns", and "Data type specification". If not, data cannot be added correctly from the position where the previous reading ends.
- While reading data from the position where the previous reading ends, if the SP.FREAD instruction with different settings or the SP.FWRITE instruction is executed, data cannot be added correctly from the position where the previous reading ends.


## Precautions

- Do not execute this instruction in interrupt programs. Doing so may cause malfunction of the module.
- Data read after converted to CSV format are decimal values. (Range: -32768 to 32767) For example, character string "A $(41 \mathrm{H})$ " is read as "65".
- When reading binary data in units of words, the file position setting range is 00000000 H to 7 FFFFFFFFH, and FFFFFFFFH.


## Operation error

| Error code (SDO) | Description |
| :---: | :---: |
| 2820H | Data is read exceeding the size of the device. |
| 3405H | The drive specified by ( s 1 ) is not the one for the SD memory card. |
|  | Any value that is set in the device specified by (d1) and later as control data is out of the range. <br> - A value other than 0000 H or 0100 H is specified in (d1). <br> - When reading binary data in units of words, a value other than 00000001 H to 7 FFFFFFFH is specified in (d1) +4 or (d1) +5 . <br> - When reading binary data, FFFFFFFFFH is specified in ( d 1 ) +4 or ( d 1 ) +5 . <br> - When reading binary data, a value other than 0 is specified in (d1)+6. <br> - A value other than 0 or 1 is specified in (d1)+7. |

When the SP.FREAD instruction completes with an error, an error code is stored in the device specified by (d1)+1. (Note that an error code is not stored if the instruction results in an operation error.)
For the error code stored in (d1)+1, refer to the following.
$\longmapsto$ Page 526 Read/write of the specified file

## Writing 16－bit data to the specified file

## SP．FWRITE

This instruction writes device data to the specified file on the SD memory card．

| Ladder |  |  |  |  |  |  | STENO：＝SP＿FWRITE（EN，U，s1，s2，s3，d1，d2）； |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\square$ （U） （s1） （d1） （s2） （s3） （d2） |  |  |  |  |  |  |  |  |
|  |  |  |  |  |  |  |  |  |

## FBD／LD

| ［－二－］ |  |
| :---: | :---: |
| EN | ENO |
| U | d1 |
| s1 | d2 |
| s2 |  |
| s3 |  |

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| SP．FWRITE | $\uparrow$ |

## Setting data

Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （U） | Dummy | - | Device name | ANY16 |
| （s1） | Drive specification | 2 （fixed）${ }^{* 1}$ | ANY16 |  |
| （d1） | Start device where the control data is stored | Refer to the control data． | Word | ANY16＿ARRAY <br> （Number of elements： <br> 8） |
| （s2） | Start device where the file name is stored | - | Unicode string | ANYSTRING＿DOUB <br> LE |
| （s3） | Start device where data is stored | - | Word | ANY16 |
| （d2） | Bit device that turns on upon completion of the processing | - | Bit | ANYBIT＿ARRAY <br> （Number of elements： <br> 2） |
| EN | Execution condition |  |  | BOOL |
| ENO | Execution result | - | Bit | BOOL |

＊1 Only drive 2 （for the SD memory card）can be set．

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロID， U3EDI（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （U） | － | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ |
| （s1） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － |
| （s3） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d2） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | － | － | － | － | － |

Control data

| Operand: (d1) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +0 | Execution/completion type | Specify the execution type. <br> - 0000H: Writing binary data <br> - 0100H: Writing data after converted to CSV format | $\begin{aligned} & 0000 \mathrm{H} \\ & 0100 \mathrm{H} \end{aligned}$ | User |
| +1 | Completion status | The completion status is stored upon completion of the instruction. <br> - 0000H: Completed successfully <br> - Other than 0000 H : Completed with an error (error code) | - | System |
| +2 | Number of data actually written | For the data in the device specified by ( s 3 ), the number of data actually written is stored. <br> The data unit is determined by (d1)+7. | - | System |
| +3 | Not used | - | - | - |
| $\begin{aligned} & +4 \\ & +5 \end{aligned}$ | File position | ■When "Writing binary data" is specified by (d1)+0 <br> - 00000000H: From the beginning of the file <br> - 00000001 H to FFFFFFFEH: From the specified position (The data unit is determined by (d1)+7.) <br> - FFFFFFFFFH: Added to the end of the file. <br> When "Converting and writing data in CSV format" is specified by (d1)+0 <br> - 00000000H to FFFFFFFEH: From the beginning of the file <br> - FFFFFFFFH: Added to the end of the file. | 00000000H to FFFFFFFFH | User |
| +6 | Number of columns | When "Writing binary data" is specified by (d1) +0 , set 0 . <br> When "Converting and writing data in CSV format" is specified by ( d 1 ) +0 , specify the number of columns to write. <br> - 0: No column (Regarded as one row.) <br> - Other than 0 : Number of specified columns | 0000H to FFFFH (0 to 65535) | User |
| +7 | Data type specification | 0: Word <br> 1: Byte | 0, 1 | User |

File name

| Operand: (s2) |  | Description | Setting range | Set by |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Item | File name character <br> string | Specify the character string of the file name. <br> When omitting an extension, omit the ". (period)" as well. <br> Specify the name within 60 characters (a period and extension excluded). <br> If 61 or more characters are used, the extension is ignored and replaced to ".BIN" or <br> ".CSV". | Unicode string | User |
| +0 to |  |  |  |  |  |

Write data

| Operand: (s3) | Description | Setting range | Set by |  |
| :--- | :--- | :--- | :--- | :--- |
| Device | Item | Number of request <br> write data | Specify the number of data to be requested to write. (in units of words) <br> Even when "Byte" is specified by (d1)+7, specify the number in units of words. | 1 to 65535 |
| +0 | The number of data to be requested to write is stored. | User |  |  |
| +1 to $+\square$ | Write data | 0000 H to FFFFH | User |  |

## Processing details

- This instruction writes the specified number of data to the specified file. Set the execution/completion type in the control data to specify whether to write binary data as is or write it after converting it to the CSV format. (The write target is the SD memory card only.)
- The bit device (d2) is used as the execution completion flag of the instruction. The bit automatically turns on upon execution of the END instruction after the instruction processing completion is detected, and the bit turns off upon execution of the END instruction in the next scan.
- If the instruction completes with an error, the bit device (d2)+1 turns on in synchronization with (d2). Therefore, the bit device (d2)+1 can be used as the error completion flag.
- SM753 turns on while the SP.FWRITE instruction is executed.
- While SM753 is on, the SP.FWRITE instruction cannot be executed. (If executed, no processing is performed.)
- Even if an error is detected during the execution of the instruction, the bit devices (d2) and (d2)+1 and SM753 do not turn on.
- (s3), (d1) +4 , and (d1) +5 should be specified in units of words.


## ■Writing binary data

- If the extension of the target file is omitted, the extension will be ".BIN".
- If a file that does not exist is specified, it will be created and data will be saved to the beginning of the file. The newly created file has the archive attribute.
- When an existing file is specified, data will be saved to the beginning of the file. If the size of data exceeds the size of the existing area in the file during writing, the excess data is additionally stored.
- If the specified position exceeds the existing file size, 0 point of data is written and the processing completes successfully.
- If the media runs out of free space during additional saving of data, an error occurs. In this case, the data already added and saved successfully is held as is and as much remaining data as possible is added and saved before termination with an error.
- In binary data write mode, when the number of requested write data and file position are specified, data is written as follows.

| Control data |  | Execution/completion type |
| :---: | :---: | :---: |
| (d1)+0 | 0000H |  |
| (d1)+1 | - | Not used |
| (d1)+2 | K3 | Number of data written |
| (d1)+3 | - | Not used |
| (d1)+4 | K1 |  |
| (d1)+5 |  | File position |
| (d1)+6 | K0 | Number of columns |
| (d1)+7 | K0 | Data type |



## Converting and writing data in CSV format

- If the extension is omitted, the extension will be ".CSV".
- When an existing file is specified, the following occurs.
- When a value other than FFFFFFFFH is specified in (d1) +4 or (d1) +5 , data is saved to the file after deleting the existing data in the file.
- When a value other than FFFFFFFFFH is specified in $(\mathrm{d} 1)+4$ or $(\mathrm{d} 1)+5$, data is added and saved to the end of the file.
- If a file that does not exist is specified, it will be created and data will be saved to the beginning of the file. The newly created file has the archive attribute.
- If the media runs out of free space during additional saving of data, an error occurs. In this case, the data already added and saved successfully is held as is and as much remaining data as possible is added and saved before termination with an error.
- When the number of columns is set to 0 , data is read as a single-row data in a CSV format file.


## Ex.

The number of columns is set to 0 when writing data after conversion to the CSV format.



[^9]- When the specified number of columns is set to a value other than 0 , a CSV format file is stored as the table with the specified number of columns.

Ex.
The number of columns is set to a value other than 0 when writing data after conversion to the CSV format.



- The following figure shows how data is added.
[Specify the file to which data will be written.] (Even if the file exists, it is deleted and re-created.)
- Execution/End type: Writing data after converted to CSV format
- Number of columns specification: 4H
- Data type specification: Words
- File position: 00000000 H (from the beginning of the file)
- Write start device: DO
- Number of request write data: 6 H

[Added to the end of the file]
- Execution/End type: Writing data after converted to CSV format
- Number of columns specification: 3H
- Data type specification: Words
- File position: FFFFFFFFH (added to the end of the file)
- Write start device: D7
- Number of request write data: 8 H

- An integral multiple of "Number of columns" should be specified for "Number of request write data".

Otherwise, numbers of columns will be apart.

- The last data is always followed by a line feed code. In addition mode, therefore, data is added starting from the beginning of a new line.
- When data is added to the end of a file, columns are shifted if "Number of columns" is changed from the previous writing.
- The following shows how the file size (total number of bytes) is calculated when a CSV format file is written to the SD memory card.
[Total number of bytes] = [Total number of bytes excluding the last row] + [Number of bytes of the last row]
([Number of bytes of each row] $=\left[\right.$ Number of columns $\left.{ }^{* 1}\right]+1+[\text { total number of bytes of all data values per line }]^{* 2}$ )
*1 The specified number of columns applies to rows other than the last row. The number of columns of the last row is calculated as shown below because it may differ from the specified number of rows depending on the number of write data.
- The number of rows excluding the last row is calculated. (Number of rows excluding the last row = number of requested write data:number of columns (remainders rounded down)
- The number of columns of the last row is calculated. (Number of columns of the last row = number of requested write data - (number of rows excluding the last row $\times$ number of columns))
*2 The following shows how the number of bytes of each data value is calculated.

| Sign of data value | Number of bytes of each data value | Range of bytes | Example |
| :--- | :--- | :--- | :--- |
| Positive | Number of digits | 1 to 5 (word specification) <br> 1 to 3 (byte specification) | $\bullet 12345: 5$ bytes <br>  |
| Negative | Number of digits +1 | 2 to 6 (word specification) 2 bytes |  |

Precautions

- Do not execute the SP.FWRITE instruction in interrupt programs. Doing so may cause malfunction of the module.
- Data written after converted to the CSV format are decimal values. (Range: -32768 to 32767) For example, character string " $\mathrm{A}(41 \mathrm{H})$ " is written as " 65 ".
- When writing binary data in units of words, the file position setting range is 00000000 H to 7 FFFFFFFFH, and FFFFFFFFH.


## Operation error

| Error code (SDO) | Description |
| :---: | :---: |
| 2820H | The value in the device specified by ( s 3 ) +0 is out of the range ( 1 to 65535 ), or exceeds the setting area specified by ( s 3 ) +1 and later in the device/label memory. |
| 3405H | The drive specified by (s1) is not the one for the SD memory card. |
|  | Any value that is set in the device specified by (d1) and later as control data is out of the range. <br> - A value other than 0000 H or 0100 H is specified in (d1). <br> - When writing binary data in units of words, a value other than 00000001 H to 7 FFFFFFFH or FFFFFFFFH is specified in (d1) +4 or (d1) +5 . <br> -When writing binary data, a value other than 0 is specified in (d1)+6. <br> - A value other than 0 or 1 is specified in (d1)+7. |
|  | The file name character string specified by (s2) cannot be read. <br> - The number of characters in the file name exceeds 65 . <br> - An inhibited value is set in the specified device. |

When the SP.FWRITE instruction completes with an error, an error code is stored in the device specified by (d1)+1. (Note that an error code is not stored if the instruction results in an operation error.)
For the error code stored in (d1)+1, refer to the following.
$\checkmark$ Page 526 Read/write of the specified file

### 7.7 Debugging and Failure Diagnostic Instruction

## Displaying the error or resetting the annunciator

## LEDR

This instruction resets the self-diagnosis error display that allows the annunciator display and the operation of the CPU module to continue.

| Ladder | ST |
| :--- | :--- |
|  | ENO:=LEDR(EN); |
| $\square-\square-\square$ |  |
| FBD/LD |  |


| $[-\overline{--}]$ |
| :---: |
| $\mathrm{EN} \quad \mathrm{ENO}$ |

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| LEDR | $\uparrow$ |

## Processing details

- This instruction resets the self-diagnosis error display that allows the annunciator display and the operation of the CPU module to continue. Executing the instruction once resets both the error display and annunciator.
- The following operation is performed when a self diagnosis error occurs.
- When a self diagnosis error that allows the operation to continue has occurred.

While a self diagnosis error that allows the CPU module to continue operation is displayed, the ERROR LED on the front of the CPU module is off. Note that SM0, SM1, and SD0 are not reset automatically and therefore they should be reset by the program.

- When a battery error has occurred.

When the LEDR instruction is executed after battery replacement, the BATTERY LED on the front of the CPU module turns off. SM51 is also turned off simultaneously.

- The following operation occurs while the annunciator $(F)$ is on.
- The USER LED turns off.
- SD62, SD63, and SD64 to SD79 are all cleared.

| Before execution |  | After execution |  |  |
| :---: | :---: | :---: | :---: | :---: |
| SD62 | 200 | SD62 | 0 |  |
| SD63 | 15 | SD63 | 0 |  |
| SD64 | 200 | SD64 | 0 |  |
| SD65 | 99 | SD65 | 0 |  |
| SD66 | 5 | SD66 | 0 | $\zeta$ All data are cleared. |
| SD67 | 255 |  |  |  |
| : |  | SD77 | 0 |  |
| SD78 | 83 | SD78 | 0 |  |
| SD79 | 0 | SD79 | 0 |  |

## Operation error

There is no operation error.

## 7．8 String Processing Instructions

## Comparing string data

## LD\＄D，AND\＄口，OR\＄口

These instructions compare string data as normally open contacts．


FBD／LD

（ $\square$ is replaced by a combination of LDSTRING＿，ANDSTRING＿，or ORSTRING＿and EQ，NE，GT，LE，LT，or GE．）
Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| LDSD，AND\＄ロ，OR\＄ם | Every scan |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Comparison data or the start device where the comparison <br> data is stored | - | String | ANYSTRING＿SINGL <br> E |
| （s2） | Comparison data or the start device where the comparison <br> data is stored | - | String | ANYSTRING＿SINGL <br> E |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3EDI（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － |

## Processing details

- These instructions perform a comparison operation between the character string data in the device specified by (s1) and the character string data in the device specified by ( s 2 ). (Devices are used as normally open contacts).
- In comparison operation, the ASCII code of character string is compared character by character from the beginning of the character string.
- The character strings in the devices specified by ( s 1 ) and ( s 2 ) mean those in the device numbers from the specified one to the one containing 00 H .
- The comparison result turns out matching if all character strings match.

|  | b15 | $\cdots$ | b8 b7 | $\cdots$ |
| :--- | :---: | :---: | :---: | :---: |
| (s1) | $42 \mathrm{H}(\mathrm{B})$ | $41 \mathrm{H}(\mathrm{A})$ |  |  |
| $(\mathrm{s} 1)+1$ | $44 \mathrm{H}(\mathrm{D})$ | $43 \mathrm{H}(\mathrm{C})$ |  |  |
|  | (s1) |  |  |  |
|  | 00 H | $45 \mathrm{H}(\mathrm{E})$ |  |  | $=$



| $\square$ Instruction symbol (ladder, FBD/LD) | Result |
| :--- | :--- |
| $\$=$, EQ | Continuity state (ENO is on.) |
| $\$<>$, NE | Non-continuity state (ENO is off.) |
| $\$>$, GT | Non-continuity state (ENO is off.) |
| $\$<=$, LE | Continuity state (ENO is on.) |
| $\$<$, LT | Continuity state (ENO is on.) |
| $\$>=$, GE |  |

- When different character strings are compared, the character string with a larger character code is greater.


| $\square$ Instruction symbol (ladder, FBD/LD) | Result |
| :--- | :--- |
| $\$=$, EQ | Non-continuity state (ENO is off.) |
| $\$<>$, NE | Continuity state (ENO is on.) |
| $\$>$, GT | Continuity state (ENO is on.) |
| $\$<=$, LE | Non-continuity state (ENO is off.) |
| $\$<$, LT | Continuity state (ENO is on.) |
| $\$>=$, GE | Continuity state (ENO is off.) |

- When different character strings are compared, the relative size of a character string is determined by the relative size of the first different character codes.


| (s2) | b8 b7 $\cdots$ b0 |  |
| :---: | :---: | :---: |
|  | 32 H (2) | 31H (1) |
| (s2)+1 | 33 H (3) | 34H (4) |
| (s2)+2 | 00H | 35H (5) |


| $\square$ Instruction symbol (ladder, FBD/LD) | Result |
| :--- | :--- |
| $\$=$, EQ | Non-continuity state (ENO is off.) |
| $\$<>$, NE | Continuity state (ENO is on.) |
| $\$>$, GT | Non-continuity state (ENO is off.) |
| $\$<=$, LE | Continuity state (ENO is on.) |
| $\$<$, LT | Continuity state (ENO is on.) |
| $\$>=$, GE | Non-continuity state (ENO is off.) |

- When the lengths of the character string data in the devices specified by ( s 1 ) and ( s 2 ) are different, the longer character string data is greater.


| (s2) | 5 … | ... |
| :---: | :---: | :---: |
|  | 32 H (2) | 31 H (1) |
| (s2)+1 | 34H (4) | 33H (3) |
| (s2)+2 | 36H (6) | 35H (5) |
| (s2)+3 | 00H | 00H |


| $\square$ Instruction symbol (ladder, FBD/LD) | Result |
| :--- | :--- |
| $\$=$, EQ | Non-continuity state (ENO is off.) |
| $\$<>$, NE | Continuity state (ENO is on.) |
| $\$>$, GT | Continuity state (ENO is on.) |
| $\$<=$, LE | Non-continuity state (ENO is off.) |
| $\$<$, LT | Non-continuity state (ENO is off.) |
| $\$>=$, GE | Continuity state (ENO is on.) |

- The character string in the device specified by (s1) or (s2) exceeds 16383 characters, the operation result will be noncontinuity (ENO OFF).
- If the LDSTRING $\square$ instruction is used in the program written in FBD/LD, always set EN to TRUE.
- If the ORSTRINGD instruction is used in the program written in FBD/LD and EN is set to TRUE, ENO turns on. EN will not be an execution condition.


## Operation error

There is no operation error.

## Point ${ }^{\circ}$

The character string comparison instructions perform the following checks while comparing character strings.

- Checking whether the device area range is exceeded
- Checking whether the character string is within 16383 characters

If 00 H does not exist in the device area or the character string exceeds 16383 characters and a character mismatch is detected, the instruction outputs comparison operation results without causing non-continuity (ENO OFF).
The following example shows the operation result when the last device number of the device area is D12287.

(1) The second character of (s1) differs from that of (s2) ((1) $\neq(\mathrm{s} 2)$ ), and accordingly the operation result will be continuity (ENO OFF).
(2) D12287 and later are outside the device area, and accordingly character string data comparison is performed using data up to D12287.
Since a character string mismatch has been detected, the condition is satisfied and processing ends.

## Concatenating string data

## \$+(P) [when two operands are set]

These instructions concatenate string data.

| Ladder | ST |
| :---: | :---: |
|  | Not supported |
| $-\square$ $(\mathrm{s})$ (d) |  |

FBD/LD
Not supported

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| $\$+$ | - |
| $\$+P$ | - |

## Setting data

■Description, range, data type

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (s) | Data to be concatenated or the start device containing the <br> data | - | String | ANYSTRING_SINGL |
| E |  |  |  |  |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | J밈 | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | U미미, J밈, U3EDl(H)G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (s) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | $\bigcirc$ | - |
| (d) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |

## Processing details

- This instruction connects the character string stored in the device number specified by (s) and later to the end of the character string data stored in the device number specified by (d) and later, and stores the connected data in the device number specified by (d) and later.



| (d) | b8 b7 |  |
| :---: | :---: | :---: |
|  | 42H (B) | 41H (A) |
| (d) +1 | 44H (D) | 43H (C) |
| (d) +2 | 31H (1) | 45H (E) |
| (d)+3 | 33 H (3) | 32 H (2) |
| (d) +4 | 35H (5) | 34H (4) |
| (d) +5 | 00H | 36H (6) |

- For concatenating character strings, the instruction ignores 00 H that indicates the end of the character string in the device specified by (d) and appends the character string in the device specified by (s) following the last character in the device specified by (d).


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2820 H | There is no NULL code $(00 \mathrm{H})$ in the setting area specified by (s) and later in the device/label memory. |
|  | There is no NULL code $(00 \mathrm{H})$ in the setting area specified by (d) and later in the device/label memory. |
| 2821 H | The device numbers for storing the strings in the devices specified by (s) and (d) are overlapping. |
| 3405 H | The number of characters in the string specified by (s) exceeds 16383. |
|  | The number of characters in the string specified by (d) exceeds 16383. |
|  The number of characters in the concatenated string ((s)+(d)) exceeds 16383. <br>  The entire string after concatenate processing cannot be stored in the setting area specified by (d) in the device/label memory. (The <br> number of required points is insufficient.) |  |

## \＄＋（P）［when three operands are set］

These instructions concatenate string data．


## －Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| $\$+$ | - |
| $\$+P$ | - |

## Setting data

## ■Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Data to be concatenated or the start device containing the <br> data | - | String | ANYSTRING＿SINGL <br> E |
| （s2） | Data to be concatenated or the start device containing the <br> data to be concatenated | - | String | ANYSTRING＿SINGL <br> E |
| （d） | Start device for storing the concatenated data | - | String | ANYSTRING＿SINGL <br> E |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロロロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- This instruction connects the character string stored in the device number specified by (s2) and later to the end of the character string data stored in the device number specified by ( $s 1$ ) and later, and stores the connected data in the device number specified by (d) and later.

| (s1) | 42H (B) | 41H (A) |
| :---: | :---: | :---: |
| (s1)+1 | 44H (D) | 43H (C) |
| (s1)+2 | 00H | 45H (E) |


| (s2) | b15 ... b8 b7 $\cdots$... b0 |  |
| :---: | :---: | :---: |
|  | 32H (2) | 31 H (1) |
| (s2)+1 | $34 \mathrm{H}(4)$ | 33H (3) |
| (s2)+2 | 36H (6) | 35H (5) |
| (s2)+3 |  |  |


| (d) | 42H (B) | 41H (A) |
| :---: | :---: | :---: |
| (d) +1 | 44H (D) | 43H (C) |
| (d) +2 | 31H (1) | 45H (E) |
| (d) +3 | 33H (3) | $32 \mathrm{H}(2)$ |
| (d) +4 | 35H (5) | $34 \mathrm{H}(4)$ |
| (d) +5 | OOH | 36H (6) |
| "ABCDE123456" |  |  |

- For concatenating character strings, the instruction ignores 00 H that indicates the end of the character string in the device specified by ( s 1 ) and appends the character string in the device specified by ( s 2 ) following the last character in the device specified by ( s 1 ).


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2820 H | After the device number specified by (s1) and later, there is no 00H before the relevant device number. |
|  | After the device number specified by (s2) and later, there is no 00H before the relevant device number. |
| 2821 H | The device numbers for storing the strings in the devices specified by (s2) and (d) are overlapping. |
| 3405 H | The number of characters in the string specified by (s1) exceeds 16383. |
|  | The number of characters in the string specified by (s2) exceeds 16383. |
| The string stored in the device specified by (d) is out of the output enable range. <br> - The number of characters in the concatenated string exceeds 16383. <br> - The entire string after concatenate processing cannot be stored in the setting area specified by (d) in the device/label memory. (The <br> number of required points is insufficient.) |  |

## Transferring string data

## \$MOV(P)

These instructions transfer string data to the specified device number and later.

| Ladder | ST |
| :---: | :---: |
|  | Not supported |
| $--\square$ (s) (d) |  |

FBD/LD

( $\square$ is replaced by STRINGMOV or STRINGMOVP.)

## -Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| \$MOV | - |
|  | $\boxed{ }$ |
| \$MOVP | - |

## Setting data

Descriptions, ranges, and data types

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (s) | Character string to be transferred (maximum of 255 <br> characters) or the start device containing such character <br> string | - | String | ANYSTRING_SINGL <br> E |
| (d) | Start device for storing the transferred character string | - | String | ANYSTRING_SINGL <br> E |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## -Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | J밈 | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | U밈, J밈, U3EDl(H)GD | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (s) | - | - | $\bigcirc$ | - | - | $\bigcirc$ | - | $\bigcirc$ | - | - | $\bigcirc$ | - |
| (d) | - | - | $\bigcirc$ | - | - | $\bigcirc$ | - | $\bigcirc$ | - | - | - | - |

## Processing details

- These instructions transfer the character string data in the device specified by (s) to the device number specified by (d) and later. The character strings from the one enclosed by double quotes (") or the device number specified by (s) to the device number containing 00 H are transferred all at once.


- Even when the device range ((s) to (s)+n)) in which the character string data to be transferred and the device range ((d) to $(d)+n)$ for storing the transferred data are overlapping, the processing is performed normally. For example, the character strings stored in the devices specified by D10 to D13 are transferred to the devices specified by D11 to D14 as shown below.

- When 00 H is stored in the lower byte of $(\mathrm{s})+\mathrm{n}, 00 \mathrm{H}$ will be stored in both upper and lower bytes of $(\mathrm{d})+\mathrm{n}$.


Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2820 H | There is no NULL code (00H) in the setting area specified by (s) and later in the device/label memory. |
| 3405 H | The number of characters in the string specified by (s) exceeds 16383. |
| 3406 H | The entire string cannot be stored in the setting area specified by (d) in the device/label memory. (The number of required points is <br> insufficient.) |

## Transferring Unicode string data

## \＄MOV（P）＿WS

These instructions transfer Unicode string data to the specified device number and later．

| Ladder | ST |
| :---: | :---: |
|  | Not supported |
| $\begin{array}{\|l\|l\|l\|} \hline-\square-\square & \text { (s) } & \text { (d) } \\ \hline \end{array}$ |  |

FBD／LD

（ $\square$ is replaced by STRINGMOV WS or STRINGMOVP WS．）

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| \＄MOV＿WS | - |
| \＄MOVP＿WS | - |

Setting data
－Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Unicode character string to be transferred（maximum of <br> 255 characters）or the start device containing the Unicode <br> character string | - | Unicode string | ANYSTRING＿DOUB <br> LE |
| （d） | Start device for storing the transferred Unicode character <br> string | - | Unicode string | ANYSTRING＿DOUB <br> LE |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロום | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロID， U3EDI（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | － | $\bigcirc$ | － |
| （d） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions transfer the Unicode character string data in the device specified by (s) to the device number specified by (d) and later. The Unicode character strings from the one enclosed by double quotes (") or the device number specified by (s) to the device number containing 0000 H are transferred all at once.

|  |  |
| :--- | :--- |
|  | 1st character |
| $(\mathrm{s})+1$ | 2nd character |
| $(\mathrm{s})+2$ | 3rd character |
|  |  |

(s) $+n$

| "n'th character |
| :---: |
| 0000 H |



- Even when the device range ((s) to $(s)+n))$ in which the Unicode character string data to be transferred and the device range $((d)$ to $(d)+n)$ for storing the transferred data are overlapping, the processing is performed normally. For example, the character strings stored in the devices specified by D10 to D13 are transferred to the devices specified by D11 to D14 as shown below.



## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2820 H | There is no 0000H in the setting area specified by (s) and later in the device/label memory. |
| 3405 H | The number of characters in the Unicode string specified by (s) exceeds 16383. |
| 3406 H | The entire Unicode string cannot be stored in the setting area specified by (d) in the device/label memory. (The number of required points <br> is insufficient.) |

## Converting 16－bit binary data to decimal ASCII

## BINDA（P）（＿U）

These instructions convert 16－bit binary data to the decimal ASCII code．

| Ladder | ST |  |
| :---: | :---: | :---: |
| $\square--\square$ （s） （d） | $\begin{aligned} & \mathrm{ENO}:=\mathrm{BINDA}(\mathrm{EN}, \mathrm{~s}, \mathrm{~d}) ; \\ & \mathrm{ENO}:=\mathrm{BINDAP}(\mathrm{EN}, \mathrm{~s}, \mathrm{~d}) \end{aligned}$ | $\begin{aligned} & \mathrm{ENO}:=\mathrm{BINDA} \text { _U(EN,s,d); } \\ & \mathrm{ENO}:=\mathrm{BINDAP} \_\mathrm{U}(\mathrm{EN}, \mathrm{~s}, \mathrm{~d}) \end{aligned}$ |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| BINDA | - |
| BINDA＿U | - |
| BINDAP | - |
| BINDAP＿U |  |

## Setting data

Descriptions，ranges，and data types

| Operand |  | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- | :--- |
| （s） | BINDA（P） | Binary data used for ASCII conversion | -32768 to 32767 | 16－bit signed binary | ANY16＿S |
|  | BINDA（P）＿U |  | 0 to 65535 | 16－bit unsigned binary | ANY16＿U |
| （d） | Start device for storing the conversion result | - | String | ANYSTRING＿SINGL <br> E |  |
| EN |  | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |  |

## －Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）Gロ | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions convert the digit at each place in hexadecimal notation of the 16-bit binary data in the device specified by (s) to the ASCII code, and store the converted data in the device number specified by (d) and later.
(s)
 When SM701 is off, 0 is stored.

Ex.
When -12345 (signed) is specified in (s)
(s)


|  | b15 | $\cdots$ | b8b7 |  |  |  |  |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| (d) | b1 | b0 | b0 |  |  |  |  |
| (d) +1 | $31 \mathrm{H}(1)$ | $33 \mathrm{H}(3)$ | $2 \mathrm{DH}(-)$ |  |  |  |  |
|  |  | $32 \mathrm{H}(2)$ |  |  |  |  |  |
| (d)+2 | $35 \mathrm{H}(5)$ |  | $34 \mathrm{H}(4)$ |  |  |  |  |
| (d) +3 |  |  |  |  |  | 00 H |  |
|  |  |  |  |  |  |  |  |

- The operation results stored in the device specified by (d) are as follows.
- As sign data, 20 H is stored if the 16 -bit binary data is positive, and 2 DH is stored if the data is negative.
- 20 H is stored for the leading zeros at the left of the effective number of digits. (Zero-suppression) In the case of " 00325 ", for example, " 00 " becomes 20 H and " 325 " is the effective number of digits.
- As for storing data in the device specified by (d) $+3,0$ is stored when SM701 is off, and the current data remains unchanged when SM701 is on.


## Operation error

There is no operation error.

## Converting 32－bit binary data to decimal ASCII

## DBINDA（P）（＿U）

These instructions convert 32－bit binary data to the decimal ASCII code．

| Ladder | ST |  |
| :---: | :---: | :---: |
|  | $\begin{aligned} & \text { ENO:=DBINDA(EN,s,d); } \\ & \text { ENO:=DBINDAP(EN,s,d); } \end{aligned}$ | $\begin{aligned} & \text { ENO:=DBINDA_U(EN,s,d); } \\ & \text { ENO:=DBINDAP_U(EN,s,d); } \end{aligned}$ |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DBINDA | - |
| DBINDA＿U | - |
| DBINDAP | - |
| DBINDAP＿U |  |

## Setting data

Descriptions，ranges，and data types

| Operand |  | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- | :--- |
| （s） | DBINDA（P） | Binary data used for ASCII conversion | -2147483648 to 2147483647 | 32－bit signed binary | ANY32＿S |
|  | DBINDA（P）＿U |  | 0 to 4294967295 | 32－bit unsigned binary | ANY32＿U |
| （d） | Start device for storing the conversion result | - | String | ANYSTRING＿SINGL <br> E |  |
| EN |  | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |  |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）Gロ | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions convert the digit at each place in decimal notation of the 32-bit binary data in the device specified by (s) to the ASCII code, and store the converted data in the device number specified by (d) and later.



## Ex.

When -12345678 (signed) is specified in (s)

| (s) +1 (s) | (d) | ... | ... | b0 |
| :---: | :---: | :---: | :---: | :---: |
|  |  | 20 H (space) | 2DH (-) |  |
|  | (d) +1 | 31 H (1) | 20H (space) |  |
| 12345678 | (d) +2 | 33H (3) | 32 H (2) |  |
|  | (d) +3 | 35H (5) | 34 H (4) |  |
|  | (d) +4 | 37H (7) | 36H (6) |  |
|  | (d) +5 | 00 H or 20 H | 38H (8) |  |

- The operation results stored in the device specified by (d) are as follows.
- As sign data, 20 H is stored if the 32-bit binary data is positive, and 2 DH is stored if the data is negative.
- 20 H is stored for the leading zeros at the left of the effective number of digits. (Zero-suppression) In the case of " 0012034560 ", for example, " 00 " becomes 20 H and "12034560" is the effective number of digits.
- As for storing data in upper 8 bits in the device specified by (d) $+5,0$ is stored when SM701 is off, and 20 H is stored when SM701 is on.


## Operation error

There is no operation error.

## Converting 16－bit binary data to hexadecimal ASCII

## BINHA（P）

These instructions convert 16－bit binary data to the hexadecimal ASCII code．


FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| BINHA | - |
| BINHAP | $\boxed{ }$ |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Binary data used for ASCII conversion | -32768 to 32767 | 16－bit signed binary | ANY16 |
| （d） | Start device for storing the conversion result | - | String | ANYSTRING＿SINGL |
|  |  | E |  |  |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## －Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions convert the digit at each place in hexadecimal notation of the 16-bit binary data in the device specified by (s) to the ASCII code, and store the converted data in the device number specified by (d) and later.
(s)

16-bit binary data


## Ex.

When 02A6H is stored in the device specified by (s)
(s)


|  | ... | b8b7 | ... | b0 |
| :---: | :---: | :---: | :---: | :---: |
| (d) | 32 H (2) |  | 30 H (0) |  |
| (d) +1 | 36H (6) |  | 41H (A) |  |
| (d) +2 | 00 H |  |  |  |

- The operation result to be stored in the device specified by (d) is processed as a 4-digit hexadecimal number. Therefore, 0 at the left side of the effective number of digits is processed as " 0 ". (Zero padding)
- As for storing data in the device specified by (d)+2, 0 is stored when SM701 is off, and the current data remains unchanged when SM701 is on.


## Operation error

There is no operation error.

## Converting 32－bit binary data to hexadecimal ASCII

## DBINHA（P）

These instructions convert 32－bit binary data to the hexadecimal ASCII code．

| Ladder | ST |
| :---: | :---: |
|  | $\begin{aligned} & \text { ENO:=DBINHA(EN,s,d); } \\ & \text { ENO:=DBINHAP(EN,s,d); } \end{aligned}$ |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DBINHA | - |
|  | $\boxed{ }$ |
| DBINHAP | - |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Binary data used for ASCII conversion | -2147483648 to 2147483647 | 32－bit signed binary | ANY32 |
| （d） | Start device for storing the conversion result | - | String | ANYSTRING＿SINGL <br> E |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3E미（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions convert the digit at each place in hexadecimal notation of the 32-bit binary data in the device specified by (s) to the ASCII code, and store the converted data in the device number specified by (d) and later.



## Ex.

When 03AC625EH is stored in the device specified by (s)
$\frac{(\mathrm{s})+1}{} \frac{(\mathrm{~s})}{6 \mathrm{ACC}}$
3 A C 625 EH

|  | ... | b8b7 | ... | b0 |
| :---: | :---: | :---: | :---: | :---: |
| (d) | 33H (3) |  | $30 \mathrm{H}(0)$ |  |
| (d) +1 | 43H (C) |  | 41 H (A) |  |
| (d) +2 | 32H (2) |  | 36H (6) |  |
| (d) +3 | 45H (E) | ! | 35H (5) |  |
| (d) +4 | OOH |  |  |  |

- The operation result to be stored in the device specified by (d) is processed as a 8 -digit hexadecimal number. Therefore, 0 at the left side of the effective number of digits is processed as " 0 ". (Zero padding)
- As for storing data in the device specified by (d)+2, 0 is stored when SM701 is off, and the current data remains unchanged when SM701 is on.


## Operation error

There is no operation error.

## Converting 16－bit binary data to string data

## STR（P）（＿U）

These instructions convert 16－bit binary data to a string by adding a decimal point to the specified place of the data．


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| STR | - |
| STR＿U | $\boxed{ }$ |
| STRP | - |
| STRP＿U | - |

## Setting data

Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :---: | :---: | :---: | :---: | :---: | :---: |
| （s1） | STR（P） | Start device where the number of digits of the conversion target data is stored | － | 16－bit signed binary | ANY16＿S＿ARRAY （Number of elements： 2） |
|  | STR（P）＿U |  |  | 16－bit unsigned binary | ANY16＿U＿ARRAY （Number of elements： 2） |
| （s2） | STR（P） | Conversion target data | －32768 to 32767 | 16－bit signed binary | ANY16＿S |
|  | STR（P）＿U |  | 0 to 65535 | 16－bit unsigned binary | ANY16＿U |
| （d） |  | Start device for storing the converted character string | － | String | ANYSTRING＿SINGL E |
| EN |  | Execution condition | － | Bit | BOOL |
| ENO |  | Execution result | － | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JपIप， U3EDl（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions add a decimal point to the 16 -bit binary data in the device specified by (s2) at the location specified by (s1), convert the data to character string data, and store the converted data in the device areas specified by (d) and later.


Ex.
When -12.3 is specified in (s2)


|  | ... | ... |
| :---: | :---: | :---: |
| (d) | 31H (1) | 2DH (-) |
| (d) +1 | 2EH (.) | 32 H (2) |
| (d) +2 | OOH | 33H (3) |

- The total number of digits that can be specified by (s1) is 2 to 8 .
- The number of digits in the decimal part that can be specified by ( $\mathbf{s} 1$ ) +1 is 0 to 5 . Note that the number of digits in the decimal part must be smaller than the total number of digits minus 3 .
- The converted character string data are stored in the device areas specified by (d) and later as shown below.
- As sign data, "20H" (space) is stored if the 16 -bit binary data is positive, and "2DH" (-) is stored if the data is negative.
- If the number of digits in the decimal part is set to other than 0, " 2 EH " (.) is automatically stored at the position before the specified number of digits. If the number of digits in the decimal part is $0, " 2 E H$ " (.) is not stored.

- If the specified number of digits in the decimal part is greater than the number of digits of the 16 -bit binary data, 0 s are automatically added and the data is regarded as "0.ㅁㅁㅁㅁ".

- If the total number of digits excluding the sign and the decimal point is greater than the number of digits of the 16-bit binary data, "20H" (space) is stored between the sign and the numeric value. If the number of digits of the 16 -bit binary data is greater, an error occurs.

- The value " 00 H " is automatically stored at the end of the converted character string.


## Operation error

| Error code (SDO) | Description |
| :---: | :---: |
| 3401H | Invalid data that cannot be converted is input to (s1). <br> - The specified total number of digits is out of the range, 2 to 8. <br> - The specified number of digits in the decimal part is out of the range, 0 to 5 . <br> - The relationship between the total number of digits specified by ( s 1 ) and the number of digits in the decimal part specified by ( s 1 ) +1 does not satisfy the following. <br> (Total number of digits)- $3 \geq$ Number of digits in the decimal part <br> - The number of digits specified by ( s 1 ) is smaller than the number of digits plus 2 of the 16 -bit binary data specified by ( s 2 ). <br> [Number of digits in ( s 1 )] < [Number of digits of 16-bit binary data excluding the sign in (s2) + Number of digits in the sign (+ or -) + (decimal point (.)] |

## Converting 32－bit binary data to string data

## DSTR（P）（＿U）

These instructions convert 32－bit binary data to a string by adding a decimal point to the specified place of the data．


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DSTR | - |
| DSTR＿U | $\boxed{ }$ |
| DSTRP | $\boxed{ }$ |
| DSTRP＿U | - |

## Setting data

Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :---: | :---: | :---: | :---: | :---: | :---: |
| （s1） | DSTR（P） | Start device where the number of digits of the conversion target data is stored | － | 16－bit signed binary | ANY16＿S＿ARRAY （Number of elements： 2） |
|  | DSTR（P）＿U |  |  | 16－bit unsigned binary | ANY16＿U＿ARRAY （Number of elements： 2） |
| （s2） | DSTR（P） | Conversion target data | －2147483648 to 2147483647 | 16－bit signed binary | ANY32＿S |
|  | DSTR（P）＿U |  | 0 to 4294967295 | 16－bit unsigned binary | ANY32＿U |
| （d） |  | Start device for storing the converted character string | － | String | ANYSTRING＿SINGL E |
| EN |  | Execution condition | － | Bit | BOOL |
| ENO |  | Execution result | － | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U미미，J밈， U3EDI（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions add a decimal point to the 32-bit binary data in the device specified by (s2) at the location specified by ( s 1 ), convert the data to character string data, and store the converted data in the device areas specified by (d) and later.


Ex.
When -654.321 is specified in (s2)


|  | ... | ... | b0 |
| :---: | :---: | :---: | :---: |
| (d) | 36H (6) | 2DH (-) |  |
| (d) +1 | 34H (4) | 35H (5) |  |
| (d)+2 | 33H (3) | 2EH (.) |  |
| (d) +3 | 31H (1) | 32 H (2) |  |
| (d) +4 | OOH |  |  |

- The total number of digits that can be specified by ( s 1 ) is 2 to 13 .
- The number of digits in the decimal part that can be specified by ( s 1 ) +1 is 0 to 10 . Note that the number of digits in the decimal part must be smaller than the total number of digits minus 3 .
- The converted character string data are stored in the device areas specified by (d) and later as shown below.
- As sign data, 20 H (space) is stored if the 32-bit binary data is positive, and $2 \mathrm{DH}(-)$ is stored if the data is negative.
- If the number of digits in the decimal part is set to other than 0, " 2 EH " (.) is automatically stored at the position before the specified number of digits. If the number of digits in the decimal part is $0, " 2 E H$ " (.) is not stored.

- If the specified number of digits in the decimal part is greater than the number of digits of the 32 -bit binary data, Os are automatically added and the data is regarded as "0.ㅁㅁㅁㅁ".

- If the total number of digits excluding the sign and the decimal point is greater than the number of digits of the 32-bit binary data, 20H (space) is stored between the sign and the numeric value. If the number of digits of the 32 -bit binary data is greater, an error occurs.

- The value " 00 H " is automatically stored at the end of the converted character string.


## Operation error

| Error code (SDO) | Description |
| :---: | :---: |
| 3401H | Invalid data that cannot be converted is input to (s1). <br> - The specified total number of digits is out of the range, 2 to 13 . <br> - The specified number of digits in the decimal part is out of the range, 0 to 10 . <br> - The relationship between the total number of digits specified by ( s 1 ) and the number of digits in the decimal part specified by ( s 1 ) +1 does not satisfy the following. <br> (Total number of digits)- $3 \geq$ Number of digits in the decimal part <br> - The number of digits specified by ( s 1 ) is smaller than the number of digits plus 2 of the 32 -bit binary data specified by ( s 2 ). <br> [Number of digits in ( s 1 )] < [Number of digits of 32-bit binary data excluding the sign in (s2) + Number of digits in the sign (+ or -) + (decimal point (.)] |

## Converting BCD 4－digit data to decimal ASCII code

## BCDDA（P）

These instructions convert BCD 4－digit data to the ASCII code．

| Ladder | ST |
| :---: | :---: |
|  | $\begin{aligned} & \mathrm{ENO}:=\mathrm{BCDDA}(\mathrm{EN}, \mathrm{~s}, \mathrm{~d}) ; \\ & \mathrm{ENO}:=\mathrm{BCDDAP}(\mathrm{EN}, \mathrm{~s}, \mathrm{~d}) ; \end{aligned}$ |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| BCDDA | - |
|  | $\boxed{ }$ |
| BCDDAP | - |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | BCD data used for ASCII conversion | 0 to 9999 | BCD 4－digit | ANY16 |
| （d） | Start device for storing the conversion result | - | String | ANYSTRING＿SINGL <br> E |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3E미（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions convert the numerical value of each digit of the BCD 4-digit data in the device specified by (s) to the ASCII code, and store the converted data in the device number specified by (d) and later.
(s)


When SM701 is off, 0 is stored.


## Ex.

When 9105 is specified in (s)
(s)


|  | ... | b8 b7 | ... | b0 |
| :---: | :---: | :---: | :---: | :---: |
| (d) | 31H (1) |  | 39H (9) |  |
| (d) +1 | 35H (5) |  | 30 H (0) |  |
| (d) +2 | 00H |  |  |  |

- 20 H is stored for the leading zeros at the left of the effective number of digits of the operation result stored in the device specified by (d). (Zero-suppression) In the case of "0050", for example, "00" becomes 20 H and " 50 " is the effective number of digits.
- As for storing data in the device specified by (d) $+2,0$ is stored when SM701 is off, and the current data remains unchanged when SM701 is on.
Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3401 H | Data in the device specified by (s) is out of the range, 0 to 9999. |

## Converting BCD 8－digit data to decimal ASCII code

## DBCDDA（P）

These instructions convert BCD 8－digit data to the ASCII code．

| Ladder | ST |
| :---: | :---: |
|  | $\begin{aligned} & \mathrm{ENO}:=\mathrm{DBCDDA}(\mathrm{EN}, \mathrm{~s}, \mathrm{~d}) ; \\ & \mathrm{ENO}:=\mathrm{DBCDDAP}(\mathrm{EN}, \mathrm{~s}, \mathrm{~d}) ; \end{aligned}$ |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DBCDDA | - |
|  | $\boxed{ }$ |
| DBCDDAP | - |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | BCD data used for ASCII conversion | 0 to 99999999 | BCD 8－digit | ANY32 |
| （d） | Start device for storing the conversion result | - | String | ANYSTRING＿SINGL <br> E |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | J밈 | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions convert the numerical value of each digit of the BCD 8-digit data in the device specified by (s) to the ASCII code, and store the converted data in the device number specified by (d) and later.



## Ex.

When 01234056 is specified in (s)


|  | $\ldots$ | b8 b7 | $\ldots$ | b0 |
| :---: | :---: | :---: | :---: | :---: |
| (d) | 31H (1) |  | 20 H (space) |  |
| (d) +1 | 33H (3) |  | 32 H (2) |  |
| (d) +2 | 30 H (0) |  | 34 H (4) |  |
| (d) +3 | 36H (6) | , | 35H (5) |  |
| (d) +4 |  | OOH |  |  |

- 20 H is stored for the leading zeros at the left of the effective number of digits of the operation result stored in the device specified by (d). (Zero-suppression) In the case of "00012098", for example, "000" becomes 20 H and "12098" is the effective number of digits.
- As for storing data in the device specified by (d) $+4,0$ is stored when SM701 is off, and the current data remains unchanged when SM701 is on.


## Operation error

| Error code (SD0) | Description |
| :--- | :--- |
| 3401 H | Data in the device specified by $(\mathrm{s})$ is out of the range, 0 to 99999999. |

## Converting single－precision real number to string data

## ESTR（P）

These instructions convert single－precision real number data to a string according to the display specification．


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| ESTR | - |
|  | $\boxed{Y}$ |
| ESTRP | - |

Setting data
Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Single－precision real number data to be converted，or the <br> start device containing the data | $0,2^{-126<\|(s 1)\|<2^{128}}$ | Single－precision real <br> number | ANYREAL＿32 |
| （s2） | Start device containing the display specification of the real <br> number to be converted | - | 16 －bit signed binary | ANY16＿ARRAY <br> （Number of elements： <br> $3)$ |
| （d） | Start device for storing the converted character string | - | String | ANYSTRING＿SINGL <br> E |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）Gロ | z | $\begin{array}{\|l\|} \hline \text { LT, LST, } \\ \text { LC } \end{array}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions convert the single-precision real number data stored in the device specified by ( s 1 ) to a character string according to the display specification stored in the device number specified by ( s 2 ) and later, and store the converted data in the device number specified by (d) and later.
- The type of the converted data varies depending on the display specification stored in the device specified by ( s 2 ).
(s2)

| 0: Decimal point format <br> 1: Exponent format |
| :--- |
| Total number of digits |
| Number of digits in the <br> decimal part |

## Decimal point format

- When 0 is specified in (s2), the decimal point format is used.

- Total number of digits in the device specified by ( s 2 ) +1 : When the number of digits in the decimal part is 0 , total number of digits (maximum of 24 ) $\geq$ number of digits in the integral part ${ }^{* 1}+1$. When it is a value other than 0 , total number of digits (maximum of 24 ) $\geq$ (number of digits in the integral part ${ }^{* 1}+$ number of digits in the decimal part +2 ).
*1 Indicates the number of digits in the integral part of the 32-bit floating point real number data in the device specified by (s1).
- The number of digits in the decimal part that can be specified by ( s 2 2)+2 is 0 to 7 . Note that the number of digits in the decimal part must be smaller than the total number of digits minus 3 .
- The converted character string data are stored in the device areas specified by (d) and later as shown below.
- As sign data, 20H (space) is stored if the single-precision real number data is positive, and 2DH (-) is stored if the data is negative.
- If the decimal part of the single-precision real number data is not stored within the range of the number of digits in the decimal part, the lower decimal digits are rounded off.

- If the number of digits in the decimal part is set to a value other than $0,2 \mathrm{EH}($.$) is automatically stored at the position of the specified number of digits in the$ decimal part plus 1. If the number of digits in the decimal part is $0, ~ " 2 E H$ " (.) is not stored.

- If the number of digits excluding the sign, decimal point, and decimal part from the total number of digits is greater than the number of digits in the integral part of single-precision real number data, 20 H (space) is stored between the sign and integral part.

- The value " 00 H " is automatically stored at the end of the converted character string.
- The number of digits in the integral part of the 32-bit floating point real number data in the device specified by (s1) can be 1 to 16 .


## Exponent format

- When 1 is specified in (s2), the exponent format is used.

- Total number of digits in the device specified by ( s 2 ) +1 : When the number of digits in the decimal part is 0 , total number of digits (maximum of 24 ) $\geq 2$. When it is a value other than 0 , total number of digits (maximum of 24 ) $\geq$ (number of digits in the decimal part+7).
- The number of digits in the decimal part that can be specified by ( s 2 ) +2 is 0 to 7 . Note that the number of digits in the decimal part must be smaller than the total number of digits minus 7 .
- The converted character string data are stored in the device areas specified by (d) and later as shown below.
- As sign data in the integral part, 20H (space) is stored if the single-precision real number data is positive, and 2DH (-) is stored if the data is negative
- The integral part is fixed to one digit. 20H (space) is stored between the integral part and sign.

- If the decimal part of the single-precision real number data is not stored within the range of the number of digits in the decimal part, the lower decimal digits are rounded off.

- If the number of digits in the decimal part is set to a value other than $0,2 \mathrm{EH}($.$) is automatically stored at the position of the specified number of digits in the$ decimal part plus 1 . If the number of digits in the decimal part is 0, " 2 EH " (.) is not stored.

|  | (s2) |
| :--- | :---: |
|  | 1 |
| $(\mathrm{~s} 2)+1$ | 12 |
|  | (s2)+2 |
|  |  |

Total number of digits


- As sign data in the exponent part, $2 \mathrm{BH}(+)$ is stored if the exponent is positive, and $2 \mathrm{DH}(-)$ is stored if it is negative.
- The exponent part is fixed to two digits. When the exponent part is one digit, $30 \mathrm{H}(0)$ is stored between the exponent part and sign.

- The value " 00 H " is automatically stored at the end of the converted character string.
- When an input value is set using the engineering tool, a rounding error may occur. Refer to the following for the precautions on setting input values using the engineering tool.


## $\bigcirc$ Page 41 Precautions

## Operation error

| Error code (SDO) | Description |
| :---: | :---: |
| 3401H | The value in the device specified by ( s 1 ) is not in the following range. $0,2^{-126} \leq\|(\mathrm{s} 1)\|<2^{128}$ |
|  | Invalid data that cannot be converted is set to (s2). <br> - The format specification in the device specified by ( s 2 ) is a value other than 0 and 1. <br> - In the decimal point format, the total number of digits specified by ( s 2 ) +1 is outside the following range. <br> The number of digits in the decimal part is 0 : Total number of digits $\geq$ number of digits in the integral part ${ }^{* 1}+1$ <br> The number of digits in the decimal part is not 0 : Total number of digits $\geq$ number of digits in the integral part ${ }^{* 1}+$ number of digits in the decimal point +2 <br> - In the exponent format, the total number of digits specified by (s2)+1 is outside the following range. <br> The number of digits in the decimal part is 0 : Total number of digits $\geq 6$ <br> The number of digits in the decimal part is not 0 : Total number of digits $\geq$ (number of digits in the decimal part +7 ) <br> - In the exponent point format, the decimal part digit specification in the device specified by (s2)+2 is outside the following range. <br> Decimal point format: Number of digits in the decimal part<(total number of digits - 3) <br> Exponent format: Number of digits in the decimal part<(total number of digits - 7) <br> - In the decimal point format, the number of digits in the integral part of 32 -bit floating point real number data in the device specified by ( s 1 ) exceeds 16. <br> - The number of digits in the decimal part specified by $(\mathrm{s} 2)+2$ is out of the range from 0 to 7 . |
|  | The specified value consists of more than 24 digits in total. |
| 3402 H | The value input to ( s 1 ) is -0 , a subnormal number, NaN ( not a number), or $\pm \infty$. |

*1 Indicates the number of digits in the integral part of the 32-bit floating point real number data in the device specified by (s1).

## Converting hexadecimal binary data to hexadecimal ASCII code

## INT2ASC（P）

These instructions convert 16－bit binary data to the hexadecimal ASCII code and store it in any specified range．


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| INT2ASC | - |
|  | $\boxed{Z}$ |
| INT2ASCP | - |

Setting data
Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Start device containing the binary data to be exchanged to <br> a character string | - | 16－bit signed binary | ANY16 |
| （d） | Start device for storing the converted character string | - | String | ANYSTRING＿SINGL <br> E |
| （n） | Number of characters to be stored | 0 to 16383 | 16－bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J밈， U3EDI（H）GD | z | LT，LST， LC | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- Converts the 16-bit binary data in the device number specified by (s) and later to hexadecimal ASCII, and stores the converted data by the number of characters in the device specified by ( n ) in the device number specified by ( d ) and later.

- Setting the number of bytes by $(\mathrm{n})$ automatically determines the range of binary data in the device specified by $(\mathrm{s})$ and the range of the device specified by (d) for storing the character string data.
- Processing is performed normally even if the device range in which the binary data to be converted and the device range for storing the converted binary data are overlapping.


|  | $\ldots$ | b8b7 | ... | b0 |
| :---: | :---: | :---: | :---: | :---: |
| D10 | 32H (2) |  | 31H (1) |  |
| D11 | 34H (4) |  | 33H (3) |  |
| D12 | 36H (6) |  | 35H (5) |  |
| D13 | 38H (8) |  | 37H (7) |  |
| D14 | 41H (A) |  | 39H (9) |  |

- If the number of characters in the device specified by $(\mathrm{n})$ is an odd number, 00 H is automatically stored in the upper 8 bits of the last device number among device numbers for storing the converted character string data.

| (s) | 1H | 2 H | 3H | 4H |
| :---: | :---: | :---: | :---: | :---: |
| (s) +1 | 5H | 6 H | 7H | 8 H |
| (s) +2 | FH | EH | DH | CH |
| (s)+3 | AH | 9 H | BH | 6 H |


|  | ... | b8b7 | ... | b0 |
| :---: | :---: | :---: | :---: | :---: |
| (d) | 33H (3) |  | 34H (4) |  |
| (d) +1 | 31H (1) |  | 32 H (2) |  |
| (d) +2 | 37H (7) |  | 38 H (8) |  |
| (d) +3 | 35H (5) |  | 36H (6) |  |
| (d) +4 | 44H (D) |  | 43H (C) |  |
| (d) +5 | 46H (F) |  | 45H (E) |  |
| (d) +6 | 42 H (B) |  | 36H (6) |  |
| (d) +7 | 00H |  | 39 H (9) |  |

- If the number of characters in the device specified by $(\mathrm{n})$ is 0 , no processing is performed.


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3405 H | An out-of-range value is set to $(\mathrm{n})$. <br> $\bullet$ The specified number of characters is not between 0 and 16383. |

## Converting Unicode character string to Shift JIS character string

## WS2SJIS(P)

These instructions convert a Unicode character string to a Shift JIS character string.


FBD/LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| WS2SJIS | - |
|  | $\boxed{ }$ |
| WS2SJISP |  |

## Setting data

■Description, range, data type

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (s) | Start device containing the character string to be converted | - | Unicode string | ANYSTRING_DOUB <br> LE |
| (d) | Start device for storing the converted character string | - | String | ANYSTRING_SINGL <br> E |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | U밈, J밈, U3E미(H)Gㅁ | Z | LT, LST, LC | LZ |  | K, H | E | \$ |  |
| (s) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | $\bigcirc$ | - |
| (d) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |

## Processing details

- These instruction convert the Unicode character string in the device specified by (s) to the shift JIS character string, and stores the converted data in the device specified by (d).
- The Unicode character string in the device specified by (s) should be specified in little endian.
- When a byte order mark is not used, conversion from Unicode to shift JIS occurs as follows.

| Before conversion |  |
| :---: | :---: |
| (s) | b15 |
|  | $\cdots$ |
| D0 | b0 |
| D1 | 4100 H |
| D2 | 4200 H |
| D3 | 4300 H |
|  | 0000 H |

(A)
(B)

(C)
After conversion
(d) b15 $\cdots$ b8 b7 $\cdots$ b0
D100

(BA)

- When a byte order mark (FEFFH) is added, conversion from Unicode to shift JIS occurs as follows.
Before conversion After conversion


After conversion

| (d) | b15 $\cdots$ b 8 b7 $\cdots$ b0 |  | (21) |
| :---: | :---: | :---: | :---: |
| D100 | 32 H | 31H |  |
| D101 | 00H | 33H | (3) |

(3)

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2821 H | The ranges of data in the devices specified by (s) and (d) are overlapping. |
| 3401 H | Byte order mark FEFFH (big endian) is added to the character string in the device specified by (s). |
|  | The range of data in the device specified by (s) includes a character code that cannot be converted. |
| 3405 H | The character string in the device specified by (s) exceeds 16383 characters. ${ }^{*}$ |

[^10]
## Converting shift JIS character string to Unicode character string (without byte order mark)

## SJIS2WS(P)

These instructions convert a Shift JIS character string to a Unicode character string.

| Ladder |  | ST |
| :---: | :---: | :---: |
|  | (d) | $\begin{aligned} & \text { ENO:=SJIS2WS(EN,s,d); } \\ & \text { ENO:=SJIS2WSP(EN,s,d); } \end{aligned}$ |
| FBD/LD |  |  |
|  |  |  |

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| SJIS2WS | - |
|  | - |
| SJIS2WSP | - |

Setting data
-Description, range, data type

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (s) | Start device containing the character string to be converted | - | String | ANYSTRING_SINGL <br> E |
| (d) | Start device for storing the converted character string | - | Unicode string | ANYSTRING_DOUB <br> LE |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ\|ロ | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | U밈, J밈, U3EDl(H)GD | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (s) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | $\bigcirc$ | - |
| (d) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |

## Processing details

- These instructions convert the shift JIS character string in the device specified by (s) to a Unicode character string, and store the converted data in the device specified by (d).
- The shift JIS character string in the device specified by (s) should be specified in little endian.
- The SJIS2WS(P) instruction does not add a byte order mark to the beginning of the data in the device specified by (d). To add a byte order mark, use the SJIS2WSB(P) instruction.
$\longmapsto$ Page 587 SJIS2WSB(P)
- The following figure shows the operation for converting shift JIS to Unicode.



## Operation error

| Error code (SD0) | Description |
| :--- | :--- |
| 2821 H | The ranges of data in the devices specified by (s) and (d) are overlapping. |
| 3401 H | The range of data in the device specified by (s) includes a character code that cannot be converted. |
| 3405 H | The character string in the device specified by (s) exceeds 16383 characters. ${ }^{* 1}$ |

[^11]
## Converting shift JIS character string to Unicode（with byte order mark）

## SJIS2WSB（P）

These instructions convert a shift JIS character string to a Unicode character string，and adds a byte order mark to the start of the converted data．

| Ladder | ST |
| :---: | :---: |
|  | $\begin{aligned} & \text { ENO:=SJIS2WSB(EN,s,d); } \\ & \text { ENO:=SJIS2WSBP(EN,s,d); } \end{aligned}$ |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| SJIS2WSB | - |
|  | $\boxed{ }$ |
| SJIS2WSBP | - |

## Setting data

DDescription，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Start device containing the character string to be converted | - | String | ANYSTRING＿SINGL <br> E |
| （d） | Start device for storing the converted character string | - | Unicode string | ANYSTRING＿DOUB <br> LE |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3EDI（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions convert the shift JIS character string in the device specified by (s) to the Unicode character string, add a byte order mark to the start of the converted data, and store it in the device specified by (d).
- The shift JIS character string in the device specified by (s) should be specified in little endian.
- The following figure shows the operation for converting shift JIS to Unicode.

Before conversion



After conversion


## Operation error

| Error code (SD0) | Description |
| :--- | :--- |
| 2821 H | The ranges of data in the devices specified by (s) and (d) are overlapping. |
| 3401 H | The range of data in the device specified by (s) includes a character code that cannot be converted. |
| 3405 H | The character string in the device specified by (s) exceeds 16383 characters. ${ }^{* 1}$ |
| $* 1$ | A two-byte character such as a kanji character represented in shift JIS code should be counted 2. |

[^12]
## Detecting a string length

## LEN（P）

These instructions detect the length of the specified string．


＊1 The LEN instruction does not support the structured text language and FBD／LD language．Use the standard function，LEN．
$\longmapsto$ Page 1628 LEN（＿E）
－Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| LEN | - |
|  | $\boxed{Z}$ |
| LENP | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Character string or the start device containing the character <br> string | - | String | ANYSTRING＿SINGL <br> E |
| （d） | Number of the device for storing the length of the detected <br> character string | - | 16－bit signed binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions detect the length of the character string in the device specified by (s) and store it in the device number specified by ( d ) and later. The data stored in device numbers starting from the one specified by ( s ) to the one containing 00 H is processed as a character string.



## Ex.

When "ABCDEFGHI" is stored in the device specified by (s) and later


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2820 H | There is no NULL code $(00 \mathrm{H})$ in each setting area in the device/label memory in the device specified by (s) and later. |
| 3405 H | The number of characters of the character string in the device specified by (s) exceeds 16383. |

## Extracting string data from the right

## RIGHT（P）

These instructions extract（ n ）characters of data from the right of string data．


FBD／LD＊${ }^{*}$

＊1 The RIGHT instruction does not support the structured text language and FBD／LD language．Use the standard function，RIGHT． ङ Page 1629 LEFT（＿E），RIGHT（＿E）

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| RIGHT | - |
|  | - |
| RIGHTP | - |

## Setting data

■Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Character string or the start device containing the character <br> string | - | String | ANYSTRING＿SINGL <br> E |
| （d） | Start device for storing $(n)$ characters of character string <br> extracted from the right of the data in the device specified by（s） | - | String | ANYSTRING＿SINGL <br> E |
| （n） | Number of characters to be extracted | 1 to 16383 | 16－bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）GD | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ $n$ ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- These instructions extract ( n ) characters of data from the right of the character string data (the end of the character string) stored in the device number specified by ( $s$ ) and later, and store the extracted data in the device number specified by (d) and later.



## Ex.

When ( n ) $=5$


- The NULL code $(00 \mathrm{H})$ indicating the end of a character string is automatically added to the end of the character string data.
- When the number of characters in the device specified by $(\mathrm{n})$ is $0, \mathrm{NULL}$ code $(00 \mathrm{H})$ is stored in the device specified by ( n ).


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2820 H | There is no NULL code $(00 \mathrm{H})$ in each setting area in the device/label memory in the device specified by (s) and later. |
| 3405 H | An out-of-range value is set to $(\mathrm{s})$. <br> • The number of characters of the character string in the device specified by (s) exceeds 16383. <br> • The number of characters of the character string in the device specified by $(\mathrm{s})$ is 0. |
|  | The number of characters in the device specify by $(\mathrm{n})$ exceeds that in the device specified by (s). |

## Extracting string data from the left

## LEFT（P）

These instructions extract（ $n$ ）characters of data from the left of the string data，and store the extracted data in the device number specified by（ d ）and later．


FBD／LD＊1

＊1 The LEFT instruction does not support the structured text language and FBD／LD language．Use the standard function，LEFT． ↔ Page 1629 LEFT（＿E），RIGHT（＿E）
Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| LEFT | - |
|  | - |
| LEFTP |  |

Setting data
Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Character string or the start device containing the character <br> string | - | String | ANYSTRING＿SINGL <br> E |
| （d） | Start device for storing（n）characters of character string <br> extracted from the left of the data in the device specified by（s） | - | String | ANYSTRING＿SINGL <br> E |
| （n） | Number of characters to be extracted | 1 to 16383 | 16－bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDI（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- These instructions extract ( $n$ ) characters of data from the left of the character string data (the start of the character string) stored in the device number specified by ( $s$ ) and later, and store the extracted data in the device number specified by (d) and later.


Ex.
When ( n )=7


- The NULL code $(00 \mathrm{H})$ indicating the end of a character string is automatically added to the end of the character string data.
- When the number of characters in the device specified by $(n)$ is $0, N U L L$ code $(00 H)$ is stored in the device specified by ( $n$ ).


## Operation error

| Error code (SDO) | Description |
| :---: | :---: |
| 2820H | There is no NULL code (00H) in each setting area in the device/label memory in the device specified by (s) and later. |
| 3405H | An out-of-range value is set to (s). <br> - The number of characters of the character string in the device specified by (s) exceeds 16383. <br> - The number of characters of the character string in the device specified by (s) is 0 . |
|  | The number of characters in the device specify by ( n ) exceeds that in the device specified by (s). |

## Extracting the specified string data

## MIDR(P)

These instructions extract data at any position in string data, and store the extracted data in the device number specified by (d) and later.


## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| MIDR | - |
|  | $\boxed{4}$ |
| MIDRP | - |

## Setting data

■Description, range, data type

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (s1) | Character string or the start device containing the <br> character string | - | ANYSTRING_SINGL <br> E |  |
| (d) | Start device for storing the character string data of the <br> operation result | - | ANYSTRING_SINGL <br> E |  |
| (s2) | Start device for storing the location of the start character <br> and the number of characters <br> (s2): Location of start character, (s2)+1: Number of <br> characters | - | 16-bit signed binary | ANY16_ARRAY <br> (Number of elements: <br> $2)$ |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ\|ロ | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | UपIGㅁ, J밈, U3EDI(H)GD | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (s1) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | $\bigcirc$ | - |
| (d) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (s2) | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - | $\bigcirc$ | - | - | - | - |

## Processing details

- These instructions extract the data by the number of characters specify by (s2)+1 from the location specified by (s2) in the character string data stored in the device number specified by ( s 1 ) and later, and store the extracted data in the device number specified by (d) and later.

- The NULL code $(00 \mathrm{H})$ indicating the end of a character string is automatically added to the end of the character string data.
- If the number of characters in the device specified by (s2)+1 is 0 , no processing is performed.
- When the number of characters of the data in the device specified by ( s 2 ) +1 is -1 , the data till the last character data in the device specified by ( s 1 ) is stored in the device specified by (d) and later.



## Operation error

| Error code (SDO) | Description |
| :---: | :---: |
| 2820 H | There is no NULL code (00H) in each setting area in the device/label memory in the device specified by ( s 1 ) and later. |
| 3405H | The number of characters of the character string in the device specified by (s1) exceeds 16383. |
|  | An out-of-range value is set to (s2). <br> - The value in the device specified by ( s 2 ) is 0 or less. <br> - The value in the device specified by ( s 2 ) +1 is other than the valid values ( $-1,0,1$ or bigger). <br> - The value in the device specify by ( s 2 ) exceeds the number of characters in the device specified by ( s 1 ). <br> - The value obtained by adding those in the devices specify by (s2) and (s2)+1 exceeds the number of characters in the device specified by (s1). |

## Replacing the specified string data

## MIDW（P）

These instructions replace the data at the specified location in the string data with the specified string．


FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| MIDW | - |
|  | $\boxed{Z}$ |
| MIDWP | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Character string or the start device containing the <br> character string | - | String | ANYSTRING＿SINGL <br> E |
| （d） | Start device for storing the character string data of the <br> operation result | - | String | ANYSTRING＿SINGL <br> E |
| （s2） | Start device for storing the location of the start character <br> and the number of characters <br> （s2）： Location of start character，（s2）＋1： Number of <br> characters | - | 16－bit signed binary | ANY16＿ARRAY <br> （Number of elements： <br> $2)$ |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，Jप\ロ， U3EDI（H）GD | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions read data by the number of characters stored in the device specified by (s2)+1 from the character string data stored in the device number specified by ( s 1 ) and later, and store the read data at the location in the device specified by (s2) and later in the character string stored in the device number specified by (d) and later.

|  | ... | b8 b7 | ... |
| :---: | :---: | :---: | :---: |
| (s1) | 31H (1) |  | $30 \mathrm{H}(0)$ |
| (s1)+1 | 33H (3) |  | 32 H (2) |
| (s1)+2 | 35H (5) |  | 34 H (4) |
| (s1)+3 | 37H (7) |  | 36H (6) |
| (s1)+4 | 00H |  | 38H (8) |



Position counted from the left of the string data specified by (d) Number of characters counted from the left of the string data specified by (s1)


- The NULL code $(00 \mathrm{H})$ indicating the end of a character string is automatically added to the end of the character string data.
- If the number of characters in the device specified by (s2)+1 is 0 , no processing is performed.
- If the number of characters in the device specified by (s2)+1 exceeds the last character of the character string data in the device specified by (d), the data is stored up to the last character.

|  | ... | b8b7 | ... | b0 |
| :---: | :---: | :---: | :---: | :---: |
| (s1) | 31H (1) |  | 30 H (0) |  |
| (s1)+1 | 33H (3) |  | 32 H (2) |  |
| (s1)+2 | 35H (5) |  | 34H (4) |  |
| (s1) +3 | 37H (7) |  | 36H (6) |  |
| (s1) +4 | 00H |  | 38H (8) |  |



|  | Before execution |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
|  | b15 ... | b8b7 | ... | b0 |
| (d) | 42H (B) |  | 41H (A) |  |
| (d) +1 | 44H (D) |  | 43H (C) |  |
| (d) +2 | 46H (F) |  | 45H (E) |  |
| (d) +3 | $48 \mathrm{H}(\mathrm{H})$ |  | 47H (G) |  |
| (d) +4 | 00H |  | 49H (I) |  |


|  | After execution b8b7 |  |  | b0 |
| :---: | :---: | :---: | :---: | :---: |
| (d) | 42H (B) |  | 41H (A) |  |
| (d) +1 | 44H (D) |  | 43H (C) |  |
| (d) +2 | 31 H (1) |  | 30 H (0) |  |
| (d) +3 | 33 H (3) |  | 32 H (2) |  |
| (d) +4 | 00H |  | 34H (4) |  |
|  |  | CD012 |  |  |

- When the number of characters of the data in the device specified by ( s 2 ) +1 is -1 , the data till the last character data in the device specified by ( s 1 ) is stored in the device specified by (d) and later.


Position counted from the left of the string data specified by (d)
Number of characters counted from the left of the string data specified by (s1)

|  | Before execution |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
|  | b15 ... | b8b7 | $\ldots$ | b0 |
| (d) | 42H (B) |  | 41H (A) |  |
| (d) +1 | 44H (D) |  | 43H (C) |  |
| (d) +2 | 46H (F) |  | 45H (E) |  |
| (d) +3 | 48H (H) |  | 47H (G) |  |
| (d) +4 | 4AH (J) |  | 49H (I) |  |
| (d) +5 | 00H |  | 4BH (K) |  |



## Operation error

| Error code (SDO) | Description |
| :---: | :---: |
| 2820 H | There is no NULL code (00H) in each setting area in the device/label memory in the device specified by (s1) and later. |
|  | There is no NULL code (00H) in each setting area in the device/label memory in the device specified by (d) and later. |
| 3405H | The number of characters of the character string in the device specified by (s1) exceeds 16383. |
|  | The number of characters of the character string in the device specified by (d) exceeds 16383. |
|  | An out-of-range value is set to (s2). <br> - The value in the device specified by ( s 2 ) is 0 or less. <br> - The value in the device specified by ( s 2 ) +1 is other than the valid values ( $-1,0,1$ or bigger). <br> - The value in the device specify by ( s 2 ) exceeds the number of characters in the device specified by (d). <br> - The value in the device specify by ( s 2 ) +1 exceeds the number of characters in the device specified by ( s 1 ). |

## Searching string data

## INSTR（P）

These instructions search string data for the specified string．

|  |  |  |  |  | ```ST ENO:=INSTR(EN,s1,s2,s3,d); ENO:=INSTRP(EN,s1,s2,s3,d);``` |
| :---: | :---: | :---: | :---: | :---: | :---: |
| ■－－－$]$（s1） |  | Ladder |  |  |  |
|  |  | （s2） | （d） | （s3） |  |

FBD／LD


## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| INSTR | - |
|  | $\boxed{Z}$ |
| INSTRP | $\boxed{ }$ |

Setting data
Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Character strings to be searched or the start device <br> containing these character strings | - | String | ANYSTRING＿SINGL <br> E |
| （s2） | Character string to be searched for or the start device <br> containing the character string to be searched for | - | String | ANYSTRING＿SINGL <br> E |
| （d） | Device for storing the search result | - | 16 －bit signed binary | ANY16 |
| （s3） | Search start position | 1 to 16383 | 16－bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3E미（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |
| （s3） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- These instructions search the character string data stored in the device number specified by ( s 2 ) and later starting from the ( s 3 )th character from the left for the character string data stored in the device number specified by ( s 1 ) and later, and store the search result in the device specified by (d). The search result stored shows the number of characters from the start character of the character string data in the device specified by (s2).

|  | ... | b8 b7 | ... | b0 | Starting position specified by (s3): 3rd character <br> $\longleftarrow$ 5th character from the beginning of the data |
| :---: | :---: | :---: | :---: | :---: | :---: |
| (s2) | 42H (B) | ! | 41H (A) |  |  |
| (s2)+1 | 44H (D) | I | 43H (C) |  |  |
| (s2)+2 | 46H (F) | I | 45H (E) |  |  |
| (s2)+3 | 48H (H) | I | 47H (G) |  |  |
| (s2)+4 | 4AH (J) | , | 49H (I) |  |  |
| (s2)+5 | 00H |  | 4BH (K) |  |  |


(d) 5

- If no character string data is matching, 0 is stored in the device specified by (d).


## Operation error

| Error code (SDO) | Description |
| :---: | :---: |
| 2820H | There is no NULL code ( 00 H ) in each setting area in the device/label memory in the device specified by ( s 1 ) and later. |
|  | There is no NULL code (00H) in each setting area in the device/label memory in the device specified by (s2) and later. |
| 3405 H | An out-of-range value is set to (s1). <br> - The number of characters of the character string that has been set is 0 . <br> - The number of characters of the character string that has been set exceeds 16383. |
|  | The number of characters of the character string that has been set in the device specified by (s2) exceeds 16383. |
|  | An out-of-range value is set to (s3). <br> - The value in the device specify by ( s 3 ) exceeds the number of characters in the device specified by ( s 2 ). <br> - The value in the device specified by ( s 3 ) is negative or 0 . |

## Inserting string data

## STRINS（P）

These instructions insert the specified string data into the specified position of the string data．


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| STRINS | - |
|  | $\boxed{Z}$ |
| STRINSP | - |

## Setting data

Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Character string to be inserted or the start device <br> containing the character string to be inserted | - | String | ANYSTRING＿SINGL |
| （d） | Start device for storing the insertion result character string | - | String | ANYSTRING＿SINGL <br> E |
| （s2） | Insertion position（bytes） | 1 to 16383 | 16－bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3E미（H）Gㅁ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- These instructions insert the character string data in the device specified by ( s 1 ) to the ( s 2 )th character (insertion position) from the start of the character string data in the device specified by (d).

|  | b15 | $\cdots$ | b8 b7 | $\cdots$ |
| :--- | :---: | :---: | :---: | :---: |
| (s1) | $31 \mathrm{H}(1)$ |  | $30 \mathrm{H}(0)$ |  |
| $(\mathrm{s} 1)+1$ | $33 \mathrm{H}(3)$ |  | $32 \mathrm{H}(2)$ |  |
| $(\mathrm{s} 1)+2$ | 00 H |  | $34 \mathrm{H}(4)$ |  |
|  |  |  |  |  |



The string data of 5 characters starting from the 3rd character are shifted to the left, and the data "01234" is inserted.


Starting position specified by (s2): 3rd character


The existing string data in (d)+5 and later are overwritten with the data by an amount equal to the number of characters to be inserted.

- If the character string after insertion in the device specified by ( $\mathbf{s} 1$ )+(d) is even, the NULL code $(00 \mathrm{H})$ is stored in the device (1 word) next to the last one containing the character string.
- If the character string after insertion in the device specified by (s1)+(d) is odd, the NULL code (00H) is stored in the last device (upper 8 bits) of the character string.
- When the number of characters in the device specified by (d) plus 1 is specified in ( s 2 ), the character string in the device specified by ( s 1 ) is concatenated to the end of the character string in the device specified by (d).

Operation error

| Error code (SD0) | Description |
| :---: | :---: |
| 2820 H | There is no NULL code (00H) in each setting area in the device/label memory in the device specified by (s1) and later. |
|  | There is no NULL code ( 00 H ) in each setting area in the device/label memory in the device specified by (d) and later. |
| 2821H | The devices specified by (s1) and (d), both containing character strings, are overlapping even partly. |
|  | The device specified by (s1)+(d) containing the character string after insertion overlaps with the character string storage device specified by ( s 1 ). |
| 3405H | The number of characters of the character string in the device specified by (s1) exceeds 16383. |
|  | An out-of-range value is set to (s2). <br> - The specified value exceeds the number of characters plus 1 of the character string in the device specified by (d). <br> - The specified value is not within the following range. $1 \leq(\mathrm{s} 2) \leq 16383$ |
|  | The number of characters of the character string in the device specified by (d) exceeds 16383. |
| 3406 H | The character string after insertion stored in the device specified by (s1)+(d) becomes data outside the output enable range. <br> - The number of characters of the character string after insertion in the device specified by (d) exceeds 16383. <br> - The character string after insertion exceeds each setting area in the specified device/label memory. |

## Deleting string data

## STRDEL(P)

These instructions delete $(\mathrm{n})$ characters starting from the specified position of string data.


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| STRDEL | - |
|  | $\boxed{Z}$ |
| STRDELP | - |

## Setting data

Descriptions, ranges, and data types

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (d) | Start device containing the character string to be deleted | - | String | ANYSTRING_SINGL <br> E |
| (s) | Deletion start position | 1 to 16383 | 16-bit unsigned binary | ANY16 |
| (n) | Number of characters to be deleted | 0 to 16384-(s) | 16 -bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ\|ロ | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | U밈, J밈, U3EDI(H)GD | z | LT, LST, LC | LZ |  | K, H | E | \$ |  |
| (d) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (s) | - | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - | $\bigcirc$ | $\bigcirc$ | - | - | - |
| ( n ) | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - | $\bigcirc$ | $\bigcirc$ | - | - | - |

## Processing details

- These instructions delete ( n ) characters starting from the position (deletion start position) specified by the (s)th character from the start of the character string data in the device specified by (d).

- If the character string after deletion in the device specified by (d) is even, the NULL code $(00 \mathrm{H})$ is stored in the device (1 word) next to the last one containing the character string.
- If the character string after deletion in the device specified by (d) is odd, the NULL code $(00 \mathrm{H})$ is stored in the last device (upper 8 bits) of the character string.
- The character string following the deleted one is shifted by $(\mathrm{n})$ characters to the right, and the NULL code $(00 \mathrm{H})$ is stored in the device that has been emptied.


## Operation error

| Error code (SD0) | Description |
| :---: | :---: |
| 2820 H | There is no NULL code (00H) in each setting area in the device/label memory in the device specified by (d) and later. |
| 3405H | The number of characters of the character string in the device specified by (d) exceeds 16383. |
|  | An out-of-range value is set to (s). <br> - The specified value is not within the following range. $1 \leq(s) \leq 16383$ <br> - The specified value exceeds the number of characters of the character string in the device specified by (d). |
|  | An out-of-range value is set to ( $n$ ). <br> - The specified value exceeds the number of characters from the data in the device specified by (s) of the character string in the device specified by (d) to the last character. <br> - The specified value is negative. |

## 7．9 Real Number Instructions

## Comparing single－precision real numbers

## LDED，ANDED，ORED

These instructions perform a comparison operation of a single－precision real number．（Devices are used as a normally open contact．）


FBD／LD

（ $\square$ is replaced by a combination of LDE ，ANDE ，or ORE and EQ，NE，GT，LE，LT，or GE．）

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| LDED，ANDED，ORED | Every scan |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Comparison data or the start device where the comparison <br> data is stored | $0,2^{-126} \leq\|(s 1)\|<2^{128}$ | Single－precision real <br> number | ANYREAL＿32 |
| （s2） | Comparison data or the start device where the comparison <br> data is stored | $0,2^{-126 \leq\|(s 2)\|<2^{128}}$ | Single－precision real <br> number | ANYREAL＿32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J밈， U3EDl（H）GD | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （s2） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | $\bigcirc$ | － | － |

## Processing details

- These instructions perform a comparison operation between the single-precision real number in the device specified by ( s 1 ) and the single-precision real number in the device specified by ( s 2 ). (Devices are used as a normally open contact.)
- The following table lists the comparison operation results of each instruction.

| Instruction symbol (ladder, FBD/LD) | Condition | Result |
| :---: | :---: | :---: |
| $\mathrm{E}=, \mathrm{EQ}$ | (s1)=(s2) | Continuity state (ENO is on.) |
| E<>, NE | (s1) $=(\mathrm{s} 2)$ |  |
| E>, GT | (s1)>(s2) |  |
| $\mathrm{E}<=$, LE | (s1) $\leq$ (s2) |  |
| $\mathrm{E}<$, LT | (s1)<(s2) |  |
| E>=, GE | (s1) $\geq$ ( s 2$)$ |  |
| $\mathrm{E}=$, EQ | (s1) $=(\mathrm{s} 2)$ | Non-continuity state (ENO is off.) |
| E<>, NE | (s1)=(s2) |  |
| E>, GT | (s1) $\leq$ (s2) |  |
| E<=, LE | (s1)>(s2) |  |
| E<, LT | (s1) $\geq$ (s2) |  |
| E>=, GE | (s1)<(s2) |  |

- If the data in the device specified by ( $s 1$ ) or ( $s 2$ ) is out of the range of setting data, the operation result will be non-continuity (ENO OFF).
- If the LDE_口 instruction is used in the program written in FBD/LD, always set EN to TRUE.
- If the ORE_ $\square$ instruction is used in the program written in FBD/LD and EN is set to TRUE, ENO turns on. EN will not be an execution condition.
- When an input value is set using the engineering tool, a rounding error may occur. Refer to the following for the precautions on setting input values using the engineering tool.
$\longmapsto$ Page 41 Precautions


## Operation error

There is no operation error.

## Point ${ }^{\rho}$

Note that two values may not be equal due to an error when the $\mathrm{E}=$ instruction is used.


## Comparing double－precision real numbers

## LDED，ANDEDD，ORED

These instructions perform a comparison operation of a double－precision real number．（Devices are used as a normally open contact．）


FBD／LD

（ $\square$ is replaced by a combination of LDED＿，ANDED＿，or ORED＿and EQ，NE，GT，LE，LT，or GE．）
Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| LDEDD，ANDEDD，OREDロ | Every scan |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Comparison data or the start device where the comparison <br> data is stored | $0,2^{-1022 \leq\|(s 1)\|<2^{1024}}$ | Double－precision real <br> number | ANYREAL＿64 |
| （s2） | Comparison data or the start device where the comparison <br> data is stored | $0,2^{-1022 \leq\|(s 2)\|<2^{1024}}$ | Double－precision real <br> number | ANYREAL＿64 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | － | － |

## Processing details

- These instructions perform a comparison operation between the double-precision real number in the device specified by ( s 1 ) and the double-precision real number in the device specified by ( s 2 ). (Devices are used as a normally open contact.)
- The following table lists the comparison operation results of each instruction.

| Instruction symbol (ladder, FBD/LD) | Condition | Result |
| :---: | :---: | :---: |
| ED=, EQ | (s1)=(s2) | Continuity state (ENO is on.) |
| ED<>, NE | (s1) $=(\mathrm{s} 2)$ |  |
| ED>, GT | (s1)>(s2) |  |
| ED<=, LE | (s1) $\leq$ (s2) |  |
| ED<, LT | (s1)<(s2) |  |
| ED>=, GE | (s1) $\geq$ (s2) |  |
| ED=, EQ | (s1) $=(\mathrm{s} 2)$ | Non-continuity state (ENO is off.) |
| ED<>, NE | (s1)=(s2) |  |
| ED>, GT | (s1) $\leq$ (s2) |  |
| ED<=, LE | (s1)>(s2) |  |
| ED<, LT | (s1) $\geq$ (s2) |  |
| ED>=, GE | (s1)<(s2) |  |

- If the data in the device specified by ( $s 1$ ) or ( $s 2$ ) is out of the range of setting data, the operation result will be non-continuity (ENO OFF).
- If the LDED_ $\square$ instruction is used in the program written in FBD/LD, always set EN to TRUE.
- If the ORED_ $\square$ instruction is used in the program written in FBD/LD and EN is set to TRUE, ENO turns on. EN will not be an execution condition.
- When an input value is set using the engineering tool, a rounding error may occur. Refer to the following for the precautions on setting input values using the engineering tool.Page 41 Precautions


## Operation error

There is no operation error.

## Precautions

The maximum number of digits of a real number which can be input using the engineering tool is 15 , and therefore these instructions cannot perform comparison with a real number consisting of 16 or more effective digits. When these instructions are used to determine the match or mismatch with a real number consisting of 16 or more effective digits, the instructions need to compare the size with the approximate values before and after the real number to be compared.

Ex.
To determine the match between E1.234567890123456+10 (16 effective digits) and a double-precision real number


Whether data in D0 to D3 are within this range is checked. (The boundary values are not included.)

## Ex.

To determine the mismatch between E1.234567890123456+10 (16 effective digits) and a double-precision real number


## Adding single－precision real numbers

## E＋（P）［when two operands are set］

These instructions add single－precision real numbers．


## FBD／LD

Not supported

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| $\mathrm{E}+$ | - |
| $\mathrm{E}+\mathrm{P}$ | $\boxed{ }$ |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Second addend data or the start device where the second <br> addend data is stored | $0,2^{-126} \leq\|(\mathrm{s})\|<2^{128}$ | Single－precision real <br> number | ANYREAL＿32 |
| （d） | Start device where the first addend data is stored | $0,2^{-126 \leq\|(d)\|<2^{128}}$ | Single－precision real <br> number | ANYREAL＿32 |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3EDI（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （d） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions add the single－precision real number in the device specified by（s）to the single－precision real number in the device specified by（d），and store the result in the device specified by（d）．
（d）+1
（d）

（d）+1
（d）

Single－precision real number
Single－precision real number
Single－precision real number
－Value 0 or $2^{-126} \leq \mid$ specified value（stored value） $\mid<2^{128}$ can be specified or stored in the devices specified by（s）and（d）．
－When an input value is set using the engineering tool，a rounding error may occur．Refer to the following for the precautions on setting input values using the engineering tool．
$\Im$ Page 41 Precautions

## Operation error

| Error code（SDO） | Description |
| :--- | :--- |
| 3402 H | The value input to（s）is -0, a subnormal number，NaN（not a number），or $\pm \infty$. |
|  | The value input to（d）is -0, a subnormal number，NaN（not a number），or $\pm \infty$. |
| 3403 H | The data in the device specified by（d）exceeds the following range．（An overflow has occurred．） <br> $\|(\mathrm{d})\|<2^{128}$ |

## $E+(P)$［when three operands are set］

These instructions add single－precision real numbers．


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| $\mathrm{E}+$ | - |
| $\mathrm{E}+\mathrm{P}$ | $\boxed{ }$ |

## Setting data

DDescription，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Augend data or the start device containing augend data | $0,2^{-126} \leq\|(s 1)\|<2^{128}$ | Single－precision real <br> number | ANYREAL＿32 |
| （s2） | Addend data or the start device where addend data is <br> stored | $0,2^{-126} \leq\|(s 2)\|<2^{128}$ | Single－precision real <br> number | ANYREAL＿32 |
| （d） | Start device for storing the operation result | - | Single－precision real <br> number | ANYREAL＿32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDI（H）Gロ | Z | LT，LST， LC | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （s2） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （d） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions add the single－precision real number in the device specified by（s2）to the single－precision real number in the device specified by（ s 1 ），and store the result in the device specified by（d）．


Single－precision real number Single－precision real number Single－precision real number
－Value 0 or $2^{-126} \leq \mid$ specified value（stored value） $\mid<2^{128}$ can be specified or stored in the devices specified by（s1），（s2），and （d）．

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The value input to (s1) is -0 , a subnormal number, NaN (not a number), or $\pm \infty$. |
|  | The value input to (s2) is -0 , a subnormal number, NaN (not a number), or $\pm \infty$. |
| 3403 H | The data in the device specified by (d) exceeds the following range. (An overflow has occurred.) <br> $\|(\mathrm{d})\|<2^{128}$ |

## Subtracting single－precision real numbers

## E－（P）［when two operands are set］

These instructions perform subtraction between single－precision real numbers．

| Ladder | ST |  |  |
| :--- | :--- | :--- | :--- |
| $-\square$ （s） （d） <br>   Not supported |  |  |  |

## FBD／LD

Not supported

## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| E－ | - |
|  | $\boxed{E}-\mathrm{P}$ |

## Setting data

## －Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Subtrahend data or the start device where subtrahend data <br> is stored | $0,2^{-126} \leq\|(s)\|<2^{128}$ | Single－precision real <br> number | ANYREAL＿32 |
| （d） | Start device where minuend data is stored | $0,2^{-126} \leq\|(d)\|<2^{128}$ | Single－precision real <br> number | ANYREAL＿32 |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDI（H）G口 | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （d） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions subtract the single－precision real number in the device specified by（s）from the single－precision real number in the device specified by（d），and store the result in the device specified by（d）．


Single－precision real number Single－precision real number Single－precision real number
－Value 0 or $2^{-126} \leq \mid$ specified value（stored value） $\mid<2^{128}$ can be specified or stored in the devices specified by（s）and（d）．
－When an input value is set using the engineering tool，a rounding error may occur．Refer to the following for the precautions on setting input values using the engineering tool．
$\longmapsto$ Page 41 Precautions

## Operation error

| Error code（SDO） | Description |
| :--- | :--- |
| 3402 H | The value input to（s）is -0, a subnormal number，NaN（not a number），or $\pm \infty$. |
|  | The value input to（d）is -0, a subnormal number，NaN（not a number），or $\pm \infty$. |
| 3403 H | The data in the device specified by（d）exceeds the following range．（An overflow has occurred．） <br> $\|(\mathrm{d})\|<2^{128}$ |

## E－（P）［when three operands are set］

These instructions perform subtraction between single－precision real numbers．


FBD／LD

（ $\square$ is replaced by either of the following：BMINUS，BMINUSP．）
Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| E－ | - |
|  | $\boxed{Z}$ |
| E－P | $\boxed{ }$ |

## Setting data

－Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Minuend data or the start device where minuend data is <br> stored | $0,2^{-126 \leq\|(s 1)\|<2^{128}}$ | Single－precision real <br> number | ANYREAL＿32 |
| （s2） | Subtrahend data or the start device where subtrahend data <br> is stored | $0,2^{-126 \leq\|(s 2)\|<2^{128}}$ | Single－precision real <br> number | ANYREAL＿32 |
| （d） | Start device for storing the operation result | - | Single－precision real <br> number | ANYREAL＿32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | Jロロロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGㅁ，J밈， U3EDl（H）GD | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （s2） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （d） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions subtract the single－precision real number in the device specified by（s2）from the single－precision real number in the device specified by（s1），and store the result in the device specified by（d）．


Single－precision real number Single－precision real number Single－precision real number
－Value 0 or $2^{-126} \leq$ specified value（stored value）$\ll 2^{128}$ can be specified or stored in the devices specified by（s1），（s2），and （d）．
－When an input value is set using the engineering tool，a rounding error may occur．Refer to the following for the precautions on setting input values using the engineering tool．
$\longmapsto$ Page 41 Precautions

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The value input to (s1) is -0 , a subnormal number, NaN (not a number), or $\pm \infty$. |
|  | The value input to (s2) is -0 , a subnormal number, NaN (not a number), or $\pm \infty$. |
| 3403 H | The data in the device specified by (d) exceeds the following range. (An overflow has occurred.) <br> $\|(\mathrm{d})\|<2^{128}$ |

## Adding double-precision real numbers

## $E D+(P)$ [when two operands are set]

These instructions add double-precision real numbers.

| Ladder | ST |
| :---: | :---: |
|  | Not supported |
| ■-- $-\square$ (s) (d) |  |

## FBD/LD

Not supported

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| ED + | - |
|  | $\boxed{ }$ |
| $E D+P$ |  |

## Setting data

Description, range, data type

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (s) | Second addend data or the start device where the second <br> addend data is stored | $0,2^{-1022 \leq\|(s)\|<2^{1024}}$ | Double-precision real <br> number | ANYREAL_64 |
| (d) | Start device where the first addend data is stored | $0,2^{-1022 \leq\|(d)\|<2^{1024}}$ | Double-precision real <br> number | ANYREAL_64 |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ\|ロ | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | UपIGㅁ, J밈, U3EDI(H)GD | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (s) | - | - | $\bigcirc$ | - | - | $\bigcirc$ | - | $\bigcirc$ | - | $\bigcirc$ | - | - |
| (d) | - | - | $\bigcirc$ | - | - | $\bigcirc$ | - | $\bigcirc$ | - | - | - | - |

## Processing details

- These instructions add the double-precision real number in the device specified by (d) to the double-precision real number in the device specified by (s), and store the result in the device specified by (d).

- Value 0 or $2^{-1022} \leq \mid$ specified value (stored value) $\mid<2^{1024}$ can be specified or stored in the devices specified by (s) and (d).
- When an input value is set using the engineering tool, a rounding error may occur. Refer to the following for the precautions on setting input values using the engineering tool.
$\mathfrak{F}$ Page 41 Precautions


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The value input to (s) is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |
|  | The value input to (d) is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |
| 3403 H | The data in the device specified by (d) exceeds the following range. (An overflow has occurred.) <br> $\|(\mathrm{d})\|<2^{1024}$ |

## ED＋（P）［when three operands are set］

These instructions add double－precision real numbers．


FBD／LD

（ $\square$ is replaced by either of the following：EDPLUS，EDPLUSP．）
Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| ED＋ | - |
| ED＋P | - |

## Setting data

－Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Augend data or the start device containing augend data | $0,2^{-1022 \leq\|(s 1)\|<2^{1024}}$ | Double－precision real <br> number | ANYREAL＿64 |
| （s2） | Addend data or the start device where addend data is <br> stored | $0,2^{-1022 \leq\|(s 2)\|<2^{1024}}$ | Double－precision real <br> number | ANYREAL＿64 |
| （d） | Start device for storing the operation result | - | Double－precision real <br> number | ANYREAL＿64 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J밈， U3EDI（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \\ & \hline \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （d） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions add the double－precision real number in the device specified by（ s 1 ）to the double－precision real number in the device specified by（s2），and store the result in the device specified by（d）．

－Value 0 or $2^{-1022} \leq \mid$ specified value（stored value） $\mid<2^{1024}$ can be specified or stored in the devices specified by（ s 1 ），（ s 2 ）， and（d）．
－When an input value is set using the engineering tool，a rounding error may occur．Refer to the following for the precautions on setting input values using the engineering tool
$\longmapsto$ Page 41 Precautions

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The value input to (s1) is -0 , a subnormal number, NaN (not a number), or $\pm \infty$. |
|  | The value input to (s2) is -0 , a subnormal number, NaN (not a number), or $\pm \infty$. |
| 3403 H | The data in the device specified by (d) exceeds the following range. (An overflow has occurred.) <br> $\|(\mathrm{d})\|<2^{1024}$ |

## Subtracting double－precision real numbers

## ED－（P）［when two operands are set］

These instructions perform subtraction between double－precision real numbers．


## FBD／LD

Not supported

## －Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| ED－ | - |
|  | $\boxed{ }$ |
| ED－P | - |

## Setting data

## －Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Subtrahend data or the start device where subtrahend data <br> is stored | $0,2^{-1022 \leq\|(s)\|<2^{1024}}$ | Double－precision real <br> number | ANYREAL＿64 |
| （d） | Start device where minuend data is stored | $0,2^{-1022 \leq\|(d)\|<2^{1024}}$ | Double－precision real <br> number | ANYREAL＿64 |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3EDI（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （d） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions subtract the double－precision real number in the device specified by（s）from the double－precision real number in the device specified by（d），and store the result in the device specified by（d）．


Double－precision real number
Double－precision real number
Double－precision real number
－Value 0 or $2^{-1022} \leq \mid$ specified value（stored value） $\mid<2^{1024}$ can be specified or stored in the devices specified by（s）and（d）．
－When an input value is set using the engineering tool，a rounding error may occur．Refer to the following for the precautions on setting input values using the engineering tool．
$\mathfrak{F}$ Page 41 Precautions

## Operation error

| Error code（SDO） | Description |
| :--- | :--- |
| 3402 H | The value input to（s）is -0, a subnormal number，NaN（not a number），or $\pm \infty$. |
|  | The value input to（d）is -0, a subnormal number，NaN（not a number），or $\pm \infty$. |
| 3403 H | The data in the device specified by（d）exceeds the following range．（An overflow has occurred．） <br> $\|(\mathrm{d})\|<2^{1024}$ |

## ED－（P）［when three operands are set］

These instructions perform subtraction between double－precision real numbers．


FBD／LD

（ $\square$ is replaced by either of the following：EDMINUS，EDMINUSP．）

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| ED－ | - |
|  | $\boxed{T}$ |
| ED－P | - |

## Setting data

－Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Minuend data or the start device where minuend data is <br> stored | $0,2^{-1022 \leq\|(s 1)\|<2^{1024}}$ | Double－precision real <br> number | ANYREAL＿64 |
| （s2） | Subtrahend data or the start device where subtrahend data <br> is stored | $0,2^{-1022 \leq\|(s 2)\|<2^{1024}}$ | Double－precision real <br> number | ANYREAL＿64 |
| （d） | Start device for storing the operation result | - | Double－precision real <br> number | ANYREAL＿64 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J미， U3EDI（H）G口 | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （d） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions subtract the double－precision real number in the device specified by（s2）from the double－precision real number in the device specified by（s1），and store the result in the device specified by（d）．

－Value 0 or $2^{-1022} \leq \mid$ specified value（stored value） $\mid<22^{1024}$ can be specified or stored in the devices specified by（ $\mathbf{s} 1$ ），（s2）， and（d）．
－When an input value is set using the engineering tool，a rounding error may occur．Refer to the following for the precautions on setting input values using the engineering tool．
W Page 41 Precautions

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The value input to (s1) is -0 , a subnormal number, NaN (not a number), or $\pm \infty$. |
|  | The value input to (s2) is -0 , a subnormal number, NaN (not a number), or $\pm \infty$. |
| 3403 H | The data in the device specified by (d) exceeds the following range. (An overflow has occurred.) <br> $\|(\mathrm{d})\|<2^{1024}$ |

## Multiplying single－precision real numbers

## $\mathrm{E}^{*}(\mathrm{P})$

These instructions multiply single－precision real numbers．


FBD／LD

（ $\square$ is replaced by either of the following：EMULTI，EMULTIP．）
Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| $\mathrm{E}^{*}$ | - |
| $\mathrm{E}^{*} \mathrm{P}$ | - |

## Setting data

－Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Multiplicand data or the start device where multiplicand <br> data is stored | $0,2^{-126 \leq\|(s 1)\|<2^{128}}$ | Single－precision real <br> number | ANYREAL＿32 |
| （s2） | Multiplier data or the start device where multiplier data is <br> stored | $0,2^{-126} \leq\|(s 2)\|<2^{128}$ | Single－precision real <br> number | ANYREAL＿32 |
| （d） | Start device for storing the operation result | - | Single－precision real <br> number | ANYREAL＿32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （s2） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （d） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions multiply the single-precision real number in the device specified by ( s 1 ) by the single-precision real number in the device specified by (s2), and store the multiplication result in the device specified by (d).

- Value 0 or $2^{-126} \leq \mid$ specified value (stored value) $\mid<2^{128}$ can be specified or stored in the devices specified by (s1), (s2), and (d).
- When an input value is set using the engineering tool, a rounding error may occur. Refer to the following for the precautions on setting input values using the engineering tool.
$\longmapsto$ Page 41 Precautions


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The value input to (s1) is -0 , a subnormal number, NaN (not a number), or $\pm \infty$. |
|  | The value input to (s2) is -0 , a subnormal number, NaN (not a number), or $\pm \infty$. |
| 3403 H | The data in the device specified by (d) exceeds the following range. (An overflow has occurred.) <br> $\|(\mathrm{d})\|<2^{128}$ |

## Dividing single－precision real numbers

## E／（P）

These instructions perform division between single－precision real numbers．


FBD／LD

（ $\square$ is replaced by either of the following：EDIVISION，EDIVISIONP．）
Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| E／ | - |
|  | E／P |
|  | - |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Dividend data or the start device where dividend data is <br> stored | $0,2^{-126 \leq\|(s 1)\|<2^{128}}$ | Single－precision real <br> number | ANYREAL＿32 |
| （s2） | Divisor data or the start device where divisor data is stored | $0,2^{-126} \leq\|(s 2)\|<2^{128}$ | Single－precision real <br> number | ANYREAL＿32 |
| （d） | Start device for storing the operation result | - | Single－precision real <br> number | ANYREAL＿32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM} \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JपIロ， U3ED（H）GD | z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （s2） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （d） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions divide the single-precision real number in the device specified by (s1) by the single-precision real number in the device specified by (s2), and store the division result in the device specified by (d).

- Value 0 or $2^{-126} \leq \mid$ specified value (stored value) $<2^{128}$ can be specified or stored in the devices specified by (s1), (s2), and (d).
- When an input value is set using the engineering tool, a rounding error may occur. Refer to the following for the precautions on setting input values using the engineering tool.
↔ Page 41 Precautions


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3400 H | The data (divisor) in the device specified by (s2) is 0. |
| 3402 H | The value input to ( $s 1$ ) is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |
|  | The value input to (s2) is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |
| 3403 H | The data in the device specified by (d) exceeds the following range. (An overflow has occurred.) <br>  <br> $\|(\mathrm{d})\|<2^{128}$ |

## Multiplying double－precision real numbers

## ED＊${ }^{*}$ ）

These instructions multiply double－precision real numbers．


FBD／LD

（ $\square$ is replaced by either of the following：EDMULTI，EDMULTIP．）
Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| ED＊ | - |
|  | $\boxed{Z}$ |
| ED＊P | - |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Multiplicand data or the start device where multiplicand <br> data is stored | $0,2^{-1022 \leq\|(s 1)\|<2^{1024}}$ | Double－precision real <br> number | ANYREAL＿64 |
| （s2） | Multiplier data or the start device where multiplier data is <br> stored | $0,2^{-1022 \leq\|(s 2)\|<2^{1024}}$ | Double－precision real <br> number | ANYREAL＿64 |
| （d） | Start device for storing the operation result | - | Double－precision real <br> number | ANYREAL＿64 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （d） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions multiply the double-precision real number in the device specified by ( s 1 ) by the double-precision real number in the device specified by (s2), and store the multiplication result in the device specified by (d).

- Value 0 or $2^{-1022} \leq \mid$ specified value (stored value) $\mid<2^{1024}$ can be specified or stored in the devices specified by (s1), (s2), and (d).
- If the operation result is -0 or an underflow occurs, the operation result turns out to 0 .
- When an input value is set using the engineering tool, a rounding error may occur. Refer to the following for the precautions on setting input values using the engineering tool.


## $\longmapsto$ Page 41 Precautions

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The value input to (s1) is -0 , a subnormal number, NaN (not a number), or $\pm \infty$. |
|  | The value input to (s2) is -0 , a subnormal number, NaN (not a number), or $\pm \infty$. |
| 3403 H | The data output from (d) exceeds the following range. (An overflow has occurred.) <br> $\|(\mathrm{d})\|<2^{1024}$ |

## Dividing double－precision real numbers

## ED／（P）

These instructions perform division between double－precision real numbers．


FBD／LD

（ $\square$ is replaced by either of the following：EDDIVISION，EDDIVISIONP．）
Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| ED／ | - |
|  | $\boxed{E D}$ |
| ED／P | - |

## Setting data

－Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Dividend data or the start device where dividend data is <br> stored | $0,2^{-1022 \leq\|(s 1)\|<2^{1024}}$ | Double－precision real <br> number | ANYREAL＿64 |
| （s2） | Divisor data or the start device where divisor data is stored | $0,2^{-1022 \leq\|(s 2)\|<2^{1024}}$ | Double－precision real <br> number | ANYREAL＿64 |
| （d） | Start device for storing the operation result | - | Double－precision real <br> number | ANYREAL＿64 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （d） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions divide the double-precision real number in the device specified by (s1) by the double-precision real number in the device specified by (s2), and store the division result in the device specified by (d).

- Value 0 or $2^{-1022} \leq \mid$ specified value (stored value) $<2^{1024}$ can be specified or stored in the devices specified by (s1), (s2), and (d).
- If the operation result is -0 or an underflow occurs, the operation result turns out to 0 .
- When an input value is set using the engineering tool, a rounding error may occur. Refer to the following for the precautions on setting input values using the engineering tool.


## $\leftrightarrows$ Page 41 Precautions

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3400 H | The data (divisor) in the device specified by (s2) is 0. |
| 3402 H | The value input to (s1) is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |
|  | The value input to (s2) is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |
| 3403 H | The data output from (d) exceeds the following range. (An overflow has occurred.) <br> $\mid$ (d) $\mid<2^{1024}$ |

## Converting 16－bit signed binary data to single－precision real number

## INT2FLT（P）

These instructions convert 16－bit signed binary data to a single－precision real number．

| Ladder | ST |
| :---: | :---: |
|  | Not supported |
| $-=-$ （s） （d） |  |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| INT2FLT | - |
|  | $\boxed{ }$ |
| INT2FLTP | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Integral data to be converted to a single－precision real <br> number，or the device containing integral data | -32768 to 32767 | 16－bit signed binary | ANY16＿S |
| （d） | Start device for storing the converted single－precision real <br> number | - | Single－precision real <br> number | ANYREAL＿32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDI（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions convert the 16－bit signed binary data in the device specified by（s）to a single－precision real number，and store the converted data in the device specified by（d）．

Before conversion
After conversion


Operation error
There is no operation error

## Converting 16－bit unsigned binary data to single－precision real number

## UINT2FLT（P）

These instructions convert 16－bit unsigned binary data to a single－precision real number．


FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| UINT2FLT | - |
|  | $\boxed{ }$ |
| UINT2FLTP |  |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Integral data to be converted to a single－precision real <br> number，or the device containing integral data | 0 to 65535 | 16－bit unsigned binary | ANY16＿U |
| （d） | Start device for storing the converted single－precision real <br> number | - | Single－precision real <br> number | ANYREAL＿32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDI（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions convert the 16－bit unsigned binary data in the device specified by（s）to a single－precision real number， and store the real number in the device specified by（d）．


Before conversion
After conversion
（s）
D0


## Operation error

There is no operation error

## Converting 32－bit signed binary data to single－precision real number

## DINT2FLT（P）

These instructions convert 32－bit signed binary data to a single－precision real number．

| Ladder | ST |
| :---: | :---: |
|  | Not supported |
| $-=-$ （s） （d） |  |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DINT2FLT | - |
|  | $\boxed{ }$ |
| DINT2FLTP | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Integral data to be converted to a single－precision real <br> number，or the start device containing integral data | -2147483648 to 2147483647 | 32－bit signed binary | ANY32＿S |
| （d） | Start device for storing the converted single－precision real <br> number | - | Single－precision real <br> number | ANYREAL＿32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，Jㅁㅁㅁ， U3EDI（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions convert the 32-bit signed binary data in the device specified by (s) to a single-precision real number, and stores the real number in the device specified by (d).

- A single-precision real number is processed in 32-bit single precision, and therefore the effective number of digits is 24 bits when it is represented in binary and is about 7 digits when represented in decimal. For this reason, if the integer value exceeds the range from -16777216 to 16777215 (24-bit binary value), an error occurs in the converted value. The operation result is an integer value in which the 25th bit from upper bits is rounded off.



## Operation error

There is no operation error.

## Converting 32－bit unsigned binary data to single－precision real number

## UDINT2FLT（P）

These instructions convert 32－bit unsigned binary data to a single－precision real number．

| Ladder | ST |
| :---: | :---: |
|  | Not supported |
| $\square-\square-\square$ （s） （d） |  |

FBD／LD


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| UDINT2FLT | - |
|  | $\boxed{ }$ |
| UDINT2FLTP |  |

Setting data
Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Integral data to be converted to a single－precision real <br> number，or the start device containing integral data | 0 to 4294967295 | 32－bit unsigned binary | ANY32＿U |
| （d） | Start device for storing the converted single－precision real <br> number | - | Single－precision real <br> number | ANYREAL＿32 |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロID， U3E미（H）Gㅁ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions convert the 32-bit unsigned binary data in the device specified by (s) to a single-precision real number, and stores the real number in the device specified by (d).

- A single-precision real number is processed in 32-bit single precision, and therefore the effective number of digits is 24 bits when it is represented in binary and is about 7 digits when represented in decimal. For this reason, if the integer value exceeds the range from 0 to 16777215 (24-bit binary value), an error occurs in the converted value. The operation result is an integer value in which the 25th bit from upper bits is rounded off.



## Operation error

There is no operation error.

## Converting double－precision real number to single－precision real number

## DBL2FLT（P）

These instructions convert a double－precision real number to a single－precision real number．


FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DBL2FLT | - |
|  | - |
| DBL2FLTP | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Integral data to be converted to a single－precision real <br> number，or the start device containing integral data | $0,2^{-1022 \leq\|(s)\|<2^{1024}}$ | Double－precision real <br> number | ANYREAL＿64 |
| （d） | Start device for storing the converted single－precision real <br> number | - | Single－precision real <br> number | ANYREAL＿32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDI（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （d） | － | － | $\bigcirc$ | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions convert the double－precision real number in the device specified by（s）to a single－precision real number， and store the real number in the device specified by（d）．

－When an input value is set using the engineering tool，a rounding error may occur．Refer to the following for the precautions on setting input values using the engineering tool．
$\Vdash$ Page 41 Precautions

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The value input to (s) is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |
| 3403 H | The data output from (d) exceeds the following range. (An overflow has occurred.) <br> $\|(\mathrm{d})\|<2^{128}$ |

## Converting 16－bit signed binary data to double－precision real number

## INT2DBL（P）

These instructions convert 16－bit signed binary data to a double－precision real number．

| Ladder | ST |
| :---: | :---: |
|  | Not supported |
| $-=-$ （s） （d） |  |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| INT2DBL | - |
|  | $\boxed{ }$ |
| INT2DBLP | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Integral data to be converted to a double－precision real <br> number，or the device containing integral data | -32768 to 32767 | 16－bit signed binary | ANY16＿S |
| （d） | Start device for storing the converted double－precision real <br> number | - | Double－precision real <br> number | ANYREAL＿64 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathbf{X}, \mathbf{Y}, \mathbf{M}, \mathbf{L}, \mathbf{S M}, \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J밈， U3EDl（H）G口 | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions convert the 16－bit signed binary data in the device specified by（ $s$ ）to a double－precision real number， and store the real number in the device specified by（d）．



## Operation error

There is no operation error．

## Converting 16－bit unsigned binary data to double－precision real number

## UINT2DBL（P）

These instructions convert 16－bit unsigned binary data to a double－precision real number．

| Ladder | ST |
| :---: | :---: |
|  | Not supported |
| $-\square$ （s） （d） |  |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| UINT2DBL | - |
|  | $\boxed{ }$ |
| UINT2DBLP | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Integral data to be converted to a double－precision real <br> number，or the device containing integral data | 0 to 65535 | 16－bit unsigned binary | ANY16＿U |
| （d） | Start device for storing the converted double－precision real <br> number | - | Double－precision real <br> number | ANYREAL＿64 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J밈， U3EDI（H）G口 | z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions convert the 16－bit unsigned binary data in the device specified by（ s ）to a double－precision real number， and store the real number in the device specified by（d）．


Before conversio
After conversion
（s）
b15 $\cdots \mathrm{b} 0$
（d）
103，D102，D101，D100


## Operation error

There is no operation error

## Converting 32－bit signed binary data to double－precision real number

## DINT2DBL（P）

These instructions convert 32－bit signed binary data to a double－precision real number．

| Ladder | ST |
| :---: | :---: |
|  | Not supported |
| $\square--\square \square$ （s） （d） |  |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DINT2DBL | - |
|  | - |
| DINT2DBLP |  |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Integral data to be converted to a double－precision real <br> number，or the start device containing integral data | -2147483648 to 2147483647 | 32－bit signed binary | ANY32＿S |
| （d） | Start device for storing the converted double－precision real <br> number | - | Double－precision real <br> number | ANYREAL＿64 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J밈， U3EDI（H）G口 | z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions convert the 32－bit signed binary data in the device specified by（s）to a double－precision real number， and store the real number in the device specified by（d）．


## Operation error

There is no operation error

## Converting 32－bit unsigned binary data to double－precision real number

## UDINT2DBL（P）

These instructions convert 32－bit unsigned binary data to a double－precision real number．

| Ladder | ST |
| :---: | :---: |
|  | Not supported |
| $-\square$ （s） （d） |  |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| UDINT2DBL | - |
|  | $\boxed{ }$ |
| UDINT2DBLP |  |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Integral data to be converted to a double－precision real <br> number，or the start device containing integral data | 0 to 4294967295 | 32－bit unsigned binary | ANY32＿U |
| （d） | Start device for storing the converted double－precision real <br> number | - | Double－precision real <br> number | ANYREAL＿64 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J밈， U3EDI（H）G口 | z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions convert the 32－bit unsigned binary data in the device specified by（ s ）to a double－precision real number， and store the real number in the device specified by（d）．

| DMOVP | K123456 | D0 |  |
| :--- | :--- | :--- | :--- |
|  | （s） |  |  |



## Operation error

There is no operation error．

## Converting single－precision real number to double－precision real number

## FLT2DBL（P）

These instructions convert a single－precision real number to a double－precision real number．


FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| FLT2DBL | - |
|  | - |
| FLT2DBLP | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Integral data to be converted to a single－precision real <br> number，or the start device containing integral data | $0,2^{-126 \leq\|(s)\|<2^{128}}$ | Single－precision real <br> number | ANYREAL＿32 |
| （d） | Start device for storing the converted double－precision real <br> number | - | Double－precision real <br> number | ANYREAL＿64 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J밈， U3EDI（H）G口 | z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （d） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － |

## Processing details

These instructions convert the single－precision real number in the device specified by（s）to a double－precision real number， and store the double－precision real number in the device specified by（d）．



## Operation error

| Error code（SDO） | Description |
| :--- | :--- |
| 3402 H | The value input to（s）is -0, a subnormal number，NaN（not a number），or $\pm \infty$. |

## Converting string data to single－precision real number

## EVAL（P）

These instructions convert a string to a single－precision real number．

| Ladder |  | ST |
| :---: | :---: | :---: |
| $-\begin{array}{\|c\|c} \hline-\square-\square & \text { (s) } \\ \hline \end{array}$ | (d) | $\begin{aligned} & \text { ENO:=EVAL(EN,s,d); } \\ & \text { ENO:=EVALP(EN,s,d); } \end{aligned}$ |
| FBD／LD |  |  |
|  | － |  |

Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| EVAL | - |
| EVALP | $\boxed{ }$ |

## Setting data

Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Character string data to be converted into single－precision <br> real number data，or the start device containing the <br> character string data | - | String | ANYSTRING＿SINGL <br> E |
| （d） | Start device for storing the converted single－precision real <br> number data | - | Single－precision real <br> number | ANYREAL＿32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## －Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J밈， U3EDI（H）GD | Z | LT，LST， LC | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － |
| （d） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions convert the character string stored in the device number specified by (s) and later to single-precision real number data, and store the converted real number in the device specified by (d).
- The specified character string can be converted to a single-precision real number in either decimal point or exponent format.

- "20H" (space) that exists in the middle of the data is ignored.
- The character string can consist of up to 24 characters. " 20 H " (space) and " 30 H " (0) are counted as one character as well.


## -Decimal point format

- When the character string in the device specified by (s) is in decimal point format, conversion is performed as shown below.

|  | ... | b8b7 | ... | b0 | (d) +1 |  | (d) |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| (s) | 31 H (1) | ! | 2DH (-) |  |  |  |  |
| (s) +1 | $30 \mathrm{H}(0)$ |  | 2EH (.) |  |  |  |  |
| (s)+2 | 38 H (8) |  | 37H (7) |  | $\square$ | -1.078 | 12 |
| (s)+3 | 32 H (2) | ! | 31H (1) |  | Single-precision real number |  |  |
| (s)+4 | OOH |  |  |  |  |  |  |  |

## H1 Hofid

- The character string in the device specified by (s) to be converted to a single-precision real number is converted by assuming that the six digits excluding the sign, decimal point, and exponent are effective and the seventh and subsequent digits are discarded.

- If $2 \mathrm{BH}(+)$ is specified for the sign or the sign is omitted in decimal point format, the converted single-precision real number is treated as a positive value. If $2 \mathrm{DH}(-)$ is specified for the sign, it is treated as a negative value.
- If 20 H (space) or $30 \mathrm{H}(0)$ exists between digits excluding the first 0 in the character string in the device specified by (s), the instruction performs conversion by ignoring 20 H and 30 H .



## Exponent format

- When the character string in the device specified by (s) is in exponent format, conversion is performed as shown below.



## -1 1010 . 10

- The character string in the device specified by (s) to be converted to a single-precision real number is converted by assuming that the six digits excluding the sign, decimal point, and exponent are effective and the seventh and subsequent digits are discarded.



## -1 5 <br> Rounded down.

- If $2 \mathrm{BH}(+)$ is specified for the sign or the sign is omitted in exponent format, the converted single-precision real number is treated as a positive value. If $2 \mathrm{DH}(-)$ is specified for the sign in the exponent, it is treated as a negative value.
- If 20 H (space) or $30 \mathrm{H}(0)$ exists between digits excluding the first 0 in the character string in the device specified by (s), the instruction performs conversion by ignoring 20 H and 30 H .
- If $30 \mathrm{H}(0)$ exists between " $E$ " and a numerical value in the character string in exponent format, the instruction performs conversion by ignoring 30 H .



## Operation error

| Error code (SDO) | Description |
| :---: | :---: |
| 2820H | OOH does not exist within the range of the relevant device specified by (s). |
| 3401H | An out-of-range value is set to (s). <br> - The integral part or decimal part contains a character other than $30 \mathrm{H}(0)$ to 39 H (9). <br> - The specified string contains two or more 2EH (.) <br> - The exponent of the specified string contains a character other than $45 \mathrm{H}(\mathrm{E}), 65 \mathrm{H}(\mathrm{e}), 2 \mathrm{BH}(+)$, and 2DH (-). <br> - The specified string contains more than one exponent 45 H (E) or 65 H (e). <br> - The exponent in the specified string contains a numerical value consisting of three digits or more. <br> - The exponent of the specified string contains more than one sign 2BH (+) or 2DH (-). <br> - The specified string contains more than one sign 2BH (+) or 2DH (-) in the integral part of decimal point format or in the mantissa of exponent format. <br> - The number of characters in the device specified by ( s ) and later is 0 or exceeds 24 . |
| 3403H | The data output from (d) exceeds the following range. (An overflow has occurred.) $\|(\mathrm{d})\|<2^{128}$ |

## Converting BCD format data to single－precision real number

## EREXP（P）

These instructions convert BCD floating point format data to single－precision real number data in accordance with the specified number of digits in the decimal part．


■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| EREXP | - |
|  | $\boxed{ }$ |
| EREXPP | - |

Setting data
Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Start device containing BCD floating point format data | - | 16－bit signed binary | ANY16＿ARRAY <br> （Number of elements： <br> $5)$ |
| （s2） | Number of digits in the decimal part | 0 to 7 | 16－bit signed binary | ANY16 |
| （d） | Start device for storing a single－precision real number | - | Single－precision real <br> number | ANYREAL＿32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3Eपl（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions convert the BCD floating-point format data stored in the device specified by ( $\mathbf{s} 1$ ) and later to singleprecision real number data in accordance with the number of decimal places stored in the device specified by (s2), and store the converted data in the device number specified by (d) and later.

(s2) $\begin{aligned} & \text { Number of digits in the } \\ & \text { decimal part (0 to 7) }\end{aligned}$
- For the sign in ( s 1 ) and exponent sign in ( s 1 ) $+3,0$ is set for positive and 1 is set for negative.
- A value of 0 to 38 can be set for the BCD exponent in (s1)+4.
- A value of 0 to 7 can be set for the number of decimal part digits in the device specified by (s2).


## Ex.

When 6 is specified in (s2)


Operation error

| Error code (SDO) | Description |
| :---: | :---: |
| 3401H | The value set to ( s 1 ) as sign data is not 0 or 1 . |
|  | A value other than 0 to 9 exists at any digit of data set to ( s 1$)+1$ and ( s 1$)+2$. |
|  | The value set to ( $\mathbf{s} 1$ ) +3 as sign data of exponent is not 0 or 1 . |
|  | The exponent data set to ( s 1 ) +4 is out of the range, 0 to 38 . |
|  | The value set to ( s 2 ) as the number of digits in the decimal part is out of the range, 0 to 7 . |
| 3403H | The data output from (d) exceeds the following range. (An overflow has occurred.) $\|(\mathrm{d})\|<2^{128}$ |

## Inverting the sign of single－precision real number

## ENEG（P）

These instructions invert the sign of single－precision real number data．

| Ladder | ST |
| :--- | :--- | :--- |
| $-\square-\square$ （d） <br>  ENO：＝ENEG（EN，d）； | ENO：＝ENEGP（EN，d）； |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| ENEG | - |
| ENEGP | $\boxed{ }$ |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （d） | Start device containing the single－precision real number <br> data subject to sign inversion | - | Single－precision real <br> number | ANYREAL＿32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3Eपl（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （d） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions invert the sign of the single－precision real number in the device specified by（d）and store the inverted data in the device specified by（d）

－The instructions are used to invert positive and negative signs．

## Operation error

| Error code（SDO） | Description |
| :--- | :--- |
| 3402 H | The value input to（d）is -0, a subnormal number，NaN（not a number），or $\pm \infty$. |

## Inverting the sign of double－precision real number

## EDNEG（P）

These instructions invert the sign of double－precision real number data．

| Ladder | ST |
| :---: | :---: |
|  | $\begin{aligned} & \text { ENO:=EDNEG(EN,d); } \\ & \text { ENO:=EDNEGP(EN,d); } \end{aligned}$ |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| EDNEG | - |
| EDNEGP | $\boxed{ }$ |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （d） | Start device containing the double－precision real number <br> subject to sign inversion | - | Double－precision real <br> number | ANYREAL＿64 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDl（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （d） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions invert the sign of the double－precision real number data in the device specified by（d）and store the inverted data in the device specified by（d）．
$(\mathrm{d})+3 \quad(\mathrm{~d})+2 \quad(\mathrm{~d})+1$
（d）
$\square$
Double－precision real number

$\qquad$ （d）
Double－precision real number
－The instructions are used to invert positive and negative signs．

## Operation error

| Error code（SDO） | Description |
| :--- | :--- |
| 3402 H | The value input to（d）is -0, a subnormal number，NaN（not a number），or $\pm \infty$. |

## Transferring single－precision real number

## EMOV（P）

These instructions transfer single－precision real number data to the specified device．

| Ladder | ST |
| :---: | :---: |
|  | $\begin{aligned} & \text { ENO:=EMOV(EN,s,d); } \\ & \text { ENO:=EMOVP(EN,s,d) } \end{aligned}$ |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| EMOV | - |
|  | $\boxed{ }$ |
| EMOVP |  |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Data to be transferred or start device containing the data to <br> be transferred | $0,2^{-126 \leq\|(s)\|<2^{128}}$ | Single－precision real <br> number | ANYREAL＿32 |
| （d） | Start device for storing transferred data | - | Single－precision real <br> number | ANYREAL＿32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JपIロ， U3EDI（H）GD | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （d） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions transfer the single－precision real number data stored in the device specified by（s）to the device specified by（d）．


## Operation error

There is no operation error

## Transferring double－precision real number

## EDMOV（P）

These instructions transfer double－precision real number data to the specified device．


FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| EDMOV | - |
| EDMOVP | $\boxed{-}$ |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Data to be transferred or start device containing the data to <br> be transferred | $0,2^{-102 L_{\leq} \leq(s) \mid<2^{1024}}$ | Double－precision real <br> number | ANYREAL＿64 |
| （d） | Start device for storing transferred data | - | Double－precision real <br> number | ANYREAL＿64 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3Eपl（H）G口 | z | $\begin{aligned} & \mathrm{LT}, \mathrm{LST}, \\ & \mathrm{LC} \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （d） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions transfer the double－precision real number data stored in the device specified by（s）to the device specified by（d）


## Operation error

There is no operation error

Calculating the sine of single－precision real number

## SIN（P）

These instructions calculate the sine of the angle specified by a single－precision real number．

| Ladder | ST＊${ }^{\text {¹ }}$ |
| :---: | :---: |
|  | ENO：＝SINP（EN，s，d）； |
| $\begin{array}{l\|l\|l\|} \hline-\square-\square & \text { (s) } & \text { (d) } \\ \hline \end{array}$ |  |

## FBD／LD＊${ }^{* 1}$


＊1 The SIN instruction does not support the structured text language and FBD／LD language．Use the standard function，SIN．
$\longmapsto$ Page 1584 SIN（＿E）

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| SIN | - |
|  | $\boxed{-}$ |
| SINP | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Angle data used for sine calculation，or the start device <br> containing the angle data | $0,2^{-126 \leq\|(s)\|<2^{128}}$ | Single－precision real <br> number | ANYREAL＿32 |
| （d） | Start device for storing the operation result | - | Single－precision real <br> number | ANYREAL＿32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDl（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （d） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions calculate the sine of the angle specified by（s），and store the operation result in the device specified by （d）．

－Set the angle data in radians（angle $\times \pi \div 180$ ）．
－If the operation result is -0 or an underflow occurs，the operation result turns out to 0 ．
－When an input value is set using the engineering tool，a rounding error may occur．Refer to the following for the precautions on setting input values using the engineering tool．
$\Vdash$ Page 41 Precautions

## Operation error

| Error code (SD0) | Description |
| :--- | :--- |
| 3402 H | The value input to $(\mathrm{s})$ is -0 , a subnormal number, NaN (not a number), or $\pm \infty$. |

Point ${ }^{\rho}$
For the angle $\leftrightarrow$ radian conversion, refer to the $\operatorname{RAD}(P)$ and $\operatorname{DEG}(P)$ instructions.
$\longmapsto$ Page 690 RAD (P)
$\longmapsto$ Page 692 DEG(P)

Calculating the cosine of single-precision real number

## COS(P)

These instructions calculate the cosine of the angle specified by a single-precision real number.


## FBD/LD* ${ }^{* 1}$


*1 The COS instruction does not support the structured text language and FBD/LD language. Use the standard function, COS. Њ ${ }^{-1}$ Page $1585 \operatorname{COS}\left(\_E\right)$

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| $\operatorname{COS}$ | - |
|  | $\boxed{Z}$ |
| $\operatorname{COSP}$ | - |

## Setting data

■Description, range, data type

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (s) | Angle data used for cosine calculation, or the start device <br> containing the angle data | $0,2^{-126 \leq\|(s)\|<2^{128}}$ | Single-precision real <br> number | ANYREAL_32 |
| (d) | Start device for storing the operation result | - | Single-precision real <br> number | ANYREAL_32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | J밈 | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | UपIGロ, JपIप, U3E미(H)Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (s) | - | - | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | $\bigcirc$ | - | - |
| (d) | - | - | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - | - | - |

## Processing details

- These instructions calculate the cosine of the angle specified by (s), and store the operation result in the device specified by (d).

- Set the angle data in radians (angle $\times \pi \div 180$ ).
- If the operation result is -0 or an underflow occurs, the operation result turns out to 0 .
- When an input value is set using the engineering tool, a rounding error may occur. Refer to the following for the precautions on setting input values using the engineering tool.
$\longmapsto$ Page 41 Precautions


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The value input to $(\mathrm{s})$ is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |

Point ${ }^{\rho}$
For the angle $\leftrightarrow$ radian conversion, refer to the $\operatorname{RAD}(P)$ and $\operatorname{DEG}(P)$ instructions.
$\longmapsto$ Page 690 RAD (P)
$\longmapsto$ Page 692 DEG(P)

Calculating the tangent of single－precision real number

## TAN（P）

These instructions calculate the tangent of the angle specified by a single－precision real number．

| Ladder | ST＊${ }^{\text {1 }}$ |
| :---: | :---: |
|  | ENO：＝TANP（EN，s，d）； |
|  |  |

## FBD／LD＊${ }^{* 1}$


＊1 The TAN instruction does not support the structured text language and FBD／LD language．Use the standard function，TAN． $\longmapsto$ Page 1586 TAN（＿E）

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| TAN | - |
|  | $\boxed{T}$ |
| TANP | - |

## Setting data

－Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Angle data used for tangent calculation，or the start device <br> containing the angle data | $0,2^{-126 \leq\|(s)\|<2^{128}}$ | Single－precision real <br> number | ANYREAL＿32 |
| （d） | Start device for storing the operation result | - | Single－precision real <br> number | ANYREAL＿32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （d） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions calculate the tangent of the angle specified by（s），and store the operation result in the device specified by（d）．

－Set the angle data in radians（angle $\times \pi \div 180$ ）．
－If the operation result is -0 or an underflow occurs，the operation result turns out to 0 ．
－When an input value is set using the engineering tool，a rounding error may occur．Refer to the following for the precautions on setting input values using the engineering tool．
$\longmapsto$ Page 41 Precautions

## Precautions

If the angle specified by (s) is $\pi / 2$ radian or (3/2) $\pi$ radian, no operation error will be issued because of the truncation error in the radian value.

## Operation error

| Error code (SD0) | Description |
| :--- | :--- |
| 3402 H | The value input to (s) is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |
| Point |  |

For the angle $\leftrightarrow$ radian conversion, refer to the RAD $(P)$ and $\operatorname{DEG}(P)$ instructions.
↔ Page 690 RAD (P)
↔ Page 692 DEG(P)

Calculating the arc sine of single-precision real number

## ASIN(P)

These instructions calculate the angle from the sine specified by a single-precision real number.

| Ladder | $\mathrm{ST}^{* 1}$ |
| :---: | :---: |
|  | ENO:=ASINP(EN,s,d); |
| $\begin{array}{l\|l\|l\|} \hline \end{array}$ |  |

## FBD/LD* ${ }^{*}$


*1 The ASIN instruction does not support the structured text language and FBD/LD language. Use the standard function, ASIN. $\longmapsto$ Page 1587 ASIN(_E)

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| ASIN | - |
|  | $\boxed{4}$ |
| ASINP | - |

## Setting data

■Description, range, data type

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (s) | Sine data used for arc sine calculation, or the start device <br> containing the sine data | -1.0 to 1.0 | Single-precision real <br> number | ANYREAL_32 |
| (d) | Start device for storing the operation result | - | Single-precision real <br> number | ANYREAL_32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | J밈 | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | UपIGロ, JपIप, U3E미(H)Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (s) | - | - | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | $\bigcirc$ | - | - |
| (d) | - | - | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - | - | - |

## Processing details

- These instructions calculate the angle based on the sine data in the device specified by ( s ), and store the operation result in the device number specified by (d).
$\mathrm{SIN}^{-1}$

Single-precision real number


(d)
le-precision real number
- The sine data in the device specified by
- The angle (operation result) is stored in radians in the device specified by (d).
- If the operation result is -0 or an underflow occurs, the operation result turns out to 0 .
- When an input value is set using the engineering tool, a rounding error may occur. Refer to the following for the precautions on setting input values using the engineering tool.
$\longmapsto$ Page 41 Precautions


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The value input to (s) is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |
| 3405 H | The value in the device specified by (s) is out of the range, -1.0 to 1.0. |

Point $\rho$
For the angle $\leftrightarrow$ radian conversion, refer to the $\operatorname{RAD}(P)$ and $\operatorname{DEG}(P)$ instructions.
$\longmapsto$ Page 690 RAD ( P )
$\longmapsto$ Page 692 DEG(P)

## Calculating the arc cosine of single－precision real number

## ACOS（P）

These instructions calculate the angle from the cosine specified by a single－precision real number．


## FBD／LD＊${ }^{*}$


＊1 The ACOS instruction does not support the structured text language and FBD／LD language．Use the standard function，ACOS． W Page 1588 ACOS（＿E）

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| ACOS | - |
|  | $\boxed{T}$ |
| ACOSP | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Cosine data used for arc cosine calculation，or the start <br> device containing the cosine data | -1.0 to 1.0 | Single－precision real <br> number | ANYREAL＿32 |
| （d） | Start device for storing the operation result | - | Single－precision real <br> number | ANYREAL＿32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （d） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions calculate the angle based on the cosine data in the device specified by（s），and store the operation result in the device number specified by（d）．

－The cosine data in the device specified by（s）can be set in the range from－1．0 to 1．0．
－The angle（operation result）is stored in radians in the device specified by（d）．
－If the operation result is -0 or an underflow occurs，the operation result turns out to 0 ．
－When an input value is set using the engineering tool，a rounding error may occur．Refer to the following for the precautions on setting input values using the engineering tool．
$\longmapsto$ Page 41 Precautions

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The value input to (s) is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |
| 3405 H | The value in the device specified by (s) is out of the range, -1.0 to 1.0. |

Point $\rho$
For the angle $\leftrightarrow$ radian conversion, refer to the $\operatorname{RAD}(P)$ and $\operatorname{DEG}(P)$ instructions.
$\longmapsto$ Page 690 RAD ( P )
$\longmapsto$ Page 692 DEG(P)

Calculating the arc tangent of single－precision real number

## ATAN（P）

These instructions calculate the angle from the tangent specified by a single－precision real number．


## FBD／LD＊${ }^{* 1}$


＊1 The ATAN instruction does not support the structured text language and FBD／LD language．Use the standard function，ATAN． ছ Page 1589 ATAN（＿E）

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| ATAN | - |
|  | $\boxed{ }$ |
| ATANP | - |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Tangent data used for arc tangent calculation，or the start <br> device containing the tangent data | $0,2^{-126 \leq\|(s)\|<2^{128}}$ | Single－precision real <br> number | ANYREAL＿32 |
| （d） | Start device for storing the operation result | - | Single－precision real <br> number | ANYREAL＿32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （d） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions calculate the angle based on the tangent data in the device specified by（s），and store the operation result in the device number specified by（d）．

－The angle（operation result）is stored in radians in the device specified by（d）．
－If the operation result is -0 or an underflow occurs，the operation result turns out to 0 ．
－When an input value is set using the engineering tool，a rounding error may occur．Refer to the following for the precautions on setting input values using the engineering tool．
↔ Page 41 Precautions

## Operation error

| Error code (SD0) | Description |
| :--- | :--- |
| 3402 H | The value input to (s) is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |

Point ${ }^{\rho}$
For the angle $\leftrightarrow$ radian conversion, refer to the $\operatorname{RAD}(P)$ and $\operatorname{DEG}(P)$ instructions.
$\longmapsto$ Page 690 RAD (P)
$\longmapsto$ Page 692 DEG(P)

Calculating the sine of double－precision real number

## SIND（P）

These instructions calculate the sine of the angle specified by a double－precision real number．


## FBD／LD＊${ }^{* 1}$


＊1 The SIND instruction does not support the structured text language and FBD／LD language．Use the standard function，SIN． $\longmapsto$ Page 1584 SIN（＿E）

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| SIND | - |
|  | $\boxed{ }$ |
| SINDP | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Angle data used for sine calculation，or the start device <br> containing the angle data | $0,2^{-1022 \leq\|(s)\|<2^{1024}}$ | Double－precision real <br> number | ANYREAL＿64 |
| （d） | Start device for storing the operation result | - | Double－precision real <br> number | ANYREAL＿64 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （d） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions calculate the sine of the angle specified by（s），and store the operation result in the device specified by （d）．

－Set the angle data in radians（angle $\times \pi \div 180$ ）．
－If the operation result is -0 or an underflow occurs，the operation result turns out to 0 ．
－When an input value is set using the engineering tool，a rounding error may occur．Refer to the following for the precautions on setting input values using the engineering tool．
$\longmapsto$ Page 41 Precautions

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The value input to $(\mathrm{s})$ is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |

Point ${ }^{\rho}$
For the angle $\leftrightarrow$ radian conversion, refer to the $\operatorname{RADD}(\mathrm{P})$ and $\operatorname{DEGD}(\mathrm{P})$ instructions.
$\longmapsto$ Page 694 RADD(P)
$\leftrightarrows$ Page 696 DEGD(P)

Calculating the cosine of double－precision real number

## COSD（P）

These instructions calculate the cosine of the angle specified by a double－precision real number．


## FBD／LD＊${ }^{*}$


＊1 The COSD instruction does not support the structured text language and FBD／LD language．Use the standard function，COS． W Page 1585 COS（＿E）

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| COSD | - |
|  | $\boxed{Z}$ |
| COSDP | - |

## Setting data

■Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Angle data used for cosine calculation，or the start device <br> containing the angle data | $0,2^{-1022 \leq(s) \mid<2^{1024}}$ | Double－precision real <br> number | ANYREAL＿64 |
| （d） | Start device for storing the operation result | - | Double－precision real <br> number | ANYREAL＿64 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （d） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions calculate the cosine of the angle specified by（s），and store the operation result in the device specified by（d）．

－Set the angle data in radians（angle $\times \pi \div 180$ ）．
－If the operation result is -0 or an underflow occurs，the operation result turns out to 0 ．
－When an input value is set using the engineering tool，a rounding error may occur．Refer to the following for the precautions on setting input values using the engineering tool．
$\longmapsto$ Page 41 Precautions

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The value input to $(\mathrm{s})$ is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |

Point ${ }^{\rho}$
For the angle $\leftrightarrow$ radian conversion, refer to the $\operatorname{RADD}(\mathrm{P})$ and $\operatorname{DEGD}(\mathrm{P})$ instructions.
$\longmapsto$ Page 694 RADD(P)
$\leftrightarrows$ Page 696 DEGD(P)

Calculating the tangent of double－precision real number

## TAND（P）

These instructions calculate the tangent of the angle specified by a double－precision real number．


## FBD／LD＊${ }^{* 1}$


＊1 The TAND instruction does not support the structured text language and FBD／LD language．Use the standard function，TAN． $\longmapsto$ Page 1586 TAN（＿E）

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| TAND | - |
|  | $\boxed{-}$ |
| TANDP | - |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Angle data used for tangent calculation，or the start device <br> containing the angle data | $0,2^{-1022 \leq\|(s)\|<2^{1024}}$ | Double－precision real <br> number | ANYREAL＿64 |
| （d） | Start device for storing the operation result | - | Double－precision real <br> number | ANYREAL＿64 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （d） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions calculate the tangent of the angle specified by（s），and store the operation result in the device specified by（d）．

－Set the angle data in radians（angle $\times \pi \div 180$ ）．
－If the operation result is -0 or an underflow occurs，the operation result turns out to 0 ．
－When an input value is set using the engineering tool，a rounding error may occur．Refer to the following for the precautions on setting input values using the engineering tool．
$\longmapsto$ Page 41 Precautions

## Precautions

If the angle specified by (s) is $\pi / 2$ radian or (3/2) $\pi$ radian, no operation error will be issued because of the truncation error in the radian value.

## Operation error

| Error code (SD0) | Description |  |  |
| :--- | :--- | :---: | :---: |
| 3402 H | The value input to (s) is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |  |  |
| Point |  |  |  |

For the angle $\leftrightarrow$ radian conversion, refer to the $\operatorname{RADD}(P)$ and $\operatorname{DEGD}(P)$ instructions.
$\longmapsto$ Page 694 RADD(P)
$\longmapsto$ Page 696 DEGD(P)

Calculating the arc sine of double－precision real number

## ASIND（P）

These instructions calculate the angle from the sine specified by a double－precision real number．

| Ladder | ST＊${ }^{*}$ |
| :---: | :---: |
|  | ENO：＝ASINDP（EN，s，d） |
| $\square--\square$ （s） （d） |  |

## FBD／LD＊${ }^{*}$


＊1 The ASIND instruction does not support the structured text language and FBD／LD language．Use the standard function，ASIN． $\longmapsto$ Page 1587 ASIN（＿E）

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| ASIND | - |
|  | $\boxed{ }$ |
| ASINDP | - |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Sine data used for arc sine calculation，or the start device <br> containing the sine data | -1.0 to 1．0 | Double－precision real <br> number | ANYREAL＿64 |
| （d） | Start device for storing the operation result | - | Double－precision real <br> number | ANYREAL＿64 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （d） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions calculate the angle based on the sine data in the device specified by（ s ），and store the operation result in the device number specified by（d）．

－The sine data in the device specified by（s）can be set in the range from－1．0 to 1．0．
－The angle（operation result）is stored in radians in the device specified by（d）．
－If the operation result is -0 or an underflow occurs，the operation result turns out to 0 ．
－When an input value is set using the engineering tool，a rounding error may occur．Refer to the following for the precautions on setting input values using the engineering tool．
$\longmapsto$ Page 41 Precautions

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The value input to (s) is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |
| 3405 H | The value in the device specified by (s) is out of the range, -1.0 to 1.0. |

## Point 8

For the angle $\leftrightarrow$ radian conversion, refer to the RADD $(P)$ and $\operatorname{DEGD}(P)$ instructions.
$\longmapsto$ Page 694 RADD(P)
$\longmapsto$ Page 696 DEGD(P)

## Calculating the arc cosine of double-precision real number

## ACOSD(P)

These instructions calculate the angle from the cosine specified by a double-precision real number.


## FBD/LD* ${ }^{*}$


*1 The ACOSD instruction does not support the structured text language and FBD/LD language. Use the standard function, ACOS. W Page 1588 ACOS(_E)

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| ACOSD | - |
|  | $\boxed{T}$ |
| ACOSDP | - |

## Setting data

Description, range, data type

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (s) | Cosine data used for arc cosine calculation, or the start <br> device containing the cosine data | -1.0 to 1.0 | Double-precision real <br> number | ANYREAL_64 |
| (d) | Start device for storing the operation result | - | Double-precision real <br> number | ANYREAL_64 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | J밈 | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | UपIGロ, Jपاロ, U3EDl(H)GD | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (s) | - | - | $\bigcirc$ | - | - | $\bigcirc$ | - | $\bigcirc$ | - | $\bigcirc$ | - | - |
| (d) | - | - | $\bigcirc$ | - | - | $\bigcirc$ | - | $\bigcirc$ | - | - | - | - |

## Processing details

- These instructions calculate the angle based on the cosine data in the device specified by (s), and store the operation result in the device number specified by (d).

- The cosine data in the device specified by (s) can be set in the range from -1.0 to 1.0
- The angle (operation result) is stored in radians in the device specified by (d).
- If the operation result is -0 or an underflow occurs, the operation result turns out to 0 .
- When an input value is set using the engineering tool, a rounding error may occur. Refer to the following for the precautions on setting input values using the engineering tool.
$\longmapsto$ Page 41 Precautions


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The value input to (s) is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |
| 3405 H | The value in the device specified by (s) is out of the range, -1.0 to 1.0. |

Point ${ }^{\rho}$
For the angle $\leftrightarrow$ radian conversion, refer to the RADD $(P)$ and $\operatorname{DEGD}(P)$ instructions.
$\longmapsto$ Page 694 RADD(P)
$\longmapsto$ Page 696 DEGD(P)

## Calculating the arc tangent of double－precision real number

## ATAND（P）

These instructions calculate the angle from the tangent specified by a double－precision real number．


## FBD／LD＊${ }^{*}$


＊1 The ATAND instruction does not support the structured text language and FBD／LD language．Use the standard function，ATAN． $\longmapsto$ Page 1589 ATAN（＿E）

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| ATAND | - |
|  | $\boxed{ }$ |
| ATANDP | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Tangent data used for arc tangent calculation，or the start <br> device containing the tangent data | $0,2^{-1022 \leq(s) \mid<2^{1024}}$ | Double－precision real <br> number | ANYREAL＿64 |
| （d） | Start device for storing the operation result | - | Double－precision real <br> number | ANYREAL＿64 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JपIप， U3ED（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （d） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions calculate the angle based on the tangent data in the device specified by（s），and store the operation result in the device number specified by（d）．

－The angle（operation result）is stored in radians in the device specified by（d）．
－If the operation result is -0 or an underflow occurs，the operation result turns out to 0 ．
－When an input value is set using the engineering tool，a rounding error may occur．Refer to the following for the precautions on setting input values using the engineering tool．
$\mathfrak{F}$ Page 41 Precautions

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The value input to $(\mathrm{s})$ is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |

Point ${ }^{\rho}$
For the angle $\leftrightarrow$ radian conversion, refer to the $\operatorname{RADD}(\mathrm{P})$ and $\operatorname{DEGD}(\mathrm{P})$ instructions.
$\longmapsto$ Page 694 RADD(P)
$\leftrightarrows$ Page 696 DEGD(P)

Calculating the sine of BCD data

## BSIN（P）

These instructions calculate the sine of the angle specified by a BCD value．

| Ladder |  |  | ST |
| :---: | :---: | :---: | :---: |
|  | (d) |  | $\begin{aligned} & \text { ENO:=BSIN(EN,s,d); } \\ & \text { ENO:=BSINP(EN,s,d); } \end{aligned}$ |
| FBD／LD |  |  |  |
|  |  |  |  |

Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| BSIN | - |
|  | $\boxed{ }$ |
| BSINP | - |

## Setting data

－Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Data used for sine calculation，or the device containing the <br> data | 0 to 9999 | BCD 4－digit | ANY16 |
| （d） | Start device for storing the operation result | - | BCD 4－digit | ANY16＿ARRAY <br> （Number of elements： <br> $3)$ |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J미， U3EDI（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions calculate the sine of the angle specified by（s），and store the sign of the operation result in the device specified by（d）and the operation result in the devices specified by（d）＋1 and（d）＋2．

－For the value to be specified in（s），set a value from 0 to $360^{\circ}$（in the DEG．unit）as a BCD value．
－For the sign of the operation result to be stored in the device specified by（d）， 0 is stored when the operation result is positive and 1 is stored when the operation result is negative．
－The operation result to be stored in the devices specified by（ $d$ ）+1 and（ $d$ ）+2 is a BCD value in the range from -1.000 to 1.000.
－The operation result is a value whose 5 th decimal place is rounded off．

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3405 H | An out-of-range value is set to $(\mathrm{s})$. <br> • The specified data is not a BCD value. <br> • The specified data is out of the range, 0 to 360. |

Calculating the cosine of BCD data

## BCOS（P）

These instructions calculate the cosine of the angle specified by a BCD value．

| Ladder |  | $\begin{array}{\|l\|} \hline \text { ST } \\ \hline \text { ENO:=BCOS(EN,s,d); } \\ \text { ENO:=BCOSP(EN,s,d); } \end{array}$ |
| :---: | :---: | :---: |
|  | （d） |  |
| FBD／LD |  |  |
|  |  |  |

Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| BCOS | - |
|  | $\boxed{ }$ |
| BCOSP | - |

## Setting data

Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Data used for cosine calculation，or the device containing <br> the data | 0 to 9999 | BCD 4－digit | ANY16 |
| （d） | Start device for storing the operation result | - | BCD 4－digit | ANY16＿ARRAY <br> （Number of elements： <br> $3)$ |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J미， U3EDI（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions calculate the cosine of the angle specified by（s），and store the sign of the operation result in the word device specified by（d）and the operation result in the word devices specified by（d）＋1 and（d）＋2．

－For the value to be specified in（s），set a value from 0 to $360^{\circ}$（in the DEG．unit）as a BCD value．
－For the sign of the operation result to be stored in the device specified by（d）， 0 is stored when the operation result is positive and 1 is stored when the operation result is negative．
－The operation result to be stored in the devices specified by（ d ）+1 and（ d ）+2 is a BCD value in the range from -1.000 to 1.000 ．
－The operation result is a value whose 5 th decimal place is rounded off．

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3405 H | An out-of-range value is set to $(\mathrm{s})$. <br> • The specified data is not a BCD value. <br> • The specified data is out of the range, 0 to 360. |

Calculating the tangent of BCD data

## BTAN（P）

These instructions calculate the tangent of the angle specified by a BCD value．


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| BTAN | - |
|  | $\boxed{ }$ |
| BTANP | - |

## Setting data

■Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Data used for tangent calculation，or the device containing <br> the data | 0 to 9999 | BCD 4－digit | ANY16 |
| （d） | Start device for storing the operation result | - | BCD 4－digit | ANY16＿ARRAY <br> （Number of elements： <br> $3)$ |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J미， U3EDI（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions calculate the tangent of the angle specified by（s），and store the sign of the operation result in the device specified by（d）and the operation result in the devices specified by（d）＋1 and（d）＋2．

－For the value to be specified in（s），set a value from 0 to $360^{\circ}$（in the DEG．unit）as a BCD value．
－For the sign of the operation result to be stored in the device specified by（d）， 0 is stored when the operation result is positive and 1 is stored when the operation result is negative．
－The operation result to be stored in the devices specified by（d）+1 and（ $d$ ）+2 is a BCD value in the range from -57.2901 to 57.2903.
－The operation result is a value whose 5 th decimal place is rounded off．

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3405 H | An out-of-range value is set to (s). <br> • The specified data is not a BCD value. <br> - The specified data is out of the range, 0 to 360. <br> - The specified data is $90^{\circ}$ or $270^{\circ}$. |

Calculating the arc sine of BCD data

## BASIN(P)

These instructions calculate the arc sine of the angle specified by a BCD value.

| Ladder |  |  | ST |
| :---: | :---: | :---: | :---: |
|  | (d) |  | $\begin{aligned} & \mathrm{ENO}:=\mathrm{BASIN}(\mathrm{EN}, \mathrm{~s}, \mathrm{~d}) ; \\ & \mathrm{ENO}:=\mathrm{BASINP}(\mathrm{EN}, \mathrm{~s}, \mathrm{~d}) ; \end{aligned}$ |
| FBD/LD |  |  |  |
|  |  |  |  |

Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| BASIN | - |
|  | $\boxed{ }$ |
| BASINP | - |

## Setting data

■Descriptions, ranges, and data types

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (s) | Start device containing the data used for arc sine <br> calculation | 0 to 9999 | BCD 4-digit | ANY16_ARRAY <br> (Number of elements: <br> 3) |
| (d) | Device for storing the operation result | - | BCD 4-digit | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ\|ロ | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | UपIGㅁ, J밈, U3EDI(H)GD | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (s) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (d) | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - | $\bigcirc$ | - | - | - | - |

## Processing details

- These instructions calculate the arc sine of the value specified by (s), and store the operation result (angle) in the device specified by (d).

|  | $\frac{(\mathrm{s})}{\mathrm{SIN}^{-1}} \sqrt{\text { Sign }}$ |
| :--- | :--- |
|  |  |

(s) +1
(s) +2
Integral part. Decimal part ) $=$ (d)

- Set the sign of the calculation data in the device specified by (s). Store 0 when the calculation data is positive, or store 1 when the calculation data is negative.
- Store a BCD value for the integral part of calculation data in (s) +1 and a BCD value for the decimal part in (s)+2. (A value from 0 to 1.0000 can be set.)
- The operation result to be stored in the device specified by (d) is a BCD value in the range from 0 to $90^{\circ}$ or 270 to $360^{\circ}$ (in DEG. unit).
- The operation result is a value whose decimal part is rounded off.


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3405 H | An out-of-range value is set to $(\mathrm{s})$. <br> • The specified data is not a BCD value. <br> - The specified data is out of the range, -1.0000 to 1.0000. |

Calculating the arc cosine of BCD data

## BACOS（P）

These instructions calculate the arc cosine of the angle specified by a BCD value．

| Ladder |  |  | ST |
| :---: | :---: | :---: | :---: |
|  | (d) |  | $\begin{aligned} & \text { ENO:=BACOS(EN,s,d); } \\ & \text { ENO:=BACOSP(EN,s,d); } \end{aligned}$ |
| FBD／LD |  |  |  |
|  |  |  |  |

Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| BACOS | - |
| BACOSP | $\boxed{ }$ |

## Setting data

Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Start device containing the data used for arc cosine <br> calculation | 0 to 9999 | BCD 4－digit | ANY16＿ARRAY <br> （Number of elements： <br> 3） |
| （d） | Device for storing the operation result | - | BCD 4－digit | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3E미（H）Gロ | z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions calculate the arc cosine of the value specified by（ $s$ ），and store the operation result（angle）in the device specified by（d）．
$\cos ^{-1}\left(\begin{array}{c}(\mathrm{s}) \\ \text { Sign } \\ \text { Integral part } \\ \text { Is })+1 \\ \text { Decimal part }\end{array}\right)=(\mathrm{d})$
－Set the sign of the calculation data in the device specified by（s）．Store 0 when the calculation data is positive，or store 1 when the calculation data is negative．
－Store a BCD value for the integral part of calculation data in（s）＋1 and a BCD value for the decimal part in（s）＋2．（A value from 0 to 1.0000 can be set．）
－The operation result to be stored in the device specified by（d）is a BCD value in the range from 0 to $180^{\circ}$（in DEG．unit）．
－The operation result is a value whose decimal part is rounded off．

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3405 H | An out-of-range value is set to $(\mathrm{s})$. <br> • The specified data is not a BCD value. <br> - The specified data is out of the range, -1.0000 to 1.0000. |

Calculating the arc tangent of BCD data

## BATAN（P）

These instructions calculate the arc tangent of the angle specified by a BCD value．

| Ladder | ST |
| :---: | :---: |
| $\begin{array}{l\|l\|l\|} \hline-\square-\square & \text { (s) } & \text { (d) } \\ \hline \end{array}$ | $\begin{aligned} & \text { ENO:=BATAN(EN,s,d); } \\ & \text { ENO:=BATANP(EN,s,d); } \end{aligned}$ |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| BATAN | - |
|  | $\boxed{ }$ |
| BATANP | - |

## Setting data

■Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Start device containing the data used for arc tangent <br> calculation | 0 to 9999 | BCD 4－digit | ANY16＿ARRAY <br> （Number of elements： <br> 3） |
| （d） | Device for storing the operation result | - | BCD 4－digit | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3E미（H）Gロ | z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions calculate the arc tangent of the value specified by（s），and store the operation result（angle）in the device specified by（d）．

－Set the sign of the calculation data in the device specified by（s）．Store 0 when the calculation data is positive，or store 1 when the calculation data is negative．
－Store a BCD value for the integral part of calculation data in（s）＋1 and a BCD value for the decimal part in（s）＋2．（A value from 0 to 9999.9999 can be set．）
－The operation result to be stored in the device specified by（d）is a BCD value in the range from 0 to $90^{\circ}$ or 270 to $360^{\circ}$（in DEG．unit）．
－The operation result is a value whose decimal part is rounded off．

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3405 H | The data in the device specified by (s) is not a BCD value. |

## Converting single-precision real number angle to radian

## RAD(P)

These instructions convert the unit of the measure of angle from the degree specified by a single-precision real number to radian.


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| RAD | $\boxed{ }$ |
| RADP | $\uparrow$ |

## Setting data

Description, range, data type

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (s) | Angle for which the unit is to be changed to radian, or the <br> start device containing the angle | $0,2^{-126} \leq\|(\mathrm{s})\|<2^{128}$ | Single-precision real <br> number | ANYREAL_32 |
| (d) | Start device for storing the angle in radians | - | Single-precision real <br> number | ANYREAL_32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | J밈 | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | UपIGロ, J미, U3EDl(H)Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (s) | - | - | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | $\bigcirc$ | - | - |
| (d) | - | - | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - | - | - |

## Processing details

- These instructions convert the unit of the measure of angle from the degree specified by (s) to the radian, and store the angle in radians in the device number specified by (d).

- Unit conversion from the degree to the radian is performed as follows.

Radian $=$ Degree $\times \frac{\pi}{180}$

- When an input value is set using the engineering tool, a rounding error may occur. Refer to the following for the precautions on setting input values using the engineering tool.
F Page 41 Precautions


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The value input to (s) is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |
| 3403 H | The data output from (d) exceeds the following range. (An overflow has occurred.) <br> $\|(\mathrm{d})\|<2^{128}$ |

## Converting single－precision real number radian to angle

## DEG（P）

These instructions convert the unit of the measure of angle from the radian specified by a single－precision real number to the degree．



Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DEG | - |
| DEGP | $\ddots$ |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Angle in radians for which the unit is to be changed to the <br> degree，or the start device containing the angle in radians | $0,2^{-126} \leq\|(\mathrm{s})\|<2^{128}$ | Single－precision real <br> number | ANYREAL＿32 |
| （d） | Start device for storing the value converted in degrees | - | Single－precision real <br> number | ANYREAL＿32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （d） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions convert the unit of the measure of angle from the radian specified by（s）to the degree，and store the angle in degrees in the device number specified by（d）．

－Unit conversion from the radian to the degree is performed as follows．
Degree $=$ Radian $\times \frac{\pi}{180}$
－When an input value is set using the engineering tool，a rounding error may occur．Refer to the following for the precautions on setting input values using the engineering tool．
F Page 41 Precautions

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The value input to (s) is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |
| 3403 H | The data output from (d) exceeds the following range. (An overflow has occurred.) <br> $\|(\mathrm{d})\|<2^{128}$ |

## Converting double－precision real number angle to radian

## RADD（P）

These instructions convert the unit of the measure of angle from the degree specified by a single－precision real number to radian．


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| RADD | - |
|  | - |
| RADDP | $\boxed{ }$ |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Angle for which the unit is to be changed to radian，or the <br> start device containing the angle | $0,2^{-1022 \leq\|(s)\|<2^{1024}}$ | Double－precision real <br> number | ANYREAL＿64 |
| （d） | Start device for storing the angle in radians | - | Double－precision real <br> number | ANYREAL＿64 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロロロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，J밈， U3EDI（H）GD | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （d） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions convert the unit of the measure of angle from the degree specified by（s）to the radian，and store the angle in radians in the device number specified by（d）．

－Unit conversion from the degree to the radian is performed as follows．
Radian $=$ Degree $\times \frac{\pi}{180}$
－If the operation result is -0 or an underflow occurs，the operation result turns out to 0 ．
－When an input value is set using the engineering tool，a rounding error may occur．Refer to the following for the precautions on setting input values using the engineering tool．
F Page 41 Precautions

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The value input to (s) is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |
| 3403 H | The data output from (d) exceeds the following range. (An overflow has occurred.) <br> $\|(\mathrm{d})\|<2^{1024}$ |

## Converting double－precision real number radian to angle

## DEGD（P）

These instructions convert the unit of the measure of angle from the radian specified by a double－precision real number to the degree．



Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DEGD | - |
|  | - |
| DEGDP | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Angle in radians for which the unit is to be changed to the <br> degree，or the start device containing the angle in radians | $0,2^{-1022 \leq\|(s)\|<2^{1024}}$ | Double－precision real <br> number | ANYREAL＿64 |
| （d） | Start device for storing the value converted in degrees | - | Double－precision real <br> number | ANYREAL＿64 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （d） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions convert the unit of the measure of angle from the radian specified by（s）to the degree，and store the angle in degrees in the device number specified by（d）．

－Unit conversion from the radian to the degree is performed as follows．
Degree $=$ Radian $\times \frac{180}{\pi}$
－If the operation result is -0 or an underflow occurs，the operation result turns out to 0 ．
－When an input value is set using the engineering tool，a rounding error may occur．Refer to the following for the precautions on setting input values using the engineering tool．
$\Vdash$ Page 41 Precautions

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The value input to (s) is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |
| 3403 H | The data output from (d) exceeds the following range. (An overflow has occurred.) <br> $\|(\mathrm{d})\|<2^{1024}$ |

Calculating the square root of single-precision real number

## ESQRT(P)

These instructions calculate the square root of the value specified by a single-precision real number.

| Ladder | ST |
| :---: | :---: |
| $\begin{array}{\|c\|c\|c\|} \hline-\square-\square & \text { (s) } & \text { (d) } \\ \hline \end{array}$ | $\begin{aligned} & \text { ENO:=ESQRT(EN,s,d); } \\ & \text { ENO:=ESQRTP(EN,s,d); } \end{aligned}$ |

FBD/LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| ESQRT | - |
|  | $\boxed{ }$ |
| ESQRTP | - |

## Setting data

■Description, range, data type

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (s) | Data used for square root operation, or the start device <br> containing the data | $0,2^{-126 \leq\|(s)\|<2^{128}}$ | Single-precision real <br> number | ANYREAL_32 |
| (d) | Start device for storing the operation result | - | Single-precision real <br> number | ANYREAL_32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ\|ロ | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | UपIGㅁ, J밈, U3EDl(H)GD | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (s) | - | - | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | $\bigcirc$ | - | - |
| (d) | - | - | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - | - | - |

## Processing details

- These instructions calculate the square root of the value specified by (s), and store the operation result in the device specified by (d).

- The value specified by (s) must be positive. (No negative value can be calculated.)
- When an input value is set using the engineering tool, a rounding error may occur. Refer to the following for the precautions on setting input values using the engineering tool.
↔ Page 41 Precautions


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The value input to (s) is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |
| 3405 H | The value in the device specified by (s) is a negative number. |

Calculating the square root of double-precision real number

## EDSQRT(P)

These instructions calculate the square root of the value specified by a double-precision real number.

| Ladder |  | ST |
| :---: | :---: | :---: |
|  | (d) | $\begin{aligned} & \text { ENO:=EDSQRT(EN,s,d); } \\ & \text { ENO:=EDSQRTP(EN,s,d); } \end{aligned}$ |
| FBD/LD |  |  |
|  |  |  |

Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| EDSQRT | - |
|  | $\boxed{ }$ |
| EDSQRTP | $\boxed{ }$ |

## Setting data

Description, range, data type

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (s) | Data used for square root operation, or the start device <br> containing the data | $0,2^{-1022 \leq\|(s)\|<2^{1024}}$ | Double-precision real <br> number | ANYREAL_64 |
| (d) | Start device for storing the operation result | - | Double-precision real <br> number | ANYREAL_64 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ\|ロ | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | UपIGㅁ, J밈, U3EDl(H)GD | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (s) | - | - | $\bigcirc$ | - | - | $\bigcirc$ | - | $\bigcirc$ | - | $\bigcirc$ | - | - |
| (d) | - | - | $\bigcirc$ | - | - | $\bigcirc$ | - | $\bigcirc$ | - | - | - | - |

## Processing details

- These instructions calculate the square root of the value specified by ( $s$ ), and store the operation result in the device specified by (d).

- The value specified by (s) must be positive. (No negative value can be calculated.)
- If the operation result is -0 or an underflow occurs, the operation result turns out to 0 .
- When an input value is set using the engineering tool, a rounding error may occur. Refer to the following for the precautions on setting input values using the engineering tool.
$\longmapsto$ Page 41 Precautions


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The value input to (s) is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |
| 3405 H | The value in the device specified by (s) is a negative number. |

Calculating the exponent of single－precision real number

## EXP（P）

These instructions calculate the exponent of the value specified by a single－precision real number．


## FBD／LD＊${ }^{* 1}$


＊1 The EXP instruction does not support the structured text language and FBD／LD language．Use the standard function，EXP． $\longmapsto$ Page 1583 EXP（＿E）

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| EXP | - |
|  | $\boxed{T}$ |
| EXPP | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Data used for exponential operation，or the start device <br> containing the data | $0,2^{-126 \leq\|(s)\|<2^{128}}$ | Single－precision real <br> number | ANYREAL＿32 |
| （d） | Start device for storing the operation result | - | Single－precision real <br> number | ANYREAL＿32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （d） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions calculate the exponent of the value specified by（s），and store the operation result in the device specified by（d）．

－Exponent operation is performed with the base（e）set to＂2．71828＂．
－When an input value is set using the engineering tool，a rounding error may occur．Refer to the following for the precautions on setting input values using the engineering tool．

W Page 41 Precautions

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The value input to (s) is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |
| 3403 H | The data output from (d) exceeds the following range. (An overflow has occurred.) <br> $\|(d)\|<2^{128}$ |

## Point 8

- The $\operatorname{EXP}(P)$ instruction performs operation using the natural logarithm. To determine a value using the common logarithm, determine a value by dividing the common logarithm value by 0.43429 and specify it in the device specified by (s).
$10^{x}=e^{\frac{x}{0.43429}}$

Calculating the exponent of double－precision real number

## EXPD（P）

These instructions calculate the exponent of the value specified by a double－precision real number．


## FBD／LD＊${ }^{* 1}$


＊1 The EXPD instruction does not support the structured text language and FBD／LD language．Use the standard function，EXP． Њ Page 1583 EXP（＿E）

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| EXPD | - |
|  | $\boxed{ }$ |
| EXPDP | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Data used for exponential operation，or the start device <br> containing the data | $0,2^{-1022 \leq\|(s)\|<2^{1024}}$ | Double－precision real <br> number | ANYREAL＿64 |
| （d） | Start device for storing the operation result | - | Double－precision real <br> number | ANYREAL＿64 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （d） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions calculate the exponent of the value specified by（s），and store the operation result in the device specified by（d）．
$\left(\begin{array}{llll}(\mathrm{s})+3 & (\mathrm{~s})+2 & (\mathrm{~s})+1 & (\mathrm{~s}) \\ \square & \square & \square & \square\end{array}\right)$
Double－precision real number
$\qquad$

－Exponent operation is performed with the base（e）set to＂2．71828＂．
－If the operation result is -0 or an underflow occurs，the operation result turns out to 0 ．
－When an input value is set using the engineering tool，a rounding error may occur．Refer to the following for the precautions on setting input values using the engineering tool．
$\longmapsto$ Page 41 Precautions

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The value input to (s) is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |
| 3403 H | The data output from (d) exceeds the following range. (An overflow has occurred.) <br> $\|(d)\|<2^{1024}$ |

## Point 8

- The EXPD $(P)$ instruction performs operation using the natural logarithm. To determine a value using the common logarithm, determine a value by dividing the common logarithm value by 0.43429 and specify it in the device specified by (s).
$10^{x}=e^{\frac{x}{0.43429}}$


## Calculating the natural logarithm of single－precision real number

## LOG（P）

These instructions calculate the logarithm using the natural logarithm（e）of the value specified by a single－precision real number as the base．


＊1 The LOG instruction does not support the structured text language and FBD／LD language．Use the standard function，LOG． に Page 1581 LOG（＿E）

## IExecution condition

| Instruction | Execution condition |
| :--- | :--- |
| LOG | $\boxed{ }$ |
| LOGP | $\boxed{ }$ |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Data used for natural logarithm operation，or the start <br> device containing the data | $0,2^{-126} \leq\|(\mathrm{s})\|<2^{128}$ | Single－precision real <br> number | ANYREAL＿32 |
| （d） | Start device for storing the operation result | - | Single－precision real <br> number | ANYREAL＿32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （d） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions calculate the logarithm using natural logarithm（e）of the value specified by（s），and store the operation result in the device specified by（d）．

－The value specified by（s）must be positive．（No negative value can be calculated．）
－When an input value is set using the engineering tool，a rounding error may occur．Refer to the following for the precautions on setting input values using the engineering tool．
3 Page 41 Precautions

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The value input to (s) is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |
| 3405 H | An out-of-range value is set to (s). <br> •The specified value is a negative number. <br> •The specified value is 0. |

Calculating the natural logarithm of double－precision real number

## LOGD（P）

These instructions calculate the logarithm using the natural logarithm（e）of the value specified by a double－precision real number as the base．


＊1 The LOGD instruction does not support the structured text language and FBD／LD language．Use the standard function，LOG． に
Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| LOGD | - |
|  | $\boxed{Z}$ |
| LOGDP | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Data used for natural logarithm operation，or the start <br> device containing the data | $0,2^{-1022 \leq\|(s)\|<2^{1024}}$ | Double－precision real <br> number | ANYREAL＿64 |
| （d） | Start device for storing the operation result | - | Double－precision real <br> number | ANYREAL＿64 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J밈， U3E미（H）Gㅁ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （d） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions calculate the logarithm using natural logarithm（e）of the value specified by（s），and store the operation result in the device specified by（d）．
$\log (\underbrace{\left.\begin{array}{llll}(\mathrm{s})+3 & (\mathrm{~s})+2 & (\mathrm{~s})+1 & (\mathrm{~s}) \\ \square & \square & \square & \square\end{array}\right), ~}$
Double－precision real number
$\begin{array}{lll}(d)+3 & (d)+2 & (d)+1\end{array}$
（d）
－The value specified by（s）must be positive．（No negative value can be calculated．）
－If the operation result is -0 or an underflow occurs，the operation result turns out to 0 ．
－When an input value is set using the engineering tool，a rounding error may occur．Refer to the following for the precautions on setting input values using the engineering tool．
$\longmapsto$ Page 41 Precautions

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The value input to (s) is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |
| 3405 H | An out-of-range value is set to (s). <br> •The specified value is a negative number. <br> •The specified value is 0. |

Calculating the square root of BCD 4－digit data

## BSQRT（P）

These instructions calculate the square root of the value specified by a BCD 4－digit data．

| Ladder | ST |
| :---: | :---: |
| $\square--\square$ （s） （d） | $\begin{aligned} & \text { ENO:=BSQRT(EN,s,d); } \\ & \text { ENO:=BSQRTP(EN,s,d); } \end{aligned}$ |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| BSQRT | - |
|  | $\boxed{ }$ |
| BSQRTP | - |

## Setting data

■Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Data used for square root operation，or the device <br> containing the data | 0 to 9999 | BCD 4－digit | ANY16 |
| （d） | Start device for storing the operation result | - | BCD 4－digit | ANY16＿ARRAY <br> （Number of elements： <br> $2)$ |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J미， U3EDI（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions calculate the square root of the BCD 4－digit data specified by（s），and store the operation result in the device specified by（d）．

－The value to be specified in（s）is a BCD value with a maximum of 4 digits（ 0 to 9999 ）．
－A BCD value from 0 to 9999.9999 is stored as the operation result in the device specified by（d）．
－The operation result is a value whose 5 th decimal place is rounded down．

## Operation error

| Error code（SDO） | Description |
| :--- | :--- |
| 3405 H | The data in the device specified by $(\mathrm{s})$ is not a BCD value． |

Calculating the square root of BCD 8－digit data

## BDSQRT（P）

These instructions calculate the square root of the value specified by a BCD 8－digit data．

| Ladder |  |  | ST |
| :---: | :---: | :---: | :---: |
|  | (d) |  | $\begin{aligned} & \text { ENO:=BDSQRT(EN,s,d); } \\ & \text { ENO:=BDSQRTP(EN,s,d); } \end{aligned}$ |
| FBD／LD |  |  |  |
|  |  |  |  |

Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| BDSQRT | - |
|  | - |
| BDSQRTP | - |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Data used for square root operation，or the start device <br> containing the data | 0 to 999999999 | BCD 8－digit | ANY32 |
| （d） | Start device for storing the operation result | - | BCD 4－digit | ANY16＿ARRAY <br> （Number of elements： <br> $2)$ |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J미， U3EDI（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions calculate the square root of the BCD 8－digit data specified by（s），and store the operation result in the device specified by（d）．

－The value to be specified in（s）is a BCD value with a maximum of 8 digits（ 0 to 99999999 ）．
－A BCD value from 0 to 9999.9999 is stored as the operation result in the device specified by（d）．
－The operation result is a value whose 5 th decimal place is rounded down．

## Operation error

| Error code（SDO） | Description |
| :--- | :--- |
| 3405 H | The data in the device specified by $(\mathrm{s})$ is not a BCD value． |

Calculating the exponentiation of single－precision real number

## POW（P）

These instructions calculate the exponentiation of a single－precision real number．


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| POW | - |
|  | $\boxed{Z}$ |
| POWP | - |

Setting data
■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Exponentiation recipient data or the start device containing <br> the exponentiation recipient data | $0,2^{-126} \leq\|(\mathrm{s} 1)\|<2^{128}$ | Single－precision real <br> number | ANYREAL＿32 |
| （s2） | Exponentiation data or the start device containing the data | $0,2^{-126} \leq\|(\mathrm{s} 2)\|<2^{128}$ | Single－precision real <br> number | ANYREAL＿32 |
| （d） | Start device for storing the operation result | - | Single－precision real <br> number | ANYREAL＿32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）GD | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （s2） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （d） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions raises the single-precision real number specified by ( $s$ 1) to the power of the single-precision real number specified by ( s 2 ), and store the operation result in the device specified by (d).

- The values that can be specified by ( s 1 ) and ( s 2 ) and the value that can be stored are $0,2^{-126} \leq \mid$ setting value (stored value) $\mid<2{ }^{128}$.
- If the operation result is -0 or an underflow occurs, the operation result turns out to 0 .
- When an input value is set using the engineering tool, a rounding error may occur. Refer to the following for the precautions on setting input values using the engineering tool.
T Page 41 Precautions


## Operation error

| Error code (SD0) | Description |
| :--- | :--- |
| 3402 H | The value input to (s1) or (s2) is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |

Calculating the exponentiation of double－precision real number

## POWD（P）

These instructions calculate the exponentiation of a double－precision real number．


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| POWD | - |
|  | $\boxed{\square}$ |
| POWDP | - |

Setting data
■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Exponentiation recipient data or the start device containing <br> the exponentiation recipient data | $0,2^{-1022 \leq\|(s 1)\|<2^{1024}}$ | Double－precision real <br> number | ANYREAL＿64 |
| （s2） | Exponentiation data or the start device containing the data | $0,2^{-1022} \leq\|(s 2)\|<2^{1024}$ | Double－precision real <br> number | ANYREAL＿64 |
| （d） | Start device for storing the operation result | - | Double－precision real <br> number | ANYREAL＿64 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）GD | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （d） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions raises the double-precision real number specified by ( s 1 ) to the power of the double-precision real number specified by ( s 2 ), and store the operation result in the device specified by (d).

- The values that can be specified by ( s 1 ) and ( s 2 ) and the value that can be stored are $0,2^{-1022} \leq \mid$ setting value (stored value)|<2 ${ }^{1024}$.
- If the operation result is -0 or an underflow occurs, the operation result turns out to 0 .
- When an input value is set using the engineering tool, a rounding error may occur. Refer to the following for the precautions on setting input values using the engineering tool.
$\longmapsto$ Page 41 Precautions


## Operation error

| Error code (SD0) | Description |
| :--- | :--- |
| 3402 H | The value input to (s1) or (s2) is -0 , a subnormal number, NaN (not a number), or $\pm \infty$. |

## Calculating the common logarithm of single－precision real number

## LOG10（P）

These instructions calculate the logarithm using the common logarithm（using 10 as the base）of the value specified by a single－precision real number

| Ladder | ST |
| :---: | :---: |
| $\begin{array}{\|l\|l\|l\|} \hline-\square-\square & \text { (s) } & \text { (d) } \\ \hline \end{array}$ | $\begin{aligned} & \text { ENO:=LOG10(EN,s,d); } \\ & \text { ENO:=LOG10P(EN,s,d); } \end{aligned}$ |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| LOG10 | - |
| LOG10P | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Data used for common logarithm operation，or the start <br> device containing the data | $0,2^{-126 \leq\|(s)\|<2^{128}}$ | Single－precision real <br> number | ANYREAL＿32 |
| （d） | Start device for storing the operation result | - | Single－precision real <br> number | ANYREAL＿32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3E미（H）G口 | z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （d） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions calculate the common logarithm（using 10 as the base）of the value specified by（s），and store the operation result in the device number specified by（d）．
$\log 10(\underbrace{\begin{array}{|c|c|}\hline(\mathrm{s})+1 & \text {（s）}\end{array}})$
Single－precision real number


Single－precision real number
－The value specified by（s）must be positive．（No negative value can be calculated．）
－If the operation result is -0 or an underflow occurs，the operation result turns out to 0 ．
－When an input value is set using the engineering tool，a rounding error may occur．Refer to the following for the precautions on setting input values using the engineering tool．
$\leftrightarrows$ Page 41 Precautions

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The value input to (s) is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |
| 3405 H | An out-of-range value is set to (s). <br> • The specified value is a negative number. <br> • The specified value is 0. |

## Calculating the common logarithm of double－precision real number

## LOG10D（P）

These instructions calculate the logarithm using the common logarithm（using 10 as the base）of the value specified by a double－precision real number．

| Ladder | ST |
| :---: | :---: |
| $\begin{array}{\|l\|l\|l\|} \hline-\square-\square & \text { (s) } & \text { (d) } \\ \hline \end{array}$ | $\begin{aligned} & \text { ENO:=LOG10D(EN,s,d); } \\ & \text { ENO:=LOG10DP(EN,s,d); } \end{aligned}$ |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| LOG10D | - |
| LOG10DP | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Data used for common logarithm operation，or the start <br> device containing the data | $0,2^{-1022 \leq\|(s)\|<2^{1024}}$ | Double－precision real <br> number | ANYREAL＿64 |
| （d） | Start device for storing the operation result | - | Double－precision real <br> number | ANYREAL＿64 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）G口 | z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | － | － |
| （d） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions calculate the common logarithm（using 10 as the base）of the value specified by（s），and store the operation result in the device number specified by（d）．

－The value specified by（s）must be positive．（No negative value can be calculated．）
－If the operation result is -0 or an underflow occurs，the operation result turns out to 0 ．
－When an input value is set using the engineering tool，a rounding error may occur．Refer to the following for the precautions on setting input values using the engineering tool．

↔ Page 41 Precautions

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The value input to (s) is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |
| 3405 H | An out-of-range value is set to (s). <br> • The specified value is a negative number. <br> • The specified value is 0. |

## Searching the maximum value of single－precision real number

## EMAX（P）

These instructions search the block data of single－precision real numbers for the maximum value．


## FBD／LD＊


＊1 The EMAX instruction does not support the structured text language and FBD language．Use the standard function，MAX． W Page 1617 MAX（＿E），MIN（＿E）

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| EMAX | - |
|  | $\boxed{ }$ |
| EMAXP | - |

## Setting data

Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Single－precision real number used for maximum value <br> search，or the start device containing single－precision real <br> numbers | $0,2^{-126} \leq\|(\mathrm{s})\|<2^{128}$ | Single－precision real <br> number | ANYREAL＿32 |
| （d） | Start device for storing the search result <br> （d）and（d）$+1:$ Maximum value <br> （d）＋2：Position <br> （d）＋3：The number of search target data points | - | Single－precision real <br> number | ANY＿REAL＿32＿ARR <br> AY <br> （Number of elements： |
| （n） | Number of single－precision real number block data points | 0 to 65535 | AN | BOOL |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit |  |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ $n$ ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- These instructions search for the maximum value in the $(\mathrm{n})$ points of single-precision real number block data in the device starting from the one specified by (s), and store the maximum value in the device specified by (d). The instructions store the location of the first maximum value by the number of points from (s) in the device specified by (d) +2 and the number of maximum values in the device specified by (d)+3.
- The start of the block data in the device specified by (s) is counted as the 1st point when the search result (location) is counted.

| $(\mathrm{s})+1,(\mathrm{~s})$ | 1.2345 |
| :--- | :---: |
| $(\mathrm{~s})+3,(\mathrm{~s})+2$ | 123.45 |
| $(\mathrm{~s})+5,(\mathrm{~s})+4$ |  |
| $(\mathrm{~s})+7,(\mathrm{~s})+6$ | -1.2345 |
| $(\mathrm{n})+9,(\mathrm{n})+8$ | -12.345 |
|  |  |



- When ( n ) is 0 , the processing is not performed.


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The block data in the device specified by (s) includes a value other than single-precision real number. |

## Searching the maximum value of double－precision real number

## EDMAX（P）

These instructions search the block data of double－precision real numbers for the maximum value．


## FBD／LD＊${ }^{*}$


＊1 The EDMAX instruction does not support the structured text language and FBD／LD language．Use the standard function，MAX． に $\mathfrak{F}$ Page 1617 MAX（＿E），MIN（＿E）

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| EDMAX | - |
|  | $\boxed{ }$ |
| EDMAXP | - |

## Setting data

Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Double－precision real number used for maximum value <br> search，or the start device containing double－precision real <br> numbers | $0,2^{-1022 \leq\|(s)\|<2^{1024}}$ | Double－precision real <br> number | ANYREAL＿64 |
| （d） | Start device for storing the search result <br> （d）to（d）＋3：Maximum value <br> （d）＋4：Position <br> （d）＋5：The number of search target data points | - | Double－precision real <br> number | ANY＿REAL＿64＿ARR <br> AY <br> （Number of elements： <br> 6） |
| （n） | Number of double－precision real number block data points | 0 to 65535 | BNY | BOOL |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - |  |  |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ $n$ ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- These instructions search for the maximum value in the $(\mathrm{n})$ points of double-precision real number block data in the device starting from the one specified by (s), and store the maximum value in the device specified by (d). The instructions store the location of the first maximum value by the number of points from (s) in the device specified by (d) +4 and the number of maximum values in the device specified by (d) +5 .
- The start of the block data in the device specified by (s) is counted as the 1st point when the search result (location) is counted.

- When $(\mathrm{n})$ is 0 , the processing is not performed.


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The block data in the device specified by (s) includes a value other than double-precision real number. |

## Searching the minimum value of single－precision real number

## EMIN（P）

These instructions search the block data of single－precision real numbers for the minimum value．


## FBD／LD＊${ }^{*}$


＊1 The EMIN instruction does not support the structured text language and FBD／LD language．Use the standard function，MIN． W Page 1617 MAX（＿E），MIN（＿E）

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| EMIN | - |
|  | $\boxed{ }$ |
| EMINP | - |

## Setting data

Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Single－precision real number used for minimum value <br> search，or the start device containing single－precision real <br> numbers | $0,2^{-126} \leq\|(\mathrm{s})\|<2^{128}$ | Single－precision real <br> number | ANYREAL＿32 |
| （d） | Start device for storing the search result <br> （d）and（d）$+1:$ Minimum value <br> （d）$+2:$ Position <br> （d）＋3：The number of search target data points | - | Single－precision real <br> number | ANY＿REAL＿32＿ARR <br> AY <br> （Number of elements： |
| （n） | Number of single－precision real number block data points | 0 to 65535 | ANY16 |  |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ $n$ ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- These instructions search for the minimum value in the ( n ) points of single-precision real number block data in the device starting from the one specified by (s), and store the maximum value in the device specified by ( d ). The instructions store the location of the first minimum value by the number of points from (s) in the device specified by (d)+2 and the number of minimum values in the device specified by (d) +3 .
- The start of the block data in the device specified by $(s)$ is counted as the 1 st point when the search result (location) is counted.

| (s) $+1,(\mathrm{~s}$ ) | 1.2345 |  |
| :---: | :---: | :---: |
| (s) $+3,(\mathrm{~s})+2$ | 123.45 |  |
| (s) $+5,(\mathrm{~s})+4$ | -1.2345 | (n) |
| (s) $+7,(\mathrm{~s})+6$ | -12.345 |  |
| (s) $+9,(\mathrm{~s})+8$ | -123.45 | $\nabla$ |



- When $(\mathrm{n})$ is 0 , the processing is not performed.


## Operation error

| Error code (SD0) | Description |
| :--- | :--- |
| 3402 H | The block data in the device specified by (s) includes a value other than single-precision real number. |

## Searching the minimum value of double－precision real number

## EDMIN（P）

These instructions search the block data of double－precision real numbers for the minimum value．


## FBD／LD＊${ }^{*}$


＊1 The EDMIN instruction does not support the structured text language and FBD／LD language．Use the standard function，MIN． に

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| EDMIN | - |
|  | $\boxed{ }$ |
| EDMINP | - |

## Setting data

Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Double－precision real number used for minimum value <br> search，or the start device containing double－precision real <br> numbers | $0,2^{-1022 \leq\|(s)\|<2^{1024}}$ | Double－precision real <br> number | ANYREAL＿64 |
| （d） | Start device for storing the search result <br> （d）to（d）＋3：Minimum value <br> （d）＋4：Position <br> （d）＋5：The number of search target data points | - | Double－precision real <br> number | ANY＿REAL＿64＿ARR <br> AY <br> （Number of elements： <br> 6） |
| （n） | Number of double－precision real number block data points | 0 to 65535 | BNY | BOOL |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - |  |  |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ $n$ ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- These instructions search for the minimum value in the ( n ) points of double-precision real number block data in the device starting from the one specified by (s), and store the maximum value in the device specified by (d). The instructions store the location of the first minimum value by the number of points from (s) in the device specified by (d)+4 and the number of minimum values in the device specified by (d) +5 .
- The start of the block data in the device specified by $(s)$ is counted as the 1 st point when the search result (location) is counted.

| (s) $+3 \cdots$ (s) | 1.2345 |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| (s) $+7 \cdots(\mathrm{~s})+4$ | 123.45 |  |  | -123.45 | Minimum value |
| (s) $+11 \cdots(\mathrm{~s})+8$ | -1.2345 |  | (d) +2 |  |  |
| (s) $+15 \cdots$ (s) +12 | -12.345 |  |  |  | atio |
| (s)+19 $\cdots$ (s)+16 | -123.45 |  | (d) +4 | 2 | Location |
|  |  |  | (d) +5 | 1 | Number of maximum values |

- When $(\mathrm{n})$ is 0 , the processing is not performed.


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The block data in the device specified by (s) includes a value other than double-precision real number. |

## 7．10 Random Number Instructions

## Generating random number

## RND（P）

These instructions generate a random number between 0 and less than 32767 ，and store the random number in the specified device．

| Ladder | ST |
| :--- | :--- | :--- |
|  | $-\square$ ENO：＝RND（EN，d）； <br> $-\square$ （d） <br>   |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| RND | - |
|  | $\boxed{ }$ |
| RNDP | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （d） | Device for storing the random number | - | 16－bit signed binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## －Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

These instructions generate a random number between 0 and 32767，and store the random number in the device specified by （d）．The RND（P）instruction generates random numbers according to a certain calculation expression．The calculation expression uses the previous calculation result as a coefficient．

## Operation error

There is no operation error．

## Changing random sequence

## SRND（P）

These instructions change the random number sequence according to the content of the 16 －bit binary data stored in the specified device．


FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| SRND | - |
|  | $\boxed{ }$ |
| SRNDP | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Random number sequence data | -32768 to 32767 | 16－bit signed binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）G口 | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

These instructions change the random number sequence according to the content of the 16－bit binary data stored in the device specified by（s）．The SRND（P）instruction can change the random number calculation pattern．

## Operation error

There is no operation error．

## 7．11 Index Register Instructions

## Saving all data of the index register

## ZPUSH（P）

These instructions save the content of the index register to the specified area．

| Ladder | ST |
| :---: | :---: |
|  | $\begin{aligned} & \text { ENO:=ZPUSH(EN,d); } \\ & \text { ENO:=ZPUSHP(EN,d); } \end{aligned}$ |

FBD／LD


## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| ZPUSH | - |
|  | - |
| ZPUSHP |  |

## Setting data

DDescription，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （d） | Start device to which the index register will be saved | - | 16－bit signed binary | ANY16＊1 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

＊1 When specifying data with a label，define the array so that an area required for operation can be secured，and specify the array label element．
■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3EDI（H）G口 | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions save the content of the index register to the device specified by (d) or the array label element and later.
- When the content of the index register is saved, the number of saves (d) is incremented by 1.
- Regardless of the number of points assigned to the index register and long index register, 24 words of data are saved. Accordingly, when 0 point is assigned to the index register, the long index register is saved by 12 points.
- The $\mathrm{ZPOP}(P)$ instructions can be used to restore data. The $\mathrm{ZPUSH}(P)$ and $\mathrm{ZPOP}(P)$ instructions are used in pairs and can be nested to be used as a stack.
$\longmapsto$ Page 732 ZPOP(P)
- A nesting structure can be created by specifying the area specified by (d) of the ZPUSH(P) instruction in (d) of the ZPUSH $(P)$ instruction again. The number of saves (d) is incremented by 1 every time the $\mathrm{ZPUSH}(\mathrm{P})$ instruction is executed.
- When another area is specified in (d) of the ZPUSH(P) instruction in the nesting structure, the content of the index register is saved to the specified another area.
- In the nesting structure, every time the $\mathrm{ZPUSH}(\mathrm{P})$ instruction is executed, saved data is added. Therefore, secure in advance the areas necessary for the number of times the instruction is executed.
- The following figure shows the configuration of the areas used after (d).



## Operation error

| Error code (SD0) | Description |
| :--- | :--- |
| 3405 H | The number of saves in (d) +0 is FFFF. |

## Returning all data of the index register

## ZPOP（P）

These instructions read the data，which has been saved to the specified area，into the index register．

| Ladder | ST |
| :---: | :---: |
| $\begin{array}{\|l\|l\|} \hline-\square-\square & \text { (d) } \\ \hline \end{array}$ | $\begin{aligned} & \text { ENO:=ZPOP(EN,d); } \\ & \text { ENO:=ZPOPP(EN,d); } \end{aligned}$ |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| ZPOP | - |
|  | $\boxed{Z}$ |
| ZPOPP | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （d） | Start device to which the index register will be restored | - | 16 －bit signed binary | ANY16 ${ }^{* 1}$ |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

＊1 When specifying data with a label，define the array so that an area required for operation can be secured，and specify the array label element．

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J미， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions read the data，which has been saved to the device specified by（d）or the array label element and later， into the index register．
－When the content of the index register is read，the number of saves（d）is decremented by 1.
－Refer to the following for the configuration of the areas used after（d）．
$\mathfrak{F}$ Page 730 ZPUSH（P）

## Operation error

| Error code（SDO） | Description |
| :--- | :--- |
| 3405 H | The number of saves in $(\mathrm{d})+0$ is 0. |

## Saving the selected data of the index register and long index register

## ZPUSH（P）

These instruction save the contents of the index register and long index register to the specified area．

| Ladder | ST |
| :---: | :---: |
| $\square-\square-\square$ （s） （d） | $\begin{aligned} & \text { ENO:=ZPUSH_2(EN,s,d); } \\ & \text { ENO:=ZPUSHP_2(EN,s,d); } \end{aligned}$ |

FBD／LD

（ $\square$ is replaced by ZPUSH＿2 or ZPUSHP＿2．）

## －Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| ZPUSH | - |
|  | $\boxed{ }$ |
| ZPUSHP |  |

Setting data
Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Type of the index register and long index register to be <br> saved | 1 to 3 | 16－bit unsigned binary | ANY16 |
| （d） | Start device to which the index register and long index <br> register will be saved | - | 16 －bit signed binary | ANY16＊1 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

＊1 When specifying data with a label，define the array so that an area required for operation can be secured，and specify the array label element．

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGㅁ，J밈， U3EDI（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions save the contents of the index register and long index register in the device specified by (s) to the device specified by (d) or array label element and later. The instructions also save the type of the saved index register and long index register to the end of the saved data.
- When the contents of the index register and long index register are saved, the number of saves (d) is incremented by 1.
- The following table lists the values specified by (s) and the index registers and long index registers to be saved.

| Value of (s) | $\mathbf{Z}$ and $L Z$ saved |
| :--- | :--- |
| 1 | Total range of $Z$ and $L Z$ |
| 2 | Total range of $Z$ |
| 3 | Total range of $L Z$ |

- The $\mathrm{ZPOP}(\mathrm{P})$ instructions (restoring the selected data of the index register and long index register) are used to restore data. The $\mathrm{ZPUSH}(\mathrm{P})$ and $\mathrm{ZPOP}(\mathrm{P})$ instructions are used in pairs and can be nested to be used as a stack.
$\leqslant$ Page $736 \mathrm{ZPOP}(\mathrm{P})$
- A nesting structure can be created by specifying the area specified by (d) of the ZPUSH(P) instruction in (d) of the ZPUSH(P) instruction again. The number of saves (d) is incremented by 1 every time the $\mathrm{ZPUSH}(\mathrm{P})$ instruction is executed.
- When another area is specified in (d) of the $\mathrm{ZPUSH}(\mathrm{P})$ instruction in the nesting structure, the content of the index register or long index register is saved to the specified another area.
- In the nesting structure, every time the $\mathrm{ZPUSH}(\mathrm{P})$ instruction is executed, saved data is added. Therefore, check the numbers of points assigned to the index register and long index register according to SD300 and SD302, and secure in advance the areas necessary for the number of times the instruction is executed.
- The following figure shows the configuration of the areas used after (d). (Z0 to Z23 and LZ0 to LZ4)



## Precautions

(d)+1 and (d)+2 for the ZPUSH(P) instructions are used for the system. Do not change the values.

The $Z$ and $L Z$ save types stored in the area specified by (d) and later are also used for the system. Do not change the values. Changing the values may cause malfunction of the module.

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3405 H | An out-of-range value is set to (s). <br> • The specified value is other than 1 to 3. <br> - When the number of index register points is 0,2 is specified. <br> • When the number of long index register points is 0,3 is specified. |
|  | The value stored in the system-reserved area in the area specified by (d) has been changed. |

## Returning the selected data of the index register and long index register

## ZPOP（P）

These instructions read the data，which has been saved to the specified area，into the index register and long index register．

| Ladder | ST |
| :---: | :---: |
| $\square-\square-\square$ （s） （d） | $\begin{aligned} & \text { ENO:=ZPOP_2(EN,s,d); } \\ & \text { ENO:=ZPOPP_2(EN,s,d); } \end{aligned}$ |

FBD／LD

（ $\mathrm{\square}$ is replaced by ZPOP 2 or ZPOPP 2．）

## －Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| ZPOP | - |
|  | $\boxed{ }$ |
| ZPOPP | - |

Setting data
■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Dummy | - | 16－bit unsigned binary | ANY16 |
| （d） | Start device to which the index register will be restored | - | 16－bit signed binary | ANY16＊1 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

＊1 When specifying data with a label，define the array so that an area required for operation can be secured，and specify the array label element．

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J밈， U3EDl（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | － | $\bigcirc$ | － | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions read the data，which has been saved to the device specified by（d）or the array label element and later， into the index register or long index register．
－When the data saved to the index register and long index register is read，the number of saves（d）is decremented by 1.
－The date data in the device specified by（s）is regarded as dummy data and ignored．
－Refer to the following for the configuration of the areas used after（d）．
$\lessgtr$ Page 733 ZPUSH（P）

## Precautions

(d)+1 and (d)+2 are used for the system. Do not change the values.

The $Z$ and $L Z$ save types stored in the area specified by (d) and later are also used for the system. Do not change the values. Changing the values may cause malfunction of the module.

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
|  | The number of saves in (d) +0 is 0. |
|  | The value stored in the system-reserved area in the area specified by (d) has been changed. |
|  | A value other than 1 to 3 is set to the $Z$, LZ save type. |

## 7．12 Data Control Instructions

## Upper and lower limit control of 16－bit binary data

## LIMIT（P）（＿U）

These instructions control the output value depending on whether the specified 16－bit binary bit value is within the upper and lower limits．

| Ladder |  |  |  |  | ST＊${ }^{*}$ |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  | ENO：＝LIMITP（EN，s1，s2，s3，d）； | ENO：＝LIMITP＿U（EN，s1，s2，s3，d）； |
|  |  |  |  |  |  |  |

FBD／LD＊${ }^{*}$

＊1 The LIMIT and LIMIT＿U instructions do not support the structured text language and FBD／LD language．Use the standard function， LIMIT．
W Page 1619 LIMIT（＿E）

## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| LIMIT | - |
| LIMIT＿U | - |
| LIMITP | - |
| LIMITP＿U |  |

## Setting data

Descriptions，ranges，and data types

\left.| Operand |  | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- | :--- |
| （s1） | LIMIT（P） | Lower limit value（minimum output threshold |  |  |  |
|  | value） | LIMIT（P）＿U | -32768 to 32767 | 16－bit signed binary | ANY16＿S |
|  | （s2） | LIMIT（P） | Upper limit value（maximum output threshold |  |  |
|  | value） |  |  |  |  |$\right)$

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDl（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s3） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions control the output value to be stored in the device specified by (d) by checking the input value (16-bit binary data) in the device specified by ( $s 3$ ) with the upper and lower limit values specified by (s1) and (s2). The output value is controlled as follows.

| Condition | Output value |
| :--- | :--- |
| Lower limit value (s1) > Input value (s3) | Lower limit value (s1) |
| Upper limit value (s2) < Input value (s3) | Upper limit value (s2) |
| Lower limit value $(\mathrm{s} 1) \leq$ Input value $(\mathrm{s} 3) \leq$ Upper limit value (s2) | Input value (s3) |



- To control the input value only with the upper limit, set the minimum value within the setting range in (s1).
- To control the input value only with the lower limit, set the maximum value within the setting range in (s2).



## Upper and lower limit control of 32－bit binary data

## DLIMIT（P）（＿U）

These instructions control the output value depending on whether the specified 32－bit binary bit value is within the upper and lower limits．

| Ladder |  |  |  |  | ST＊1 |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  | ENO：＝DLIMITP（EN，s1，s2，s3，d）； | ENO：＝DLIMITP＿U（EN，s1，s2，s3，d）； |
|  | （s1） | （s2） | （s3） | （d） |  |  |
| FBD／LD＊1 |  |  |  |  |  |  |
|  |  |  |  |  |  |  |
|  |  |  |  |
|  |  | － |  |  |  |  |
|  |  |  |  |

＊1 The DLIMIT and DLIMIT＿U instructions do not support the structured text language and FBD／LD language．Use the standard function， LIMIT．
Ю Page 1619 LIMIT（＿E）
Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DLIMIT | - |
| DLIMIT＿U | - |
| DLIMITP |  |
| DLIMITP＿U |  |

## Setting data

■Descriptions，ranges，and data types

| Operand |  | Description | Range | Data type | Data type（label） |
| :---: | :---: | :---: | :---: | :---: | :---: |
| （s1） | DLIMIT（P） | Device for storing the lower limit value（minimum output threshold value） | －2147483648 to 2147483647 | 32－bit signed binary | ANY32＿S |
|  | DLIMIT（P）＿U |  | 0 to 4294967295 | 32－bit unsigned binary | ANY32＿U |
| （s2） | DLIMIT（P） | Device for storing the upper limit value （maximum output threshold value） | －2147483648 to 2147483647 | 32－bit signed binary | ANY32＿S |
|  | DLIMIT（P）＿U |  | 0 to 4294967295 | 32－bit unsigned binary | ANY32＿U |
| （s3） | DLIMIT（P） | Device for storing the input value controlled by upper／lower limit control | －2147483648 to 2147483647 | 32－bit signed binary | ANY32＿S |
|  | DLIMIT（P）＿U |  | 0 to 4294967295 | 32－bit unsigned binary | ANY32＿U |
| （d） | DLIMIT（P） | Start device for storing the controlled output value | － | 32－bit signed binary | ANY32＿S |
|  | DLIMIT（P）＿U |  |  | 32－bit unsigned binary | ANY32＿U |
| EN |  | Execution condition | － | Bit | BOOL |
| ENO |  | Execution result | － | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | J밈 | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3E미（H）G口 | Z | LT, LST, | LZ |  | K，H | E | \＄ |  |
| （s1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s3） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions control the output value to be stored in the device specified by (d) by checking the input value (32-bit binary data) in the device specified by ( s 3 ) with the upper and lower limit values specified by ( s 1 ) and ( s 2 ). The output value is controlled as follows.

| Condition | Output value |
| :--- | :--- |
| Lower limit value ((s1), (s1)+1) > Input value ((s3), (s3)+1) | Lower limit value ((s1), (s1)+1) |
| Lower limit value ((s2), (s2)+1) < Input value ((s3), (s3)+1) | Upper limit value ((s2), (s2)+1) |
| Lower limit value ((s1), (s1)+1) $\operatorname{\text {Inputvalue((s3),(s3)+1)}\leq \text {Upperlimitvalue((s2),(s2)+1)}}$ | Input value ((s3), (s3)+1) |



- To control the input value only with the upper limit, set the minimum value within the setting range in (s1).
- To control the input value only with the lower limit, set the maximum value within the setting range in (s2).


## Operation error

## Dead band control of 16－bit binary data

## BAND（P）（＿U）

These instructions control the output value depending on whether the specified 16－bit binary bit value is within the upper and lower limits of the dead band．

| Ladder |  |  |  | ST |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  | （s2） | （s3） | （d） | $\begin{aligned} & \text { ENO:=BAND(EN,s1,s2,s3,d); } \\ & \text { ENO:=BANDP(EN,s1,s2,s3,d); } \end{aligned}$ | $\begin{aligned} & \text { ENO:=BAND_U(EN,s1,s2,s3,d); } \\ & \text { ENO:=BANDP_U(EN,s1,s2,s3,d); } \end{aligned}$ |
| FBD／LD |  |  |  |  |  |
|  | － |  |  |  |  |

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| BAND | - |
| BAND＿U | - |
| BANDP | - |
| BANDP＿U | - |

Setting data
Descriptions，ranges，and data types

| Operand |  | Description | Range | Data type | Data type（label） |
| :---: | :---: | :---: | :---: | :---: | :---: |
| （s1） | BAND（P） | Lower limit of dead band（no－output band） | －32768 to 32767 | 16－bit signed binary | ANY16＿S |
|  | BAND（P）＿U |  | 0 to 65535 | 16－bit unsigned binary | ANY16＿U |
| （s2） | BAND（P） | Upper limit of dead band（no－output band） | －32768 to 32767 | 16－bit signed binary | ANY16＿S |
|  | BAND（P）＿U |  | 0 to 65535 | 16－bit unsigned binary | ANY16＿U |
| （s3） | BAND（P） | Input value to be controlled by dead band control | －32768 to 32767 | 16－bit signed binary | ANY16＿S |
|  | BAND（P）＿U |  | 0 to 65535 | 16－bit unsigned binary | ANY16＿U |
| （d） | BAND（P） | Device for storing the output value controlled by dead band control | － | 16－bit signed binary | ANY16＿S |
|  | BAND（P）＿U |  |  | 16－bit unsigned binary | ANY16＿U |
| EN |  | Execution condition | － | Bit | BOOL |
| ENO |  | Execution result | － | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3E미（H）Gㅁ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s3） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions control the output value to be stored in the device specified by (d) by checking whether the input value (16-bit binary data) in the device specified by ( $s 3$ ) is within range of the upper and lower limits of the dead band in the devices specified by ( s 1 ) and ( s 2 ). The output value is controlled as follows.

| Condition | Output value |
| :--- | :--- |
| Dead band lower limit value (s1) > input value (s3) | Input value (s3) - dead band lower limit value (s1) |
| Dead band upper limit value (s2) < input value (s3) | Input value (s3) - dead band upper limit value (s2) |
| Dead band lower limit value $(\mathrm{s} 1) \leq$ input value $(\mathrm{s} 3) \leq$ dead band upper limit <br> $(\mathrm{s} 2)$ | 0 |



- The following example shows the case where the operation result of the BAND $(P)$ instruction is out of the range from 32768 to 32767.


## Ex.

When (s1) is 10 and (s3) is -32768 , output value is $-32768-10=8000 \mathrm{H}-000 \mathrm{AH}=7 \mathrm{FF} 6 \mathrm{H}=32758$.

- The following example shows the case when the operation result of the $\operatorname{BAND}(\mathrm{P})_{-} \mathrm{U}$ instruction is out of the range from 0 to 65535.


## Ex.

When (s1) is 100 and ( s 3 ) is 50 , output value is $50-100=0032 \mathrm{H}-0064 \mathrm{H}=\mathrm{FFCEH}=65486$.

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3405 H | The lower limit value specified by (s1) is greater than the upper limit value specified by (s2). |

## Dead band control of 32－bit binary data

## DBAND（P）（＿U）

These instructions control the output value depending on whether the specified 32－bit binary bit value is within the upper and lower limits of the dead band．


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DBAND | - |
| DBAND＿U | - |
| DBANDP | - |
| DBANDP＿U |  |

Setting data
Descriptions，ranges，and data types

| Operand |  | Description | Range | Data type | Data type（label） |
| :---: | :---: | :---: | :---: | :---: | :---: |
| （s1） | DBAND（P） | Device for storing the lower limit value of dead band（no－output band） | －2147483648 to 2147483647 | 32－bit signed binary | ANY32＿S |
|  | DBAND（P）＿U |  | 0 to 4294967295 | 32－bit unsigned binary | ANY32＿U |
| （s2） | DBAND（P） | Device for storing the upper limit value of dead band（no－output band） | －2147483648 to 2147483647 | 32－bit signed binary | ANY32＿S |
|  | DBAND（P）＿U |  | 0 to 4294967295 | 32－bit unsigned binary | ANY32＿U |
| （s3） | DBAND（P） | Device for storing the input value controlled by dead band control | －2147483648 to 2147483647 | 32－bit signed binary | ANY32＿S |
|  | DBAND（P）＿U |  | 0 to 4294967295 | 32－bit unsigned binary | ANY32＿U |
| （d） | DBAND（P） | Start device for storing the output value controlled by dead band control | － | 32－bit signed binary | ANY32＿S |
|  | DBAND（P）＿U |  |  | 32－bit unsigned binary | ANY32＿U |
| EN |  | Execution condition | － | Bit | BOOL |
| ENO |  | Execution result | － | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3E미（H）Gㅁ | z | LT，LST， LC | LZ |  | K，H | E | \＄ |  |
| （s1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s3） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions control the output value to be stored in the device specified by (d) by checking whether the input value (32-bit binary data) in the device specified by (s3) is within range of the upper and lower limits of the dead band in the devices specified by ( s 1 ) and ( s 2 ). The output value is controlled as follows.

| Condition | Output value |
| :--- | :--- |
| Dead band lower limit value ((s1), (s1)+1) > Input value ((s3), (s3)+1) | Input value ((s3), (s3)+1)- Dead band lower limit value ((s1), (s1)+1) |
| Dead band upper limit value ((s2), (s2)+1) < Input value ((s3), (s3)+1) | Input value ((s3), (s3)+1) - Dead band upper limit value ((s2), (s2)+1) |
| Dead band lower limit value ((s1), (s1)+1) $\leq$ Input value ((s3), $(\mathrm{s} 3)+1) \leq$ Dead <br> band upper limit ((s2), (s2)+1) | 0 |



- The following example shows the case when the operation result of the DBAND(P) instruction is out of the range from 2147483648 to 2147483647.


## Ex.

When ((s1), (s1)+1) is 1000 and ((s3), (s3)+1) is -2147483648 , output value is $-2147483648-1000=80000000 \mathrm{H}-000003 \mathrm{E} 8 \mathrm{H}$ $=7$ FFFFC $18 \mathrm{H}=2147482648$.

- The following example shows the case when the operation result of the $\operatorname{DBAND}(P) \_U$ instruction is out of the range from 0 to 4294967295 .


## Ex.

When ((s1), (s1)+1) is 100 and $((\mathrm{s} 3),(\mathrm{s} 3)+1)$ is 50 , output value is $50-100=00000032 \mathrm{H}-00000064 \mathrm{H}=\mathrm{FFFFFFCEH}=$ 4294967246.

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3405 H | The lower limit value specified by (s1) is greater than the upper limit value specified by (s2). |

## Zone control of 16－bit binary data

## ZONE（P）（＿U）

These instructions add a bias value to the specified input value（16－bit binary）．

| Ladder |  |  |  |  | ST |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\square-\square$     <br> $-\square$ （s1） （s2） （s3） （d） |  |  |  |  | $\begin{aligned} & \mathrm{ENO}:=\mathrm{ZONE}(\mathrm{EN}, \mathrm{~s} 1, \mathrm{~s} 2, \mathrm{~s} 3, \mathrm{~d}) ; \\ & \text { ENO:=ZONEP(EN,s1,s2,s3,d); } \end{aligned}$ | $\begin{aligned} & \text { ENO:=ZONE_U(EN,s1,s2,s3,d); } \\ & \text { ENO:=ZONEP_U(EN,s1,s2,s3,d); } \end{aligned}$ |
|  |  |  |  |  |  |  |

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## －Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| ZONE | - |
| ZONE＿U | - |
| ZONEP | - |
| ZONEP＿U |  |

## Setting data

## Descriptions，ranges，and data types

| Operand |  | Description | Range | Data type | Data type（label） |
| :---: | :---: | :---: | :---: | :---: | :---: |
| （s1） | ZONE（P） | Negative bias value to be added to the input value | －32768 to 32767 | 16－bit signed binary | ANY16＿S |
|  | ZONE（P）＿U |  | 0 to 65535 | 16－bit unsigned binary | ANY16＿U |
| （s2） | ZONE（P） | Positive bias value to be added to the input value | －32768 to 32767 | 16－bit signed binary | ANY16＿S |
|  | ZONE（P）＿U |  | 0 to 65535 | 16－bit unsigned binary | ANY16＿U |
| （s3） | ZONE（P） | Input value used for zone control | －32768 to 32767 | 16－bit signed binary | ANY16＿S |
|  | ZONE（P）＿U |  | 0 to 65535 | 16－bit unsigned binary | ANY16＿U |
| （d） | ZONE（P） | Device for storing the output value controlled by zone control | － | 16－bit signed binary | ANY16＿S |
|  | ZONE（P）＿U |  |  | 16－bit unsigned binary | ANY16＿U |
| EN |  | Execution condition | － | Bit | BOOL |
| ENO |  | Execution result | － | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s3） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions add the bias value specified by (s1) or (s2) to the input value (16-bit binary) specified by (s3), and store the result in the device number specified by (d). The bias value is controlled as follows.

| Condition | Output value |
| :---: | :---: |
| Input value (s3) < 0 | Input value (s3) + negative bias value (s1) |
| Input value (s3) <0 | 0 |
| Input value (s3) < 0 | Input value (s3) + positive bias value (s2) |
|  |  |

- The following example shows the case where the operation result of the $\mathrm{ZONE}(\mathrm{P})$ instruction is out of the range from 32768 to 32767.


## Ex.

When ( s 1 ) is -100 and ( s 3 ) is -32768 , output value is $-32768+(-100)=8000 \mathrm{H}-\mathrm{FF9CH}=7 \mathrm{F9CH}=32668$.

- The following example shows the case where the operation result of the $\operatorname{ZONE}(\mathrm{P}) \_\cup$ instruction is out of the range from 0 to 65535 .


## Ex.

When (s2) is 100 and ( s 3 ) is 65535 , output value is $65535+100=\mathrm{FFFFH}-0064 \mathrm{H}=0063 \mathrm{H}=99$.

- The ZONE(P)_U instruction treats the data in the device specified by (s1) as dummy and does not use it.


## Operation error

There is no operation error.

## Zone control of 32－bit binary data

## DZONE（P）（＿U）

These instructions add a bias value to the specified input value（32－bit binary）．

| Ladder |  |  |  |  | ST |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $[-\square-\square$ （s1） （s2） （s3） （d） |  |  |  |  | $\begin{aligned} & \text { ENO:=DZONE(EN,s1,s2,s3,d); } \\ & \text { ENO:=DZONEP(EN,s1,s2,s3,d); } \end{aligned}$ | $\begin{aligned} & \text { ENO:=DZONE_U(EN,s1,s2,s3,d); } \\ & \text { ENO:=DZONEP_U(EN,s1,s2,s3,d); } \end{aligned}$ |
|  |  |  |  |  |  |  |

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Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DZONE | - |
| DZONE＿U | - |
| DZONEP | - |
| DZONEP＿U |  |

## Setting data

DDescriptions，ranges，and data types

| Operand |  | Description | Range | Data type | Data type（label） |
| :---: | :---: | :---: | :---: | :---: | :---: |
| （s1） | DZONE（P） | Device for storing the negative bias value to be added to the input value | －2147483648 to 2147483647 | 32－bit signed binary | ANY32＿S |
|  | DZONE（P）＿U |  | 0 to 4294967295 | 32－bit unsigned binary | ANY32＿U |
| （s2） | DZONE（P） | Device for storing the positive bias value to be added to the input value | －2147483648 to 2147483647 | 32－bit signed binary | ANY32＿S |
|  | DZONE（P）＿U |  | 0 to 4294967295 | 32－bit unsigned binary | ANY32＿U |
| （s3） | DZONE（P） | Device for storing the Input value used for zone control | －2147483648 to 2147483647 | 32－bit signed binary | ANY32＿S |
|  | DZONE（P）＿U |  | 0 to 4294967295 | 32－bit unsigned binary | ANY32＿U |
| （d） | DZONE（P） | Start device for storing the output value controlled by zone control | － | 32－bit signed binary | ANY32＿S |
|  | DZONE（P）＿U |  |  | 32－bit unsigned binary | ANY32＿U |
| EN |  | Execution condition | － | Bit | BOOL |
| ENO |  | Execution result | － | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDI（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s3） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions add the bias value specified by (s1) or (s2) to the input value (32-bit binary) specified by (s3), and store the result in the device number specified by (d). The bias value is controlled as follows.

| Condition | Output value |
| :--- | :--- |
| Input value ((s3), (s3)+1)<0 | Input value ((s3), (s3)+1) + negative bias value (s1), (s1) +1 |
| Input value ((s3), (s3)+1) $=0$ | 0 |
| Input value ((s3), (s3)+1)>0 | Input value ((s3), (s3)+1) + positive bias value (s2), (s2)+1 |



- The following example shows the case where the operation result of the $\operatorname{DZONE}(\mathrm{P})$ instruction is out of the range from 2147483648 to 2147483647.


## Ex.

When ((s1), (s1)+1) is -1000 and ((s3), (s3)+1) is -2147483648 , output value is $-2147483648+(-1000)=80000000 \mathrm{H}-$
FFFFFC18H = 7FFFFC18H = 2147482648 .

- The following example shows the case where the operation result of the $\operatorname{DZONE}(\mathrm{P}) \_\mathrm{U}$ instruction is out of the range from 0 to 4294967295 .


## Ex.

When ((s2), (s2)+1) is 1000 and ((s3), (s3)+1) is 4294967295, output value is $4294967295+1000=F F F F F F F F H-00003 E 8 H=$ $000003 \mathrm{E} 7 \mathrm{H}=999$.

- The DZONE(P)_U instruction treats the data in the device specified by ( s 1 ) and ( s 1 ) +1 as dummy and does not use them.


## Operation error

There is no operation error.

## Scaling 16－bit binary data（point coordinates）

## SCL（P）（＿U）

These instructions scale the scaling conversion data（16－bit data）on the basis of the specified input value（point coordinates）．

| Ladder | ST |  |
| :---: | :---: | :---: |
| - －$-\square$ （s1） （s2） （d） | $\begin{aligned} & \text { ENO:=SCL(EN,s1,s2,d); } \\ & \text { ENO:=SCLP(EN,s1,s2,d); } \end{aligned}$ | $\begin{aligned} & \text { ENO:=SCL_U(EN,s1,s2,d); } \\ & \text { ENO:=SCLP_U(EN,s1,s2,d); } \end{aligned}$ |

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■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| SCL | - |
| SCL＿U | - |
| SCLP | - |
| SCLP＿U |  |

## Setting data

Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :---: | :---: | :---: | :---: | :---: | :---: |
| （s1） | SCL（P） | Input value to be scaled or the device containing the input value | －32768 to 32767 | 16－bit signed binary | ANY16＿S |
|  | SCL（P）＿U |  | 0 to 65535 | 16－bit unsigned binary | ANY16＿U |
| （s2） | SCL（P） | Start device containing scaling conversion data | － | 16－bit signed binary ${ }^{* 1}$ | ANY16＿S |
|  | SCL $(\mathrm{P})_{-} \mathrm{U}$ |  |  | 16－bit unsigned binary＊1 | ANY16＿U |
| （d） | SCL（P） | Device for storing the output value controlled by scaling | － | 16－bit signed binary | ANY16＿S |
|  | SCL（P）＿U |  |  | 16－bit unsigned binary | ANY16＿U |
| EN |  | Execution condition | － | Bit | BOOL |
| ENO |  | Execution result | － | Bit | BOOL |

＊1 The number of coordinate points in（s2）is represented in 16－bit unsigned binary．

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGㅁ，JपIロ， U3EDI（H）GD | z | LT，LST， LC | LZ |  | K，H | E | \＄ |  |
| （s1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions scale the scaling conversion data (16-bit data) in the device specified by (s2) on the basis of the input value in the device specified by ( s 1 ), and stores the operation result in the device specified by (d). Scaling conversion is performed based on the scaling conversion data stored in the device specified by (s2) and later.

| Setting item ( n is the number of coordinate points specified by ( $\mathbf{s} 2$ ).) |  | Device assignment |
| :---: | :---: | :---: |
| Number of coordinate points |  | (s2) |
| Point 1 | X coordinate | (s2)+1 |
|  | Y coordinate | (s2)+2 |
| Point 2 | X coordinate | (s2)+3 |
|  | Y coordinate | (s2)+4 |
| $\vdots$ |  |  |
| Point n | X coordinate | (s2) $+2 \mathrm{n}-1$ |
|  | Y coordinate | (s2)+2n |



- If the operation result is not an integer, the first decimal place is rounded off.
- Set the $X$ coordinate data of the scaling conversion data in ascending order.
- Set the value in ( $s 1$ ) within the range of the scaling conversion data (device value in (s2)).
- If two or more points indicate the same X coordinate, the Y coordinate value of the largest point number is output.
- Specify a value from 1 to 65535 for the number of coordinate points of the scaling conversion data specified by (s2).


## Precautions

- The search method and the number of searches vary depending on whether SM755 is on or off.

| SM755 | Search method | Number of searches |
| :--- | :--- | :--- |
| Off | Sequential search | $1 \leq$ number of searches $\leq 65535$ |
| On | Binary search | $1 \leq$ number of searches $\leq 16$ |

- When the scaling conversion data is sorted in ascending order, the search method varies depending on the status of SM755 and therefore the processing speed also varies. The processing speed depends on the number of searches and is faster as the number of searches is less.
- Case in which the processing speed of sequential search is faster

When the coordinate point specified by ( $\mathbf{s} 1$ ) is one from 1 to 15 while the number of coordinate points is the maximum, the number of sequential searches is equal to or less than 15 and therefore the processing speed of the sequential search becomes faster.

- Case in which the processing speed of binary search is faster

The maximum number of searches is 16 and therefore when coordinate point 17 or later is specified by ( $\mathbf{s} 1$ ), the number of binary searches is equal to or greater than the number of sequential searches, and accordingly the processing speed of the binary search becomes faster.

(1) The processing speed of the binary search is faster because the number of sequential searches is less than the number of binary searches.
(2) The processing speed of the binary search is faster because the number of binary searches is less than the number of sequential searches.

## Operation error

| Error code (SD0) | Description |
| :--- | :--- |
| 3405 H | The X-coordinate data of the scaling conversion data before the point specified in (s1) is not sorted in ascending order. <br> (Note that this error is not detected when SM755 is on.) |
|  | The input value specified by ( s 1 ) is out of the range of the specified scaling conversion data. |
|  | The number of coordinate points starting from the device specified by (s2) is out of the range, 1 to 65535. |

## Scaling 32－bit binary data（point coordinates）

## DSCL（P）（＿U）

These instructions scale the scaling conversion data（32－bit data）on the basis of the specified input value（point coordinates）．

| Ladder | ST |  |
| :---: | :---: | :---: |
| $\square-\square . \square$ （s1） （s2） （d） | $\begin{aligned} & \text { ENO:=DSCL(EN,s1,s2,d); } \\ & \text { ENO:=DSCLP(EN,s1,s2,d); } \end{aligned}$ | $\begin{aligned} & \text { ENO:=DSCL_U(EN,s1,s2,d); } \\ & \text { ENO:=DSCLP_U(EN,s1,s2,d); } \end{aligned}$ |

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Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DSCL | - |
| DSCL＿U | $\boxed{ }$ |
| DSCLP | - |
| DSCLP＿U | - |

## Setting data

Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :---: | :---: | :---: | :---: | :---: | :---: |
| （s1） | DSCL（P） | Input value to be scaled or the start device containing the input value | $\begin{aligned} & -2147483648 \text { to } \\ & 2147483647 \end{aligned}$ | 32－bit signed binary | ANY32＿S |
|  | DSCL（P）＿U |  | 0 to 4294967295 | 32－bit unsigned binary | ANY32＿U |
| （s2） | DSCL（P） | Start device containing scaling conversion data | － | 32－bit signed binary ${ }^{* 1}$ | ANY32＿S |
|  | DSCL（P）＿U |  |  | 32－bit unsigned binary ${ }^{* 1}$ | ANY32＿U |
| （d） | DSCL（P） | Start device for storing the output value controlled by scaling | － | 32－bit signed binary | ANY32＿S |
|  | DSCL（P）＿U |  |  | 32－bit unsigned binary | ANY32＿U |
| EN |  | Execution condition | － | Bit | BOOL |
| ENO |  | Execution result | － | Bit | BOOL |

＊1 The number of coordinate points in（s2）＋0 and（s2）＋1 is represented in 32－bit unsigned binary．

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDI（H）Gロ | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions scale the scaling conversion data (32-bit data) in the device specified by (s2) on the basis of the input value in the device specified by ( s 1 ), and stores the operation result in the device specified by (d). Scaling conversion is performed based on the scaling conversion data stored in the device specified by (s2) and later.

| Setting item ( n is the number of coordinate points specified by ( $\mathbf{s} 2$ ).) |  | Device assignment |
| :---: | :---: | :---: |
| Number of coordinate points |  | (s2)+1, (s2) |
| Point 1 | X coordinate | (s2)+3, (s2)+2 |
|  | Y coordinate | (s2) $+5,(\mathrm{~s} 2)+4$ |
| Point 2 | X coordinate | (s2)+7, (s2)+6 |
|  | Y coordinate | (s2)+9, (s2)+8 |
| ! |  |  |
| Point n | X coordinate | (s2) $+4 \mathrm{n}-1,(\mathrm{~s} 2)+4 \mathrm{n}-2$ |
|  | Y coordinate | (s2) $+4 \mathrm{n}+1,(\mathrm{~s} 2)+4 \mathrm{n}$ |



- If the operation result is not an integer, the first decimal place is rounded off.
- Set the X coordinate data of the scaling conversion data in ascending order.
- Set the value in (s1) within the range of the scaling conversion data (device value in (s2), (s2)+1).
- If two or more points indicate the same X coordinate, the Y coordinate value of the largest point number is output.
- Specify a value from 1 to 4294967295 for the number of coordinate points of the scaling conversion data specified by (s2).


## Precautions

- The search method and the number of searches vary depending on whether SM755 is on or off.

| SM755 | Search method | Number of searches |
| :--- | :--- | :--- |
| Off | Sequential search | $1 \leq$ number of searches $\leq 4294967295$ |
| On | Binary search | $1 \leq$ number of searches $\leq 32$ |

- When the scaling conversion data is sorted in ascending order, the search method varies depending on the status of SM755 and therefore the processing speed also varies. The processing speed depends on the number of searches and is faster as the number of searches is less.
- Case in which the processing speed of sequential search is faster

When the coordinate point specified by ( $\mathbf{s} 1$ ) is one from 1 to 15 while the number of coordinate points is the maximum, the number of sequential searches is equal to or less than 15 and therefore the processing speed of the sequential search becomes faster.

- Case in which the processing speed of binary search is faster

The maximum number of searches is 32 and therefore when coordinate point 33 or later is specified by ( $\mathbf{s} 1$ ), the number of binary searches is equal to or greater than the number of sequential searches, and accordingly the processing speed of the binary search becomes faster.

(1) The processing speed of the binary search is faster because the number of sequential searches is less than the number of binary searches.
(2) The processing speed of the binary search is faster because the number of binary searches is less than the number of sequential searches.

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
|  | The X-coordinate data of the scaling conversion data before the point specified in (s1) is not sorted in ascending order. <br> (Note that this error is not detected when SM755 is on.) |
|  | The input value specified by ( s 1 ) is out of the range of the specified scaling conversion data. |
|  | The number of coordinate points starting from the device specified by (s2) is out of the range, 1 to 4294967295. |

## Scaling 16－bit binary data（XY coordinates）

## SCL2（P）（U）

These instructions scale the scaling conversion data（16－bit data）on the basis of the specified input value（XY coordinates）．


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| SCL2 | - |
| SCL2＿U | $\boxed{ }$ |
| SCL2P | - |
| SCL2P＿U |  |

## Setting data

Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :---: | :---: | :---: | :---: | :---: | :---: |
| （s1） | SCL2（P） | Input value to be scaled or the device containing the input value | －32768 to 32767 | 16－bit signed binary | ANY16＿S |
|  | SCL2（P）＿U |  | 0 to 65535 | 16－bit unsigned binary | ANY16＿U |
| （s2） | SCL2（P） | Start device containing scaling conversion data | － | 16－bit signed binary ${ }^{*}{ }^{1}$ | ANY16＿S |
|  | SCL2（P）＿U |  |  | 16－bit unsigned binary＊1 | ANY16＿U |
| （d） | SCL2（P） | Device for storing the output value controlled by scaling | － | 16－bit signed binary | ANY16＿S |
|  | SCL2（P）＿U |  |  | 16－bit unsigned binary | ANY16＿U |
| EN |  | Execution condition | － | Bit | BOOL |
| ENO |  | Execution result | － | Bit | BOOL |

＊1 The number of coordinate points in（s2）is represented in 16－bit unsigned binary．

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3Eपl（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions scale the scaling conversion data (16-bit data) in the device specified by (s2) on the basis of the input value in the device specified by ( s 1 ), and stores the operation result in the device specified by (d). Scaling conversion is performed based on the scaling conversion data stored in the device specified by (s2) and later.

| Setting item ( $\mathbf{n}$ is the number of coordinate points specified by (s2).) |  | Device assignment |
| :--- | :--- | :--- |
| Number of coordinate points | (s2) |  |
|  | Point 1 | $(\mathrm{~s} 2)+1$ |
|  | Point 2 | $(\mathrm{~s} 2)+2$ |
|  | $\vdots$ | $\vdots$ |
|  | Point n | $(\mathrm{s} 2)+\mathrm{n}$ |
| Y coordinate | Point 1 | $(\mathrm{~s} 2)+\mathrm{n}+1$ |
|  | Point 2 | $(\mathrm{~s} 2)+\mathrm{n}+2$ |
|  | $\vdots$ | $\vdots$ |
|  | Point n | $(\mathrm{s} 2)+2 \mathrm{n}$ |



- If the operation result is not an integer, the first decimal place is rounded off.
- Set the $X$ coordinate data of the scaling conversion data in ascending order.
- Set the value in ( $s 1$ ) within the range of the scaling conversion data (device value in ( s 2 )).
- If two or more points indicate the same X coordinate, the Y coordinate value of the largest point number is output.
- Specify a value from 1 to 65535 for the number of coordinate points of the scaling conversion data.


## Precautions

When the scaling conversion data is sorted in ascending order, the search method varies depending on the status of SM755 and therefore the processing speed also varies. For details, refer to the SCL(P)(_U) instruction.
$\longmapsto$ Page 750 SCL(P)(_U)
Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3405 H | The X-coordinate data is not sorted in ascending order. |
|  | The input value specified by ( s 1 ) is out of the range of the specified scaling conversion data. |
|  | The number of coordinate points starting from the device specified by (s2) is out of the range, 1 to 65535. |

## Scaling 32-bit binary data (XY coordinates)

## DSCL2(P)(_U)

These instructions scale the scaling conversion data (32-bit data) on the basis of the specified input value (XY coordinates).


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DSCL2 | - |
| DSCL2_U | $\boxed{ }$ |
| DSCL2P | - |
| DSCL2P_U |  |

## Setting data

Description, range, data type

| Operand |  | Description | Range | Data type | Data type (label) |
| :---: | :---: | :---: | :---: | :---: | :---: |
| (s1) | DSCL2(P) | Input value to be scaled or the start device containing the input value | $\begin{aligned} & -2147483648 \text { to } \\ & 2147483647 \end{aligned}$ | 32-bit signed binary | ANY32_S |
|  | DSCL2(P)_U |  | 0 to 4294967295 | 32-bit unsigned binary | ANY32_U |
| (s2) | DSCL2(P) | Start device containing scaling conversion data | - | 32-bit signed binary ${ }^{* 1}$ | ANY32_S |
|  | DSCL2(P)_U |  |  | 32-bit unsigned binary ${ }^{* 1}$ | ANY32_U |
| (d) | DSCL2(P) | Start device for storing the output value controlled by scaling | - | 32-bit signed binary | ANY32_S |
|  | DSCL2(P)_U |  |  | 32-bit unsigned binary | ANY32_U |
| EN |  | Execution condition | - | Bit | BOOL |
| ENO |  | Execution result | - | Bit | BOOL |

*1 The number of coordinate points in (s2) to (s2)+1 is represented in 32-bit unsigned binary.

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | J밈 | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | U밈, J밈, U3EDI(H)Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (s1) | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - | - |
| (s2) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (d) | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - | - | - |

## Processing details

- These instructions scale the scaling conversion data (32-bit data) in the device specified by (s2) on the basis of the input value in the device specified by ( s 1 ), and stores the operation result in the device specified by (d). Scaling conversion is performed based on the scaling conversion data stored in the device specified by (s2) and later.

| Setting item ( n is the number of coordinate points specified by (s2).) |  | Device assignment |
| :---: | :---: | :---: |
| Number of coordinate points |  | (s2)+1, (s2) |
| X coordinate | Point 1 | (s2)+3, (s2)+2 |
|  | Point 2 | (s2)+5, (s2)+4 |
|  | $\vdots$ | ! |
|  | Point n | (s2) $+2 \mathrm{n}+1,(\mathrm{~s} 2)+2 \mathrm{n}$ |
| Y coordinate | Point 1 | (s2) $+2 \mathrm{n}+3,(\mathrm{~s} 2)+2 \mathrm{n}+2$ |
|  | Point 2 | (s2) $+2 \mathrm{n}+5,(\mathrm{~s} 2)+2 \mathrm{n}+4$ |
|  | $\vdots$ | $\vdots$ |
|  | Point n | (s2) $+4 \mathrm{n}+1,(\mathrm{~s} 2)+4 \mathrm{n}$ |



- If the operation result is not an integer, the first decimal place is rounded off.
- Set the $X$ coordinate data of the scaling conversion data in ascending order.
- Set the value in ( s 1 ) within the range of the scaling conversion data (device value in (s2) to ( s 2 ) $\mathrm{+1}$ ).
- If two or more points indicate the same $X$ coordinate, the $Y$ coordinate value of the largest point number is output.
- Specify a value from 1 to 4294967295 for the number of coordinate points of the scaling conversion data.


## Precautions

When the scaling conversion data is sorted in ascending order, the search method varies depending on the status of SM755 and therefore the processing speed also varies. For details, refer to the DSCL(P)(_U) instruction.
W Page 753 DSCL(P)(_U)

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
|  | The X-coordinate data is not sorted in ascending order. |
|  | The input value specified by (s1) is out of the range of the specified scaling conversion data. |
|  | The number of coordinate points starting from the device specified by (s2) is out of the range, 1 to 4294967295. |

## 7．13 Special Counter Instructions

## Counting up or down the current value（1－phase input）

## UDCNT1

This instruction updates the current value of the specified counter．


FBD／LD


■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| UDCNT1 | - |

## Setting data

Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | （s1）＋0：Count input number | - | Bit | ANYBIT＿ARRAY＊1 <br> （Number of elements： <br> 2） |
|  | （s1）＋1：Count up／down flag <br> Off indicates count－up（counting up the current value）． <br> On indicates count－down（counting down the current value）． |  |  |  |
| （d） | Number of the counter（device name）to be counted by the <br> UDCNT1 instruction | - | Device name | ANY16＊2 |
| （s2） | Set value | -32768 to 32767 | 16－bit signed binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

＊1 Only labels assigned to device（X）can be used．
＊2 Only labels assigned to device（C）can be used．

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，JロIロ， U3E미（H）Gㅁ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | $\bigcirc{ }^{* 1}$ | － | － | － | － | － | － | － | － | － | － | － |
| （d） | － | － | $0^{*}$ | － | － | － | － | － | － | － | － | － |
| （s2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

[^13]
## Processing details

- When the input specified by ( s 1 ) is turned on, this instruction updates the current value of the counter specified by (d).
- Counting up or down is determined by whether the input specified by ( s 1 ) +1 is on or off.
- Off: Count-up (counting up the current value)
- On: Count-down (counting down the current value)
- Count processing is performed as follows.
- When the current value equals the value specified by (s2) during count-up, the contact of the counter specified by (d) is turned on. The current value is kept counting even when the contact of the counter is turned on.
- When the current value equals "the set value -1 " during countdown, the contact of the counter specified by (d) is turned off.
- The counter specified by (d) is a ring counter. Counting up the counter when the current value is 32767 proceeds to -32768 . Similarly, counting down the counter when the current value is -32768 proceeds to 32767 . The following figure shows the processing for counting the current value.
$-32768 \rightarrow-32767--------2 \rightarrow-1 \rightarrow 0 \rightarrow 1 \rightarrow 2 \rightarrow-------\rightarrow 32766 \rightarrow 32767$


When counting down

- The following figure shows the operation of count processing using the UDCNT1 instruction.


## Ex.

Program which uses C0 (up/down counter) to count the number of times X0 turns off and on after X20 turns on
[Program]




- When executed, the UDCNT1 instruction starts counting when the execution command turns on and stops counting when the command turns off. If the execution command is turned on again, the instruction resumes counting from the current value with which it stopped counting previously.
- The RST instruction is used to clear the current value of the counter specified by (d) and turn off the contact.


## Point/ $\rho$

- The UDCNT1 instruction stores the device data of the argument in the work area of the CPU module, and performs the actual count operation using system interrupts. (The device data stored in the work area of the CPU module is cleared by turning off the execution command or setting it to STOP then RUN.) For this reason, the pulses that can be counted must have longer on/off time than the interval of the CPU module. The interrupt interval of the CPU module is 1 ms .
- The set value cannot be changed during counting by the UDCNT1 instruction (while the execution command is on). To change the set value, turn off the execution command in advance.
- The counter specified by the UDCNT1 instruction cannot be used by any other instruction. If another instruction uses it, normal counting is disabled.
- The UDCNT1 instruction can be used a maximum of six times in all running programs. The seventh or subsequent UDCNT1 instruction, if issued, causes no processing.


## Operation error

There is no operation error.

## Counting up or down the current value（2－phase input）

## UDCNT2

This instruction updates the current value of the counter depending on the status of phases A and B pulses．

| Ladder | ST |
| :---: | :---: |
|  | ENO：＝UDCNT2（EN，s1，s2，d）； |
| －－二－ （s1） （d） （s2） |  |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| UDCNT2 | $\square$ |

Setting data
Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | （s1）＋0：Count input number（phase A pulse） | - | Bit | ANYBIT＿ARRAY＊1 <br> （Number of elements： <br> $2)$ |
|  | （s1）＋1：Count input number（phase B pulse） |  | Device name | ANY16＊2 |
| （d） | Number of the counter（device name）to be counted by the <br> UDCNT2 instruction | - | -32768 to 32767 | 16－bit signed binary |
| （s2） | Set value | - | ANY16 |  |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result |  | BOOL |  |

＊1 Only labels assigned to device（X）can be used．
＊2 Only labels assigned to device（C）can be used．

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3E미（H）Gㅁ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | $\bigcirc{ }^{* 1}$ | － | － | － | － | － | － | － | － | － | － | － |
| （d） | － | － | ${ }^{*}{ }^{2}$ | － | － | － | － | － | － | － | － | － |
| （s2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

＊1 Only X can be used．Note，however，that it can be used only within the range of the number of I／O points（the number of points that can access I／O modules）．
＊2 Only C can be used．

## Processing details

- This instruction updates the current value of the counter specified by (d) depending on the status of the input (phase A pulse) specified by ( s 1 ) and the status of the input (phase B pulse) specified by ( s 1 ) +1 .
- Counting up or down is determined as follows.
- (s1) +1 is turned on while ( $s 1$ ) is on: Count-up (counting up the current value)
- ( $s 1$ ) +1 is turned off while ( $s 1$ ) is on: Countdown (counting down the current value)
- The instruction does not count while ( s 1 ) is off.
- Count processing is performed as follows.
- When the current value equals the value specified by (s2) during count-up, the contact of the counter specified by (d) is turned on. The current value is kept counting even when the contact of the counter is turned on.
- When the current value equals "the set value -1 " during countdown, the contact of the counter specified by (d) is turned off.
- The counter specified by ( d ) is a ring counter. Counting up the counter when the current value is 32767 proceeds to -32768 . Similarly, counting down the counter when the current value is -32768 proceeds to 32767 . The following figure shows the processing for counting the current value.


When counting down

- The following figure shows the operation of count processing using the UDCNT2 instruction.

Ex.
Program which uses C0 (up/down counter) to count the states of X0 and X1 after X20 turns on [Program]
[Operation]


- When executed, the UDCNT2 instruction starts counting when the execution command turns on and stops counting when the command turns off. If the execution command is turned on again, the instruction resumes counting from the current value with which it stopped counting previously.
- The RST instruction is used to clear the current value of the counter specified by (d) and turn off the contact.


## Point $\rho$

- The UDCNT2 instruction stores the device data of the argument in the work area of the CPU module, and performs the actual count operation using system interrupts. (The device data stored in the work area of the CPU module is cleared by turning off the execution command or setting it to STOP then RUN.) For this reason, the pulses that can be counted must have longer on/off time than the interval of the CPU module. The interrupt interval of the CPU module is 1 ms .
- The set value cannot be changed during counting by the UDCNT2 instruction (while the execution command is on). To change the set value, turn off the execution command in advance.
- The counter specified by the UDCNT2 instruction cannot be used by any other instruction. If another instruction uses it, normal counting is disabled.
- The UDCNT2 instruction can be used a maximum of five times in all running programs. The sixth or subsequent UDCNT1 instruction, if issued, causes no processing.


## Operation error

There is no operation error.

### 7.14 Special Timer Instructions

## Teaching timer

## TTMR

This instruction measures the on time of the measurement command in seconds, multiplies it by a multiplier, and stores the operation result.


FBD/LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| TTMR | $-\square$ |

## Setting data

Descriptions, ranges, and data types

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (d) | (d) +0 : Device for storing the measurement value | - | 16-bit signed binary | ANY16_ARRAY <br> (Number of elements: |
|  | (d)+1: Device for the system of CPU module |  |  | 2) |
| (s) | Multiplier of measurement value | 0 to 2 | 16-bit signed binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | JロIロ | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | UपIGㅁ, J밈, U3EDl(H)GD | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (d) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (s) | - | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - | $\bigcirc$ | $\bigcirc$ | - | - | - |

## Control data

| Operand: (d) |  |  |  |
| :--- | :--- | :--- | :--- | :--- |
| Device | Description | Setting range | Set by |
| +0 | Device for storing the measurement value | - |  |
| +1 | Device for the system of CPU module | - | System |

## Processing details

- This instruction measures the on time of the execution command in seconds, multiplies it by the multiplier specified by (s), and stores the resultant value in the device specified by (d).
- When the execution command is turned on, the instruction clears the device specified by (d)+0, (d)+1.
- The table below lists the multipliers that can be specified by (s).

| (s) | Multiplier |
| :--- | :--- |
| 0 | 1 |
| 1 | 10 |
| 2 | 100 |

## Point ${ }^{\circ}$

- When executed, the TTMR instruction implements time measurement. Do not use the JMP instruction to skip the TTMR instruction. Otherwise, accurate measurement is disabled.
- Do not change the multiplier specified by (s) during execution of the TTMR instruction. Otherwise, accurate values cannot be determined.
- The device specified by ( d ) +1 is used by the system of the CPU module. Do not change the value. If the value is changed, an accurate resultant value is not stored in the device specified by (d).
- When the value in the device specified by (s) is not in the range from 0 to 2 , no processing is performed.


## Operation error

There is no operation error.

## Special function timer

## STMR

This instruction implements the following four types of timer output．
－Off delay timer output
－After－off one－shot timer output
－After－on one－shot timer output
－On delay＋off delay timer output


FBD／LD


## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| STMR | Every scan |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :---: | :---: | :---: | :---: | :---: |
| （s1） | Timer device or timer type label | － | Device name | ANY16 |
| （s2） | Set value | 0 to 32767 | 16－bit signed binary | ANY16 |
| （d） | （d）+0 ：Off delay timer output | － | Bit | ANYBIT＿ARRAY <br> （Number of elements： <br> 4） |
|  | （d）＋1：After－off one－shot timer output |  |  |  |
|  | （d）＋2：After－on one－shot timer output |  |  |  |
|  | （d）＋3：On delay＋off delay timer output |  |  |  |
| EN | Execution condition | － | Bit | BOOL |
| ENO | Execution result | － | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3EDI（H）Gロ | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc{ }^{* 1}$ | － | － | － | － | － | － | － | － | － |
| （s2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | － | － | － | － | － | － | － | － | － | － | － |

[^14]
## Processing details

- This instruction uses four points from the device specified by (d) to implement four types of timer output.

| Setting data |  | Description |  |
| :--- | :--- | :--- | :--- |
| (d) | +0 | Off delay timer output: | Turns on on the rising edge of the command of the STMR instruction, <br> and turns off after a lapse of the time specified by (s2) after the falling <br> edge of the command. |
|  | +1 | After-off one-shot timer output | Turns on on the falling edge of the command of the STMR <br> instruction, and turns off after a lapse of the time specified by (s2). |
|  | +2 | After-on one-shot timer output | Turns on on the falling edge of the command of the STMR <br> instruction, and turns off after a lapse of the time specified by (s2) or <br> when the command of the instruction turns off. |
|  | +3 | On delay + off delay timer output | Turns on on the falling edge of the timer coil, and turns off after a <br> lapse of the time specified by (s2) after the falling edge of the <br> command of the STMR instruction. |

- The coil of the timer specified by (s) turns on, on the rising and falling edges of the command of the STMR instruction, to start measurement of the current value.
- The coil of the timer keeps measurement during the time specified by (s2) and turns off when the time is up.
- The coil of the timer is kept on even if the STMR instruction is turned off before time-up. Timer measurement is continued. When the STMR instruction is turned on again, the coil resets the current value to 0 and restarts measurement.
- The contact of the timer turns on, on the rising edge of the command of the STMR instruction, and turns off on the falling edge of the command after the coil of the timer falls. Users cannot use the contact of the timer because it is reserved for the system.

- Measurement of the current value of the timer specified by the STMR instruction is executed regardless of whether the command of the STMR instruction is on or off. If the STMR instruction is skipped such as by the JMP instruction, normal measurement is not performed.
- The measurement unit of the timer specified by (d) is the same as that of the low-speed timer.
- A value from 0 to 32767 can be specified in (s2). If a value out of the range is specified, no processing is performed.
- Do not use the OUT instruction for the timer specified by (s1). If the same timer device or timer type label is used for the STMR and OUT instructions, normal operation is not performed.


## Precautions

If there is an STMR instruction within the range for changing the ladder block online or writing data to the running programmable controller, the STMR instruction is executed.
For details, refer to the following.
([]] MELSEC iQ-R CPU Module User's Manual (Application))

## Operation error

There is no operation error

### 7.15 Shortcut Control Instruction

## Rotary table shortest direction control

## ROTC

This instruction controls shortcut rotation on the rotary table divided equally by the specified value.


FBD/LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| ROTC | $-\square$ |

## Setting data

Descriptions, ranges, and data types

| Operand | Description | Range | Data type | Data type (label) |
| :---: | :---: | :---: | :---: | :---: |
| (s) | (s) +0 : Device for measuring the number of table rotations (reserved for the system) | - | 16-bit signed binary | ANY16_ARRAY <br> (Number of elements: <br> 3) |
|  | (s)+1: Call counter number |  | 16-bit unsigned binary |  |
|  | (s)+2: Call item number |  | 16-bit unsigned binary |  |
| ( n 1 ) | Number of table divisions | 2 to 65535 | 16-bit unsigned binary | ANY16 |
| (n2) | Number of low-speed sections | 0 to less than ( n 1 ) | 16-bit unsigned binary | ANY16 |
| (d) | (d) +0 : Phase $A$ input signal | - | Bit | ANYBIT_ARRAY <br> (Number of elements: 8) |
|  | (d)+1: Phase B input signal |  |  |  |
|  | (d)+2: 0-point detection input signal |  |  |  |
|  | (d) +3 : High-speed forward rotation output signal (reserved for the system) |  |  |  |
|  | (d)+4: Low-speed forward rotation output signal (reserved for the system) |  |  |  |
|  | (d) +5 : Stop output signal (reserved for the system) |  |  |  |
|  | (d)+6: Low-speed reverse rotation output signal (reserved for the system) |  |  |  |
|  | (d)+7: High-speed reverse rotation output signal (reserved for the system) |  |  |  |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3Eपl（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ n 1 ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （n2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | － | － | － | － | － | － | － | － | － | － | － |

## Processing details

－This instruction controls the rotation of the rotary table divided equally by the value specified by（ n 1 ）so that it rotates at short cut to the position of the counter number specified by（s）＋1 to get in and out the item of the number specified by（s）＋2．
－It performs control by assuming that the item numbers and counter numbers are assigned counterclockwise．
－（s）+0 is the counter used for the system to count items to determine which item is in the 0th counter．Do not rewrite the data with the program．Otherwise，accurate control cannot be performed．
－The value specified in（ n 2 ）must be less than the number of table divisions specified by（ n 1 ）．
－（d）+0 and（d）+1 are the phase $A$ input signal and phase B input signal used to detect the forward and reverse rotations of the rotary table．The direction of rotation is determined by whether phase $B$ is on the rising or falling edge when phase $A$ is on
－Phase $B$ is on the rising edge：Forward rotation（clockwise）
－Phase $B$ is on the falling edge：Reverse rotation（counterclockwise）
－（d）+2 is the 0－point detection signal that turns on when the 0th item reaches the 0th counter．When the device specified by （d）＋2 turns on during execution of the ROTC instruction，the device specified by（s）＋0 is cleared．Start shortcut control with the ROTC instruction after performing this clearing operation．
－（d）+3 to（d）+7 are output signals for controlling table operations．One of the output signals in（d）+3 to（d）+7 is turned on according to the execution result of the ROTC instruction．
－When the command of the ROTC instruction is off，shortcut control is not performed and（d）＋3 to（d）＋7 are all turned off．
－The ROTC instruction can be used only once in all running programs．If it is used more than once，normal operation cannot be performed．
－If the value in（s）＋0 to（s）＋2 or（n2）is greater than（n1），no processing is performed．

## Program Example

A program that determines the rotation direction and control speed of the motor to remove or place the item at D2 on a 10division rotary table at station D1 and rotate the table at low speed from two sections before D2 to two sections after D2


## Operation error

There is no operation error.

## 7．16 Ramp Signal Instruction

## Ramp signal

## RAMPQ

This instruction shifts from a specified value to another specified value in（ n ）times．

| Ladder |  |  |  |  |  | STENO：$=$ RAMPQ（EN，s1，s2，n，d1，d2 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\square-=-=$ |  |  |  |  |  |  |
|  | （s1） | （s2） | （d1） | （ n ） | (d2) |  |

FBD／LD


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| RAMPQ | $-\square$ |

## Setting data

Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Initial value | -32768 to 32767 | 16－bit signed binary | ANY16 |
| （s2） | Last value | -32768 to 32767 | 16－bit signed binary | ANY16 |
| （d1） | （d1）＋0：Current value | - | 16－bit signed binary | ANY16＿ARRAY <br> （Number of elements： <br>  $\operatorname{ld1)+1:~Number~of~executions~}$ |
|  | Number of shifts | 1 to 32767 |  |  |
| （n） | （d2）＋0：Completion device | - | Bit | ANY16 |
| （d2） | （d2）＋1：Bit for selecting data retention at completion |  | ANYBIT＿ARRAY <br> （Number of elements： <br> 2） |  |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d2） | $\bigcirc$ | － | － | － | － | － | － | － | － | － | － | － |

## Processing details

- When the execution command is on, this instruction performs processing as follows.
- Shifting from the value specified by ( s 1 ) to the value specified by ( s 2 ) in the number of times specified by ( n ).
- For ( n ), specify the number of scans (number of shifts) to be performed to shift from ( s 1 ) to ( s 2 ). If the value specified in ( n ) is out of the range between 0 and 32768 , no processing is performed.
- (d) +1 is used for the system to store the number of times the RAMPQ instruction has been executed.
- The change value per scan is calculated by the following equation.

Amount of change in 1 scan $=\frac{(\text { Value specified by (s2)) }-(\text { Value specified by }(\mathrm{s} 1))}{(\text { Value specified by }(\mathrm{n}))}$

- The following figure shows how to change values from 0 to 350 in seven scans.

- If the change value in one scan is indivisible, correct it so that it becomes the value specified by (s2) in the number of shifts specified by ( n ). For this reason, a linear ramp may not be created.
- The following figure shows the operation of processing using the RAMPQ instruction.


## Ex.

Program which, when X0 turns on, changes the content of D0 from 10 to 100 in six scans and holds the content of D0 when the change is completed
[Program]

[Operation]


Mo OFF


- After scanning is performed the number of shifts specified by ( n ), the completion device specified by ( d 2 ) +0 turns on. The on/off status of the completion device and the data in (d1)+0 are determined by on/off of the device specified by (d2)+1. When ( d 2 ) +1 is off, the RAMPQ instruction turns off ( d 2 ) +0 in the next scan and restarts shifting from the initial value. When (d2) +1 is on, (d2) +0 is kept on and the data in (d1) +0 remains unchanged.
- If the command turns off during execution of the RAMPQ instruction, the data in (d1)+0 will not change thereafter. When the command turns on again, the RAMPQ instruction restarts shifting from the initial value.
- Do not change the values in ( s 1 ) and ( s 2 ) before the completion device specified by ( d 2 2)+0 turns on. The value to be stored in (d1) +1 is calculated using the same calculation formula every scan, and therefore changing the values in ( s 1 ) and ( s 2 ) may result in a sudden change.
- When making the digit specification using a bit device in (d1), specify it in K8Dn format.


## Precautions

When the digit specification is made using a bit device in (d1), it is acceptable only when the number of digits is specified in K8.

## Operation error

There is no operation error.

### 7.17 Pulse Related Instructions

## Measuring the density of pulses

## SPD

This instruction counts the device input only for the specified time.


FBD/LD

-Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| SPD | - |

## Setting data

Descriptions, ranges, and data types

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (s1) | Pulse input device number | - | Bit | ANY_BOOL $^{* 1}$ |
| (s2) | Measurement time, or the device number of the device <br> containing the measurement time (unit: ms ) | -32768 to 32767 | 16-bit signed binary | ANY16 |
| (d) | Device for storing the measurement result | - | 16 -bit signed binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

*1 Only labels assigned to device $(X)$ can be used.

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ\|ロ | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | UपIGㅁ, J밈, U3EDI(H)GD | z | LT, LST, LC | LZ |  | K, H | E | \$ |  |
| (s1) | $O^{* 1}$ | - | - | - | - | - | - | - | - | - | - | - |
| (s2) | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - | $\bigcirc$ | $\bigcirc$ | - | - | - |
| (d) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |

[^15]
## Processing details

- This instruction counts the number of times the input of the device specified by ( s 1 ) turns on for the duration specified by (s2), and stores the count result in the device specified by (d).

- Upon completion of measurement, the SPD instruction starts measurement from 0 again. To stop measurement by the SPD instruction, turn off the execution command.
- If the value specified in (s2) is 0 , no processing is performed.


## Operation error

There is no operation error.

- The SPD instruction stores the data of the argument device in the work area of the CPU module, and performs the actual count operation using system interrupts. (The device data stored in the work area of the CPU module is cleared by turning off the execution command or setting it to STOP then RUN.) For this reason, the pulses that can be counted must have longer on/off time than the interval of the CPU module. The interrupt interval of the CPU module is 1 ms .
- The SPD instruction can be used a maximum of six times in all running programs. The seventh or subsequent UDCNT1 instruction, if issued, causes no processing.
- The set value cannot be changed during measurement by the SPD instruction (while the command input is on). To change the set value, turn off the command input in advance.


## Outputting pulses at regular intervals

## PLSY

This instruction outputs the pulses of the specified frequency to the output module．


## FBD／LD



Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| PLSY | $-\square$ |

## Setting data

Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Frequency，or the start number of the device containing the <br> frequency | 1 to 100 | 16 －bit signed binary | ANY16＊1 |
| （n） | Number of outputs，or the start number of the device <br> containing the number of outputs | 0 to 65535 | 16－bit unsigned binary | ANY16 |
| （d） | Device used for pulse output | - | Bit | ANY＿BOOL |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

＊1 Only labels assigned to device $(\mathrm{Y})$ can be used．

## －Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，Jㅁㅁ， U3EDI（H）GD | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\mathrm{O}^{* 1}$ | － | － | － | － | － | － | － | － | － | － | － |

＊1 Only Y can be used．

## Processing details

－This instruction outputs the pulses at the frequency specified by（ s ），by the number of times specified by（ n ），to the output module with the output number（ Y ）in the device specified by（d）．
－A frequency from 1 Hz to 100 Hz can be specified in（s）．If the specified value in（s）is not in the range from 1 to 100 ，no processing is performed．
－A value from 0 to 65535 （ 0000 H to FFFFH）can be specified for the number of outputs in（ n ）．If 0 is specified in（ n ），pulses are output continuously．
－Only the output number（ Y ）corresponding to the output module can be specified for the pulse output in（d）．
－The PLSY instruction starts pulse output on the rising edge of the command．When the command turns off，the PLSY instruction stops pulse output．

## Operation error

There is no operation error
Point/

- The PLSY instruction stores the device data of the argument in the work area of the CPU module, and performs the actual output operation using system interrupts. (The device data stored in the work area of the CPU module is cleared by turning off the execution command or setting it to STOP then RUN.) For this reason, the pulses that can be out must have longer on/off time than the interval of the CPU module. The interrupt interval of the CPU module is 1 ms .
- Do not change the argument of the PLSY instruction during pulse output by the instruction (the execution command is on). To change the argument, turn off the execution command in advance.
- The PLSY instruction can be used only once in all programs running in the CPU module. The second or subsequent PLSY instruction, if issued, causes no processing.


## Performing the pulse width modulation

## PWM

When on continues for the specified time，this instruction outputs the pulse of the period to the output module．


FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| PWM | $-\square$ |

Setting data
Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | On time，or the start number of the device containing the on <br> time <br> （Unit： ms ） | 1 to 65535 | 16－bit unsigned binary | ANY16＊1 |
| （s2） | Period，or the start number of the device containing the <br> period <br> （Unit：ms） | 1 to 65535 | 16－bit unsigned binary | ANY16 |
| （d） | Pulse output device number | - | Bit | ANY＿BOOL |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

＊1 Only labels assigned to device $(\mathrm{Y})$ can be used．

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3Eㅁ（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\mathrm{O}^{* 1}$ | － | － | － | － | － | － | － | － | － | － | － |

[^16]
## Processing details

- This instruction outputs the on time specified by (s1) and the pulse of the period specified by (s2) to the output module specified by (d).

- A value in the range from 1 to $65535(0001 \mathrm{H}$ to FFFFH) can be specified in ( s 1 ) and ( s 2 ). (The value specified in ( s 1 ) must be less than the value specified in (s2).)
- No processing is performed in the following cases.
- (s1) and (s2) are 0.
-(s1) $($ (s2)
- The PWM instruction is executed more than once.


## Operation error

There is no operation error.

## Point ${ }^{\rho}$

- The PWM instruction stores the device data of the argument in the work area of the CPU module, and performs the actual output operation using system interrupts. (The device data stored in the work area of the CPU module is cleared by turning off the execution command or setting it to STOP then RUN.) The


### 7.18 Matrix Input Instruction

## Matrix input

## MTR

This instruction sequentially reads the input of 16 points $\times$ n columns connected to the specified input number and after.


FBD/LD


■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| MTR | $-\square$ |

## Setting data

Descriptions, ranges, and data types

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| $(\mathrm{s})$ | Start device of input | - | Bit | ANY_BOOL |
| $(\mathrm{d} 1)$ | Start device of output | - | Bit | ANY_BOOL |
| $(\mathrm{d} 2)$ | Start device for storing the matrix input data | - | Bit | ANY_BOOL |
| $(\mathrm{n})$ | Number of input columns | 2 to 8 | 16 -bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

[^17]*2 Only labels assigned to device (Y) can be used.

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ\|ロ | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | U밈, J밈, U3E미(H)Gㅁ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (s) | $\mathrm{O}^{* 1}$ | - | - | - | - | - | - | - | - | - | - | - |
| (d1) | $\mathrm{O}^{*}$ | - | - | - | - | - | - | - | - | - | - | - |
| (d2) | $\bigcirc$ | - | - | - | - | - | - | - | - | - | - | - |
| ( n ) | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - | $\bigcirc$ | $\bigcirc$ | - | - | - |

*1 Only $X$ can be used.
*2 Only Y can be used.

## Processing details

- This instruction sequentially reads the input of 16 points $\times n$ columns connected to the ( $s$ ) specified input number and after, and stores the input data that has been read in the device specified by (d2) and later.
- One scan reads one column (16 points) of data.
- The instruction sequentially repeats the reading of data from column 1 to column ( n ).
- In the device specified by (d2) and later, the data in column 1 is stored in the 16 points from the start and the data in column 2 is stored in the next 16 points. For this reason, the MTR instruction occupies $16 \times(\mathrm{n})$ points from the device specified by (d2).
- (d1) is the output for selecting the column to be read and is turned on and off automatically by the system. The (n) points from the device specified by (d1) is used.
- Only a device number which is a multiple of 16 can be specified in (s), (d1), and (d2).
- A value from 2 to 8 can be specified in ( n ).
- No processing is performed in the following cases.
- The device number specified by (s), (d1), or (d2) is not a multiple of 16.
- The device specified by $(\mathrm{s})$ is outside the range of actual inputs.
- The device specified by ( d 1 ) is outside the range of actual outputs.
- In the device specified by (d2) and later, $16 \times(\mathrm{n})$ points of data is outside the range of the relevant device.
- $(\mathrm{n})$ is outside the range from 2 to 8.


## Precautions

- Note that the MTR instruction directly operates the actual input/output. Even when the command of the MTR instruction turns off, the output that has been turned on by the MTR instruction is not turned off. Turn off the output specified by (d1) in the program.
- The MTR instruction execution interval should be longer than the total response time of the input and output modules. If the MTR instruction execution interval is shorter than the above time, inputs cannot be read normally. If the scan time in the program is short, select the constant scan and set longer scan time than the total of the response time.


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2820 H | A device other than the input $(\mathrm{X})$ is specified by (s). |
|  | A device other than the output $(\mathrm{Y})$ is specified by (d1). |

## 7．19 Data Processing Instructions

## Searching 16－bit binary data

## SERDATA（P）

These instructions search $(\mathrm{n})$ points from 16－bit binary data using the specified 16－bit binary data as a keyword．


FBD／LD


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| SERDATA | - |
|  | $\boxed{T}$ |
| SERDATAP | - |

## Setting data

Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Search data or the device containing the search data | -32768 to 32767 | 16－bit signed binary | ANY16 |
| （s2） | Data to be searched or the start device containing the data <br> to be searched | - | 16 －bit signed binary | ANY16 |
| （d） | Start device for storing the search result | - | 16 －bit signed binary | ANY16＿ARRAY <br> （Number of elements： <br> 2） |
| （n） | Number of search target data points | 0 to 65535 | BNY16 |  |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## －Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J미， U3EDI（H）GD | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- These instructions search the ( n ) points in units of 16 -bit binary data from the device specified by ( s 2 ) using the 16 -bit binary data in the device specified by ( s 1 ) as a keyword. Each instruction stores the number of data which matches the keyword in the device specified by (d)+1 and also stores the relative value of the first-matched device number from (s2) in the device specified by (d).

- If the value specified in ( n ) is 0 , no processing is performed.
- If no matching data is found as the result of search, 0 is stored in the devices specified by (d) and (d)+1.


## Point $\rho$

- If the data to be searched by the SERDATA(P) instruction has been sorted in ascending order, turning on SM702* ${ }^{* 1}$ enables a binary search which can process the search faster. If SM702 is turned on even though the data to be search has not been sorted in ascending order, normal search results cannot be obtained. The following figure shows an example of binary search.

*1 SM702 is a special relay for setting the search method.
SM702 is off: Sequential search (linear search)
This method compares the search data with the data to be searched for starting from the start of data
SM702 is on: Binary search
For the data that has been sorted in ascending order, this method checks the center value of the search range, determining whether the center value is larger or smaller than the search value, and thereby narrows the search range to either side. Thus, target data is searched for by repeating this processing.


## Operation error

There is no operation error.

## Searching 32－bit binary data

## DSERDATA（P）

These instructions search（n）points from 32－bit binary data using the specified 32－bit binary data as a keyword．


FBD／LD


■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DSERDATA | - |
|  | $\boxed{ }$ |
| DSERDATAP | $\boxed{ }$ |

Setting data

## Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Search data or the start device containing the search data | -2147483648 to 2147483647 | 32－bit signed binary | ANY32 |
| （s2） | Data to be searched or the start device containing the data <br> to be searched | - | 32－bit signed binary | ANY32 |
| （d） | Start device for storing the search result | - | 16－bit signed binary | ANY16＿ARRAY <br> （Number of elements： <br> 2） |
| （n） | Number of search target data points | 0 to 65535 | ANY16 |  |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDI（H）Gロ | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- These instructions search the ( n ) points of data in units of 32 -bit binary data ( $2 \times(\mathrm{n})$ points of data in units of 16 bits) from the device specified by ( s 2 ), using the 16-bit binary data in the device specified by ( s 1 ) as a keyword. Each instruction stores the number of data which matches the keyword in the device specified by (d)+1 and also stores the relative value of the first-matched device number from ( s 2 ) in the device specified by (d).

- If the value specified in ( n ) is 0 , no processing is performed.
- If no matching data is found as the result of search, 0 is stored in the devices specified by (d) and (d)+1.


## Point $\rho$

- If the data to be searched by the DSERDATA $(P)$ instruction has been sorted in ascending order, turning on SM702*1 enables a binary search which can process the search faster. If SM702 is turned on even though the data to be search has not been sorted in ascending order, normal search results cannot be obtained. The following figure shows an example of binary search.

*1 SM702 is a special relay for setting the search method.
SM702 is off: Sequential search (linear search)
This method compares the search data with the data to be searched for starting from the start of data
SM702 is on: Binary search
For the data that has been sorted in ascending order, this method checks the center value of the search range, determining whether the center value is larger or smaller than the search value, and thereby narrows the search range to either side. Thus, target data is searched for by repeating this processing.


## Operation error

There is no operation error.

## Checking 16-bit binary data

## SUM(P)

These instructions store the total number of "1" bits in the 16-bit binary data stored in the specified device.

| Ladder | ST |
| :---: | :---: |
| $\begin{array}{\|l\|l\|l\|} \hline-\square-\square & \text { (s) } & \text { (d) } \\ \hline \end{array}$ | $\begin{aligned} & \text { ENO:=SUM(EN,s,d); } \\ & \text { ENO:=SUMP(EN,s,d); } \end{aligned}$ |

FBD/LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| SUM | - |
|  | $\boxed{ }$ |
| SUMP | - |

## Setting data

Descriptions, ranges, and data types

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (s) | Device containing data in which the total number of "1" bits <br> is to be counted | -32768 to 32767 | 16-bit signed binary | ANY16 |
| (d) | Device for storing the total number of bits | - | 16-bit signed binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## -Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ\|ロ | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | UपIGㅁ, J밈, U3EDl(H)GD | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (s) | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - | $\bigcirc$ | $\bigcirc$ | - | - | - |
| (d) | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - | $\bigcirc$ | - | - | - | - |

## Processing details

- These instructions store the total number of "1" bits in the 16-bit binary data, which is stored in the device specified by (s), in the device specified by (d).




## Operation error

There is no operation error.

## Checking 32－bit binary data

## DSUM（P）

These instructions store the total number of＂1＂bits in the 32－bit binary data stored in the specified device．

| Ladder | ST |
| :---: | :---: |
| $-\square-\square$ （s） （d） | $\begin{aligned} & \text { ENO:=DSUM(EN,s,d); } \\ & \text { ENO:=DSUMP(EN,s,d); } \end{aligned}$ |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DSUM | - |
|  | $\boxed{ }$ |
| DSUMP | - |

## Setting data

Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Start device containing data in which the total number of＂1＂ <br> bits is to be counted | -2147483648 to 2147483647 | 32－bit signed binary | ANY32 |
| （d） | Device for storing the total number of bits | - | 16－bit signed binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロID， U3EDI（H）G口 | z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions store the total number of＂1＂bits in the 32－bit binary data，which is stored in the device specified by（s）， in the device specified by（d）．


## Operation error

There is no operation error．

## Searching the maximum value of 16－bit binary data

## MAX（P）（＿U）

These instructions search the $(\mathrm{n})$ points of 16－bit binary data in the specified device for the maximum value．

＊1 The MAX and MAX＿U instructions do not support the structured text language and FBD／LD language．Use the standard function，MAX． に $\mathfrak{F}$ Page 1617 MAX（＿E），MIN（＿E）

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| MAX | - |
| MAX＿U | - |
| MAXP | - |
| MAXP＿U | - |

## Setting data

Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :---: | :---: | :---: | :---: | :---: | :---: |
| （s） | MAX（P） | Start device where the data for searching for the maximum value are stored | － | 16－bit signed binary | ANY16＿S |
|  | MAX（P）＿U |  |  | 16－bit unsigned binary | ANY16＿U |
| （d） | MAX（P） | Start device for storing the search result of the maximum value | － | 16－bit signed binary | ANY16＿S＿ARRAY <br> （Number of elements： <br> 3） |
|  | MAX（P）＿U |  |  | 16－bit unsigned binary | ANY16＿U＿ARRAY <br> （Number of elements： <br> 3） |
| （ n ） |  | Number of search data | 0 to 65535 | 16－bit unsigned binary | ANY16 |
| EN |  | Execution condition | － | Bit | BOOL |
| ENO |  | Execution result | － | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDI（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- These instructions search the ( n ) points of 16-bit binary data in the device specified by ( s ) for the maximum value, and store the maximum value in the device specified by (d). Each instruction searches data starting from the device specified by (s) and detects first the maximum value in the $x$ th point from (s), and stores $x$ in (d) +1 and the number of maximum values in (d) +2 .

(d)

| $5678(\mathrm{BIN})$ |
| :---: |
| 2 |
| 2 | Maximum value Location

(d) +2
Number of maximum values

## Searching the maximum value of 32－bit binary data

## DMAX（P）（＿U）

These instructions search the $(\mathrm{n})$ points of 32－bit binary data in the specified device for the maximum value．


FBD／LD＊${ }^{* 1}$

＊1 The DMAX and DMAX＿U instructions do not support the structured text language and FBD／LD language．Use the standard function， MAX． W Page 1617 MAX（＿E），MIN（＿E）
■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DMAX | - |
| DMAX＿U | - |
| DMAXP | - |
| DMAXP＿U |  |

## Setting data

Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :---: | :---: | :---: | :---: | :---: | :---: |
| （s） | DMAX（P） | Start device where the data for searching for the maximum value are stored | － | 32－bit signed binary | ANY32＿S |
|  | DMAX（P）＿U |  |  | 32－bit unsigned binary | ANY32＿U |
| （d） | DMAX（P） | Start device for storing the search result of the maximum value | － | 32－bit signed binary | ANY32＿S＿ARRAY （Number of elements： 4） |
|  | DMAX（P）＿U |  |  | 32－bit unsigned binary | ANY32＿U＿ARRAY <br> （Number of elements： <br> 4） |
| （ n ） |  | Number of search data | 0 to 65535 | 16－bit unsigned binary | ANY16 |
| EN |  | Execution condition | － | Bit | BOOL |
| ENO |  | Execution result | － | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- These instructions search the ( n ) points of 32-bit binary data in the device specified by ( s ) for the maximum value, and store the maximum value in the devices specified by (d) and (d)+1. Each instruction searches data starting from the device specified by (s) and detects first the maximum value in the $x$ th point from (s), and stores $x$ in (d)+2 and the number of maximum values in (d)+3.

| s) | 54321000 (BIN) |
| :---: | :---: |
| +2 | 4321000 (BIN) |
|  | 3254000 |
|  | 54321000 (BIN) |
| ) +9 , (s) +8 | 12345678 (BIN) |


| (d) (d) +1 | -54321000 (BIN) |
| :---: | :---: |
| (d) +2 | 1 |
| (d) +3 | 2 |

Maximum value
Location
Number of maximum values

## Operation error

There is no operation error.

## Searching the minimum value of 16－bit binary data

## MIN（P）（＿U）

These instructions search the $(\mathrm{n})$ points of 16－bit binary data in the specified device for the minimum value．

＊1 The MIN and MIN＿U instructions do not support the structured text language and FBD／LD language．Use the standard function，MIN． に Page 1617 MAX（＿E），MIN（＿E）

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| MIN | - |
| MIN＿U | - |
| MINP | - |
| MINP＿U |  |

## Setting data

Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :---: | :---: | :---: | :---: | :---: | :---: |
| （s） | $\operatorname{MIN}(\mathrm{P})$ | Start device where the data for searching for the minimum value are stored | － | 16－bit signed binary | ANY16＿S |
|  | $\operatorname{MIN}(\mathrm{P})_{-}$U |  |  | 16－bit unsigned binary | ANY16＿U |
| （d） | $\operatorname{MIN}(\mathrm{P})$ | Start device for storing the search result of the minimum value | － | 16－bit signed binary | ANY16＿S＿ARRAY <br> （Number of elements： <br> 3） |
|  | $\operatorname{MIN}(\mathrm{P})$＿U |  |  | 16－bit unsigned binary | ANY16＿U＿ARRAY <br> （Number of elements： <br> 3） |
| （ n ） |  | Number of search data | 0 to 65535 | 16－bit unsigned binary | ANY16 |
| EN |  | Execution condition | － | Bit | BOOL |
| ENO |  | Execution result | － | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDI（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- These instructions search the ( n ) points of 16-bit binary data in the device specified by ( s ) for the minimum value, and store the minimum value in the device specified by (d). Each instruction searches data starting from the device specified by (s) and detects first the minimum value in the $x$ th point from (s), and stores $x$ in (d) +1 and the number of minimum values in (d) +2 .

| (s) | 5015 (BIN) |
| :---: | :---: |
| (s) +1 | 6192 (BIN) |
| (s) +2 | 5571 (BIN) |
| $\vdots$ | - |
| (s)+(n)-2 | 5015 (BIN) |
| (s)+(n)-1 | 5571 (BIN) |


| (d) | $5015(\mathrm{BIN})$ |
| :--- | :---: |
|  | Minimum value |
| (d) +1 | 1 |
| (d) +2 | 2 |
|  | Number of minimum values |

## Operation error

There is no operation error.

## Searching the minimum value of 32－bit binary data

## DMIN（P）（＿U）

These instructions search the $(\mathrm{n})$ points of 32－bit binary data in the specified device for the minimum value．


## FBD／LD＊${ }^{*}$


＊1 The DMIN and DMIN＿U instructions do not support the structured text language and FBD／LD language．Use the standard function，MIN． W Page 1617 MAX（＿E），MIN（＿E）

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DMIN | - |
| DMIN＿U | - |
| DMINP | - |
| DMINP＿U |  |

## Setting data

Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- | :--- |
| （s） | DMIN（P） | Start device where the data for searching for the <br> minimum value are stored | - |  | 32－bit signed binary |
|  | DMIN（P）＿U | ANY32＿S |  |  |  |
| （d） | DMIN（P） | Start device for storing the search result of the <br> minimum value | - | 32－bit unsigned binary | ANY32＿U |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3E미（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- These instructions search the ( n ) points of 32-bit binary data in the device specified by ( s ) for the minimum value, and store the minimum value in the devices specified by (d) and (d)+1. Each instruction searches data starting from the device specified by (s) and detects first the minimum value in the $x$ th point from (s), and stores $x$ in (d) +2 and the number of minimum values in (d)+3.

| (s)+1, (s) | 22342001 (BIN) |  | (d) |  | Minimum value |
| :---: | :---: | :---: | :---: | :---: | :---: |
| (s) $+3,(\mathrm{~s})+2$ | 37282010 (BIN) |  | (d) +1 | -22342001 (BIN)- |  |
| (s) $+5,(\mathrm{~s})+4$ | 22342001 (BIN) | ( | (d) +2 | 1 | Location |
| (s) $+7,(\mathrm{~s})+6$ | 59872019 (BIN) | - | (d)+3 | 2 | Number of minimum values |

## Operation error

There is no operation error.

## Sorting 16－bit binary data

## SORTD（＿U）

These instructions sort（ n ）points of 16－bit binary data in ascending or descending order．


## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| SORTD | - |
| SORTD＿U | - |

## Setting data

－Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- | :--- |
| $(\mathrm{s} 1)$ | SORTD | Start device of the table data to be sorted | - | 16－bit signed binary | ANY16＿S |
|  | SORTD＿U |  |  | 16－bit unsigned binary | ANY16＿U |
| （n） | Number of sort data | 0 to 65535 | 16－bit unsigned binary | ANY16 |  |
| （s2） | Number of data to be compared once | 0 to 65535 | 16 －bit unsigned binary | ANY16 |  |
| （d1） | Number of the bit device to be turned on upon <br> completion of sort | - | Bit | ANY＿BOOL |  |
| （d2） | Device used by the system | - | 16－bit signed binary | ANY16＿ARRAY <br> （Number of elements： <br> 2） |  |
| EN |  | - | Bit | BOOL |  |
| ENO | Execution condition | - | Bit | BOOL |  |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3E［l（H）G口 | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d1） | $\bigcirc$ | － | $\bigcirc{ }^{* 1}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

[^18]
## Processing details

- These instructions sort ( n ) points of 16-bit binary data from ( s 1 ) in ascending or descending order. Data is sorted in ascending order when SM703 is off and in descending order when SM703 is on.

- Sorting by the SORTD $\left(\_U\right)$ instruction requires several scans. The number of scans required till completion of sorting is determined by dividing the maximum number of executions performed before completion of sorting by the number of data compared once specified by (s2). (The decimal fractions are rounded up.) When the value in (s2) is increased, the number of scans before completion of sorting is decreased but the scan time is increased.
- The maximum number of executions before completion of sorting is calculated by $(n) \times(n-1) \div 2$ (times). When $(n)=10$, for example, $10 \times(10-1) \div 2=45$ times. At this time, setting $(s 2)=2$, for example, makes $45 \div 2=22.5$ meaning that 23 scans are required before completion of sorting.
- The completion device specified by (d1) turns off at start of execution of the SORT(_U) instruction and turns on upon completion of sorting. After completion of sorting, the device specified by (d1) is kept on. Turn it off as needed.
- The two points from the device specified by (d2) are used by the system at execution of the SORT(_U) instruction. Do not change the two points from the device specified by (d2). If they are changed, an error may occur. (Error code: 3405H)
- If the value in ( n ) is changed during sorting, the new number of sort data is used for sorting.
- If the execution command is turned off during sorting, sorting is interrupted. If the execution command is turned on again, sorting is performed from the beginning.
- If the next sorting is performed continuously after completion of the previous sorting, the execution command needs to be turned off and turned on again.


## Operation error

| Error code (SDO) | Description |
| :---: | :---: |
| 2821H | The device range of $(\mathrm{n})$ points from the device specified by ( s 1 ) and the device range of two points from the device specified by (d2) are overlapping. |
| 3405H | The value in ( s 2 ) is 0 . |
|  | In the second scan or after, the value in (d2) used by the system is equal to or greater than the value in ( n ). |
|  | In the second scan or after, the value in (d2) used by the system is (d2)<(d2)+1. |

## Sorting 32-bit binary data

## DSORTD(_U)

These instructions sort ( n ) points of 32-bit binary data in ascending or descending order.


FBD/LD


## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DSORTD | $-\square$ |
| DSORTD_U | - |

## Setting data

-Description, range, data type

| Operand |  | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- | :--- |
| $(\mathrm{s} 1)$ | DSORTD | Start device of the table data to be sorted | - | 32-bit signed binary | ANY32_S |
|  | DSORTD_U |  |  | 32-bit unsigned binary | ANY32_U |
| (n) | Number of sort data | Number of data to be compared once | 0 to 6553535 | 16-bit unsigned binary | ANY16 |
| (s2) | Number of the bit device to be turned on upon <br> completion of sort | - | 16-bit unsigned binary | ANY16 |  |
| (d1) | Device used by the system | - | Bit | ANY_BOOL |  |
| (d2) |  | - | 16-bit signed binary | ANY16_ARRAY <br> (Number of elements: <br> 2) |  |
| EN | Execution condition | Execution result | - | Bit | BOOL |
| ENO |  |  | Bit |  |  |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | J밈 | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | UपIGロ, J밈, U3Eपl(H)G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (s1) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| ( n ) | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - | $\bigcirc$ | $\bigcirc$ | - | - | - |
| (s2) | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - | $\bigcirc$ | $\bigcirc$ | - | - | - |
| (d1) | $\bigcirc$ | - | $\bigcirc{ }^{* 1}$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (d2) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |

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## Processing details

- These instructions sort ( n ) points of 32-bit binary data from ( s 1 ) in ascending or descending order. Data is sorted in ascending order when SM703 is off and in descending order when SM703 is on.

|  | Data before sort |  | (s1) $+1,(\mathrm{~s} 1)$ | -1000 | Data are sorted in ascending order. |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | When SM703 is off | (s1) $+3,(\mathrm{~s} 1)+2$ | -124 |  |
| (s1) $+1,(\mathrm{~s} 1)$ | 35000 |  | (s1) $+5,(\mathrm{~s} 1)+4$ | 500 |  |
| (s1) $+3,(\mathrm{~s} 1)+2$ | -1000 |  | (s1)+7, (s1)+6 | 35000 |  |
| (s1) $+5,(\mathrm{~s} 1)+4$ | 500 |  |  |  |  |
| (s1)+7, (s1)+6 | -124 |  | (s1) $+1,(\mathrm{~s} 1)$ | 35000 |  |
|  |  |  | (s1) $+3,(\mathrm{~s} 1)+2$ | 500 | Data are sorted in descending order. |
|  |  | When SM703 is on | (s1) $+5,(\mathrm{~s} 1)+4$ | -124 | Data are sored in descending order. |
|  |  |  | (s1)+7, (s1)+6 | -1000 |  |

- Sorting by the DSORTD(_U) instruction requires several scans. The number of scans required till completion of sorting is determined by dividing the maximum number of executions performed before completion of sorting by the number of data compared once specified by (s2). (The decimal fractions are rounded up.) When the value in (s2) is increased, the number of scans before completion of sorting is decreased but the scan time is increased.
- The maximum number of executions before completion of sorting is calculated by $(n) \times(n-1) \div 2$ (times). When $(n)=10$, for example, $10 \times(10-1) \div 2=45$ times. At this time, setting $(s 2)=2$, for example, makes $45 \div 2=22.5$ meaning that 23 scans are required before completion of sorting.
- The completion device specified by (d1) turns off at start of execution of the DSORTD(_U) instruction and turns on upon completion of sorting. After completion of sorting, the device specified by (d1) is kept on. Turn it off as needed.
- The two points from the device specified by (d2) are used by the system at execution of the DSORTD(_U) instruction. Do not change the two points from the device specified by (d2). If they are changed, an error may occur. (Error code: 3405H)
- If the value in ( n ) is changed during sorting, the new number of sort data is used for sorting.
- If the execution command is turned off during sorting, sorting is interrupted. If the execution command is turned on again, sorting is performed from the beginning.
- If the next sorting is performed continuously after completion of the previous sorting, the execution command needs to be turned off and turned on again.


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2821 H | The device range of $2 \times(\mathrm{n})$ points from the device specified by (s1) and the device range of two points from the device specified by (d2) are <br> overlapping. |
| }{} | The value in (s2) is 0. |
|  | In the second scan or after, the value in (d2) used by the system is equal to or greater than the value in (n). |
|  | In the second scan or after, the value in (d2) used by the system is (d2)<(d2) +1. |

## Adding 16－bit binary data

## WSUM（P）（＿U）

These instructions add the $(\mathrm{n})$ points of 16－bit binary data from the specified device．


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| WSUM | - |
| WSUM＿U | $\boxed{ }$ |
| WSUMP | - |
| WSUMP＿U |  |

## Setting data

Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :---: | :---: | :---: | :---: | :---: | :---: |
| （s） | WSUM（P） | Start device where the data for calculating the total value are stored | － | 16－bit signed binary | ANY16＿S |
|  | WSUM（P）＿U |  |  | 16－bit unsigned binary | ANY16＿U |
| （d） | WSUM（P） | Start device for storing the total value | － | 32－bit signed binary | ANY32＿S |
|  | WSUM（P）＿U |  |  | 32－bit unsigned binary | ANY32＿U |
| （ n ） |  | Number of data | 0 to 65535 | 16－bit unsigned binary | ANY16 |
| EN |  | Execution condition | － | Bit | BOOL |
| ENO |  | Execution result | － | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J밈， U3E미（H）Gㅁ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

－These instructions add the $(\mathrm{n})$ points of 16－bit binary data in the device starting from the one specified by（ s ），and store the result in the device specified by（d）．

| （s） | 4444 （BIN） |
| :---: | :---: |
| （s）＋1 | 3333 （BIN） |
| ＋2 | 1234 （BIN） |
| ）＋3 | －5426（BIN） |
| ＋ | 329 （BIN） |
| （s）+5 | 10000 （BIN） |

（d）
（d）$+1-13914$（BIN）

## Operation error

There is no operation error

## Adding 32－bit binary data

## DWSUM（P）（＿U）

These instructions add the（ n ）points of 32－bit binary data in the devices starting from the specified one．


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DWSUM | - |
| DWSUM＿U | $\boxed{ }$ |
| DWSUMP | - |
| DWSUMP＿U |  |

## Setting data

Descriptions，ranges，and data types

| Operand |  | Description | Range | Data type | Data type（label） |
| :---: | :---: | :---: | :---: | :---: | :---: |
| （s） | DWSUM（P） | Start device where the data for calculating the total value are stored | － | 32－bit signed binary | ANY32＿S |
|  | DWSUM（P）＿U |  |  | 32－bit unsigned binary | ANY32＿U |
| （d） | DWSUM（P） | Start device for storing the total value | － | 64－bit signed binary | ANY32＿ARRAY <br> （Number of elements： <br> 2） |
|  | DWSUM（P）＿U |  |  | 64－bit unsigned binary |  |
| （ n ） |  | Number of data | 0 to 65535 | 16－bit unsigned binary | ANY16 |
| EN |  | Execution condition | － | Bit | BOOL |
| ENO |  | Execution result | － | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロID， U3EDI（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

－These instructions add the（ n ）points of 32－bit binary data in the device starting from the one specified by（ s ），and store the result in the device specified by（d）．


## Operation error

There is no operation error

Calculating the mean value of 16－bit binary data

## MEAN（P）（＿U）

These instructions calculate the average value of the $(\mathrm{n})$ points of 16－bit data in the devices starting from the specified one．


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| MEAN | - |
| MEAN＿U | $\boxed{ }$ |
| MEANP | $\boxed{ }$ |
| MEANP＿U | - |

## Setting data

Descriptions，ranges，and data types

| Operand |  | Description | Range | Data type | Data type（label） |
| :---: | :---: | :---: | :---: | :---: | :---: |
| （s） | MEAN（P） | Start device where the data for calculating the average value are stored | － | 16－bit signed binary | ANY16＿S |
|  | MEAN（P）＿U |  |  | 16－bit unsigned binary | ANY16＿U |
| （d） | MEAN（P） | Device for storing the mean value | － | 16－bit signed binary | ANY16＿S |
|  | MEAN（P）＿U |  |  | 16－bit unsigned binary | ANY16＿U |
| （ n ） |  | Number of data，or the device number where the number of data is stored | 0 to 65535 | 16－bit unsigned binary | ANY16 |
| EN |  | Execution condition | － | Bit | BOOL |
| ENO |  | Execution result | － | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3E미（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \mathbf{L C} \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

－These instructions calculate the average value of the $(\mathrm{n})$ points of 16－bit binary data in the devices starting from the one specified by（s），and stores the average value in the device specified by（d）．

－If the calculation result is not an integer，the first decimal place is rounded down．
－When $(\mathrm{n})$ is 0 ，the processing is not performed．

## Operation error

There is no operation error

Calculating the mean value of 32－bit binary data

## DMEAN（P）（＿U）

These instructions calculate the average value of the $(\mathrm{n})$ points of 32－bit data in the devices starting from the specified one．


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DMEAN | - |
| DMEAN＿U | $\boxed{ }$ |
| DMEANP | $\boxed{ }$ |
| DMEANP＿U | - |

## Setting data

Descriptions，ranges，and data types

| Operand |  | Description | Range | Data type | Data type（label） |
| :---: | :---: | :---: | :---: | :---: | :---: |
| （s） | DMEAN（P） | Start device where the data for calculating the average value are stored | － | 32－bit signed binary | ANY32＿S |
|  | DMEAN（P）＿U |  |  | 32－bit unsigned binary | ANY32＿U |
| （d） | DMEAN（P） | Start device for storing the average value | － | 32－bit signed binary | ANY32＿S |
|  | DMEAN（P）＿U |  |  | 32－bit unsigned binary | ANY32＿U |
| （ n ） |  | Number of data，or the device number where the number of data is stored | 0 to 65535 | 16－bit unsigned binary | ANY16 |
| EN |  | Execution condition | － | Bit | BOOL |
| ENO |  | Execution result | － | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロID， U3EDl（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \mathbf{L C} \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

－These instructions calculate the average value of the（n）points of 32－bit binary data in the devices starting from the one specified by（s），and stores the average value in the device specified by（d）．
（s）$+1,(\mathrm{~s})$
（s）$+3,(\mathrm{~s})+2$
（s）$+2(\mathrm{n})-1,(\mathrm{~s})+2(\mathrm{n})-2$

（d）+1 ，（d）
－If the calculation result is not an integer，the first decimal place is rounded down．
－When（ n ）is 0 ，the processing is not performed．

## Operation error

There is no operation error

## 7．20 Database Access Instructions

The database access instructions add，update，obtain，or delete data with respect to the tabular data such as product and production information managed as databases in the programmable controller．
These instructions construct a database from the Unicode text file that defines information such as a table configuration，and operates the database thus constructed．（ $\mathbb{\square}]$ MELSEC iQ－R CPU Module User＇s Manual（Application））

## Importing data to the data base

## DBIMPORT（P）

These instructions import the data stored in the Unicode text file at the path specified by（s）and construct a database．


FBD／LD


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DBIMPORT | - |
|  | - |
| DBIMPORTP | - |

## Setting data

Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :---: | :---: | :---: | :---: | :---: |
| （s） | Start device for storing the Unicode text file name Example：＂2：Idatabase1 \recipelrecipe＿db．txt＂ Within 128 characters | － | Unicode string | ANYSTRING＿DOUB LE |
| （d1） | Completion device（start device that turns on one scan upon completion of instruction） <br> －（d1）＋0：Completion signal <br> －（d1）＋1：Error completion signal | － | Bit | ANYBIT＿ARRAY <br> （Number of elements： 2） |
| （d2） | Completion status <br> －0000：Normal completion <br> －Other than 0000：Error completion（error code） | － | Word | ANY16 |
| EN | Execution condition | － | Bit | BOOL |
| ENO | Execution result | － | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDl（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － |
| （d1） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions import the data stored in the Unicode text file specified by (s) and construct a database. Information such as a table configuration needs to be defined in advance in the Unicode text file used by the DBIMPORT(P) instruction. ([]] MELSEC iQ-R CPU Module User's Manual (Application))
- When the database that is already open exists and the $\operatorname{DBIMPORT}(\mathrm{P})$ instruction is executed, it is completed with an error.
- Upon successful completion, the completion signal in the completion device ( d 1 ) +0 is turned on and 0 is stored as the completion status in the device (d2).
- The following figure shows the operation of the completion device at completion of the DBIMPORT(P) instruction.



## Precautions

In the following cases, the error completion signal in (d1)+1 is turned on and an error code is stored as the completion status in the device (d2).

- The DBIMPORT(P) instruction is executed during execution of the database access instruction.
- The Unicode text file specified by (s) does not exist.
- The number of fields specified in the field name row of the Unicode text file does not match the number of fields in the record row.
- A table definition start tag or end tag is missing in the Unicode text file.
- An out-of-range value is set for the key constraint in the Unicode text file.
- The database name, table name, or field name in the Unicode text file exceeds 32 characters.
- The number of tables or fields in the Unicode text file exceeds 16.
- An out-of-spec data type is specified in the Unicode text file.
- The number of records in the Unicode text file exceeds the maximum number.
- An access to the database has failed.
- The database name contains an invalid character.
- The total number of characters used in the database name specified in the Unicode text file and those used in the folder path (including the drive path character) specified by (s) exceeds 128.
- The database that is already open exists and the $\operatorname{DBIMPORT}(P)$ instruction is executed.

If an error is detected because of the Unicode text format, the DBIMPORT(P) instruction turns on the error termination signal in (d1)+1 and stores the Unicode text line where an error was detected in SD760 and SD761.

## Operation error

| Error code (SD0) | Description |
| :--- | :--- |
| 2840 H | A numerical value other than 2 is specified for the drive number in (s). |
| 3405 H | The character string (path) in the device specified by (s) exceeds 255 characters. |

For the error code stored in the completion status of the operand, refer to the following.
$\longmapsto$ Page 844 Error codes related to database access instructions

## Exporting data from the data base

## DBEXPORT（P）

These instructions export the data stored in the specified database to the Unicode text file．


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DBEXPORT | - |
|  | $\boxed{\square}$ |
| DBEXPORTP | - |

## Setting data

Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :---: | :---: | :---: | :---: | :---: |
| （s） | Start device for storing the database folder path Example：＂2：Idatabase11recipe＂ Within 128 characters | － | Unicode string | ANYSTRING＿DOUB LE |
| （d1） | Completion device（start device that turns on one scan upon completion of instruction） <br> －（d1）＋0：Completion signal <br> －（d1）＋1：Error completion signal | － | Bit | ANYBIT＿ARRAY <br> （Number of elements： 2） |
| （d2） | Completion status <br> －0000：Normal completion <br> －Other than 0000：Error completion（error code） | － | Word | ANY16 |
| EN | Execution condition | － | Bit | BOOL |
| ENO | Execution result | － | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J미， U3EDI（H）GD | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － |
| （d1） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions export the data in the database stored in the database folder at the path specified by (s) to the Unicode text file.
- The Unicode text file is created in the folder where the database folder is stored. The file name is "database_name.txt". If the same Unicode text file already exists, the file is overwritten with the exported data.


## Ex.

When the path of the database folder is "2:Idatabaselrecipe1", executing the instruction creates Unicode text file "2:Idatabaselrecipe1.txt" and exports data to the file.

- Upon successful completion, the completion signal in the completion device ( d 1 ) +0 is turned on and 0 is stored as the completion status in the device (d2).
- The following figure shows the operation of the completion device at completion of the DBEXPORT $(\mathrm{P})$ instruction.

- The internal configuration of the Unicode text file to which data is exported is the same as the file to which data is imported by the DBIMPORT(P) instruction. ( $\square \square$ MELSEC iQ-R CPU Module User's Manual (Application))


## Precautions

In the following cases, the error completion signal in (d1)+1 is turned on and an error code is stored as the completion status in the device (d2).

- The DBEXPORT( P ) instruction is executed during execution of the database access instruction.
- The path specified by (s) is not a database.
- Writing data to the Unicode text failed due to the failure to access the database.
- The number of characters of the path (including the drive path character) specified by (s) exceeds 128.


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2840 H | A numerical value other than 2 is specified for the drive number in (s). |

For the error code stored in the completion status of the operand, refer to the following.
$\longmapsto$ Page 844 Error codes related to database access instructions

Opening the data base

## DBOPEN（P）

These instructions connect to the database specified by（s）and make it available．


FBD／LD


## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DBOPEN | - |
|  | $\boxed{Z}$ |
| DBOPENP | $\boxed{ }$ |

Setting data
Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :---: | :---: | :---: | :---: | :---: |
| （s） | Start device for storing the folder path of the database to be opened． <br> Example：＂2：\database1 1recipe＂ <br> Within 128 characters | － | Unicode string | ANYSTRING＿DOUB LE |
| （d1） | Database identification number | 1 to 4 | 16－bit signed binary | ANY16 |
| （d2） | Completion device（start device that turns on one scan upon completion of instruction） <br> －（d2）＋0：Completion signal <br> －（d2）＋1：Error completion signal | － | Bit | ANYBIT＿ARRAY <br> （Number of elements： 2） |
| （d3） | Completion status <br> －0000：Normal completion <br> －Other than 0000：Error completion（error code） | － | Word | ANY16 |
| EN | Execution condition | － | Bit | BOOL |
| ENO | Execution result | － | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDl（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － |
| （d1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d2） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d3） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions connect to the database stored in the folder path specified by (s) and makes it available.
- Specify "(drive number):(database folder path)" for the storage location. The drive number is fixed to 2 (SD memory card).
- Upon normal completion, the instruction stores the identification number of the connected database in the device (d1). The completion signal in the completion device ( d 2 ) +0 is turned on and 0 is stored as the completion status in the device (d3).
- The following figure shows the operation of the completion device at completion of the DBOPEN $(P)$ instruction.

- The DBOPEN $(P)$ instruction enables connections to a maximum of four different databases at the same time.
- During transaction, the database cannot be newly opened.


## Precautions

In the following cases, the error termination signal in (d2)+1 is turned on and an error code is stored in (d3).

- The DBOPEN(P) instruction is executed during execution of the database access instruction.
- The storage location specified by (s) does not exist.
- An attempt is made to connect to the database that has already been connected.
- The DBOPEN(P) instruction is executed for a database exceeding the maximum number of databases that can be connected concurrently.
- The number of characters of the path (including the drive path character) specified by (s) exceeds 128.
- The database is opened during transaction.


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2820 H | The area specified by (s) or (d) exceeds the applicable range of the device/label used. |
| 2840 H | A numerical value other than 2 is specified for the drive number in (s). |

For the error code stored in the completion status of the operand, refer to the following.
↔ Page 844 Error codes related to database access instructions

## Closing the data base

## DBCLOSE（P）

These instructions clear the connection from the specified database．


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DBCLOSE | - |
|  | $\boxed{\square}$ |
| DBCLOSEP | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Database identification number | 1 to 4 | 16－bit signed binary | ANY16 |
| （d1） | Completion device（start device that turns on one scan <br> upon completion of instruction） <br> $\bullet(d 1)+0:$ Completion signal <br> $\cdot(\mathrm{d} 1)+1:$ Error completion signal | - | Bit | ANYBIT＿ARRAY <br> （Number of elements： <br> $2)$ |
| （d2） | Completion status <br> $\bullet$ 0000： Normal completion <br> $\bullet$ Other than 0000：Error completion（error code） | - | Word | ANY16 |
| EN | Execution condition | - | Bit |  |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathbf{X}, \mathbf{Y}, \mathbf{M}, \mathbf{L}, \mathbf{S M} \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions release the connection between the database identification number specified by (s) and the corresponding database.
- Upon successful completion, the completion signal in the completion device ( d 1 ) +0 is turned on and 0 is stored as the completion status in the device (d2).
- The following figure shows the operation of the completion device at completion of the DBCLOSE $(\mathrm{P})$ instruction.

- If the $\operatorname{DBCLOSE}(\mathrm{P})$ instruction is executed before $\operatorname{DBCOMMIT}(\mathrm{P})$ or $\operatorname{DBROLBAK}(\mathrm{P})$ while the transaction is run by the DBTRANS $(P)$ instruction, the transaction is determined in the status at the execution of the DBCLOSE $(P)$ instruction.


## Precautions

In the following cases, the error completion signal in (d1)+1 is turned on and an error code is stored as the completion status in the device (d2).

- The DBCLOSE(P) instruction is executed during execution of the database access instruction.
- The identification number specified by (s) is an already disconnected database.
- An identification number outside the specified range is specified by (s).


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2820 H | The area specified by (s) or (d) exceeds the applicable range of the device/label used. |

For the error code stored in the completion status of the operand, refer to the following.
$\longmapsto$ Page 844 Error codes related to database access instructions

## Adding a record to the data base

## DBINSERT（P）

These instructions add a record to the table of the database corresponding to the specified identification number．


FBD／LD

| ■－－－$]$ |  |
| :---: | :---: |
| EN | ENO |
| s1 | d1 |
| s2 | d2 |
| s3 |  |
| s4 |  |

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DBINSERT | - |
|  | $\boxed{ }$ |
| DBINSERTP |  |

Setting data
■Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Database identification number | 1 to 4 | 16－bit signed binary | ANY16 |
| （s2） | Start device for storing the database table names． | - | Unicode string | ANYSTRING＿DOUB <br> LE |
| （s3） | Start device for storing the database field names． | - | Word | ANY16 |
| （s4） | Start device for storing insertion data | - | Word | ANY16 |
| （d1） | Completion device（start device that turns on one scan <br> upon completion of instruction） <br> $\bullet(d 1)+0:$ Completion signal <br> $\cdot(d 1)+1:$ Error completion signal | - | Bit | ANYBIT＿ARRAY <br> （Number of elements： <br> $2)$ |
| （d2） | Completion status <br> $\bullet$ 0000：Normal completion <br> $\bullet$ Other than 0000：Error completion（error code） | - | Word | ANY16 |
| EN | Execution condition | - | Bit | Bit |
| ENO | Execution result | - | BOOL |  |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | J미민 | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDl（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － |
| （s3） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s4） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Control data

| Operand: (s3) | Description | Setting range | Set by |  |
| :--- | :--- | :--- | :--- | :--- |
| Device | Item | Number of fields | Specify the number of fields to which a value is to be added. <br> Specify a value equal to or less than the number of fields of the table specified in (s2). | 1 to 16 |
| +0 | Field name | Specify the name of each field. Specify field names, each fixed to 32 characters, by <br> the number of fields with Unicode character strings. For the name less than 32 <br> characters, the character string should be right-justified and filled with 0000 H to <br> become a 32-character string. <br> The last address $\square$ in (s3) varies according to the number of fields. <br> $\square=32 \times n ~(n: ~ N u m b e r ~ o f ~ f i e l d s) ~$ | - | User |
| +1 to $+\square$ |  | User |  |  |


| Operand: (s4) | Sescription | Setting range | Set by |  |
| :--- | :--- | :--- | :--- | :--- |
| Device | Item | Number of records | Specify the number of records to be added. | 1 to 16 |
| +0 | Size | Specify the size of one record. | Depends on the <br> data type | User |
| +1 | Value to be added | Specify the data for the number of fields specified by (s3) for the number of records <br> specified by (s4)+0.* | Depends on the <br> data type | User |
| +2 to $+\square$ |  |  |  |  |

*1 The following figure shows the format of (s4). Set the value corresponding to each field set in (s3). A maximum of 16 records can be set.


- The data size of each value follows the size of the data type of each field.

| Data type of field | Data size (unit: word) |
| :--- | :--- |
| BOOL | 1 |
| WORD | 1 |
| DWORD | 2 |
| INT | 1 |
| DINT | 2 |
| REAL | 2 |
| LREAL | 4 |
| STRING | •年 <br> • Odd number of characters: Rounding up the number of characters $\div 2$ <br> [Example] <br> STRING: $32:(32 \div 2)+1=17$ <br> STRING: $15:(15 \div 2)=7.5$ rounding up $\rightarrow 8$ |
| WSTRING | Number of characters +1 <br> [Example] <br> WSTRING: $32:(32)+1=33$ |

Ex.
As shown below, set the name of the field to be added and the value to be added in the device and execute the DBINSERT(P) instruction. One record related to product ID = 3 can be registered in "prolnfo" in the product information table in the database.


## Processing details

- These instructions add a record to the table specified by (s2) in the database corresponding to the identification number specified by ( s 1 ).
- Specify the number of fields of the record to be added, field names, and data types in (s3). For the field names to be added, not all fields making up the table need to be specified. Store NULL in the fields which are not specified.
- Specify the number of records to be added and the size and value per record in (s4). One to sixteen records can be set.

- Upon successful completion, the completion signal in the completion device (d1)+0 is turned on and 0 is stored as the completion status in the device (d2).
- Upon completion with an error, the error completion signal in the completion device ( d 1 ) +1 is turned on and an error code is stored as the completion status in the device (d2).
- The following figure shows the operation of the completion device at completion of the DBINSERT(P) instruction.



## Precautions

In the following cases, the error completion signal in (d1)+1 is turned on and an error code is stored as the completion status in the device (d2).

- The DBINSERT(P) instruction is executed during execution of the database access instruction.
- An identification number outside the range is specified by ( s 1 ).
- An identification number of a database which is not open is specified by ( s 1 ).
- The table name specified by ( s 2 ) does not exist.
- The number of characters of the table name specified by (s2) exceeds 32 .
- An out-of-range value is specified in ( s 3 ) for the number of fields to be added.
- An out-of-range value is specified in (s4) for the number of records to be added.
- Database insertion processing failed.
- The range of the data for one record set in (s4)+2 does not match the size specified by (s4)+1.


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2820 H | The area specified by (s) or (d) exceeds the applicable range of the device/label used. |

For the error code stored in the completion status of the operand, refer to the following.
$\longmapsto$ Page 844 Error codes related to database access instructions

## Updating the record in the data base

## DBUPDATE(P)

These instructions update all records that meet the specified condition in the specified table corresponding to the specified identification number.


## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DBUPDATE | - |
|  | $\boxed{T}$ |
| DBUPDATEP | $\boxed{Z}$ |

## Setting data

■Descriptions, ranges, and data types

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (s1) | Database identification number | 1 to 4 | 16-bit signed binary | ANY16 |
| (s2) | Start device for storing the table name of the database <br> table to be updated. | - | Unicode string | ANYSTRING_DOUB <br> LE |
| (s3) | Start device for storing the field name of the database to be <br> updated. | - | Word | ANY16 |
| (s4) | Start device for storing the updated data | - | Word | ANY16 |
| (s5) | Start device for storing the update conditions (a maximum <br> of two conditions). | - | Word | ANY16 |
| (d1) | Completion device (start device that turns on one scan <br> upon completion of instruction) <br> $\cdot($ (d1)+0: Completion signal <br> $\cdot($ d1)+1: Error completion signal | - | Bit | ANYBIT_ARRAY <br> (Number of elements: <br> $2)$ |
| (d2) | Completion status <br> $\cdot$ 0000: Normal completion <br> $\cdot$ Other than 0000: Error completion (error code) | - | Word | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | BOOL |  |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3EDI（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － |
| （s3） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s4） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s5） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Control data

| Operand：（s3） |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| ＋0 | Number of fields | Specify the number of fields． | 1 to 16 | User |
| ＋1 to＋$\square$ | Field name | Specify the name of each field．Specify field names，each fixed to 32 characters，by the number of fields with Unicode character strings．For the name less than 32 characters，the character string should be right－justified and filled with 0000 H to become a 32－character string． <br> The last address $\square$ in（ s 3 ）varies according to the number of fields． $\square=32 \times n$（ n ：number of fields） | － | User |
| Operand：（s4） |  |  |  |  |
| Device | Item | Description | Setting range | Set by |
| ＋0 | Data size | Set the data size of the field to be updated． | － | User |
| ＋1 | Not used | － | － | － |
| ＋2 to＋$\square$ | Set value | Set the updated data． | Depends on the data type | User |

The following figure shows the setting format of（s4）．Set the value corresponding to each field in（s3）．The data size of each value follows the size of the data type of each field．（ $\mathfrak{F}$ Page 816 DBINSERT（P））


| Operand：（s5） |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| ＋0 | Data size of the first condition | Set the data size of the first update condition． | 1 to 125 | User |
| ＋1 | Data size of the second condition | Set the data size of the second update condition． <br> －0：No condition <br> －Other than 0：Data size | 0 to 125 | User |


| Operand: (s5) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +2 to + $\square$ | Update condition | Set the update conditions. The last address in (s5) varies depending on the data type of the determination value. <br> Field name <br> Specify the field name with a Unicode character string in 32 characters (fixed). For the name less than 32 characters, the character string should be right-justified and filled with 0000 H to become a 32-character string. <br> -Condition number* ${ }^{*}$ <br> Set the number indicating an update condition. <br> ■Operator between conditions <br> Set the operator with an adjacent condition. <br> - 0: No operator <br> -1: AND <br> - 2: OR <br> When one update condition is specified, set the inter-condition operator of the first update condition to 0 . In this case, the second update condition is ignored if specified. <br> Be sure to set the inter-condition operator of the second update condition to 0 . <br> -Determination value <br> Set the value used to determine the update condition. Also when the condition number is 7 or 8 , prepare a determination value area for the data size. | - | User |

The following figure shows the setting format of (s5). When the data type is WORD, set as many field names, condition numbers, inter-condition operators, and determination values as there are update conditions. The data size of the determination value follows the size of the data type of each field. ( 5 Page 816 DBINSERT(P))

*1 The following table lists the set values and the corresponding update conditions.

| Set value | Corresponding symbol | Description |
| :--- | :--- | :--- |
| 1 | $=$ | Equal to the determination value |
| 2 | $!=$ | Other than the determination value |
| 3 | $<$ | Smaller than the determination value |
| 4 | $>$ | Greater than the determination value |
| 5 | $=<$ | Equal to or less than the determination value |
| 6 | $>=$ | Equal to or greater than the determination value |
| 7 | is NULL | NULL (no value is set) |
| 8 | is not NULL | Not NULL (a value is set) |

Ex.
As shown below, set the name of the field to be updated and the value to be added in the device and execute the DBUPDATE $(P)$ instruction. Product ID $=2$ can be produced and the number of products in "prolnfo" in the product information table in the database can be updated to 35 .

(1) First condition data size $\rightarrow$ WORD type (1 word)
(2) 0 for the second condition which is not used
(3) Field name of first update condition
(4) The condition number is " $=$ ". Set K1.
(5) No inter-condition operator is used. Set K0.
(6) Set K2 for the determination value.

## Processing details

- Updates all record that meets the condition specified by ( $s 5$ ) in the table specified by $(\mathrm{s} 2)$ in the database specified by the identification number specified by (s1).
- Specify the field name of the record to be updated in (s3). Not all fields in the table need to be specified but at least one field needs to be specified.
- Specify the value of the record to be updated in (s4). The set value in any field not specified in (s3) is not updated.
- Specify the condition to be updated in (s5). At least one condition needs to be specified and a maximum of two conditions can be specified.
- When the DBUPDATE $(P)$ instruction updates the table recipeA record that matches "field1="2" of the update condition in which the update target field name is 2 and the updated data is New-Product1, the following occurs.

Table recipeA (before update)

| Field 1 WORD | Field 2 <br> WSTRING (16 characters maximum) | Field 3 INT |
| :---: | :---: | :---: |
| 1 | ProductA | 100 |
| 2 | ProductB | 200 |
| 3 | ProductC | 300 |

Table recipeA (after update)

| Field 1 | Field 2 | Field 3 |
| :---: | :---: | :---: |
| WORD | WSTRING (16 characters maximum) | INT |
| 1 | ProductA | 100 |
| 2 | New-Product1 | 200 |
| 3 | ProductC | 300 |

- When the DBUPDATE $(P)$ instruction updates the table recipeA record that matches "field1>=" 2 " and field $1<=3$ of the update condition in which the update target field name is 2 and the updated data is New-Product1, the following occurs.


Table recipeA (after update)

| Field 1 WORD | Field 2 WSTRING (16 characters maximum) | Field 3 INT |
| :---: | :---: | :---: |
| 1 | ProductA | 100 |
| 2 | New-Product1 | 200 |
| 3 | New-Product1 | 300 |
| 4 | ProductD | 400 |

- Upon successful completion, the completion signal in the completion device (d1)+0 is turned on and 0 is stored as the completion status in the device (d2).
- The following figure shows the operation of the completion device at completion of the DBUPDATE $(P)$ instruction.



## Precautions

In the following cases, the error completion signal in (d1)+1 is turned on and an error code is stored as the completion status in the device ( d 2 ).

- The DBUPDATE(P) instruction is executed during execution of the database access instruction.
- An identification number outside the range is specified by (s1).
- An identification number of a database which is not open is specified by ( s 1 ).
- The table name specified by (s2) does not exist.
- The number of characters of the table name specified by (s2) exceeds 32 .
- An out-of-range value is specified in (s3) for the number of fields to be updated.
- An out-of-range value is specified in (s5) for the condition size.
- The size of the first condition in ( s 5 ) is set to 0 .
- An out-of-range value is specified in (s5) for the condition symbol.
- An out-of-range value is specified in (s5) for the inter-condition operator.
- Database update processing failed.
- The inter-condition operator specified by (s5)+35 is 1 or 2 , and that in ( s 5 ) +1 is set to 0 .
- The range of the data for one record set in (s4)+2 does not match the size specified by (s4)+1.
- The field name to be set in (s5)+2 is left unset.


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2820 H | The area specified by (s) or (d) exceeds the applicable range of the device/label used. |

For the error code stored in the completion status of the operand, refer to the following.
$\longmapsto$ Page 844 Error codes related to database access instructions

## Searching the record in the data base

## DBSELECT(P)

These instructions search the records in the table in the database corresponding to the specified identification number.

| Ladder |  |  |  |  |  |  |  | ```ST ENO:=DBSELECT(EN,s1,s2,s3,s4,d1,d2,d3); ENO:=DBSELECTP(EN,s1,s2,s3,s4,d1,d2,d3);``` |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  |  |  |  |  |  |
|  | (s1) | (s2) | (s3) | (s4) | (d1) | (d2) | (d3) |  |  |

FBD/LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DBSELECT | $\boxed{\square}$ |
|  | $\boxed{ }$ |
| DBSELECTP | $\boxed{ }$ |

Setting data
■Descriptions, ranges, and data types

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (s1) | Database identification number | 1 to 4 | 16-bit signed binary | ANY16 |
| (s2) | Start device for storing the table name of the database to <br> be searched. | - | Unicode string | ANYSTRING_DOUB <br> LE |
| (s3) | Start device for storing the field name of the database to be <br> searched. | - | Word | ANY16 |
| (s4) | Start device for storing the search conditions (a maximum <br> of two conditions). | - | Word | ANY16 |
| (d1) | Search result | - | Word | ANY16 |
| (d2) | Completion device (start device that turns on one scan <br> upon completion of instruction) <br> $\cdot($ d2)+0: Completion signal <br> $\cdot($ (d2)+1: Error completion signal | - | ANYBIT_ARRAY |  |
| (Number of elements: |  |  |  |  |
| (d3) | Completion status <br> $\bullet$ 0000: Normal completion <br> $\cdot$ Other than 0000: Error completion (error code) | - | Word | ANY16 |
| EN | Execution condition | - | Bit | Bit |
| ENO | Execution result | - | BOOL |  |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathbf{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | J밈 | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）G口 | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － |
| （s3） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s4） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d2） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d3） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Control data

| Operand：（s2） |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| ＋0 to＋$\square$ | Table name | Specify the search target table name with up to 32 characters． | － | User |
| Operand：（s3） |  |  |  |  |
| Device | Item | Description | Setting range | Set by |
| ＋0 | Number of fields | Specify the number of fields to be searched． If 0 is specified，all fields of the table are subject to output． | 0 to 16 | User |
| ＋1 to＋ | Field name | Specify each search target field name with 32 characters．The last address in（s3）varies according to the number of fields． $\square=33 \times n$ <br> －Field name <br> Specify field names，each fixed to 32 characters，by the number of fields with Unicode character strings．For the name less than 32 characters，the character string should be right－ justified and filled with 0000 H to become a 32 －character string． | － | User |


| Operand: (s4) |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by | Data type |
| +0 | Data size of the first condition | Set the data size of the first search condition in units of words in (s4)+4. If 0 is set in (s4), all records in the table are to be output. <br> - 0: No condition <br> - Other than 0 : Data size of field <br> (When (s4) is set to 0 , set (s4) +1 also to 0 .) | 0 to 125 | User | WORD |
| +1 | Data size of the second condition | Set the data size of the second search condition in (s4)+4. <br> - 0: No condition <br> - Other than 0: Data size of field | 0 to 125 | User |  |
| +2 | Maximum output size | Set the maximum output size in the search result (d1). <br> - 0: Default value ( 1024 words) | 0 to 3072 | User |  |
| +3 | Maximum number of outputs | Set the maximum number of outputs in the search result (d1). <br> - 0: Default number of outputs (16 outputs) | 0 to 64 | User |  |
| +4 to + | Search condition*1 | Set the search conditions. The last address in (s4) varies depending on the data type of the determination value. <br> Field name <br> Specify field names, each fixed to 32 characters, by the number of fields with Unicode character strings. <br> For the name less than 32 characters, the character string should be rightjustified and filled with 0000 H to become a 32 -character string. <br> ■Condition number <br> Set the number indicating a search condition. ${ }^{*}$ 2 <br> -Operator between conditions <br> Set the operator with an adjacent condition. <br> - 0: No operator <br> - 1: AND <br> - 2: OR <br> When one search condition is specified, set the inter-condition operator of the first search condition to 0 . In this case, the second search condition is ignored if specified. <br> Be sure to set the inter-condition operator of the second update condition to 0 . <br> -Determination value <br> Set the value used to determine the update condition. ${ }^{* 3}$ <br> Also when the condition number is 7 or 8 , prepare a determination value area for the data size. | - | User |  |

*1 The following figure shows the setting format of (s4). (In the case of data type WORD)
Set as many field names, condition numbers, inter-condition operators, and determination values as there are update conditions.

| (s4) | b15 ... b0 |  |
| :---: | :---: | :---: |
|  | Data size | A data size of the 1 st search condition |
| (s4)+1 | Data size | A data size of the 2st search condition |
| (s4)+2 | Maximum output size | Maximum size of the search result |
| (s4)+3 | Number of output records | Maximum number of records can be output |
| $\begin{aligned} & \text { (s4)+4 to } \\ & \text { (s4)+35 } \end{aligned}$ | Field name | A condition for the |
| (s4) +36 | Condition number | Set a condition number. 1st field |
| (s4)+37 | Logical operator setting value | Set a logical operator for an adjacent condition. |
| (s4)+38 | Determination value |  |
| $\begin{aligned} & \text { (s4)+39 to } \\ & \text { (s4)+70 } \end{aligned}$ | Field name | A condition for the |
| (s4)+71 | Condition number | Set a condition number. 2nd field |
| (s4) +72 | Logical operator setting value | The value is fixed to 0 . |
| (s4)+73 | Determination value | 1 |

*2 The condition numbers are the same as those of the DBUPDATE instruction. For details, refer to the control data of (s5) of the DBUPDATE ( P ) instruction. ( $\Im$ Page 820 DBUPDATE (P) )
*3 The data size of the determination value follows the size of the data type of each field. (以 Page 820 DBUPDATE(P))

| Operand: (d1) |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by | Data type |
| +0 | Number of outputs* ${ }^{*}$ | The number of records that meet the condition set in (s4) is output. <br> The number of outputs should be within the maximum number of outputs set in (s4)+3, and any record exceeding the maximum number of outputs is not output. | 0 to 64 | System | WORD |
| +1 to + $\square$ | Output value*4 | The value of the records that meet the condition set in (s4) is output. Records are output right-justified in the range from (d1) to (d1)+(maximum output size), and any record that does not fit in the range is not output. | Depends on the data type | System |  |

*4 The following figure shows the setting format of (d1). Data is output according to the data type corresponding to the field specified by (s3)


Ex.
A parameter that matches the specified condition in product information can be retrieved. As shown below, by executing the DBSELECT $(P)$ instruction, the "Size(z)" value of product ID=1 can be retrieved.

(1) First condition data size $\rightarrow$ WORD type (1 word)
(2) 0 for the second condition which is not used
(3) Maximum output size $\rightarrow 20$ words
(4) Maximum number of outputs $\rightarrow$ default ( 0 )
(5) Field name of first search condition
(6) The condition number is " $=$ ". Set K1.
(7) No inter-condition operator is used. Set K0.
(8) Set K1 for the determination value.
(9) K60 is output in WORD type (1 word).

The following shows an example of using the DBSELECT instruction to retrieve the "Size(z)" value greater than product $I D=1 \mathrm{H}$ and less than product $I D=3 \mathrm{H}$.

(1) First condition data size $\rightarrow$ WORD type (1 word)
(2) Second condition data size $\rightarrow$ WORD type (2 word)
(3) Maximum output size $\rightarrow 20$ words
(4) Maximum number of outputs $\rightarrow$ default ( 0 )
(5) Field name of first search condition
(6) The condition number is ">". Set K4.
(7) The inter-condition operator is "AND". Set K1.
(8) Set K1 for the determination value.
(9) Field name of second search condition
(10)The condition number is " " $"$. Set K3.
(11)No inter-condition operator is used. Set K0.
(12)Set K3 for the determination value.
(13)K40 is output in WORD type (1 word).

## Processing details

- These instructions search the records in the table specified by (s2) in the database corresponding to the identification number specified by ( $s 1$ ). The maximum number of outputs is 64 .
- Specify the field name of the record to be searched for in (s3).
- Specify the search conditions in (s4). A maximum of two conditions can be specified.
- The search result is stored in (d1).
- Even when executed from the interrupt program, this instruction performs a record search.
- When the DBSELECT $(P)$ instruction retrieves and outputs a table recipeA record which matches the conditions in which the output field is 2 and the condition is field $1=2$, the following occurs.

Table recipeA


- Upon normal completion, the completion signal in the completion device ( d 2 ) is turned on and 0 is stored as the completion status in the device (d3).
- The following figure shows the operation of the completion device at completion of the DBSELECT $(\mathrm{P})$ instruction.



## Precautions

In the following cases, the error completion signal in (d2)+1 is turned on and an error code is stored as the completion status in the device (d3).

- The DBSELECT(P) instruction is executed during execution of the database access instruction.
- An invalid identification number is specified by ( s 1 ).
- An identification number of a database which is not open is specified by ( s 1 ).
- The table name specified by ( s 2 ) does not exist.
- The number of fields to be searched according to ( s 3 ) exceeds the maximum value.
- The number of records to be searched according to ( s 4 ) exceeds the maximum value.
- The size of the field to be searched according to (s4) is outside the range.
- Database selection processing failed.
- The number of records output to (d1) exceeds the number specified by (s4).
- The size of the records output to (d1) exceeds the size specified by (s4).
- The inter-condition operator specified by ( $s 4$ ) is out of the range.
- The inter-condition operator specified by (s4)+38 is 1 or 2 , and that in ( $s 4$ ) +1 is set to 0 .
- 0 is set in (s4) and a value other than 0 is set in (s4)+1.


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2820 H | The area specified by (s) or (d) exceeds the applicable range of the device/label used. |

For the error code stored in the completion status of the operand, refer to the following.
$\rightsquigarrow$ Page 844 Error codes related to database access instructions

## Deleting the record in the data base

## DBDELETE（P）

These instructions delete the record that meets the specified condition in the specified table in the database corresponding to the specified identification number．


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DBDELETE | - |
|  | - |
| DBDELETEP | - |

## Setting data

■Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Database identification number | 1 to 4 | 16－bit signed binary | ANY16 |
| （s2） | Start device for storing the table name of the database to <br> be deleted． | - | Unicode string | ANYSTRING＿DOUB <br> LE |
| （s3） | Start device for storing the deletion conditions（a maximum <br> of two conditions）． | - | Word | ANY16 |
| （d1） | Completion device（start device that turns on one scan <br> upon completion of instruction） <br> $\bullet($（d1）＋0：Completion signal <br> $\bullet(d 1)+1:$ Error completion signal | - | Bit | ANYBIT＿ARRAY <br> （Number of elements： <br> $2)$ |
| （d2） | Completion status <br> $\bullet$ 0000： Normal completion <br> - Other than 0000：Error completion（error code） | - | Word | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3E미（H）Gㅁ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － |
| （s3） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

Control data

| Operand: (s3) |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by | Data type |
| +0 | Data size of the first condition | Set the data size of the first deletion condition. | 1 to 125 | User | WORD |
| +1 | Data size of the second condition | Set the data size of the second deletion condition. <br> - 0: No condition <br> - Other than 0: Data size | 0 to 125 | User |  |
| +2 to + $\square$ | Deletion condition ${ }^{* 1}$ | Set the deletion conditions. Set as many deletion conditions as there are records to be deleted. <br> The last address in (s3) varies depending on the data type of the determination value. <br> -Field name <br> Specify the field name with a Unicode character string in 32 characters (fixed). For the name less than 32 characters, the character string should be rightjustified and filled with 0000 H to become a 32-character string. <br> ■Condition number <br> Set the number indicating a deletion condition. The condition number is the same as that of (s5) of the DBUPDATE (P) instruction. ( $\longmapsto$ Page 820 <br> DBUPDATE(P)) <br> ■Operator between conditions <br> Set the operator with an adjacent condition. <br> - 0: No operator <br> - 1: AND <br> - 2: OR <br> When one deletion condition is specified, set the inter-condition operator of the first deletion condition to 0 . In this case, the second deletion condition is ignored if specified. <br> Be sure to set the inter-condition operator of the second update condition to 0 . <br> Determination value <br> Set the value used to determine the update condition. The data size of the determination value follows the size of the data type of each field. ( $\longmapsto$ Page 816 DBINSERT(P)) <br> Also when the condition number is 7 or 8 , prepare a determination value area for the data size. | - | User |  |

*1 Set as many field names, condition numbers, inter-condition operators, and determination values as there are update conditions. ( $\longmapsto$ Page 820 DBUPDATE(P))

## Processing details

- These instructions delete the record that meets the condition specified by ( s 3 ) in the table specified by ( s 2 ) in the database corresponding to the identification number specified by ( s 1 ).
- Specify the deletion conditions in (s3). A maximum of two deletion conditions can be specified.
- If this instruction is executed by an interrupt program during execution of another instruction, no processing is performed.
- When the DBSELECT $(\mathrm{P})$ instruction deletes a table recipeA record which matches the conditions in which the condition is field $1=2$, the following occurs.


Table recipeA (after deletion)


- Upon successful completion, the completion signal in the completion device (d1)+0 is turned on and 0 is stored as the completion status in the device (d2).
- The following figure shows the operation of the completion device at completion of the DBDELETE $(\mathrm{P})$ instruction.



## Precautions

In the following cases, the error completion signal in (d1)+1 is turned on and an error code is stored as the completion status in the device (d2).

- This instruction is executed during execution of the database access instruction.
- An identification number outside the range is specified by ( $s 1$ ).
- An identification number of a database which is not open is specified by ( s 1 ).
- The table name to be deleted according to (s2) does not exist.
- The number of characters of the table name specified by (s2) exceeds 32 .
- An out-of-range value is specified in (s3) for the deletion condition.
- Database deletion processing failed.
- An out-of-range value is specified in ( s 3 ) for the inter-condition operator.
- The inter-condition operator specified by (s3)+35 is 1 or 2 , and that in ( s 3 ) +1 is set to 0 .
- The field name to be set in (s3)+2 is left unset.


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2820 H | The area specified by (s) or (d) exceeds the applicable range of the device/label used. |

For the error code stored in the completion status of the operand, refer to the following.
$\rightsquigarrow$ Page 844 Error codes related to database access instructions

## Starting a transaction

## DBTRANS（P）

These instructions declare the start of a transaction in relation to the database corresponding to the specified identification number．


## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DBTRANS | - |
|  | $\boxed{Z}$ |
| DBTRANSP | $\boxed{ }$ |

## Setting data

## Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Database identification number | - | 16－bit signed binary | ANY16 |
| （d1） | Completion device（start device that turns on one scan <br> upon completion of instruction） <br> $\bullet(d 1)+0:$ Completion signal <br> $\cdot(\mathrm{d} 1)+1:$ Error completion signal | - | Bit | ANYBIT＿ARRAY <br> （Number of elements： <br> $2)$ |
| （d2） | Completion status <br> $\cdot$ 0000： Normal completion <br> $\bullet$ Other than 0000：Error completion（error code） | - | Word | ANY16 |
| EN | Execution condition | - | Bit | Bit |
| ENO | Execution result | - | BOOL |  |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロロロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGㅁ，J밈， U3EDI（H）GD | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions declare the start of a transaction in relation to the database corresponding to the identification number specified by ( s ). However, if a data base other than the one specified by ( s ) is open, the DBTRANS instruction cannot start a transaction and is completed with an error.
- Upon normal completion, the completion signal in the completion device ( d 1 ) is turned on and 0 is stored as the completion status in the device (d2).
- The following figure shows the operation of the completion device at completion of the DBTRANS(P) instruction.

- After a transaction is started by the DBTRANS $(P)$ instruction, the $\operatorname{DBCOMMIT}(P)$ instruction needs to be executed to determine the transaction or the DBROLBAK $(P)$ instruction needs to be executed to restore the state before the start of the transaction. (If the DBCLOSE $(\mathrm{P})$ instruction is executed before $\operatorname{DBCOMMIT}(\mathrm{P})$ or DBROLBAK $(\mathrm{P})$ ), the transaction is determined in the status at the execution of the $\operatorname{DBCLOSE}(\mathrm{P})$ instruction.)


## Precautions

In the following cases, the error completion signal in (d1)+1 is turned on and an error code is stored as the completion status in the device (d2).

- The DBTRANS $(P)$ instruction is executed during execution of the database access instruction.
- An identification number outside the range is specified.
- The DBTRANS(P) instruction is executed while the transaction has already been started.
- A data base other than the one specified by (s) is open.


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2820 H | The area specified by (s) or (d) exceeds the applicable range of the device/label used. |

For the error code stored in the completion status of the operand, refer to the following.
$\longmapsto$ Page 844 Error codes related to database access instructions

## Committing a transaction

## DBCOMMIT（P）

These instructions commit the transaction in relation to the database corresponding to the specified identification number．


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DBCOMMIT | - |
|  | $\boxed{T}$ |
| DBCOMMITP | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Database identification number | - | 16－bit signed binary | ANY16 |
| （d1） | Completion device（start device that turns on one scan <br> upon completion of instruction） <br> $\bullet(d 1)+0:$ Completion signal <br> $\cdot(\mathrm{d} 1)+1:$ Error completion signal | - | Bit | ANYBIT＿ARRAY <br> （Number of elements： <br> $2)$ |
| （d2） | Completion status <br> $\cdot$ 0000： Normal completion <br> $\bullet$ Other than 0000：Error completion（error code） | - | Word | ANY16 |
| EN | Execution condition | - | Bit |  |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3E미（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions commit the transaction in relation to the database corresponding to the identification number specified by (s).
- Upon normal completion, the completion signal in the completion device (d1) is turned on and 0 is stored as the completion status in the device (d2).
- The following figure shows the operation of the completion device at completion of the DBCOMMIT(P) instruction.



## Precautions

In the following cases, the error completion signal in (d1)+1 is turned on and an error code is stored as the completion status in the device (d2).

- The DBCOMMIT(P) instruction is executed during execution of the database access instruction.
- An identification number outside the range is specified.
- The DBCOMMIT(P) instruction is executed while no transaction is going on.


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2820 H | The area specified by (s) or (d) exceeds the applicable range of the device/label used. ${ }^{* 1}$ |

[^20]
## Performing a database rollback

## DBROLBAK（P）

These instructions execute the rollback of the database corresponding to the specified identification number．


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DBROLBAK | - |
|  | $\boxed{\square}$ |
| DBROLBAKP | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Database identification number | - | 16－bit signed binary | ANY16 |
| （d1） | Completion device（start device that turns on one scan <br> upon completion of instruction） <br> $\bullet(d 1)+0:$ Completion signal <br> $\cdot(\mathrm{d} 1)+1:$ Error completion signal | - | Bit | ANYBIT＿ARRAY <br> （Number of elements： <br> $2)$ |
| （d2） | Completion status <br> $\cdot$ 0000： Normal completion <br> $\bullet$ Other than 0000：Error completion（error code） | - | Word | ANY16 |
| EN | Execution condition | - | Bit |  |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions execute the rollback in relation to the database corresponding to the identification number specified by (s).
- Upon normal completion, the completion signal in the completion device ( d 1 ) is turned on and 0 is stored as the completion status in the device (d2).
- The following figure shows the operation of the completion device at completion of the DBROLBAK(P) instruction.



## Precautions

In the following cases, the error completion signal in (d1)+1 is turned on and an error code is stored as the completion status in the device (d2).

- The DBROLBAK(P) instruction is executed during execution of the database access instruction.
- An identification number outside the specified range is specified.
- The DBROLBAK $(\mathrm{P})$ instruction is executed while no transaction is going on.


## Operation error

| Error code (SD0) | Description |
| :--- | :--- |
| 2820 H | The area specified by (s) or (d) exceeds the applicable range of the device/label used. |

For the error code stored in the completion status of the operand, refer to the following.
$\geqslant$ Page 844 Error codes related to database access instructions

## Error codes related to database access instructions

The following table lists the error codes that could be stored in the completion status of database access instructions.

| Error code | Error content | Action |
| :---: | :---: | :---: |
| 0103H | CPU internal error | Turn power off and turn it on again or reset the system and check whether the instruction can be executed. |
| 0104H | Another database access instruction is being executed. | Check that another database access instruction is not executed before one database access instruction is completed. |
| 0105H | The specified path is not a database folder. | Check whether the folder specified by the path contains a database file. If not, correct the path properly. |
| 0106H | A file in the database cannot be opened. | Check whether the folder specified by the path contains a database file. If not, correct the path properly. |
| 0107H | An invalid drive number is specified by the database access instruction. | Check whether the specified drive number is 2 . If not, specify drive number 2 correctly. |
| 07D1H | The specified table is not found in the database. | Check that the database contains the specified table. |
| 0940H | An invalid character is included in the table name. | Begin the table name with a non-numeric character and do not include an invalid character. |
| 0941H | The table name is duplicated. | Database table names are not case-sensitive. Check whether same characters are used to set Unicode text file table names by differentiating them with uppercase and lowercase characters. |
| 0942H | The specified table is not found in the database. | Check that the database contains the specified table. |
| 094AH | The database name is not specified. | Specify a correct database name. |
| 094BH | No database is stored in the specified folder. | Check that the folder name is correct and that the folder contains a database-related file. |
| 094CH | The field name contains an invalid character. | Do not include any invalid character in the field name. |
| 094DH | The specified field name is duplicated. | Check for duplicated field names in Unicode text files or instruction arguments. If any, correct it to prevent duplication. |
| 094EH | A non-existent field is specified. | Check that the specified field exists in the table. |
| 0950H | An invalid character is included in the index. | Do not include any invalid character in the index. |
| 0951H | An attempt was made to set empty data (NULL value) in the main key or in the field that has a NOT NULL constraint. | Confirm the table structure. Check whether the field permits NULL. If not, set significant data. |
| 0952H | An attempt was made to add the same value to the field that has a primary key constraint. | Check that the setting value of the record attempted to add to the field that has a primary key constraint is not the same as the record already registered. |
| 0954H | The setting value cannot be automatically converted to the format appropriate for the data type. | Check that the format of the setting value meets the specifications. |
| 0959H | The data type that is checked during importing differs from the data type defined in the table. | Check the Unicode text to verify that the setting value to be imported is appropriate for the data type of the field. |
| 095CH | The data types of compared values are incorrect such as comparing the sizes of character strings. | Correct the values to be compared. |
| 095DH | Converting the data type of the setting value specified in an argument of the database access instruction failed and therefore the setting value cannot be stored in the field. | Check that the setting value meets the data type specifications. |
| 095EH | An attempt was made to assign more than one index to one field. | Check that an attempt has not been made to add an index to the field where the primary key or external key is set. Check also that an attempt has not been made to assign more than one index to one field. |
| 095FH | The index name is duplicated. | Check that each index name is not duplicated. |
| 096CH | The setting value to be added to the field where the external key is specified as a key constraint is not included in the setting values of the reference. | Check the setting values of the reference. |
| 096EH | The record cannot be deleted because it is referenced by another table. | Check whether the setting values of the record to be deleted include the value of a table referenced by an external key. |
| 0970H | An attempt was made to add or delete a record but failed because no table was available for referencing a setting value included in the record. | Check that a referenced table is available. |
| 0971H | An attempt was made to add a setting value not registered in the referenced table. | Check the setting values in the referenced table. |
| 0972H | The data type in the field referenced by an external key is inconsistent. | Check that the data types in both fields match. |
| 0974H | There is no table to be referenced by an external key. | Specify a correct table name. |
| 0976H | There is no field of the table to be referenced by an external key. | Specify a correct field name. |

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7.20 Database Access Instructions

| Error code | Error content | Action |
| :---: | :---: | :---: |
| 0984H | A transaction is already running. | Check that multiple transactions are not executed concurrently. |
| 09D7H | BOOL data is not specified. | Specify BOOL data. |
| 09F9H | A non-existent field is specified to add a record. | Check the table structure. |
| OAOAH | A prohibited field name is specified. | Specify another field name. |
| 0A16H | An attempt was made to add a record with a null value set in the field where the primary key was set. | Check the Unicode text to verify that a null value is not set in the field where the primary key is set. |
| 0A17H | An attempt was made to add a record with the same value included in the field where the primary key was set. | Check the Unicode text to verify that the same value is not set in the field where the primary key is set. |
| OA2BH | An attempt was made to set multiple primary keys. | Check the key setting line in the Unicode text to verify that multiple primary keys are not set. |
| 0A2FH | The primary key does not exist in the referenced table. | Check the Unicode text to verify that the setting value to be imported is appropriate for the data type of the field. |
| OA30H | The setting values in the field where an external key is set do not include a setting value in the field where the primary key is set. | Check that the setting value in the field where an external key is to be added is included in the setting values in the field where the primary key is set. |
| 0A31H | An attempt was made to change or delete a setting value in the field referenced by an external key. | Check the reference relationships among the tables. |
| 0A50H | An attempt was made to assign more than one index name to the same table. | Check the content of the Unicode text to avoid duplication of index names. |
| 0A5DH | The data types of the fields to be connected do not match. | Check that the data types of the fields to be connected match. |
| 0A5FH | A field name is duplicated in the same table. | Check whether a field name is specified more than once in the same table. If so, correct either field name. |
| OAAAH | A negative value is specified in the unsigned integral data type field. | Check that the field data type accepts negative values. |
| 0AB1H | An attempt was made to specify a character string for the integral data type. | Check that no character string is specified for an integral data type field. |
| 0AB9H | An attempt was made to assign an index to a field where no value has been set. | Check that values have been set in all field records where an index is to be specified. |
| OAECH | Table opening failed. | Turn power off and turn it on again or reset the system and check whether the table can be opened. |
| 1000 H | The Unicode text file specified for importing does not exist. | Check whether the file indicated by the folder path is a Unicode text file. If not, specify a correct folder path. |
| 1002H | There is no identification number that can be allocated. | A maximum of four databases can be opened. Adjust the number of databases that will be opened to 4 . |
| 1003H | The identification number of a database which is not open is specified. | Use the $\operatorname{DBOPEN}(\mathrm{P})$ instruction to open the database in advance and obtain an identification number. |
| 1004H | The identification number specified by the instruction is out of range. | Specify the identification number obtained by the DBOPEN(P) instruction. |
| 1007H | The number of fields to be specified by the instruction is not specified. | Set 1 or greater for the number of fields in the argument. |
| 1008H | The number of fields specified by the instruction is out of range. | Set 16 or less for the number of fields in the argument. |
| 1009H | The number of records to be specified by the instruction is not specified. | Set 1 or greater for the number of records in the argument. |
| 100AH | The number of records specified by the instruction is out of range. | Set the number of records within the range in the argument. |
| 100 CH | The Unicode text table or set-value delimitation does not follow the format. | Check that the delimiters in the table or set-value delimitations follow the format. |
| 100DH | An error occurred in the database. | Turn power off and turn it on again or reset the system and check whether the same symptom recurs. |
| 100EH | The condition number specified by the instruction is out of range. | Check that the condition number range is correct. |
| 100FH | When two conditions are specified by the instruction, the inter-condition operator is 0 . | Check that the inter-condition operator in the argument is 1 or 2. |
| 1010H | The maximum number of databases that can be transacted is exceeded. | Check that a transaction is not executed for two or more databases or that another transaction is not executed while one transaction is already running. |
| 1011H | The commit or rollback instruction is executed without starting a transaction. | Check that the commit or rollback instruction is not executed while no transaction is running. |
| 1012H | The value of the size per record used for adding or update is not appropriate. | Check that the data size of each field matches the size of the data to be added or updated. |
| 1013H | A data type which is not supported is specified. | Check the data type of the Unicode text. |
| 1014H | Another database is opened during execution of the transaction. | Do not open a database newly during execution of the transaction. |


| Error code | Error content | Action |
| :---: | :---: | :---: |
| 1015H | A transaction is executed for the multiple databases that were already open. | Close the databases that are not targeted for a transaction. |
| 101BH | The first condition is not set in the relevant argument of each instruction. | Check that the field size set as the first condition in the argument is other than 0 or whether the field name is null. |
| 101 CH | The data size of a field is not included in multiple condition settings. | Check that the data size of the field specified by the condition is 0 . |
| 101DH | A field name is not included in multiple condition settings. | Check that the field name specified by the condition is not null. |
| 101EH | The value of the inter-condition operator is out of range. | Check that the value of the inter-condition operator is correct. |
| 101FH | The value of the inter-condition operator in the second condition setting is out of range. | Check that a value other than 0 is not set in the inter-condition operator of the second condition. |
| 1020H | The setting value is not within the range specified for the data type. | Check that the setting value is within the range. |
| 1021H | An attempt was made to open a database which was already open. | Check that the path corresponding to (s) of the DBOPEN(P) instruction is not specified more than once. |
| 1022H | The DBIMPORT(P) instruction was executed while the database was already open. | Close the database that is already open, and execute the DBIMPORT(P) instruction. |
| 1023H | A table number outside the range is specified. | Check that the table number is within the range. |
| 102EH | The field size specified in the conditions is out of range. | Check that the field size in the conditions is within the range. |
| 1030H | The output size exceeds the setting value. | Adjust the output size. |
| 1031H | The number of outputs exceeds the setting value. | Adjust the number of outputs. |
| 2000 H | The format of the Unicode text file is incorrect. | Check whether the format of the Unicode text file is correct. If not, correct it. |
| 2001H | The key setting of the Unicode text file is incorrect. | Check that an out-of-range value is not set with regard to the key constraint of the Unicode text file. |
| 2002H | The number of characters making up a database name, table name, or field name exceeds the limit. | Check that the database name, table name, or field name in the Unicode text file does not exceed 32 characters. |
| 2003H | The number of tables exceeds the limit. | Check that the number of tables in the Unicode text file does not exceed 16. |
| 2004H | The format of the setting value of the record in the Unicode text file is incorrect. | Check that the format of the setting value in the Unicode text file is correct. |
| 2005H | The maximum number of records that can be imported is exceeded. | Check that the number of records in the text file does not exceed the maximum number ( 100,000 ). |
| 2006H | An invalid character is included in the database name. | Check that an invalid character is not included in the database name in the text file. |
| 2007H | The format of the row index is incorrect. | Check that the format of the row index is correct. |
| 2009H | Failed to read data from the Unicode text file. | Check the status of the SD memory card. |
| 200AH | Failed to write data to the Unicode text file. | Check the status of the SD memory card. |
| 200BH | The number of fields in the Unicode text file exceeds the maximum number. | Check that the number of fields in the Unicode text file does not exceed 16. |
| 200 CH | In the Unicode text file, the number of columns set in the field row does not match that set in another row. | Check the Unicode text file to verify that the number of columns set in the field row (number of fields) matches that set in another row. |
| 200DH | The number of indexes in the Unicode text file exceeds the maximum number. | Check that the number of indexes in the Unicode text file does not exceed the number of table fields. |
| 200EH | The number of characters of the main key name in the Unicode text file, external key name, or index name exceeds the maximum number. | Check whether the numbers of key name and index name characters are each 16 or less. |
| 200FH | The database access instruction cannot be executed because the SD memory card is write-protected. | Clear the write protection of the SD memory card. |
| 2010H | The database access instruction cannot be executed because the SD memory card does not have enough free space. | Secure enough free space of the SD memory card. |
| 2011H | The number of characters making up the path of the database exceeds 128. | Reduce the number of characters making up the path to 128 or less. |
| Others | - The character code of the Unicode text file is incorrect. <br> - The database to be accessed is in an invalid status. | - Check the character code of the Unicode text file. <br> - If the same error occurs even after powering off and on or reset the CPU module, delete the access-target database folder, and create a new database by using the DBIMPORT(P) instruction. |

## 7．21 File Register Operation Instructions

## Switching the file register block number

## RSET（P）

These instructions change the block number of the file register used in the program．

| Ladder | ST |
| :---: | :---: |
|  | $\begin{aligned} & \text { ENO:=RSET(EN,s); } \\ & \text { ENO:=RSETP(EN,s); } \end{aligned}$ |

FBD／LD


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| RSET | - |
|  | $\boxed{ }$ |
| RSETP | - |

Setting data
DDescription，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Block number data to be changed or the device number <br> where the block number data is stored | 0 to 32767 | 16－bit signed binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

－Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathbf{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM} \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロID， U3E미（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- These instructions switch the block number of the file register used in the program to that stored in the device specified by (s). After the block number is changed, all file registers processed by the sequence program are those linked to the new block number.



## Precautions

For the restrictions of the file register, refer to the following.
$\longmapsto$ Page 57 Restrictions on using file registers

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2820 H | The specified file register does not exist. |
| 3405 H | The block number in the device specified by (s) does not exist. |

## Changing the file register file name

## QDRSET（P）

These instructions change the file name of the file register used in the program to that stored in the device specified by（file name）．


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| QDRSET | $\boxed{ }$ |
|  | $\boxed{ }$ |
| QDRSETP | $\boxed{~}$ |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （File name） | String data of drive number file name of the file register to <br> be set，or the start device where the string data is stored <br> Example：＂1：ABC＂ | - | Unicode string | ANYSTRING＿DOUB <br> LE |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDI（H）Gロ | Z | LT，LST， LC | LZ |  | K，H | E | \＄ |  |
| （File name） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － |

## Processing details

- These instructions change the file name of the file register used in the program to that stored in the device specified by (file name). After the file name is changed, all file registers processed by the program are those linked to block number 0 of the new file name. The RSET(P) instruction is used to change the block number.

- For the drive number, 1 or 3 can be specified.
- When drive 1 is specified, the drive works as drive 3 . The use status of the drive is reflected to SD614. It is not reflected to SD604.
- Extension ".QDR" need not be specified for the file name.
- Even if the drive number file name is specified by a parameter, the file name specified by the QDRSET(P) instruction takes precedence.


## Point ${ }^{\rho}$

- If the file name is changed by the QDRSET $(P)$ instruction, operating the CPU module switch from STOP to RUN restores the file name specified by the parameter. To continue to use the file name specified by the QDRSET(P) instruction even when the CPU module switch is changed from STOP to RUN, execute the QDRSET(P) instruction using SM402 that triggers one scan when the switch is changed from STOP to RUN.
- When a file register is specified for the refresh device, do not use the QDRSET(P) instruction to change the file name of the file register.


## Precautions

- Even when the NULL code $(0000 \mathrm{H})$ is specified for the file name, the file name setting is not cleared and no processing is performed.
- For the restrictions of the file register, refer to the following.
$W$ Page 57 Restrictions on using file registers


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2840 H | The drive number/file name specified by (file name) does not exist. |
| 3405 H | Out-of-range data is set to (file name). <br>  <br>  <br> • A drive number other than 1 and 3 is specified. <br> • Only the drive number is specified. |

## 7．22 File Register Read／Write Instructions

## Reading 1－byte data from the file register

## ZRRDB（P）

These instructions read the data from the file register with the specified serial byte number．

| Ladder | ST |
| :---: | :---: |
| $\square-\square$   <br> $-\square$ （s） （d） | $\begin{aligned} & \mathrm{ENO}:=\mathrm{ZRRDB}(\mathrm{EN}, \mathrm{~s}, \mathrm{~d}) ; \\ & \text { ENO:=ZRRDBP(EN,s,d); } \end{aligned}$ |

FBD／LD


## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| ZRRDB | - |
|  | $\boxed{Y}$ |
| ZRRDBP | - |

## Setting data

Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Serial byte number of the file register to be read | 0 to 4294967295 | 32－bit unsigned binary | ANY32 |
| （d） | Start number of the device for storing the data that has <br> been read | - | 16－bit signed binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDI（H）G口 | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- Without recognizing block numbers, these instructions read the data from the file register with the serial byte number specified by ( s ), and store the data in the lower 8 bits of the device specified by (d). 00 H is stored in the upper 8 bits of the device specified by (d).

- The following figure shows the file register numbers corresponding to serial byte numbers.



## Ex.

When 23560 is specified in (s), the data in the lower 8 bits of $Z R 11780$ is read.
(s) $\qquad$
Specifying the read-target area
ZR11780

(d)


Ex.
When 43257 is specified in (s), the data in the upper 8 bits of $Z R 21628$ is read.
(s) $\square$
Specifying the
read-target are read-target area


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2820 H | The specified device number (serial byte number) is out of range. |

## Writing 1－byte data to the file register

## ZRWRB（P）

These instructions write the data in the lower bits of the specified device to the file register with the specified serial byte number．

| Ladder |  | ST |
| :---: | :---: | :---: |
|  | (s2) | $\begin{aligned} & \text { ENO:=ZRWRB(EN,s1,s2); } \\ & \text { ENO:=ZRWRBP(EN,s1,s2); } \end{aligned}$ |
| FBD／LD |  |  |
|  | － |  |

■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| ZRWRB | - |
|  | $\boxed{Y}$ |
| ZRWRBP | - |

Setting data
Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Serial byte number of the file register to be written | 0 to 4294967295 | 32－bit unsigned binary | ANY32 |
| （s2） | Device number where the write data is stored | -32768 to 32767 | 16－bit signed binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDl（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- Without recognizing block numbers, these instructions write the lower 8 -bit data stored in the device specified by (s2) to the file register with the serial byte number specified by ( s 1 ). The instructions ignore upper 8 -bit data in the device specified by (s2).

- The following figure shows the file register numbers corresponding to serial byte numbers.



## Ex.

When 12340 is specified in (s1), data is written to the lower 8 bits of ZR11170.


Ex.
When 43257 is specified in (s1), data is written to the upper 8 bits of ZR21628.


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2820 H | The specified device number (serial byte number) is out of range. |

## 7．23 Indirect Address Read Instructions

## Reading the indirect address

## ADRSET（P）

These instructions read the indirect address of the specified device．


FBD／LD


## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| ADRSET | - |
|  | $\boxed{Y}$ |
| ADRSETP | - |

## Setting data

Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Number of the device from which the indirect address is to <br> be read | - | Device name | ANY＿ELEMENTARY |
| （d） | Start number of the device for storing the indirect address <br> of the device specified by（s） | - | 32－bit signed binary | ANY32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDI（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | － | $\bigcirc$ | － | － | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － |
| （d） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions store the indirect address of the device specified by (s), and stores it in the device specified by (d). The address stored in the devices specified by (d)+0 and (d)+1 is used for indirect addressing of the device in the program.

(1) The address of W100 is stored in D100 and D101.
(2) The data (address of W100) stored in D100 and D101 is specified.
(3) "1234" is written to W100.
- Digit specification of bit device or bit specification of word device in (s) is not permitted
- For the indirect specification of devices, refer to the following.
([]] MELSEC iQ-R CPU Module User's Manual (Application))
- Index specification of labels is not supported. When using a label by specifying it in (d), follow the method below.

| Label | Description |
| :---: | :---: |
| Global label specifying a device | When using it as an indirect address, use the indirect specification of the device specified in the global label. <br> ■Example of structured text language <br> ADRSET(TRUE , intVar, gvAddr); // Read the intVar indirect address to gvAddr. <br> INC (TRUE , @DO); // Use the indirect specification of device D0 specified for gvAddr. |
| Automatic assignment global label/ local label | Transfer the indirect address to the device and use the indirect specification of the transfer destination device. <br> ■Example of structured text language <br> ADRSET(TRUE , intVar , IvAddr); // Read the intVar indirect address to IvAddr. <br> DMOV(TRUE , IvAddr , DO); // Transfer the indirect address, which has been read to IvAddr, to the device. <br> INC (TRUE , @DO); // Use the indirect specification of the device to which the indirect address was <br> transferred. |

## Operation error

There is no operation error.

## 7．24 Clock Instructions

## Reading clock data

## DATERD（P）

These instructions read＂year，month，day，hour，minute，second，and day of week＂from the clock element of the CPU module．

| Ladder | ST |
| :---: | :---: |
|  | $\begin{aligned} & \text { ENO:=DATERD(EN,d); } \\ & \text { ENO:=DATERDP(EN,d); } \end{aligned}$ |

FBD／LD


## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DATERD | - |
|  | - |
| DATERDP |  |

## Setting data

DDescription，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （d） | Start device for storing the clock data that has been read | - | 16－bit signed binary | ANY16＿ARRAY <br> （Number of elements： <br> $7)$ |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ\ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J미， U3EDl（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions read "year, month, day, hour, minute, second, and day of week" from the clock element of the CPU module, and store the read data in binary in the device specified by (d) and later.

|  | (d) | Year | (1980 to 2079 |
| :---: | :---: | :---: | :---: |
|  | (d) +1 | Month | (1 to 12) |
|  | (d) +2 | Day | (1 to 31) |
| Clock elements | (d) +3 | Hour | (0 to 23) |
|  | (d) +4 | Minute | (0 to 59) |
|  | (d) +5 | Second | (0 to 59) |
|  | (d) +6 | Day of week | (0 to 6) |

- "Year" stored in the device specified by (d) is a 4-digit year.
- "Day of week" stored in the device specified by (d)+6 is a number from 0 to 6 corresponding to Sunday to Saturday.

| Day of week | Day | Month | Tuesday | Wednesday | Thursday | Friday | Saturday |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Stored data | 0 | 1 | 2 | 3 | 4 | 5 |  |

- Data is automatically corrected in leap years.


## Operation error

There is no operation error

## Writing clock data

## DATEWR（P）

These instructions write the clock data stored in the specified device number and later to the clock element of the CPU module．

| Ladder | ST |
| :---: | :---: |
|  | $\begin{aligned} & \text { ENO:=DATEWR(EN,s); } \\ & \text { ENO:=DATEWRP(EN,s); } \end{aligned}$ |
| FBD／LD |  |
|  |  |

Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DATEWR | $\boxed{ }$ |
| DATEWRP | $\uparrow$ |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Start device where the clock data to be written to the clock <br> element is stored | Refer to＂Processing details＂． | 16－bit signed binary | ANY16＿ARRAY <br> （Number of elements： <br> 7） |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3E미（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions write the clock data stored in the device number specified by ( $s$ ) and later to the clock element of the CPU module.
(s)

Year
s)+1 Month
(s)+2 Day
$(\mathrm{s})+3$ Hour $\square \quad$ Clock elements
(s)+4 Minute
(s)+5 Second
(s)+6 Day of week

- Set each data in binary.
- Set the year data in the range from 1980 to 2079 in the device specified by (s).
- Set the month data in the range from 1 to 12 in the device specified by $(\mathrm{s})+1$.
- Set the day data in the range from 1 to 31 in the device specified by ( s ) +2 .
- Set the hour data in the range from 0 to 23 in the device specified by (s)+3. (Set in 24 -hour format.)
- Set the minute data in the range from 0 to 59 in the device specified by (s)+4.
- Set the second data in the range from 0 to 59 in the device specified by (s)+5.
- Set the day of week in the range from 0 to 6 corresponding to Sunday to Saturday in the device specified by ( s )+6.

| Day of week | Day | Month | Tuesday | Wednesday | Thursday | Friday | Saturday |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Stored data | 0 | 1 | 2 | 3 | 4 | 5 |  |

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3405 H | The data in the device specified $(\mathrm{s})$ is out of range. |

When clock data is changed, "clock setting" (event code: 24000) is saved to the event history. That is, "clock setting" is saved to the event history when this instruction is executed.

## Adding clock data

## DATE $+(P)$

These instructions add time data．


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DATE + | - |
|  | $\boxed{Z}$ |
| DATE + P | - |

## Setting data

■Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Start device where the augend clock data is stored | Refer to＂Processing <br> details＂． | 16－bit signed binary <br> ANY16＿ARRAY <br> （Number of elements： <br> 3） |  |
| （s2） | Start device where the addend time（clock）data is stored | Refer to＂Processing <br> details＂． | 16－bit signed binary | ANY16＿ARRAY <br> （Number of elements： <br> 3） |
| （d） | Start device for storing the addition result time（clock）data | - | ANY16＿ARRAY <br> （Number of elements： <br> 3） |  |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UवIGロ，JロIロ， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions add the time data in the device specified by (s2) to the time data in the device specified by (s1), and store the addition result in the device number specified by (d) and later.
(s1)

|  |  |
| :--- | :--- |
| s1) | Hour |
| $(\mathrm{s} 1)+1$ | Minute |
|  | s1) |
|  | Second |

Data range

| (0 to 23) | (s2) | Hour |
| :---: | :---: | :---: |
| (0 to 59) | (s2)+1 | Minute |
| (0 to 59) | (s2)+2 | Second |

Data range (0 to 23$)$
$(0$ to 59$)$
$\square$

| (d) | Hour | (0 to 23) |
| :---: | :---: | :---: |
| (d) +1 | Minute | (0 to 59) |
| (d) +2 | Second | (0 to 59) |

Ex.
7:48:10 is added to 6:32:40.
(s1)

| $)$ | 6 |
| :---: | ---: |
|  | 32 |
|  | 40 |

$+$

|  | (s2) |
| :--- | ---: |
|  | 7 |
|  | $72)+1$ |
| $(\mathrm{~s} 2)+2$ | 48 |
|  | 10 |

$\qquad$
(d)
(d) +1

| 14 |
| :--- |
| 20 |
| 50 |

- If the time obtained as the result of addition exceeds 24 hours, 24 hours are subtracted from the resultant time to produce the operation result. For example, when $20: 20: 20$ is added to $14: 20: 30$, the operation result is 10:40:50 rather than 34:40:50.

| $(s 1)$ | 14 |
| :--- | :--- |
| $(s 1)+1$ | 20 |
| $(s 1)+2$ | 30 |

$+$

|  | $(s 2)$ |
| :--- | :--- |
|  | 20 |
|  | $(s 2)+1$ |
| $(s 2)+2$ | 20 |
|  |  |

$\qquad$

| (d) | 10 |
| :---: | :---: |
| (d) +1 | 40 |
| (d) +2 | 50 |

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3405 H | The data in the device specified by (s1) or (s2) is out of range. |

## Subtracting clock data

## DATE－（P）

These instructions subtract time data．


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DATE－ | - |
| DATE－P | $\boxed{-}$ |

## Setting data

■Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Start device where minuend clock data is stored | Refer to＂Processing <br> details＂． | 16－bit signed binary <br> ANY16＿ARRAY <br> （Number of elements： <br> 3） |  |
| （s2） | Start device where the subtrahend time（clock）data is stored | Refer to＂Processing <br> details＂． | 16－bit signed binary | ANY16＿ARRAY <br> （Number of elements： <br> 3） |
| （d） | Start device for storing the subtraction result time（clock）data | - | - | ANY16＿ARRAY <br> （Number of elements： <br> 3） |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result |  | BOOL |  |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions subtract the time data in the device specified by ( s 2 ) from the time data in the device specified by ( s 1 ), and store the subtraction result in the device number specified by (d) and later.
(s1)

|  | Hour |
| :--- | :--- |
|  | Minute |
|  | Second |

Data range
(0 to 23 )
$(0$ to 59$) \quad-$
$(0$ to 59$)$

| (s2) | Hour |
| :---: | :---: |
| (s2)+1 | Minute |
| (s2)+2 | Second |

Data range $(0$ to 23$)$
$(0$ to 59$)$
$(0$ )
$\square$

| (d) | Hour | (0 to 23) |
| :---: | :---: | :---: |
| (d) +1 | Minute | (0 to 59) |
| (d) +2 | Second | (0 to 59) |

Ex.
3:50:10 is subtracted from 10:40:20.
(s1)

| +10 |  |
| :---: | :---: |
|  | 40 |
| 20 |  |

(s2)
(s2)+1

| 3 |
| ---: |
| 50 |
| 10 |

$\qquad$
(d)
(d) +1
(d) +2

| 6 |
| ---: |
| 50 |
| 10 |

- If the time obtained as the result of subtraction becomes a negative value, 24 hours are added to the resultant time to produce the operation result. For example, when 10:42:12 is subtracted from 4:50:32, the operation result is 18:8:20 rather than -6:8:20
(s1)

| +1 | 4 |
| ---: | ---: |
|  | 50 |
| 32 |  |


|  | (s2) |
| :--- | :--- |
|  | 10 |
|  | $(s 2)+1$ |
| $(s 2)+2$ | 42 |
|  |  |

$\qquad$
(d)

| d) | 18 |
| :--- | ---: |
| (d) +1 | 8 |
| (d) +2 | 20 |

Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3405 H | The data in the device specified by (s1) or (s2) is out of range. |

## Converting time data from hour／minute／second to second

## TIME2SEC（P）

These instructions convert time data to seconds．

| Ladder | ST |  |
| :--- | :--- | :--- |
| $\|$$-\square$ （s） （d） <br>   ENO：＝TIME2SEC（EN，s，d）； |  |  |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| TIME2SEC | - |
|  | $\boxed{ }$ |
| TIME2SECP | - |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Start device where the time data to be converted is stored | - | 16－bit signed binary | ANY16＿ARRAY <br> （Number of elements： <br> 3） |
| （d） | Start device for storing the clock data after conversion | - | 32－bit signed binary | ANY32 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロID， U3Eपl（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions convert the time data stored in the device number specified by（s）and later to seconds，and store the operation result in the device specified by（d）．

| $(\mathrm{s})$ | Hour |
| :--- | :--- |
| $(\mathrm{s})+1$ | Minute |
| $(\mathrm{s})+2$ | Second |

Data range
（0 to 23） $\qquad$ （d）+1
（d）
（0 to 59）


Ex．
4：29：31 is specified in（s）．

$\longrightarrow$
（d）+1
（d） 31

| Error code (SDO) | Description |
| :--- | :--- |
| 3405 H | The data in the device specified (s) is out of range. |

## Converting time data from second to hour／minute／second

## SEC2TIME（P）

These instructions convert seconds data to hour／minute／second data．

| Ladder | ST |
| :---: | :---: |
| $\begin{array}{\|l\|l\|l\|} \hline \square--\square & \text { (s) } & \text { (d) } \\ \hline \end{array}$ | $\begin{aligned} & \text { ENO:=SEC2TIME(EN,s,d); } \\ & \text { ENO:=SEC2TIMEP(EN,s,d); } \end{aligned}$ |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| SEC2TIME | - |
|  | $\boxed{ }$ |
| SEC2TIMEP | - |

## Setting data

■Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Start device where the time data to be converted is stored | 0 to 86399 | 32－bit signed binary | ANY32 |
| （d） | Start device for storing the clock data after conversion | - | 16－bit signed binary | ANY16＿ARRAY <br> （Number of elements： <br> $3)$ |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J미， U3Eपl（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions convert the seconds data stored in the device number specified by（s）and later to hour／minute／second data，and store the operation result in the device specified by（d）and later．

| （s）＋1（s） | Data range |  |  | Data range <br> （0 to 23） |
| :---: | :---: | :---: | :---: | :---: |
|  | （0 to 86399） | （d）$(\mathrm{d})+1$ | Hour |  |
| Second |  |  | Minute | （0 to 59） |
|  |  | （d）＋2 | Second | （0 to 59） |

## Ex．

45325 seconds are specified in（s）．


$\qquad$ |  | （d） |
| :--- | :--- |
|  | 12 |
|  | 12 |
| （d）+1 | 35 |
|  |  |
|  |  |

## Comparing date data

## LDDTロ，ANDDTロ，ORDTロ

These instructions compare the specified date data，or compare the date data with the current date．


FBD／LD

（ $\square$ is replaced by a combination of LDDT＿， ANDDT＿＿$_{-}$，or ORDT＿and EQ，NE，GT，LE，LT，or GE．）

## EExecution condition

| Instruction | Execution condition |
| :--- | :--- |
| LDDTロ，ANDDTロ，ORDTロ | Every scan |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| $(\mathrm{s} 1)$ | Start device where the data to be compared is stored | - | 16 －bit signed binary | ANY＿DT |
| $(\mathrm{s} 2)$ | Start device where the data to be compared is stored | - | 16 －bit signed binary | ANY＿DT |
| $(\mathrm{s} 3)$ | Comparison target setting value or the number of <br> comparison target data | 0001 H to $0007 \mathrm{H}, 8001 \mathrm{H}$ to <br> 8007 H | 16－bit signed binary | ANY16 |
| EN | Execution condition | - | Bit |  |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDl（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s3） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- These instructions compare the date data in the devices specified by ( s 1 ) and ( s 2 ), or compare the date data in the device specified by ( s 1 ) with the current date. Set the comparison target by ( s 3 ).
- Comparing two specified date data

These instructions compare the date data in the device specified by (s1) with the date data in the device specified by (s2) in accordance with the conditions set by (s3). (Devices are used as a normally open contact.)

|  |  | Data range |  |  |  | Data range |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| (s1) | Year | (1980 to 2079) (1 to 12) | Relational operator | (s2) | Year | (1980 to 2079) |  |
| (s1) +1 | Month |  |  |  | Month | (1 to 12) | Comparison |
| (s1)+2 | Day | (1 to 31) |  | (s2)+2 | Day | (1 to 31) |  |

- Comparing the specified date data with the current date

These instructions compare the date data in the device specified by ( $s 1$ ) with the current date data in accordance with the conditions set by ( s 3 ). (Devices are used as a normally open contact.) The date data in the device specified by (s2) is regarded as dummy data and ignored.


- Set each data in binary.
- Set the year data in the range from 1980 to 2079 in the devices specified by ( $s 1$ ) and (s2).
- Set the month data in the range from 1 to 12 in the devices specified by ( s 1 ) +1 and ( s 2 ) +1 .
- Set the date data in the range from 1 to 31 in the devices specified by $(\mathrm{s} 1)+2$ and ( s 2 ) +2 .
- Set the following in ( s 3 ) as comparison target setting values. The following shows the bit configuration of ( s 3 ).

(1) Set "day" as comparison target.
(2) Set "month" as comparison target.
(3) Set "year" as comparison target.

4) Set 0 . If a value other than 0 is set, the operation result will be noncontinuity.
(5) When 1 is set to the 15 bit, the data in the device specified by ( $s 1$ ) is compared with the current date in accordance with the conditions set in the 0 to 2 bits.

- When 0 is set to the 0 to 2 bits, the date data are not compared. When 1 is set, the entire date data (year, month, and day) are compared.
- When 0 is set to the 15 bit, the data in the device specified by ( s 1 ) and the date data in the device specified by ( s 2 ) are compared. When 1 is set, the data in the device specified by ( $s 1$ ) is compared with the current date. The date data in the device specified by ( s 2 ) is ignored.
- The following table lists processing details of each bit.

| (s3) value when <br> comparing two specified <br> date data | (s3) value when <br> comparing the specified <br> date data with the <br> current date | Comparison <br> target | Description |
| :--- | :--- | :--- | :--- |
| 0001 H | 8001 H | Day | Only data in the device specified by (s1)+2 is compared. |
| 0002 H | 8002 H | Month | Only data in the device specified by (s1)+1 is compared. |
| 0003 H | 8003 H | Month, day | Data in the device areas specified by (s1)+1 and (s1)+2 are compared. |
| 0004 H | 8004 H | Year | Only data in the device specified by (s1) is compared. |
| 0005 H | 8005 H | Year, day | Data in the device areas specified by (s1) and (s1)+2 are compared. |
| 0006 H | 8006 H | Data in the device areas specified by (s1) and (s1)+1 are compared. |  |
| 0007 H | 8007 H | Year, month, day | The entire date data in the device areas specified by (s1), (s1)+1, and (s1)+2 <br> are compared. |
| Other than 0001 H to $0007 \mathrm{H}, 8001 \mathrm{H}$ to 8007 H | The entire date data in the device areas specified by (s1), (s1)+1, and (s1)+2 <br> are not compared. (The operation result will be non-continuity.) |  |  |

- If the comparison target data in the device are not recognized as date data, SM709 turns on after the instruction is executed and the operation result will be non-continuity. Even if the data are not recognized as date data, SM709 dose not turn on if the data are within the setting range. If the device areas specified by ( s 1 ) to ( s 1 ) +2 or ( s 2 ) to ( s 2 ) +2 exceed the setting area in the device/label memory, SM709 turns on after the instruction is executed and the operation result will be non-continuity as well. Once SM709 turns on, the on state is held until the CPU module is powered off or reset. Turn off SM 709 as needed.
- The following table lists the comparison operation results of each instruction.

| Instruction symbol (ladder, FBD/LD) | Condition | Result |
| :--- | :--- | :--- |
| DT $=$, EQ | $(s 1)=(s 2)$ | Continuity state (ENO is on.) |
| DT<>, NE | $(s 1) \neq(s 2)$ |  |
| DT>, GT | $(s 1)>(s 2)$ |  |
| DT<=, LE | $(s 1) \leq(s 2)$ |  |
| DT<, LT | $(s 1)<(s 2)$ | Non-continuity state (ENO is off.) |
| DT>=, GE | $(s 1) \geq(s 2)$ |  |
| DT $=$, EQ | $(s 1) \neq(s 2)$ |  |
| DT<>, NE | $(s 1)=(s 2)$ |  |
| DT>, GT | $(s 1) \leq(s 2)$ |  |
| DT<=, LE | $(s 1)>(s 2)$ |  |
| DT<, LT | $(s 1)>\geq(s 2)$ | $(s 1)<(s 2)$ |
| DT>=, GE |  |  |

## Ex.

The date data $\mathrm{A}, \mathrm{B}$, and C are compared.


- The following table lists the comparison operation results between $A, B$, and $C$. Even when the data are compared under the same conditions, the results differ depending on the comparison target data.

| Comparison target data | ${ }^{\text {Condition }}{ }^{* 1}$ |  |  |
| :--- | :--- | :--- | :--- |
|  | $\mathbf{A}<\mathbf{B}$ | $\mathbf{B}<\mathbf{C}$ | A<C |
| Day | Continuity | Non-continuity | Non-continuity |
| Month | Non-continuity | Continuity | Non-continuity |
| Month, day | Non-continuity | Continuity | Non-continuity |
| Year | Continuity | Continuity | Continuity |
| Year, day | Continuity | Continuity | Continuity |
| Year, month | Continuity | Continuity | Continuity |
| Year, month, day | Continuity | Continuity | Continuity |
| None | Non-continuity | Non-continuity | Non-continuity |

*1 In FBD/LD, ENO ON indicates continuity and ENO OFF indicates non-continuity.

- Even though the specified date does not exist, the comparison operation is performed in accordance with the conditions in the following table as long as the date data are within the valid range.
- Date A: 2006/02/30 (Even though the date does not exist, this date can be set.)
- Date B: 2007/03/29
- Date A: 2008/02/31 (Even though the date does not exist, this date can be set.)

| Comparison target data | Condition ${ }^{* 2}$ |  |  |
| :--- | :--- | :--- | :--- |
|  | $\mathbf{A}<\mathbf{B}$ | $\mathbf{B}<\mathbf{C}$ | A<C |
| Day | Non-continuity | Non-continuity | Continuity |
| Month | Non-continuity | Non-continuity | Non-continuity |
| Month, day | Continuity | Non-continuity | Continuity |
| Year | Continuity | Continuity | Continuity |
| Year, day | Continuity | Continuity | Continuity |
| Year, month | Continuity | Continuity | Continuity |
| Year, month, day | Continuity | Continuity | Continuity |
| None | Non-continuity | Non-continuity | Non-continuity |

*2 In FBD/LD, ENO ON indicates continuity and ENO OFF indicates non-continuity.

- If the LDDTD instruction is used in the program written in FBD/LD, always set EN to TRUE.
- If the ORDT_ $\square$ instruction is used in the program written in FBD/LD and EN is set to TRUE, ENO turns on. EN will not be an execution condition.


## Operation error

There is no operation error

## Comparing time data

## LDTMD，ANDTMD，ORTMD

These instructions compare the specified time data，or compare the specified time data with the current time．


FBD／LD

（ $\square$ is replaced by a combination of LDTM＿，ANDTM＿，or ORTM＿and EQ，NE，GT，LE，LT，or GE．）

## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| LDTMD，ANDTMD，ORTMD | Every scan |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Start device where the data to be compared is stored | - | 16－bit signed binary | ANY＿TM |
| （s2） | Start device where the data to be compared is stored | - | 16－bit signed binary | ANY＿TM |
| （s3） | Comparison target setting value or the number of <br> comparison target data | 0001 H to 0007 H, <br> 8001 H to 8007 H | 16－bit signed binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDl（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s3） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- These instructions compare the time data in the device specified by (s1) and (s2), or compare the time data in the device specified by ( s 1 ) with the current time. Set the comparison target by ( s 3 ).
- Comparing the specified two sets of time data

These instructions compare the time data in the device specified by (s1) with the date data in the device specified by (s2) in accordance with the conditions set by (s3). (Devices are used as a normally open contact.)

|  |  | Data range |  |  |  | Data range |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| (s1) | Hour | (0 to 23)(0 to 59) | Relational operator | (s2) | Hour | (0 to 23) |  |  |
| (s1) +1 | Minute |  |  | (s2) +1 | Minute | (0 to 59) | $\Rightarrow$ | Comparison |
| (s1)+2 | Second | (0 to 59) |  | (s2)+2 | Second | (0 to 59) |  |  |

- Comparing the specified time data with the current time data

These instructions compare the time data in the device specified by ( $s 1$ ) with the current time data in accordance with the conditions set by ( $s 3$ ). (Devices are used as a normally open contact.) The time data in the device specified by ( s 2 ) is regarded as dummy data and ignored.

| (s1) | Hour | Data range <br> (0 to 23) |  | Data range |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  |  |  |
| (s1)+1 | Minute | (0 to 59) | Relational | Current time | $\rightarrow$ | Comparison |
| (s1)+2 | Second | (0 to 59) |  |  |  |  |

- Set each data in binary.
- Set the hour data in the range from 0 to 23 in 24 -hour format in the devices specified by ( s 1 ) and ( s 2 ).
- Set the minute data in the range from 0 to 59 in the devices specified by ( s 1 ) +1 and ( s 2 2)+1.
- Set the second data in the range from 0 to 59 in the devices specified by ( s 1 )+2 and ( s 2 ) +2 .
- Set the following in ( s 3 ) as comparison target setting values. The following shows the bit configuration of ( s 3 ).

(1) Set "second" as comparison target.
(2) Set "minute" as comparison target.
(3) Set "hour" as comparison target.
(4) Set 0 . If a value other than 0 is set, the operation result will be noncontinuity.
(5) When 1 is set to the 15 bit, the data in the device specified by ( $s 1$ ) is compared with the current time in accordance with the conditions set in the 0 to 2 bits.
- When 0 is set to bits 0 to 2 , the time data are not compared. When 1 is set, the comparison target time data (hour, minute, second) are compared.
- When 0 is set to bit 15 , the data in the device specified by ( $s 1$ ) and the time data in the device specified by ( s 2 ) are compared. When 1 is set, the time data in the device specified by ( s 1 ) is compared with the current time. The time data in the device specified by ( s 2 ) is ignored.
- The following table lists processing details of each bit.

| (s3) value when <br> comparing two specified <br> time data | (s3) value when <br> comparing with current <br> time data | Comparison <br> target time | Description |
| :--- | :--- | :--- | :--- |
| 0001 H | 8001 H | Second | Only data in the device specified by (s1)+2 is compared. |
| 0002 H | 8002 H | Minute | Only data in the device specified by (s1)+1 is compared. |
| 0003 H | 8003 H | Minute, second | Data in the device areas specified by (s1)+1 and (s1)+2 are compared. |
| 0004 H | 8004 H | Hour | Only data in the device specified by (s1) is compared. |
| 0005 H | 8005 H | Hour, second | Data in the device areas specified by (s1) and (s1)+2 are compared. |
| 0006 H | 8006 H | Hour, minute, <br> second | The entire date data in the device areas specified by (s1), (s1)+1, and (s1)+2 <br> are compared. |
| 0007 H | 8007 H | None | The entire date data in the device areas specified by (s1), (s1)+1, and (s1)+2 <br> are not compared. (The operation result will be non-continuity.) |
| Other than 0001 H to $0007 \mathrm{H}, 8001 \mathrm{H}$ to 8007 H | Data in the device areas specified by (s1) and (s1)+1 are compared. |  |  |

- If the comparison target data in the device are not recognized as time data, SM709 turns on after the instruction is executed and the operation result will be non-continuity (ENO OFF). If the device areas specified by (s1) to (s1)+2 or (s2) to (s2)+2 exceed the setting area in the device/label memory, SM709 turns on after the instruction is executed and the operation result will be non-continuity (ENO OFF) as well. Once SM709 turns on, the on state is held until the CPU module is powered off or reset. Turn off SM 709 as needed.
- The following table lists the comparison operation results of each instruction.

| Instruction symbol (ladder, FBD/LD) | Condition | Result |
| :---: | :---: | :---: |
| TM $=$, EQ | (s1)=(s2) | Continuity state (ENO is on.) |
| TM<>, NE | (s1) $=(\mathrm{s} 2)$ |  |
| TM>, GT | (s1)>(s2) |  |
| TM<=, LE | (s1) $\leq$ ( s 2$)$ |  |
| TM<, LT | (s1)<(s2) |  |
| TM>=, GE | (s1) $\geq$ (s2) |  |
| TM=, EQ | (s1) $=$ (s2) | Non-continuity state (ENO is off.) |
| TM<>, NE | (s1)=(s2) |  |
| TM>, GT | (s1) $\leq$ ( s 2$)$ |  |
| TM<=, LE | (s1)>(s2) |  |
| TM<, LT | (s1) $\geq$ (s2) |  |
| TM>=, GE | (s1)<(s2) |  |

## Ex.

The time data $\mathrm{A}, \mathrm{B}$, and C are compared.


- The following table lists the comparison operation results between A, B, and C. Even when the data are compared under the same conditions, the results differ depending on the comparison target data.

| Comparison target data | Condition*1 |  |  |
| :--- | :--- | :--- | :--- |
|  | A<B | B<C | A<C |
| Second | Continuity | Non-continuity | Non-continuity |
| Minute | Non-continuity | Continuity | Non-continuity |
| Minute, second | Non-continuity | Continuity | Non-continuity |
| Hour | Continuity | Continuity | Continuity |
| Hour, second | Continuity | Continuity | Continuity |
| Hour, minute | Continuity | Continuity | Continuity |
| Hour, minute, second | Continuity | Continuity | Continuity |
| None | Non-continuity | Non-continuity | Non-continuity |

*1 In FBD/LD, ENO ON indicates continuity and ENO OFF indicates non-continuity.

- If the LDTMD instruction is used in the program written in FBD/LD, always set EN to TRUE.
- If the ORTM_ם instruction is used in the program written in FBD/LD and EN is set to TRUE, ENO turns on. EN will not be an execution condition.


## Operation error

There is no operation error.

## Reading expansion clock data

## S（P）．DATERD

These instructions read clock data including millisecond from the clock elements in the CPU module．

| Ladder |  | ST |
| :---: | :---: | :---: |
|  |  | $\begin{aligned} & \text { ENO:=S_DATERD(EN,d); } \\ & \text { ENO:=SP_DATERD(EN,d); } \end{aligned}$ |
| FBD／LD |  |  |
|  | － |  |

Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．DATERD | - |
|  | $\boxed{ }$ |
| SP．DATERD | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （d） | Start device for storing the clock data that has been read | - | 16－bit signed binary | ANY16＿ARRAY <br> （Number of elements： <br> 8） |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）GD | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

－These instructions read＂year，month，day，hour，minute，second，day of week，and millisecond＂from the clock element of the CPU module，and store the read data in binary in the device specified by（d）and later．

|  | （d） | Year | （1980 to 2079） <br> （1 to 12） |
| :---: | :---: | :---: | :---: |
|  | （d）+1 | Month |  |
|  | （d）+2 | Day | （1 to 31） |
| Clock elements | （d）+3 | Hour | （0 to 23） |
|  | （d）+4 | Minute | （0 to 59） |
|  | （d）+5 | Second | （0 to 59） |
|  | （d）＋6 | Day of week | （0 to 6） |
|  | （d）＋7 | Millisecond | （0 to 999） |

－＂Year＂stored in the device specified by（d）is a 4－digit year．
－＂Day of week＂stored in the device specified by（d）＋6 is a number from 0 to 6 corresponding to Sunday to Saturday．

| Day of week | Day | Month | Tuesday | Wednesday | Thursday | Friday | Saturday |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Stored data | 0 | 1 | 2 | 3 | 4 | 5 | 6 |

－Data is automatically corrected in leap years．

## Precautions

- These instructions read clock data and store it in the device even when incorrect click data is set in the CPU module. (Example: February 30) When setting clock data with the DATEWR(P) instruction or engineering tool, be careful not to set incorrect clock data.
- When millisecond clock data is read, the maximum error is 2 ms . (This error means the difference between the data stored in clock elements in the CPU module and the data read by the S(P).DATERD instruction.)


## Operation error

There is no operation error.

## Adding expansion clock data

## S（P）．DATE +

These instructions add time data．


FBD／LD

（ $\square$ is replaced by either of the following：S＿DATEPLUS，SP＿DATEPLUS．）

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．DATE + | - |
|  | - |
| SP．DATE + |  |

Setting data
■Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Start device where the augend clock data is stored | Refer to＂Processing details＂． | 16－bit signed binary | ANY16＿ARRAY <br> （Number of elements： <br> $5)$ |
| （s2） | Start device where the addend time（clock）data is stored | Refer to＂Processing details＂． | 16－bit signed binary | ANY16＿ARRAY <br> （Number of elements： <br> $5)$ |
| （d） | Start device for storing the addition result time（clock）data | - | Bit | ANY16＿ARRAY <br> （Number of elements： <br> $5)$ |
| EN | Execution condition |  | - | Bit |
| ENO | Execution result | - | BOOL |  |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions add the time data in the device specified by (s2) to the time data in the device specified by (s1), and store the addition result in the device number specified by (d) and later.

|  |  | Data range |  |  | Data range |  |  | Data range <br> (0 to 23) |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| (s1) | Hour | (0 to 23) | (s2) | Hour | (0 to 23) | (d) | Hour |  |
| (s1)+1 | Minute | (0 to 59) | (s2)+1 | Minute | (0 to 59) | (d) +1 | Minute | (0 to 59) |
| (s1)+2 | Second | (0 to 59) | (s2)+2 | Second | (0 to 59) | (d) +2 | Second | (0 to 59) |
| (s1)+3 | - |  | (s2)+3 | - |  | (d) +3 | - |  |
| (s1)+4 | Millisecond | (0 to 999) | (s2)+4 | Millisecond | (0 to 999) | (d) +4 | Millisecond | (0 to 999) |

## Ex.

7:48:10:500 is added to 6:32:40:875.

| $(s 1)$ | 6 |
| :--- | :---: |
| $(s 1)+1$ | 32 |
| $(s 1)+2$ | 40 |
| $(s 1)+3$ | - |
| $(s 1)+4$ | 875 |
|  |  |

$+$

|  | (s2) |
| :--- | :---: |
| (s2)+1 | 7 |
| $(\mathrm{~s} 2)+2$ | 48 |
| $(\mathrm{~s} 2)+3$ | - |
| $(\mathrm{s} 2)+4$ | 500 |
|  |  |


| (d) | 14 |
| :---: | :---: |
| (d) +1 | 20 |
| (d) +2 | 51 |
| (d) +3 | - |
| (d) +4 | 375 |

- If the time obtained as the result of addition exceeds 24 hours, 24 hours are subtracted from the resultant time to produce the operation result. For example, when 20:20:20:500 is added to 14:20:30:875, the operation result is 10:40:51:375 rather than 34:40:51:375.
(s1)

| $(s 1)$ | 14 |
| :--- | :---: |
| $(s 1)+1$ | 20 |
| $(s 1)+2$ | 30 |
| $(s 1)+3$ | - |
| $(s 1)+4$ | 875 |
|  |  |

$+$

|  | $(s 2)$ |
| :--- | :---: |
| $(\mathrm{s} 2)+1$ | 20 |
| $(\mathrm{~s} 2)+2$ | 20 |
| $(\mathrm{~s} 2)+3$ | - |
| $(\mathrm{s} 2)+4$ | 500 |
|  |  |


| (d) | 10 |
| :---: | :---: |
| (d) +1 | 40 |
| (d) +2 | 51 |
| (d) +3 | - |
| (d) +4 | 375 |

## Point ${ }^{\circ}$

- Devices (s1)+3, (s2)+3, and (d)+3 are not used for operation.
- The clock data that has been read by the $S(P)$.DATERD instruction can be added without conversion.

When clock data is read by the $S(P)$.DATERD instruction, "day of week" is inserted between "second" and "millisecond".
If the $S(P) D A T E+$ instruction is used to read clock data, the data can be directly used for addition since it does not perform calculation for the day of week.


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3405 H | The data in the device specified by (s1) or (s2) is out of range. |

## Subtracting expansion clock data

## S（P）．DATE－

These instructions subtract time data．


FBD／LD

（ $\square$ is replaced by either of the following：S＿DATEMINUS，SP＿DATEMINUS．）

## －Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．DATE－ | - |
|  | - |
| SP．DATE－ | $\boxed{ }$ |

Setting data
■Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Start device where minuend clock data is stored | Refer to＂Processing details＂． | 16－bit signed binary | ANY16＿ARRAY <br> （Number of elements： <br> $5)$ |
| （s2） | Start device where the subtrahend time（clock）data is <br> stored | Refer to＂Processing details＂． | 16－bit signed binary | ANY16＿ARRAY <br> （Number of elements： <br> $5)$ |
| （d） | Start device for storing the subtraction result time（clock） <br> data | - | 16－bit signed binary | ANY16＿ARRAY <br> （Number of elements： <br> $5)$ |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions subtract the time data in the device specified by ( s 2 ) from the time data in the device specified by ( s 1 ), and store the subtraction result in the device number specified by (d) and later.

| (s1) |  | Data range |  |  | Data range |  |  | Data range (0 to 23) |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | Hour | (0 to 23) | (s2) | Hour | (0 to 23) | (d) | Hour |  |
| (s1)+1 | Minute | (0 to 59) | (s2)+1 | Minute | (0 to 59) | (d) +1 | Minute | (0 to 59) |
| (s1)+2 | Second | (0 to 59) | (s2)+2 | Second | (0 to 59) | (d) +2 | Second | (0 to 59) |
| (s1)+3 | - |  | (s2)+3 | - |  | (d) +3 | - |  |
| (s1)+4 | Millisecond | (0 to 999) | (s2) +4 | Millisecond | (0 to 999) | (d) +4 | Millisecond | (0 to 999) |

## Ex.

3:50:10:500 is subtracted from 10:40:20:875.

| $(s 1)$ | 10 |
| :--- | :---: |
| $(s 1)+1$ | 40 |
| $(s 1)+2$ | 20 |
| $(s 1)+3$ | - |
| $(s 1)+4$ | 875 |


| $(s 2)$ | 3 |
| :--- | :---: |
| $(s 2)+1$ | 50 |
| $(\mathrm{~s} 2)+2$ | 10 |
| $(\mathrm{~s} 2)+3$ | - |
| $(\mathrm{s} 2)+4$ | 500 |
|  |  |


| (d) | 6 |
| :--- | :---: |
| (d)+1 | 50 |
| (d)+2 | 10 |
| (d)+3 | - |
| (d)+4 | 375 |
|  |  |

- If the time obtained as the result of subtraction becomes a negative value, 24 hours are added to the resultant time to produce the operation result. For example, when 10:42:12:500 is subtracted from 4:50:32:875, the operation result is 18:8:20:375 rather than -6:8:20:375.

| $(s 1)$ | 4 |
| :--- | :---: |
| $(\mathrm{~s} 1)+1$ | 50 |
| $(\mathrm{~s} 1)+2$ | 32 |
| $(\mathrm{~s} 1)+3$ | - |
| $(\mathrm{s} 1)+4$ | 875 |
|  |  |


| $(s 2)$ | 10 |
| :--- | :---: |
| $(s 2)+1$ | 42 |
| $(s 2)+2$ | 12 |
| $(s 2)+3$ | - |
| $(s 2)+4$ | 500 |
|  |  |


| (d) | 18 |
| :--- | :---: |
| (d) +1 | 8 |
| $(d)+2$ | 20 |
| (d) +3 | - |
| $(d)+4$ | 375 |
|  |  |

## Point ${ }^{\circ}$

- Devices (s1)+3, (s2)+3, and (d)+3 are not used for operation.
- The clock data that has been read by the S(P).DATERD instruction can be subtracted without conversion.


When clock data is read by the S(P).DATERD instruction, "day of week" is inserted between "second" and "millisecond".
If the $S(P)$.DATE- instruction is used to read clock data, the data can be directly used for subtraction since it does not perform calculation for the day of week.

Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3405 H | The data in the device specified by (s1) or (s2) is out of range. |

### 7.25 Timing Check Instructions

## Generating timing pulses

## DUTY

This instruction turns on the user timing clock for the specified number of scans and off for the specified number of scans.


FBD/LD


■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| DUTY | - |

## Setting data

DDescriptions, ranges, and data types

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| $(\mathrm{n} 1)$ | Number of scans during which the clock is turned on | 0 to 65535 | 16-bit unsigned binary | ANY16 |
| $(\mathrm{n} 2)$ | Number of scans during which the clock is turned off | 0 to 65535 | 16-bit unsigned binary | ANY16 |
| (d) | Special relay device number of user timing clock to be <br> operated | SM420 to SM424 | Bit | ANY_BOOL* |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

*1 Only labels assigned to SM420 to SM424 can be used.

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | Jロ\|ロ | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | UपIGㅁ, J밈, U3EDI(H)GD | Z | LT, LST, LC | LZ |  | K, H | E | \$ |  |
| ( n 1 ) | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - | $\bigcirc$ | $\bigcirc$ | - | - | - |
| (n2) | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - | $\bigcirc$ | $\bigcirc$ | - | - | - |
| (d) | $O^{* 1}$ | - | - | - | - | - | - | $\bigcirc$ | - | - | - | - |

[^21]
## Processing details

- This instruction turns on SM420 to SM424 in the device specified by (d) for the number of scans specified by ( n 1 ) and turns it off for the number of scans specified by (2).

SM420 to SM424


- The scan execution type program uses SM420 to SM424.
- When 0 is specified in ( n 1 ) and ( n 2 ) is equal to or greater than 0 , SM420 to SM424 stay off. When ( n 1 ) is greater than 0 and (n2) is 0, SM420 to SM424 stay on.
- When the DUTY instruction is executed, the data specified by ( n 1 ), ( n 2 ), and ( d ) is stored in the system, and the timing pulses are turned on or off by the END processing.


## Operation error

| Error code (SD0) | Description |
| :--- | :--- |
| 2820 H | The device other than SM420 to SM424 is specified by (d). |

## Measuring time of the specified data

## TIMCHK

This instruction measures the on time of the device and，if the on time has continued as specified or longer，turns on the specified device．


| ■－－－$\square$ |  |
| :---: | :---: |
| EN | ENO |
| s1 | d |
| s2 |  |

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| TIMCHK | $-\square$ |

## Setting data

## Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Device for storing the current value measured（unit： <br> 100 ms ） | - | 16－bit signed binary | ANY16 |
| （s2） | Set value for measurement or the device where the set <br> value for measurement is stored（unit： 100 ms ） | 0 to 32767 | 16－bit signed binary | ANY16 |
| （d） | Device to be turned on at time－up | - | Bit | ANY＿BOOL |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDl（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

－This instruction measures the on time of the device specified by（s1）and，if the on time has continued as specified in the device specified by（s2）or longer，turns on the device specified by（d）．
－The current value in the device specified by（ s 1 ）is cleared to 0 and the device specified by（ d ）is turned off on the rising edge of the execution command．The current value in the device specified by（s1）and the on state of the device specified by（d）are retained even after the execution command turns off．
－The current value measured is stored in units of 100 ms ．Set the measurement time in increments of 100 ms
－If 0 is specified in（ s 2 ），the current value in the device specified by（ s 1 ）is cleared to 0 and the device specified by（d）is turned off on the rising edge of the execution command．

## Operation error

There is no operation error

## 7．26 Module Access Instructions

## Performing I／O refresh

## RFS（P）

These instructions refresh the n points of data from the specified device，and import external inputs or outputs data to the output module．


FBD／LD


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| RFS | - |
| RFSP | - |

## Setting data

■Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| $(\mathrm{s})$ | Start device to be refreshed | - | Bit | ANY＿BOOL＊1 |
| $(\mathrm{n})$ | Number of refreshed points | 1 to 4096 | 16－bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

＊1 Only labels assigned to devices $(\mathrm{X}, \mathrm{Y})$ can be used．

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，Jロ\ロ， U3EDl（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc{ }^{* 1}$ | － | － | － | － | － | － | － | － | － | － | － |
| （ n ） | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

[^22]
## Processing details

- This instruction refreshes only the relevant device during one scan, and imports external inputs or outputs data to the output module.
- The instruction imports external inputs and outputs data to the outside altogether only after execution of the END instruction of the program, and therefore cannot output pulse signals to the outside during one scan. When executed, the I/O refresh instruction forcibly refreshes the relevant input $(\mathrm{X})$ and output $(\mathrm{Y})$ during program execution and therefore can output pulse signals to the outside during one scan.
- To refresh the input $(X)$ or output $(Y)$ in units of points, use the direct access input (DX) or direct access output (DY).

[Program using the direct access input and direct access output]



## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2820 H | The range of $(\mathrm{n})$ points from the device specified by $(\mathrm{s})$ exceeds the range of the proximal I/O. |

## Selecting refresh to be performed

## COM(P)

These instructions perform I/O refresh, network refresh, and device/label access service processing.

| Ladder | ST |
| :---: | :---: |
|  | $\begin{aligned} & \text { ENO:=COM(EN); } \\ & \text { ENO:=COMP(EN); } \end{aligned}$ |
| FBD/LD |  |
| $-$$[----]$ <br> $E N \quad E N O$ |  |

Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| COM | - |
|  | $\boxed{ }$ |
| COMP | - |

## Processing details

- The $\operatorname{COM}(P)$ instructions are used to perform processing such as $I / O$ refresh at any time during execution of the sequence program.
- The processing performed by the $\operatorname{COM}(\mathrm{P})$ instruction includes the following.
- I/O refresh
- CC-Link refresh
- CC-Link IE Controller Network refresh
- CC-Link IE Field Network refresh
- Intelligent function module refresh
- Inter-CPU refresh of multiple CPU system
- Import of input/output outside the group of multiple CPU system
- Service processing (communication with the engineering tool, GOT, or other external devices)
- When SM775 is turned off, every processing except I/O refresh is performed.

| Description | SM775 is off | SM775 is on |
| :---: | :---: | :---: |
| I/O refresh and import of input/output outside the group of multiple CPU system | Non-execution | Execution or nonexecution can be selected. |
| CC-Link refresh | Execution |  |
| CC-Link IE Controller Network refresh |  |  |
| CC-Link IE Field Network refresh |  |  |
| Intelligent function module refresh |  |  |
| Refresh of multiple CPU system using the CPU buffer memory (in END processing) |  |  |
| Device/label access service processing (communication with the engineering tool, GOT, or other external devices) |  |  |

- Select execution or non-execution by turning on SM775 and using b0 to b4, b6, and b15 of SD775.

(1) I/O refresh and import of input/output outside the group of multiple CPU system
(2) CC-Link refresh
(3) CC-Link IE Controller Network refresh
(4) Intelligent function module auto refresh
(5) Refresh of multiple CPU system using the CPU buffer memory (in END processing)
(6) CC-Link IE Field Network refresh
(7) Device/label access service processing (communication with the engineering tool, GOT, or other external devices)
- When executed, the $\operatorname{COM}(\mathrm{P})$ instruction performs the specified refresh processing.

- In the following program example, CC-Link IE Field Network refresh is executed when MO turns on.



## Precautions

- The $\operatorname{COM}(\mathrm{P})$ instruction can be used as many times as needed in the program. Note, however, that the scan time of the program is extended by the time of the processing selected by SD775.
- Interrupts are enabled during execution of the $\operatorname{COM}(\mathrm{P})$ instruction. If refresh data is used by an interrupt program, data separation may occur.
- If device/label access service processing is performed by the $\operatorname{COM}(\mathrm{P})$ instruction while an Ethernet device is connected to the Ethernet port, the processing time of the instruction may be extended.


## Point 9

The $\operatorname{COM}(\mathrm{P})$ instruction cannot be used in the interrupt program.

## Operation error

There is no operation error

## Performing module refresh

## S（P）．ZCOM

These instructions perform refresh processing for the specified module．

| Ladder | ST |
| :---: | :---: |
| $\begin{array}{\|l\|l} \hline---\square & (\mathrm{J} / \mathrm{U}) \\ \hline \end{array}$ | $\begin{aligned} & \text { ENO:=S_ZCOM(EN,J); } \\ & \text { ENO:=S_ZCOM(EN,U); } \\ & \text { ENO:=SP_ZCOM(EN,J); } \\ & \text { ENO:=SP_ZCOM(EN,U); } \end{aligned}$ |
| FBD／LD |  |
|  |  |

Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．ZCOM | - |
|  | - |
| SP．ZCOM | - |

## Setting data

IDescriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| $(J)$ | Own station network number | 1 to 239 | Device name | ANY16 |
| $(U)$ | Start I／O number（first three digits in four－digit hexadecimal <br> representation）of a module | 0 H to FFH |  |  |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （J／U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）GD | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （J／U） | － | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ |

－The S（P）．ZCOM instructions are used to perform refresh at any time during execution of the sequence program．The following lists the targets of refresh by the $S(P) . Z C O M$ instructions．
－CC－Link IE Controller Network refresh（in refresh setting）
－CC－Link IE Field Network refresh（in refresh setting）
－CC－Link refresh（in refresh setting）
－Intelligent function module refresh（when a refresh parameter is specified）

## Processing details

- When executed, the $S(P) . Z C O M$ instruction temporarily stops sequence program processing by the CPU module and perform refresh processing for the module specified by (J/U).

- The following is applicable when refresh processing of the CC-Link IE Controller Network is performed
- When the scan time of the sequence program of the host station is longer than that of another station, the $S(P) . Z C O M$ instruction is used to ensure the import of data from the other station.
[When the $S(P)$.ZCOM instruction is not used]

[When the $S(P)$.ZCOM instruction is used]

- When the link scan time is longer than the sequence program scan time, using the $S(P) . Z C O M$ instruction will not make data communication faster



## Precautions

- The S(P).ZCOM instruction can be used as many times as needed in the program. Note, however, that the scan time of the program is extended by the refresh time.
- Interrupts are enabled during execution of the $S(P)$.ZCOM instruction. If refresh data is used by an interrupt program, data separation may occur.


## Operation error

| Error code (SD0) | Description |
| :--- | :--- |
| 2800 H | The specified start I/O number is out of the range, 0 to FFH. |
| 2801 H | No module exists at the position specified by the start I/O number. |
| 2804 H | The network number set to $(\mathrm{J})$ is out of the range, 1 to 239. |
| 2820 H | The specified network number is not connected to the host station. |

## Point $\rho$

- The S(P).ZCOM instruction cannot be used in the interrupt program.
- To communicate only with external devices, use the COM(P) instruction.


## Reading 1－word／2－word data from another module（16－bit specification）

## FROM（P），DFROM（P）

－FROM（P）：
These instructions read n words of data from the buffer memory address in the specified module or another CPU module．
－DFROM（P）：
These instructions read $\mathrm{n} \times 2$ words of data from the buffer memory address in the specified module or another CPU module．

| Ladder |  |  |  | ST | T |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  | （s） | （d） | （ n ） |  |  |
| FBD／LD |  |  |  |  |  |
| $\square-\square-\square$  <br> EN ENO <br> U／H  <br> s  <br> n  | － |  |  |  |  |

Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| FROM | - |
| DFROM | - |
| FROMP | - |
| DFROMP |  |

Setting data
Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （U／H） | Start I／O number（first three digits in four－digit <br> hexadecimal representation）of a module or CPU <br> module | OH to FFH，3EOH to 3E3H | 16－bit unsigned binary | ANY16 |
| （s） | Start address of buffer memory or CPU built－in <br> memory containing the data to be read | 0 to 65535 | 16－bit unsigned binary | ANY16 |
| （d） | FROM（P） | Start device for storing the data that has been <br> read | - | 16－bit signed binary |
|  | DFROM（P） | ANY16 |  |  |
| （n） | Number of read data | 32－bit signed binary | ANY32 |  |
| EN | Execution condition | - | 16－bit unsigned binary | ANY16 |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロID， U3EDI（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （U／H） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ |
| （s） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | $\bigcirc$ | － | $\bigcirc$ | － | － | O＊1 | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

[^23]- For $(\mathrm{U} / \mathrm{H})$, specify the start I/O number of a module or CPU module with upper 3 digits when it is represented by 4 hexadecimal digits.


To read the module name of a CPU module, specify the read start I/O number as in the following table.

| CPU module | Read start I/O number |
| :--- | :--- |
| CPU No. 1 | 3 EOH |
| CPU No. 2 | 3 E 1 H |
| CPU No. 3 | 3 E 2 H |
| CPU No. 4 | 3 E 3 H |

## FROM(P)

- These instructions read ( n ) words of data from the buffer memory address specified by ( s ) in the module specified by (U/H) or another CPU module.
- Reading word data from module

- Reading word data from another CPU module

- If the read data $(\mathrm{n})$ is 0 , no processing is performed.
- An instruction which has been executed will result in non-processing if it fails to access the target module because the module is faulty or busy in processing.


## ■DFROM(P)

- These instructions read ( n ) $\times 2$ words of data from the buffer memory address specified by ( s ) in the module specified by ( $\mathrm{U} /$ H ) or another CPU module.
- Reading double word data from module

- Reading double word data from another CPU module

- If the read data $(\mathrm{n})$ is 0 , no processing is performed.
- An instruction which has been executed will result in non-processing if it fails to access the target module because the module is faulty or busy in processing.


## Operation error

| Error code (SDO) | Description |
| :---: | :---: |
| 2820 H | The module with the I/O number specified by (U) does not have buffer memory. |
| 2823H | The module with the I/O number specified by (H) does not have buffer memory. |
|  | The address specified by (s) is outside the range of buffer memory or CPU buffer memory. |
|  | The (n) points of data starting from the address specified by (s) are not within the range of buffer memory or CPU buffer memory. ( $\mathrm{FROM}(\mathrm{P})$ instruction) |
|  | The ( n ) $\times 2$ points of data starting from the address specified by ( s ) are not within the range of buffer memory or CPU buffer memory. (DFROM(P) instruction) |

## Point $P$

- Module data can also be read using the module access device. ([] MELSEC iQ-R CPU Module User's Manual (Application))
- If refresh settings are not made for the refresh area of the read/write enabled area in the CPU buffer memory, the area can be used as a read/write specifiable area. Even when refresh settings are made, the area can be used as a read/write specifiable area in the reference send range and later.

- A CPU buffer memory access device can be used to read data from the CPU buffer memory. For the CPU buffer memory access devices, refer to the following.
([]] MELSEC iQ-R CPU Module User's Manual (Application))


## Writing 1－word／2－word data to a module（16－bit specification）

## TO（P），DTO（P）

－TO（P）：These instructions write the n points of data from the specified device to the buffer memory in the module／host CPU module．
－DTO（P）：These instructions write the $\mathrm{n} \times 2$ points of data from the specified device to the buffer memory in the module／host CPU module．

| Ladder | ST |
| :---: | :---: |
| $-\square=-$ $(\mathrm{U} / \mathrm{H})$ （s1） （s2） （n） | ENO：＝TO（EN，U／H，s1，s2，n）； <br> ENO：＝TOP（EN，U／H，s1，s2，n）； <br> ENO：＝DTO（EN，U／H，s1，s2，n）； <br> ENO：＝DTOP（EN，U／H，s1，s2，n）； |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| TO | - |
| DTO | $\boxed{ }$ |
| TOP | - |
| DTOP |  |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （U／H） | Start I／O number（first three digits in four－digit <br> hexadecimal representation）of a module or CPU <br> module | 0 H to $\mathrm{FFH}, 3 \mathrm{EOH}$ to 3 E 3 H | 16－bit unsigned binary | ANY16 |
| （s1） | Start address of buffer memory or CPU built－in <br> memory to which data is to be written | 0 to 65535 | 16－bit unsigned binary | ANY16 |
| （s2） | TO（P） | Write data or the start device where the write |  |  |
|  | DTO（P） | data is stored | -32768 to 32767 | 16－bit signed binary |
| （n） | Number of write data | -2147483648 to 2147483647 | 32－bit signed binary | ANY32 |
| EN | Execution condition | 0 to 65535 | 16－bit unsigned binary | ANY16 |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロロロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （U／H） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ |
| （s1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | $\bigcirc$ | － | $\bigcirc$ | － | － | $\bigcirc{ }^{* 1}$ | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

[^24]
## Processing details

- For $(\mathrm{U} / \mathrm{H})$, specify the start I/O number of a module or CPU module with upper 3 digits when it is represented by 4 hexadecimal digits.


To read the module name of a CPU module, specify the read start I/O number as in the following table.

| CPU module | Read start I/O number |
| :--- | :--- |
| CPU No. 1 | 3 EOH |
| CPU No. 2 | 3 E 1 H |
| CPU No. 3 | 3 E 2 H |
| CPU No. 4 | 3 E 3 H |

TO(P)

- These instructions write the n points of data from the device specified by ( s 2 ) to the buffer memory address specified by ( s 1 ) and later in the buffer memory in the module or host CPU module specified by $(\mathrm{U} / \mathrm{H})$.
- Writing word data to a module

- Writing word data to the host CPU module

- If a constant is specified in (s2), the instructions write the same data (the value in the device specified by (s2)) to the (n) words from the specified buffer memory address.
- Writing word data to a module

- Writing word data to the host CPU module

- An instruction which has been executed will result in non-processing if it fails to access the target module because the module is faulty or busy in processing.


## IDTO(P)

- These instructions write the $(\mathrm{n}) \times 2$ points of data from the device specified by ( s 2 ) to the buffer memory address specified by (s1) and later in the buffer memory in the module or host CPU module specified by (U/H).
- Writing double word data to a module

- Writing double word data to the host CPU module

- If a constant is specified in (s2), the instructions write the same data (the value in the device specified by (s2)) to the (n) $\times 2$ words from the specified buffer memory address.
- Writing double word data to a module

- Writing double word data to the host CPU module

- An instruction which has been executed will result in non-processing if it fails to access the target module because the module is faulty or busy in processing.


## Operation error

| Error code (SDO) | Description |
| :---: | :---: |
| 2820 H | The module with the I/O number specified by (U) does not have buffer memory. |
| 2823H | The module with the I/O number specified by (H) does not have buffer memory. |
|  | The address specified by (s1) is outside the range of buffer memory or CPU buffer memory. |
|  | The ( n ) points of data starting from the address specified by ( s 1 ) are not within the range of buffer memory or CPU buffer memory. (TO(P) instruction) |
|  | The $(\mathrm{n}) \times 2$ points of data starting from the address specified by ( s 1 ) are not within the range of buffer memory or CPU buffer memory. (DTO(P) instruction) |

## Point/

- If refresh settings are not made for the refresh area of the read/write enabled area in the CPU buffer memory, the area can be used as a read/write specifiable area. Even when refresh settings are made, the area can be used as a read/write specifiable area in the reference send range and later.

- A CPU buffer memory access device can be used to write data to the CPU buffer memory. For the CPU buffer memory access devices, refer to the following.
([]] MELSEC iQ-R CPU Module User's Manual (Application))


## Reading 1－word／2－word data from another module（32－bit specification）

## FROMD（P），DFROMD（P）

－FROMD（P）：
These instructions read n words of data from the buffer memory address in the specified module or another CPU module．
－DFROMD（P）：
These instructions read $\mathrm{n} \times 2$ words of data from the buffer memory address in the specified module or another CPU module．

| Ladder |  |  |  | ST | T |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  | （s） | (d) | （ n ） |  | $\begin{aligned} & \mathrm{ENO}:=\mathrm{FROMD}(\mathrm{EN}, \mathrm{U} / \mathrm{H}, \mathrm{~s}, \mathrm{n}, \mathrm{~d}) ; \\ & \mathrm{ENO}:=\mathrm{FROMDP}(\mathrm{EN}, \mathrm{U} / \mathrm{H}, \mathrm{~s}, \mathrm{n}, \mathrm{~d}) ; \\ & \mathrm{ENO}:=\mathrm{DFROMD}(\mathrm{EN}, \mathrm{U} / \mathrm{H}, \mathrm{~s}, \mathrm{~d}) ; \\ & \mathrm{ENO}:=\mathrm{DFROMDP}(\mathrm{EN}, \mathrm{U} / \mathrm{H}, \mathrm{~s}, \mathrm{n}, \mathrm{~d}) ; \end{aligned}$ |
| FBD／LD |  |  |  |  |  |
|  | － |  |  |  |  |

Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| FROMD | - |
| DFROMD | - |
| FROMDP | - |
| DFROMDP |  |

Setting data
Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （U／H） | Start I／O number（first three digits in four－digit <br> hexadecimal representation）of a module or CPU <br> module | 0 H to $\mathrm{FFH}, 3 \mathrm{EOH}$ to 3 E 3 H | 16－bit unsigned binary | ANY16 |
| （s） | Buffer memory from which the data is read or the <br> start device where the start address of the CPU <br> memory is stored | 0 to 4294967295 | 32－bit unsigned binary | ANY32 |
| （d） | FROMD（P） | Start device for storing the data that has been <br> read | - | 16－bit signed binary |
| （n） | DFROMD（P） | Number of read data | 0 to 4294967295 | 32－bit signed binary |
| EN | Execution condition | - | ANY32 |  |
| ENO | Execution result | - | Bit | Bit |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ\ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （U／H） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ |
| （s1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | $\bigcirc$ | － | $\bigcirc$ | － | － | $\mathrm{O}^{* 1}$ | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |

＊1 Only the DFROMD $(P)$ instruction can be used．

- For $(\mathrm{U} / \mathrm{H})$, specify the start I/O number of a module or CPU module with upper 3 digits when it is represented by 4 hexadecimal digits.


To read the module name of a CPU module, specify the read start I/O number as in the following table.

| CPU module | Read start I/O number |
| :--- | :--- |
| CPU No. 1 | 3 EOH |
| CPU No. 2 | 3 E 1 H |
| CPU No. 3 | 3 E 2 H |
| CPU No. 4 | 3 E 3 H |

## FROMD(P)

- These instructions read ( $n$ ) words of data from the buffer memory address specified by ( s ) in the module specified by (U/H) or another CPU module.
- Reading word data from module

- Reading word data from another CPU module

CPU buffer memory of another CPU module (U/H)


Device/label memory


- If the read data $(\mathrm{n})$ is 0 , no processing is performed.
- An instruction which has been executed will result in non-processing if it fails to access the target module because the module is faulty or busy in processing.


## ■DFROMD(P)

- These instructions read $(\mathrm{n}) \times 2$ words of data from the buffer memory address specified by $(\mathrm{s})$ in the module specified by ( U / H ) or another CPU module.
- Reading double word data from module

- Reading double word data from another CPU module

CPU buffer memory of another CPU module (U/H)


Device/label memory


- If the read data $(\mathrm{n})$ is 0 , no processing is performed.
- An instruction which has been executed will result in non-processing if it fails to access the target module because the module is faulty or busy in processing.


## Operation error

| Error code (SDO) | Description |
| :---: | :---: |
| 2820H | The module with the I/O number specified by (U) does not have buffer memory. |
| 2823H | The module with the I/O number specified by (H) does not have buffer memory. |
|  | The address specified by (s) is outside the range of buffer memory or CPU buffer memory. |
|  | The ( n ) points of data starting from the address specified by ( s ) are not within the range of buffer memory or CPU buffer memory. (FROMD(P) instruction) |
|  | The $(\mathrm{n}) \times 2$ points of data starting from the address specified by (s) are not within the range of buffer memory or CPU buffer memory. (DFROMD(P) instruction) |

Point 9

- If refresh settings are not made for the refresh area of the read/write enabled area in the CPU buffer memory, the area can be used as a read/write specifiable area. Even when refresh settings are made, the area can be used as a read/write specifiable area in the reference send range and later.

- A CPU buffer memory access device can be used to write data to the CPU buffer memory. For the CPU buffer memory access devices, refer to the following.
([]] MELSEC iQ-R CPU Module User's Manual (Application))


## Writing 1－word／2－word data to a module（32－bit specification）

## TOD（P），DTOD（P）

－TOD（P）：These instructions write the n points of data from the specified device to the buffer memory in the module／host CPU module．
－$D T O D(P)$ ：These instructions write the $n \times 2$ points of data from the specified device to the buffer memory in the module／host CPU module．

| Ladder | ST |
| :---: | :---: |
| $-\square-$ $(\mathrm{U} / \mathrm{H})$ （s1） （s2） （n） | ENO：＝TOD（EN，U／H，s1，s2，n）； <br> ENO：＝TODP（EN，U／H，s1，s2，n）； <br> ENO：＝DTOD（EN，U／H，s1，s2，n）； <br> ENO：＝DTODP（EN，U／H，s1，s2，n）； |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| TOD | - |
| DTOD | $\boxed{ }$ |
| TODP | - |
| DTODP |  |

## Setting data

Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :---: | :---: | :---: | :---: | :---: | :---: |
| （U／H） |  | Start I／O number（first three digits in four－digit hexadecimal representation）of a module or CPU module | OH to FFH，3E0H to 3E3H | 16－bit unsigned binary | ANY16 |
| （s1） |  | Buffer memory to which the data is written or the start device where the start address of CPU memory is stored | 0 to 4294967295 | 32－bit unsigned binary | ANY32 |
| （s2） | TOD（P） | Write data or the start device where the write data is stored | －32768 to 32767 | 16－bit signed binary | ANY16 |
|  | DTOD（P） |  | －2147483648 to 2147483647 | 32－bit signed binary | ANY32 |
| （ n ） |  | Number of write data | 0 to 4294967295 | 32－bit unsigned binary | ANY32 |
| EN |  | Execution condition | － | Bit | BOOL |
| ENO |  | Execution result | － | Bit | BOOL |

## －Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，Jㅁㅁ， U3EDl（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （U／H） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ |
| （s1） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | $\bigcirc$ | － | $\bigcirc$ | － | － | $0^{* 1}$ | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （ n ） | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | － | － | － |

[^25]
## Processing details

- For $(\mathrm{U} / \mathrm{H})$, specify the start I/O number of a module or CPU module with upper 3 digits when it is represented by 4 hexadecimal digits.


To read the module name of a CPU module, specify the read start I/O number as in the following table.

| CPU module | Read start I/O number |
| :--- | :--- |
| CPU No. 1 | 3 EOH |
| CPU No. 2 | 3 E 1 H |
| CPU No. 3 | 3 E 2 H |
| CPU No. 4 | 3 E 3 H |

## TOD(P)

- These instructions write the n points of data from the device specified by ( s 2 ) to the buffer memory address specified by ( s 1 ) and later in the buffer memory in the module or host CPU module specified by $(\mathrm{U} / \mathrm{H})$.
- Writing word data to a module

- Writing word data to the host CPU module

- If a constant is specified in (s2), the instructions write the same data (the value in the device specified by ( s 2 )) to the ( n ) words from the specified buffer memory address.
- Writing word data to a module

- Writing word data to the host CPU module

- An instruction which has been executed will result in non-processing if it fails to access the target module because the module is faulty or busy in processing.


## IDTOD(P)

- These instructions write the $(\mathrm{n}) \times 2$ points of data from the device specified by (s2) to the buffer memory address specified by ( s 1 ) and later in the buffer memory in the module or host CPU module specified by (U/H).
- Writing double word data to a module

- Writing double word data to the host CPU module

- If a constant is specified in (s2), the instructions write the same data (the value in the device specified by (s2)) to the (n) $\times 2$ words from the specified buffer memory address.
- Writing double word data to a module

- Writing double word data to the host CPU module

- An instruction which has been executed will result in non-processing if it fails to access the target module because the module is faulty or busy in processing.

Operation error

| Error code (SDO) | Description |
| :---: | :---: |
| 2820H | The module with the I/O number specified by (U) does not have buffer memory. |
| 2823H | The module with the I/O number specified by (H) does not have buffer memory. |
|  | The address specified by (s1) is outside the range of buffer memory or CPU buffer memory. |
|  | The ( n ) points of data starting from the address specified by ( s 1 ) are not within the range of buffer memory or CPU buffer memory. (TOD(P) instruction) |
|  | The $2 \times(\mathrm{n})$ points of data from the address specified by ( s 1 ) is outside the range of buffer memory or CPU buffer memory. (DTOD(P) instruction) |

Point $\rho$

- If refresh settings are not made for the refresh area of the read/write enabled area in the CPU buffer memory, the area can be used as a read/write specifiable area. Even when refresh settings are made, the area can be used as a read/write specifiable area in the reference send range and later.

- A CPU buffer memory access device can be used to write data to the CPU buffer memory. For the CPU buffer memory access devices, refer to the following.
([]] MELSEC iQ-R CPU Module User's Manual (Application))


## Reading the module model name

## TYPERD（P）

These instructions read the module name of the specified slot．

| Ladder | ST |
| :---: | :---: |
|  | $\begin{aligned} & \text { ENO:=TYPERD(EN,H,d); } \\ & \text { ENO:=TYPERDP(EN,H,d); } \end{aligned}$ |

FBD／LD

|  |  |
| :---: | :---: |
|  |  |
|  |  |

Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| TYPERD | - |
|  | $\boxed{ }$ |
| TYPERDP | - |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （H） | Start I／O number（first three digits in four－digit hexadecimal <br> representation）of a model read target module | OH to FFH，3EOH to 3E3H | 16－bit unsigned binary | ANY16 |
| （d） | （d）＋0：Instruction execution result <br> （d）+1 to（d）＋9：Module name | - | Word | ANY16＿ARRAY <br> （Number of elements： <br> $10)$ |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## －Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3E미（H）G口 | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （H） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions read the module name in the slot specified by $(\mathrm{H})$, and store the model name in the device specified by (d) and later. The target modules are as follows.
- CPU module
- Input module
- Output module
- I/O combined module
- Intelligent function module
- For (H), specify the start I/O number of the target module with upper 3 digits when it is represented by 4 hexadecimal digits.

- For the slot to be specified for the read target when specifying a module that occupies two slots, refer to the number of occupied I/O points described in the manual for each module.
- To read the module name of a CPU module, specify the read start I/O number as in the following table.

| CPU module | Read start I/O number |
| :--- | :--- |
| CPU No. 1 | 3 E 0 H |
| CPU No. 2 | 3 E 1 H |
| CPU No. 3 | 3 E 2 H |
| CPU No. 4 | 3 E 3 H |

- The result of instruction execution is stored in (d) +0 , and a module name is stored in (d) +1 to (d) +9 . The following table lists the values to be stored in (d).

| Condition | (d) $+\mathbf{0}$ | (d) $\mathbf{+ 1}$ to (d)+9 |
| :--- | :--- | :--- |
| The read target module has a module name. | 0 | Module name held by a module |
| The read target module does not have a module name. | 1 | Character string consisting of module type and the <br> number of points |
| The read target slot is an empty slot. | -1 | 0000 H |
| The read target module is in the course of online exchange. |  |  |
| The I/O number specified in $(\mathrm{H})$ is not the start number of a module. |  |  |

- When the read target module has a module name, the module name to be stored in (d)+1 and later is as follows.
- Nine words are used.
- The name is stored in ASCII characters.
- 00 H is stored in the 18th character.
- If the number of characters is less than $17,00 \mathrm{H}$ is stored in the remaining characters.
- The module name held by a module is stored. (Note that it may differ from the module name written to the rating plate.)


## Ex.

The following table lists module name examples that are stored.

| Target module | Module name example stored |
| :--- | :--- |
| CPU module | R04CPU |
| I/O module | INPUT_16 |
| Network module | RJ71GP21-SX |

## Point ${ }^{\rho}$

If the module name in the I/O assignment setting differs from that of the mounted module, the module name held by the mounted module is stored.

- When the read target module does not have a module name, the character string to be stored in (d)+1 and later is as follows.
- Nine words are used.
- The name is stored in ASCII characters
- 00 H is stored in the 18 th character.
- If the number of characters is less than $17,00 \mathrm{H}$ is stored in the remaining characters.
- A character string consisting of a combination of "character string indicating the module type" and "character string indicating the number of points" is stored.


## Ex.

The following table lists character string examples that are stored.

| Target module | Character string example stored |
| :--- | :--- |
| Input module | INPUT_16 |
| Output module | OUTPUT_32 |
| I/O combined module | MIXED_64 |
| Intelligent function module | INTELLIGENT_128 |

The following table lists character string examples that indicate the numbers of points.

| Number of points | Character string example that indicates the number of points. |
| :--- | :--- |
| 16 point | -16 |
| 32 point | -32 |
| 48 point | -48 |
| 64 point | -64 |
| 128 point | -128 |
| 256 point | -256 |
| 512 point | -512 |
| 1024 point | -1024 |

## Point ${ }^{\circ}$

If the number of points in the I/O assignment setting differs from that of the mounted module, the number of points of the mounted module is stored.

- In a multiple CPU system, the module name can also be read by specifying the module controlled by the CPU module of another CPU.
- In the following program example, when M0 turns on, the module name of the module mounted at $\mathrm{I} / \mathrm{O}$ number 0020 H is stored in D0 and later.

- Module having a module name (example: RJ71GP-SX)

- Module having no module name (example: RX40)

- Empty slot



## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2800 H | The value set to $(\mathrm{H})$ is out of the range, 0 to FFH and 3 E 0 to 3 E 3 H. |
| 2810 H | Communications with the read target module is disabled due to a failure of the module. |

## Reading module specific information

## UNIINFRD（P）

These instructions read the specified points of module information．

| Ladder | ST |
| :---: | :---: |
| ■－च－$-\square$ （H） （d） （n） | $\begin{aligned} & \text { ENO:=UNIINFRD(EN,H,n,d); } \\ & \text { ENO:=UNIINFRDP(EN,H,n,d); } \end{aligned}$ |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| UNIINFRD | - |
|  | $\boxed{ }$ |
| UNIINFRDP | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （H） | Start I／O number（first three digits in four－digit hexadecimal <br> representation）of an information read target module | OH to FFH | 16－bit unsigned binary | ANY16 |
| （d） | Start device for storing module information | - | Word | ANY16 |
| （n） | Number of read data points | 0 to 256 | 16 －bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J미， U3EDl（H）G口 | z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （H） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

- These instructions read the module information by the number of points specified by ( n ) from the module specified by $(\mathrm{H})$, and store the information in the device areas specified by (d) and later. Even if the module type or the number of points is changed in I/O assignment, the status of the mounted module is read.
- For (H), specify the start I/O number of the module, whose information is to be read, with upper 3 digits when it is represented by 4 hexadecimal digits.
- If an I/O number other than the start I/O number of the read target module is specified, module information in which only the module mount status is on and any other status is off is stored.

- The following shows detailed module information.
(d)
(d) +1
b15 b14 b13 b12 b11 b10 b9 b8 b7 b6 b5 b4 b3 b2 b1 b0


*1 For example, the module is being connected or removed.
*2 For a module which occupies 32 points or more, information is stored in (d) +0 and (d) +1 , and ON information is stored in the later devices only when the module is mounted.
- In the following program example, when M0 turns on, the information on the module mounted at I/O number 0040H is stored in D0 and later.


| 0000 H | 0010 H | 0020 H | 0030 H | 0040 H | 0060 H | Start I/O number set in the I/O assignment parameter |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |


| M0 |  | (H) | (d) | ( n ) |
| :---: | :---: | :---: | :---: | :---: |
|  | UNIINFRD | K4 | D0 | K2 |

[Result of reading]

- Module information of the RJ71EN71 is read.

(1) (2) (3)
(4)
(5)
(6)
(1) Module mounted
(2) Start of slot
(3) CPU No. 1 control
(4) MELSEC iQ-R series module
(5) Intelligent function module
(6) 32-point module

(1) Module access enabled
(2) Fixed to 0
(3) Not during online module change
(4) Fixed to 0
(5) Fuse not blown
(6) Fixed to 0
(7) Fixed to 0
(8) Inter-module synchronized
(9) Fixed to 0
(10)Module ready
(11)Normal (no module error)

(1) A 32-point module is connected in the latter 16 points.
(2) All Os because information is stored in D0 and D1.

(1) All Os because information is stored in D0 and D1.
- If an I/O number other than the start I/O number is specified in (H) for a module having 32 or more I/O points, module information in which only the module mount status is on and any other status is off is stored. In the following program example, when M0 turns on, the information on the module mounted at I/O number 0050H is stored in D0 and later.


| 0000 H | 0010 H | 0020 H | 0030 H | 0040 H | 0060 H |
| :--- | :--- | :--- | :--- | :--- | :--- |
| Start I/O number set in the I/O assignment parameter |  |  |  |  |  |


[Result of reading]

- Only the module mount status is on because the I/O number in the latter 16 points of the RJ71EN71 is specified by (H).

(1)
(2)
(1) A 32-point module is connected in the latter 16 points.
(2) All 0 s because the latter 16 points of a 32-point module is specified in n 1 .
b15 b14 b13 b12 b11 b10 b9 b8 b7 b6 b5 b4 b3 b2 b1 b0
D1

(1) All 0 s because the latter 16 points of a 32-point module is specified in n 1 .
- Module information of RX10 is read.

(1) Module mounted
(2) Start of slot
(3) CPU No. 1 control
(4) MELSEC iQ-R series module
(5) Input module
(6) 16-point module

(1) Module access enabled
(2) Fixed to 0
(3) Not during online module change
(4) Fixed to 0
(5) Fuse not blown
(6) Fixed to 0
(7) Fixed to 0
(8) Inter-module synchronized
(9) Fixed to 0
(10)Module ready
(11)Normal (no module error)


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2800 H | The value set to $(\mathrm{H})$ is out the range, 0 to FFH. |
| 3405 H | The value set to $(\mathrm{n})$ is out of the range, 0 to 256. |
|  | The total of the values in $(\mathrm{H})$ and $(\mathrm{n})$ is 257 or greater. |

## 7．27 Routing Information Instructions

## Reading routing information

## S（P）．RTREAD

These instructions read the data of the specified transfer destination network number from the routing information set in parameter．


FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．RTREAD | - |
|  | - |
| SP．RTREAD | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s） | Transfer destination network number | 1 to 239 | 16－bit signed binary | ANY16 |
| （d） | Start device for storing the read data | - | Word | ANY16＿ARRAY <br> （Number of elements： <br> $3)$ |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDl（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions read the data of the transfer destination network number specified by (s) from the routing information set in parameter, and store the information in the device specified by (d) and later.
- If the data of the transfer destination network number specified by (s) is not set in parameter, 0 is stored in the device specified by (d) and later.
- The following figure shows the data stored in the device specified by (d) and later.

|  | Data range |  |
| :--- | :--- | :--- |
| (d) | Relay network number | (1 to 239) |
|  | (d) +1 | Relay station number |
| (Refer to the following table.) |  |  |
|  |  |  |

- The specification ranges of relay station number are as follows.

| Network type | Specification range |
| :--- | :--- |
| CC-Link IE Controller Network | 1 to 120 |
| CC-Link IE Field Network (master station) | Fixed to 125 (The fixed value is stored.) |
| CC-Link IE Field Network (local station) | 1 to 120 (The relevant station number is stored.) |

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3405 H | The value set to $(\mathrm{s})$ is out of the range, 1 to 239. |

## Registering routing information

## S（P）．RTWRITE

These instructions write routing information in the area with the specified transfer destination network number．

| Ladder | ST |
| :---: | :---: |
| $\begin{array}{l\|l\|l\|} \hline \text { ■-二- }] & \text { (s1) } & \text { (s2) } \\ \hline \end{array}$ | $\begin{aligned} & \text { ENO:=S_RTWRITE(EN,s1,s2); } \\ & \text { ENO:=SP_RTWRITE(EN,s1,s2); } \end{aligned}$ |

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|  |  |
| :---: | :---: |
|  |  |
| s1 |  |
| s2 |  |

Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．RTWRITE | - |
|  | $\boxed{Z}$ |
| SP．RTWRITE | - |

Setting data
Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （s1） | Transfer destination network number | 1 to 239 | 16－bit signed binary | ANY16 |
| （s2） | Start device where the write data is stored | - | Word | ANY16＿ARRAY <br> （Number of elements： <br> $3)$ |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDI（H）G口 | Z | LT，LST， LC | LZ |  | K，H | E | \＄ |  |
| （s1） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions write the routing information stored in the device specified by ( s 2 ) and later to the area with the transfer destination network number specified by (s1).
- The following figure shows the data stored in the device specified by (s2) and later.

|  |  | Data range |  |
| :--- | :--- | :--- | :---: |
|  | Relay network number | (1 to 239) |  |
| (s2) +1 | Relay station number | (Refer to the following table.) |  |
|  |  |  |  |

- The specification ranges of relay station number are as follows.

| Network type | Specification range |
| :--- | :--- |
| CC-Link IE Controller Network | 1 to 120 |
| CC-Link IE Field Network (master station) | Fixed to 125 |
| CC-Link IE Field Network (local station) | 1 to 120 |

- If the data of the transfer destination network number specified by ( $s 1$ ) has already been set in parameter, the data is overwritten with the data stored in the device specified by (s2) and later.
- If the data in the device areas specified by (s2) to (s2)+2 are all 0 , the data of the transfer destination network number specified by ( s 1 ) is deleted from parameter.

| Error code (SD0) | Description |
| :---: | :---: |
| 3405H | The value set to (s1) is out of the range, 1 to 239. |
|  | Any of the data in the device areas specified by (s2) and later exceeds the setting range. |
|  | The total number of routing information registered in parameter of the network module and registered by using the RTWRITE instruction exceeds 238. |
|  | A transfer destination network number which is not registered in parameter is specified as a deletion target. |
|  | A zero is specified in only either of the device areas specified by (s2) and (s2)+1. |

### 7.28 Logging Instructions

## Setting trigger logging

## LOGTRG

This instruction generates a trigger condition for the specified logging setting number in trigger logging.

| Ladder | ST |
| :---: | :---: |
|  | ENO:=LOGTRG(EN,s); |
| $\begin{array}{l\|l\|} \hline & (\mathrm{s}) \\ \hline \end{array}$ |  |

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## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| LOGTRG | - |

Setting data
Description, range, data type

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| $(\mathrm{s})$ | Logging setting number | 1 to 10 | 16-bit signed binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | J밈 | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | U밈, J밈, U3EDI(H)Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (s) | - | - | $\bigcirc$ | - | $\bigcirc$ | - | - | $\bigcirc$ | $\bigcirc$ | - | - | - |

## Processing details

- This instruction generates a trigger for trigger logging with the logging setting number specified by (s).
- Specify a value 1 to 10 in (s).
- The LOGTRG instruction turns on the special relay (logging trigger) with the logging setting number in the device specified by (s), executes trigger logging for the specified number of records, latches data, and stops trigger logging.
- The instruction is enabled when "When trigger instruction executed" in the "Trigger condition".
- Even if the LOGTRG instruction is executed, no processing is performed in the following cases.
- A logging setting number in which an item other than "When trigger instruction executed" is selected in the "Trigger condition" is specified.
- A logging setting number with no setting is specified.
- A logging setting number specifying the execution of continuous logging is specified.
- Another LOGTRG instruction is executed without executing the LOGTRGR instruction after a LOGTRG instruction was executed once.


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3405 H | The value set to $(\mathrm{s})$ is out of the range, 1 to 10. |

## Resetting trigger logging

## LOGTRGR

This instruction resets the trigger condition of the specified logging setting number．

| Ladder | ST |
| :--- | :--- |
|  | ENO：＝LOGTRGR（EN，s）； |
| $\square-\square-\square$ | $(\mathrm{s})$ |
|  |  |

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Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| LOGTRGR | $\ddots$ |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| $(\mathrm{s})$ | Logging setting number | 1 to 10 | 16－bit signed binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDl（H）GD | z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

－This instruction resets the LOGTRG instruction of the logging setting number specified by（s）．The instruction disables the LOGTRG instruction of the specified trigger logging setting number．
－The LOGTRGR instruction turns off the special relays（logging completion，logging trigger，and after logging trigger）with the logging setting number in the device specified by（s）．
－If the instruction is executed while buffer data is saved to an SD memory card，the execution of the instruction is made to wait until all data is saved completely．

## Operation error

| Error code（SDO） | Description |
| :--- | :--- |
| 3405 H | The value set to $(\mathrm{s})$ is out of the range， 1 to 10. |

## 7．29 Program Control Instructions

## Changing the program execution type to standby type

## PSTOP（P）

These instructions change the execution type of the program with the file name stored in the specified device to a standby type．

| Ladder | ST |
| :---: | :---: |
| - －－$\square$ （File name） | ENO：＝PSTOP（EN，filename）； <br> ENO：＝PSTOPP（EN，filename）； |

## FBD／LD



## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| PSTOP | - |
|  | $\boxed{ }$ |
| PSTOPP | $\boxed{ }$ |

Setting data
■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （file name） | Character string data of the file name of the program to be <br> changed to a standby type，or the start device where the <br> character string data is stored | - | Unicode string | ANYSTRING＿DOUB <br> LE |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDI（H）Gロ | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （file name） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － |

## Processing details

－These instructions change the execution type of the program with the file name stored in the device specified by（file name） to a standby type．
－Only programs stored in program memory can be changed to a standby type．
－The execution type of the specified program changes to a standby type during END processing．
－The PSTOP $(P)$ instruction takes precedence even when the execution type is specified in parameter．
－Extension＂．PRG＂does not need to be specified as a part of file name．（Only ．PRG files can be processed by these instructions．）

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2840 H | The program with the file name specified by (file name) does not exist. |
| 2841 H | The program with the file name specified by (file name) is not registered in parameter. |

## Changing the program execution type to standby type（output off）

## POFF（P）

These instructions change the program execution type of the program with the file name stored in the specified device．

| Ladder | ST |  |
| :--- | :--- | :--- |
| $\mid$ |  |  |
| $\square-\square$ | （File name） | ENO：＝POFF（EN，filename）； |
|  | ENO：＝POFFP（EN，filename）； |  |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| POFF | - |
|  | $\boxed{ }$ |
| POFFP | - |

Setting data
Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （file name） | File name of the program to be changed to the standby <br> type（with output set to off），or the device where the file <br> name is stored | - | Unicode string | ANYSTRING＿DOUB |
| LE |  |  |  |  |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3EDI（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （file name） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － |

## Processing details

－These instructions change the execution type of the program with the file name stored in the device specified by（file name）．If the program is a scan execution type，the output is turned off（non－execution processing）in the next scan．The program will be a standby type in the following scan and later．When the program is a fixed scan execution type or event execution type，it becomes a scan execution type in the next scan and turns off（non－execution processing）the output．The program will be a standby type in the following scan and later．
－Only programs stored in program memory can be changed to a standby type．
－The $\operatorname{POFF}(P)$ instruction takes precedence even when the execution type is specified in parameter．
－Extension＂．PRG＂does not need to be specified as a part of file name．（Only ．PRG files can be processed by these instructions．）

Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2840 H | The program with the file name specified by (file name) does not exist. |
| 2841 H | The program with the file name specified by (file name) is not registered in parameter. |

## Point ${ }^{\rho}$

Non-execution processing is the same as the processing performed by each coil instruction with the condition contact set to off.


The operation results of each coil instruction after the non-execution processing will be as follows, regardless of the on/off state of the condition contact.

- OUT instruction: The output is forcibly turned off.
- SET, RST, SFT, basic, and application instructions: Status is held.
- PLS and PLS conversion instructions (ロP): Same processing as when the condition contact is set to off
- OUT T instruction: The current value of the low-speed/high-speed timer is 0 .
- OUT ST and OUT C instructions: Current value is held.


## Changing the program execution type to scan execution type

## PSCAN（P）

These instructions change the execution type of the program with the file name stored in the specified device to a scan execution type．


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Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| PSCAN | - |
|  | $\boxed{\square}$ |
| PSCANP | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （file name） | File name of the program to be changed to a scan <br> execution type，or the start device where the file name is <br> stored | - | Unicode string | ANYSTRING＿DOUB <br> LE |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，Jपlप， U3EDI（H）Gㅁ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （file name） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | － |

## Processing details

－These instructions change the execution type of the program with the file name stored in the device specified by（filename） to a scan execution type．
－Only programs stored in program memory can be changed to a scan execution type．
－The execution type of the specified program changes to a scan execution type during END processing．

## Ex．

While there are programs $A, B$ ，and $C$ ，the PSCAN $(P)$ instruction is executed for program $D$ within program $A$ ．

－The PSCAN $(P)$ instruction takes precedence even when the execution type is specified in parameter．
－Extension＂．PRG＂does not need to be specified as a part of file name．（Only ．PRG files can be processed by these instructions．）

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2840 H | The program with the file name specified by (file name) does not exist. |
| 2841 H | The program with the file name specified by (file name) is not registered in parameter. |

## 8．1 Open／Close Processing Instructions

## Opening a connection

## SP．SOCOPEN

This instruction opens the specified connection．


FBD／LD

| ■－－－ |  |
| :---: | :---: |
| EN | ENo |
| U | d |
| s1 |  |
| s2 |  |

## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| SP．SOCOPEN | - |

## Setting data

■Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （U） | Dummy | - | String | ANYSTRING＿SINGL <br> E |
| （s1） | Connection number | 1 to 16 | 16－bit signed binary | ANY16 |
| （s2） | Start device containing the control data | Refer to the control data． | Word | ANY16＿ARRAY <br> （Number of elements： <br> 10） |
| （d） | Device that turns on one scan upon completion of <br> instruction <br> If the instruction is completed with an error，（d）+1 is also <br> turned on． | - | Bit | ANYBIT＿ARRAY <br> （Number of elements： <br> $2)$ |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM} \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J밈， U3EDl（H）GD | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （U） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

Control data

| Operand: (s2) |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description |  |  |  | Setting range | Set by |
| +0 | Execution/completion type | Specify whether to use the parameter value set by the engineering tool or the value set in (s2)+2 to (s2)+9 of control data for opening a connection. <br> - 0000 H : Performs open processing according to the "open setting" by the engineering tool. <br> - 8000H: Performs open processing according to the setting in (s2)+2 to (s2)+9 of control data. |  |  |  | $\begin{aligned} & 0000 \mathrm{H} \\ & 8000 \mathrm{H} \end{aligned}$ | User |
| +1 | Completion status | The completion status is stored upon completion of the instruction. <br> - 0000H: Completed successfully <br> - Other than $0000 \mathrm{H}:$ Completed with an error (error code) |  |  |  | - | System |
| +2 | Application setting area | Specify the application of a connection. <br> (1) Communication method (protocol) <br> - 0: TCP/IP <br> -1: UDP/IP <br> (2) Socket communications function procedure <br> - 1: No procedure (fixed) <br> (3) Open method <br> - 00: Active open or UDP/IP <br> - 10: Unpassive open <br> - 11: Fullpassive open <br> (4) Communication protocol setting <br> - 0 : Do not use the communication protocol support function (use the socket communications function). <br> - 1: Use the protocol support function. |  |  |  | - | User |
| +3 | Own station port number. | Specify the port number of the own station. |  |  |  | 0001 H to 1387 H , 1392H to FFFEH ( 0400 H or later recommended) | User |
| $\begin{aligned} & +4 \\ & +5 \end{aligned}$ | IP address of external device*1 | Specify the IP address of an external device. |  |  |  | 00000001 H to FFFFFFFFH (FFFFFFFFH: simultaneous broadcast) | User |
| +6 | Destination port number | Specify the destination port number. |  |  |  | 0001H to FFFFH <br> (FFFFH: <br> simultaneous broadcast) | User |
| +7 to +9 | - | Use prohibited/reserved |  |  |  | - | System |

*1 In Unpassive open mode, the IP address of the external device and destination port number are ignored.

Port numbers 0001H to 03FFH are generally reserved port numbers (WELL KNOWN PORT NUMBERS), and therefore port numbers 0400 H or later should be used.

## Processing details

- This instruction opens the connection specified by (s1). The setting value used for open processing is selected by (s2)+0.
- The execution of the SP.SOCOPEN instruction and whether it has been completed normally or with an error can be checked with the completion device (d) or completion status indication device (d)+1.
- Completion device (d)

The completion device turns on in END processing of scan performed upon completion of the SP.SOCOPEN instruction and turns off in the next END processing.

- Completion status indication device (d)+1

The completion device turns on or off depending on the completion status of the SP.SOCOPEN instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on in END processing of the scan performed upon completion of the SP.SOCOPEN instruction and turns off in the next END processing.

- The following figure shows the execution timing of the SP.SOCOPEN instruction.

- A connection which has not been set by a parameter (a connection whose protocol field is left blank) can be opened and used. To do so, set (s2)+0 to 8000 H and specify the details of open in (s2)+1 to (s2+9 of control data.


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3405 H | The connection number specified by (s1) is a value other than 1 to 16. |

Upon completion with an error, the completion status indication device (d)+1 is turned on and an error code is stored in the completion status (s2)+1.
For the error code stored in the completion status (s2)+1, refer to the following.
$\square \square$ MELSEC iQ-R Ethernet User's Manual (Application)

## Closing a connection

## SP．SOCCLOSE

This instruction closes the specified connection．


FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| SP．SOCCLOSE | - |

Setting data
Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （U） | Dummy | - | String <br> E |  |
| （s1） | Connection number | 1 to 16 | 16－bit signed binary | ANY16 |
| （s2） | Start device containing the control data | Refer to the control data． | Word | ANY16＿ARRAY <br> （Number of elements： <br> 2） |
| （d） | Device that turns on one scan upon completion of <br> instruction <br> If the instruction is completed with an error，（d）＋1 is also <br> turned on． | - | Bit | ANYBIT＿ARRAY <br> （Number of elements： <br> $2)$ |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J밈， U3EDI（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （U） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## ■Control data

| Operand：（s2） | Description | - | Setting range | Set by |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Item | System area | The completion status is stored upon completion of the instruction． <br> $\bullet 0000 \mathrm{H}:$ Completed successfully <br> $\cdot$ Other than $0000 \mathrm{H}:$ Completed with an error（error code） | - | - |
| +0 | Completion status | - | System |  |  |
| +1 |  |  |  |  |  |

## Processing details

- This instruction performs close processing for the connection specified by ( $\mathbf{s} 1$ ). The setting value used for open processing is selected by ( s 2 ) +0 .
- The execution of the SP.SOCCLOSE instruction and whether it has been completed normally or with an error can be checked with the completion device (d) or completion status indication device (d)+1.
- Completion device (d)

The completion device turns on in END processing of scan performed upon completion of the SP.SOCCLOSE instruction and turns off in the next END processing.

- Completion status indication device (d)+1

The completion device turns on or off depending on the completion status of the SP.SOCOPEN instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on in END processing of the scan performed upon completion of the SP.SOCCLOSE instruction and turns off in the next END processing

- The following figure shows the execution timing of the SP.SOCCLOSE instruction.


Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3405 H | The connection number specified by (s1) is a value other than 1 to 16. |

Upon completion with an error, the completion status indication device (d)+1 is turned on and an error code is stored in the completion status (s2)+1.
For the error code stored in the completion status (s2)+1, refer to the following.
$[\square]$ MELSEC iQ-R Ethernet User's Manual (Application)

## Point?

When a connection waiting for opening the SP.SOCCLOSE instruction is specified in TCP Passive mode, a successful completion occurs when the SP.SOCOPEN or SP.SOCCLOSE instruction is issued and the connection is closed.

### 8.2 Socket Communications Instructions

## Reading receive data during the END processing

## SP.SOCRCV

This instruction reads the receive data of the specified connection, during END processing after instruction execution, from the socket communications receive data area.


FBD/LD


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| SP.SOCRCV | - |

## Setting data

■Description, range, data type

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (U) | Dummy | - | String | ANYSTRING_SINGL <br> E |
| (s1) | Connection number | 1 to 16 | 16-bit signed binary | ANY16 |
| (s2) | Start device containing the control data | Refer to the control data. | Word | ANY16_ARRAY <br> (Number of elements: <br> 2) |
| (d1) | Start device for storing the receive data | - | Word | ANY16 |
| (d2) | Device that turns on one scan upon completion of <br> instruction <br> (d2)+1 also turns on when the instruction completes with <br> an error. | - | Bit | ANYBIT_ARRAY <br> (Number of elements: <br> $2)$ |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | BOOL |  |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> (U) |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | JロIロ | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | U밈, J미, U3E ${ }^{\text {I (H)G }}$ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (U) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | $\bigcirc$ | $\bigcirc$ |
| (s1) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | $\bigcirc$ | - | - | - |
| (s2) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (d1) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (d2) | $\bigcirc$ | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |

## Control data

| Operand: (s2) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +0 | System area | - | - | - |
| +1 | Completion status | The completion status is stored upon completion of the instruction. <br> - 0000H: Completed successfully <br> - Other than 0000 H : Completed with an error (error code) | - | System |
| Operand: (d1) |  |  |  |  |
| Device | Item | Description | Setting range | Set by |
| +0 | Receive data length | The length of the data read from the socket communications receive data area is stored. <br> (Number of bytes) | 0 to 10238 | System |
| +1 to + | Receive data | The data read from the socket communications receive data area is stored sequentially in ascending order of addresses. ${ }^{* 1}$ | - | System |

*1 The received data is stored in units of bytes sequentially from lower bytes. When an odd number of bytes of data is received, the last receive data is stored in the lower byte of the last data storage area.

## Point ${ }^{\rho}$

- When the SP.SOCRCV instruction is executed, receive data is read from the socket communications receive data area during END processing. For this reason, executing the SP.SOCRCV instruction prolongs the scan time.
- When an odd number of bytes of data is received, invalid data is stored in the higher byte of the device where the last receive data is stored.


## Processing details

- The SP.SOCRCV instruction reads the receive data of the connection specified by ( s 1 ) from the socket communications receive data area (where the data received from an external device in each connection is stored) by the END processing after the instruction execution.

- The execution of the SP.SOCRCV instruction and whether it has been completed normally or with an error can be checked with the completion device (d2) or completion status indication device (d2)+1.
- Completion device (d2)

The completion device turns on in END processing of the scan performed upon completion of the SP.SOCRCV instruction and turns off in the next END processing.

- Completion status indication device (d2)+1

The completion device turns on or off depending on the completion status of the SP.SOCRCV instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on in END processing of the scan performed upon completion of the SP.SOCRCV instruction and turns off in the next END processing.

- The following figure shows the execution timing of the SP.SOCRCV instruction.



## Precautions

When reading receive data from the same connection, do not use this command together with the S.SOCRCVS instruction.

## Operation error

| Error code (SD0) | Description |
| :--- | :--- |
| 2820 H | The amount of data received exceeds the relevant setting area in the device/label memory in the receive data storage device. |
| 3405 H | The connection number specified by (s1) is a value other than 1 to 16. |

Upon completion with an error, the completion status indication device (d2)+1 is turned on and an error code is stored in the completion status (s2)+1.
For the error code stored in the completion status ( s 2 ) +1 , refer to the following.
[] MELSEC iQ-R Ethernet User's Manual (Application)

- To avoid receiving too much amount of data, the SP.SOCRMODE instruction can be used to set the size of receive data to limit the amount of receive data.
- By connecting the completion device of the SP.SOCRCV instruction to the execution instruction through a normally closed contact, data can be read continuously even when it is received continuously.


## Reading receive data when the instruction is executed

## S．SOCRCVS

This instruction reads the receive data of the specified connection from the socket communications receive data area．


FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．SOCRCVS | $-\square$ |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| $($ U） | Dummy | - | String | ANYSTRING＿SINGL <br> E |
| （s） | Connection number | 1 to 16 | 16－bit unsigned binary | ANY16 |
| （d） | Start device for storing the receive data | - | Word | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDI（H）Gロ | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （U） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |


| Operand: (d) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +0 | Receive data length | The length of the data read from the socket communications receive data area is stored. <br> (Number of bytes) | 0 to 10238 | System |
| +1 to + $\square$ | Receive data | The data read from the socket communications receive data area is stored sequentially in ascending order of addresses. ${ }^{* 1}$ | - | System |

*1 The received data is stored in units of bytes sequentially from lower bytes. When an odd number of bytes of data is received, the last receive data is stored in the lower byte of the last data storage area.

## Point $\rho$

- The default receive data size is 2046 bytes. To receive 2047 bytes of data or more, change the receive data size using the SP.SOCRMODE instruction.
- When an odd number of bytes of data is received, invalid data is stored in the higher byte of the device where the last receive data is stored.


## Processing details

- The S.SOCRCVS instruction reads the receive data of the connection specified by (s) from the socket communications receive data area (where the data received from an external device in each connection is stored).

- The following figure shows the timing of receive processing using the S.SOCRCVS instruction.



## Precautions

When reading receive data from the same connection, do not use this command together with the SP.SOCRCV instruction.

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2820 H | The amount of data received exceeds the relevant setting area in the device/label memory in the receive data storage device. |
| 3405 H | The connection number specified by (s) is a value other than 1 to 16. |

## Point $\rho$

To avoid receiving too much amount of data, the SP.SOCRMODE instruction can be used to set the size of receive data to limit the amount of receive data.

## Sending data

## SP．SOCSND

This instruction sends the data to the external device of the specified connection．


FBD／LD


## EExecution condition

| Instruction | Execution condition |
| :--- | :--- |
| SP．SOCSND | $\ddots$ |

Setting data
■Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （U） | Dummy | - | String | ANYSTRING＿SINGL <br> E |
| （s1） | Connection number | 1 to 16 | 16－bit signed binary | ANY16 |
| （s2） | Start device containing the control data | Refer to the control data． | Word | ANY16＿ARRAY <br> （Number of elements： <br> 2） |
| （s3） | Start device for storing the send data | Device that turns on one scan upon completion of <br> instruction <br> If the instruction is completed with an error，（d）＋1 is also <br> turned on． | - | Word |
| （d） | Execution condition | - | Bit | ANY16 |
| EN | Execution result | - | Bit | （Number of elements： |
| ENO | Bit | BOOL |  |  |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J미， U3EDI（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （U） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s3） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Control data

| Operand: (s2) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +0 | System area | - | - | - |
| +1 | Completion status | The completion status is stored upon completion of the instruction. <br> - 0000H: Completed successfully <br> - Other than 0000 H : Completed with an error (error code) | - | System |
| Operand: (s3) |  |  |  |  |
| Device | Item | Description | Setting range | Set by |
| +0 | Send data length | Specify the send data length. (Number of bytes) | 1 to 10238 | User |
| +1 to + $\square$ | Send data | Specify the send data. ${ }^{*}$ | - | User |

*1 The send data is sent in units of bytes sequentially from lower bytes. When an odd number of bytes of data is received, the last send data is stored in the lower byte of the last data storage area.

## Point $\%$

When TCP is used, the send data length should be equal to or less than the maximum window size (TCP receive buffer) of the external device. Data which exceeds the maximum window size of the external device cannot be sent.

- Sends the data in the device specified by (s3) to the external device of the connection specified by (s1).

- The execution of the SP.SOCSND instruction and whether it has been completed normally or with an error can be checked with the completion device (d) or completion status indication device (d)+1.
- Completion device (d)

The completion device turns on in END processing of the scan performed upon completion of the SP.SOCSND instruction and turns off in the next END processing.

- Completion status indication device (d)+1

The completion device turns on or off depending on the completion status of the SP.SOCSND instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on in END processing of the scan performed upon completion of the SP.SOCSND instruction and turns off in the next END processing.

- The following figure shows the timing of receive processing using the SP.SOCSND instruction.


Even after the completion device turns on, data may be sent continuously.

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3405 H | The connection number specified by $(\mathrm{s} 1)$ is a value other than 1 to 16. |

Upon completion with an error, the completion status indication device (d) +1 is turned on and an error code is stored in the completion status (s2)+1.
For the error code stored in the completion status (s2)+1, refer to the following.
[] MELSEC iQ-R Ethernet User's Manual (Application)

## Reading connection information

## SP．SOCCINF

This instruction reads the connection information of the connection specified by（s1）and stores it in the device specified by（d） and later．

| Ladder |  |  |  |  | $\begin{aligned} & \text { ST } \\ & \text { ENO:=SP_SOCCINF(EN,U,s1,s2,d); } \end{aligned}$ |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  |  |  |
|  |  |  |  |  |  |  |

FBD／LD

| ■－－－ |  |
| :---: | :---: |
| EN | ENo |
| U | d |
| s1 |  |
| s2 |  |

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| SP．SOCCINF | $\ddots$ |
|  | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （U） | Dummy | - | String | ANYSTRING＿SINGL <br> E |
| （s1） | Connection number | 1 to 16 | 16－bit unsigned binary | ANY16 |
| （s2） | Start device containing the control data | Refer to the control data． | Word | ANY16＿ARRAY <br> （Number of elements： <br> 2） |
| （d） | Head device for storing connection information | - | Word | ANY16＿ARRAY <br> （Number of elements： <br> $5)$ |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （U） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Control data

| Operand: (s2) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +0 | System area | - | - | - |
| +1 | Completion status | The completion status is stored upon completion of the instruction. <br> - 0000H: Completed successfully <br> - Other than 0000 H : Completed with an error (error code) | - | System |
| Operand: (d) |  |  |  |  |
| Device | Item | Description | Setting range | Set by |
| $\begin{aligned} & +0 \\ & +1 \end{aligned}$ | IP address of external device | Store the IP address of an external device. | 00000001 H to FFFFFFFFH 00000000H: No communication destination (FFFFFFFFH: simultaneous broadcast) | System |
| +2 | Destination port number | Store the destination port number of an external device. | 0001 H to FFFFH <br> (FFFFH: <br> Simultaneous broadcast) | System |
| +3 | Own station port number. | Store the own station port number. | 0001 H to 1387 H 1392 H to FFFEH | System |
| +4 | Connection use application | Store the usage of a connection. <br> (1) Communication method (protocol) <br> - 0: TCP/IP <br> - 1: UDP/IP <br> (2) Socket communications function procedure <br> - 1: No procedure (fixed) <br> (3) Open method <br> - 00: Active open or UDP/IP <br> - 10: Unpassive open <br> - 11: Fullpassive open | - | System |

## Processing details

Reads the connection information of the connection specified by (s1).

## Operation error

| Error code (SD0) | Description |
| :--- | :--- |
| 3405 H | The connection number specified by (s1) is a value other than 1 to 16. |

When completed with an error, an error code is stored in the completion status (s2)+1.
For the error code stored in the completion status (s2)+1, refer to the following.
[] MELSEC iQ-R Ethernet User's Manual (Application)

## Changing the communication target（UDP／IP）

## SP．SOCCSET

This instruction changes the communication target IP address and port number of the specified connection．


FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| SP．SOCCSET | - |
|  | - |

Setting data
Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| $(\mathrm{U})$ | Dummy | - | String | ANYSTRING＿SINGL <br> E |
| （s1） | Connection number | 1 to 16 | 16－bit unsigned binary | ANY16 |
| （s2） | Start device containing the control data | Refer to the control data． | Word | ANY16＿ARRAY <br> （Number of elements： <br> $5)$ |
| EN | Execution condition |  |  | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J밈， U3E미（H）Gㅁ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （U） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

Control data

| Operand: (s2) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +0 | System area | - | - | - |
| +1 | Completion status | The completion status is stored upon completion of the instruction. <br> - 0000H: Completed successfully <br> - Other than 0000 H : Completed with an error (error code) | - | System |
| $\begin{aligned} & +2 \\ & +3 \end{aligned}$ | IP address of external device | Store the IP address of an external device. | 00000001H to FFFFFFFFH (FFFFFFFFH: simultaneous broadcast) | User |
| +4 | Destination port number | Store the destination port number of an external device. | 0001H to FFFFH <br> (FFFFH: <br> simultaneous <br> broadcast) | User |

## Processing details

In UDP/IP communications, this instruction changes the communication target IP address and port number of the connection specified by (s1)

## Point ${ }^{\rho}$

- Using the SP.SOCCSET instruction allows the user to change the communication destination without closing the connection.
- If the SP.SOCCSET instruction is executed while there is data in the receive data area, the instruction is validated after the SP.SOCRCV or S.SOCRCVS dedicated instruction is executed. If the SP.SOCCSET instruction is executed while there is no data in the receive data area, the instruction is validated soon after it is executed.


## Precautions

Do not use the SP.SOCCSET instruction to change the communication destination during execution of the SP.SOCSND instruction.

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3405 H | The connection number specified by ( s 1 ) is a value other than 1 to 16. |

When completed with an error, an error code is stored in the completion status (s2)+1.
For the error code stored in the completion status (s2)+1, refer to the following.
[] MELSEC iQ-R Ethernet User's Manual (Application)

## Changing the receive mode

## SP．SOCRMODE

This instruction changes the TCP receive mode and receive data size for the specified connection（invalid for UDP communications connections）．


FBD／LD

|  |  |
| :---: | :---: |
|  |  |
| u |  |
| s1 |  |
| s2 |  |

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| SP．SOCRMODE | - |

## Setting data

## Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （U） | Dummy | - | String | ANYSTRING＿SINGL <br> E |
| （s1） | Connection number | 1 to 16 | ANY16 |  |
| （s2） | Start device containing the control data | Refer to the control data． | Word | ANY16＿ARRAY <br> （Number of elements： <br> $4)$ |
| EN |  |  |  | BOOL |
| ENO | Execution condition | - | Bit | Bit |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDl（H）G口 | z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （U） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Control data

| Operand：（s2） |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| ＋0 | System area | － | － | － |
| ＋1 | Completion status | The completion status is stored upon completion of the instruction． <br> －0000H：Completed successfully <br> －Other than 0000 H ：Completed with an error（error code） | － | System |
| ＋2 | TCP receive mode＊1 | Store the TCP receive mode． <br> －0：TCP standard receive mode <br> －1：TCP fixed－length receive mode | 0， 1 | User |
| ＋3 | Receive data size | Store the size of socket communications receive data（Number of bytes） | 1 to 10238 | User |

＊1 This item is invalid for UDP communications connections．

## Processing details

- This instruction changes the TCP receive mode and receive data size for the connection (other than a UDP communications connection) specified by ( s 1 ).
- For TCP connections, the function enables the mode specified by (s2)+2.


## ITCP standard receive mode

Upon receipt of data, the instruction stores the data in the socket communications receive data area and turns on SD1506 (socket communications receive status signal).
If the received data exceeds the specified receive data size, the excess data becomes the next receive data.
If data is received later before data is read from the socket communications receive data area using the SP.SOCRCV or S.SOCRCVS instruction, it is stored in the receive data area in the OS.

If the receive data area in the OS contains data when data is read from the socket communications receive data area using the SP.SOCRCV or S.SOCRCVS instruction, the instruction stores the data in the socket communications receive data area and turns on SD1506 (socket communications receive status signal).

Ex.
When 500 bytes of data is received while the receive data size is set to 300 bytes


## TCP fixed-length receive mode

Upon receipt of data, the instruction stores the data in the socket communications receive data area. If the specified receive data size is not reached, SD1506 (socket communications receive status signal) does not turn on.
Data reception is repeated until the received data reaches the receive data size. When it reaches the receive data size, SD1506 (socket communications receive status signal) turns on.

If the received data exceeds the specified receive data size, the excess data becomes the next receive data
If data is received later before data is read from the socket communications receive data area using the SP.SOCRCV or S.SOCRCVS instruction, it is stored in the receive data area in the OS.

If the receive data area in the OS contains data when data is read from the socket communications receive data area using the SP.SOCRCV or S.SOCRCVS instruction, the instruction stores the data in the socket communications receive data area, but does not turn on SD1506 (socket communications receive status signal) if the data has not reached the specified receive data size.

Thereafter, data reception is repeated until the received data reaches the receive data size. When it reaches the receive data size, SD1506 (socket communications receive status signal) turns on.

## Ex.

When 200 bytes of data is received continuously while the receive data size is set to 300 bytes


## Point ${ }^{\circ}$

- Effective use of devices

The receive data storage device used by the SP.SOCRCV or S.SOCRCVS instruction needs a 1024-word area by default. Specifying the receive data size in 1024 words or less enables effective use of the device.

- Preventing receive data from being divided

Depending on the line type, data to be received from the external device may be divided before arrival. In this case, specifying the receive data size in TCP fixed-length receive mode can prevent receive data from being divided.

- Preventing receive data from being connected

Due to a delay in receive processing of the sequence program, data which has been divided and sent may be connected before receiving depending on the external device.
Specifying the receive data size in TCP fixed-length receive mode enables data to be correctly divided and received.

- The size of the receive data to be read once by the SP.SOCRCV or S.SOCRCVS instruction is specified in (s2)+3. In the case of UDP, if the received data exceeds the specified receive data size, the excess data becomes the next receive data.


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3405 H | The connection number specified by (s1) is a value other than 1 to 16. |

When completed with an error, an error code is stored in the completion status (s2)+1.
For the error code stored in the completion status (s2)+1, refer to the following.
[] MELSEC iQ-R Ethernet User's Manual (Application)

## Point ${ }^{\circ}$

When the receive status signal does not turn on in TCP fixed-length receive mode, the data received as of the current time can be read with the SP.SOCRDATA instruction to check whether the data sent from the external device is missing.

## Reading socket communications receive data

## S（P）．SOCRDATA

These instructions read data by the number of words specified by（ $n$ ）from the socket communications receive data area of the connection specified by（ s 1 ），and store them in the device specified by（d）and later．


FBD／LD


## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．SOCRDATA | - |
|  | $\boxed{ }$ |
| SP．SOCRDATA |  |

## Setting data

## ■Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （U） | Dummy | - | String | ANYSTRING＿SINGL <br> E |
| （s1） | Connection number | 1 to 16 | 16－bit signed binary | ANY16 |
| （s2） | Start device containing the control data | Refer to the control data． | Word | ANY16＿ARRAY <br> （Number of elements： <br> $2)$ |
| （d） | Head device for storing the data that has been read | - | Word | ANY16 |
| （n） | Number of read data | 1 to 5120 | 16－bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## －Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）GD | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （U） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Control data

| Operand: (s2) | Description | Setting range | Set by |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Item | - | - |  |
| +0 | System area | The completion status is stored upon completion of the instruction. <br> $-0000 \mathrm{H}:$ Completed successfully <br> - Other than $0000 \mathrm{H}:$ Completed with an error (error code) | - |  |
| +1 | Completion status |  |  |  |

## Processing details

These instructions read data by the number of words specified by ( n ) from the socket communications receive data area of the connection specified by ( s 1 ), and store them in the device specified by (d) and later. If the read data ( n ) is 0 , no processing is performed.

## Point/

- The receive data length can be read by setting the number of read data to 1 word. As a result, the device for storing receive data when the SP.SOCRCV or S.COSCRCVS instruction is executed can be changed.
- After issuing the $S(P)$.SOCRDATA instruction to check the data to be received this time and issuing the SP.SOCRMODE instruction to specify the size of the data to be received next time, the SP.SOCRCV or S.SOCRCVS instruction can be used to read the data of this time. As a result, based on the data received this time, the size of data to be received next can be specified.


## Operation error

| Error code (SD0) | Description |
| :--- | :--- |
| 3405 H | The connection number specified by $(\mathrm{s} 1)$ is a value other than 1 to 16. |
|  | The value of the device specified by $(\mathrm{n})$ exceeds 5120. |

When completed with an error, an error code is stored in the completion status (s2)+1.
For the error code stored in the completion status (s2)+1, refer to the following.
[] MELSEC iQ-R Ethernet User's Manual (Application)

### 8.3 Predefined Protocol Support Function Instruction

## Executing the registered protocols

## SP.ECPRTCL

This instruction executes the protocol that has been set by the predefined protocol support function.


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| SP.ECPRTCL | - |

## Setting data

■Descriptions, ranges, and data types

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (U) | Dummy | - | String | ANYSTRING_SINGL <br> E |
| (s1) | Connection number | 1 to 16 | 16-bit unsigned binary | ANY16 |
| (s2) | Number of protocols to be executed continuously | 1 to 8 | 16-bit unsigned binary | ANY16 |
| (s3) | Start device containing the control data | Refer to the control data. | Word | ANY16_ARRAY <br> (Number of elements: <br> 2) |
| (d) | Device that turns on one scan upon completion of <br> instruction <br> If the instruction is completed with an error, (d)+1 is also <br> turned on. | - | Bit | ANYBIT_ARRAY <br> (Number of elements: <br> 2) |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> (U) |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | Jロ\|ロ | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | U밈, J밈, U3EDl(H)GD | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (U) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | $\bigcirc$ | $\bigcirc$ |
| (s1) | $\bigcirc$ | - | $\bigcirc^{* 1}$ | - | - | - | - | $\bigcirc^{* 1}$ | $\bigcirc$ | - | - | - |
| (s2) | $\bigcirc$ | - | $\mathrm{O}^{* 1}$ | - | - | - | - | $\bigcirc{ }^{* 1}$ | $\bigcirc$ | - | - | - |
| (s3) | $\bigcirc$ | - | ${ }^{* 1}$ | - | - | - | - | $\bigcirc{ }^{* 1}$ | - | - | - | - |
| (d) | $\mathrm{O}^{* 1}$ | - | $\bigcirc$ | - | - | - | - | $O^{* 1}$ | - | - | - | - |

[^26]| Operand: (s3) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +0 | Resulting number of executed protocols | The number of protocols executed by the SP.ECPRTCL instruction is stored. Any protocol where an error occurred is also included in the execution number. If the setting of setting data or control data contains an error, "0" is stored. | 0, 1 to 8 | System |
| +1 | Completion status | The completion status is stored upon completion of the instruction. <br> When two or more protocols are executed, the execution result of the protocol executed last is stored. <br> - 0: Completed successfully <br> - Other than 0: Completed with an error (error code) | - | System |
| +2 | Execution protocol number 1 | Specify the number of the protocol to be executed first. | 1 to 128 | User |
| +3 | Execution protocol number 2 | Specify the number of the protocol to be executed second. | 0, 1 to 128 | User |
| +4 | Execution protocol number 3 | Specify the number of the protocol to be executed third. | 0, 1 to 128 | User |
| +5 | Execution protocol number 4 | Specify the number of the protocol to be executed fourth. | 0, 1 to 128 | User |
| +6 | Execution protocol number 5 | Specify the number of the protocol to be executed fifth. | 0, 1 to 128 | User |
| +7 | Execution protocol number 6 | Specify the number of the protocol to be executed sixth. | 0, 1 to 128 | User |
| +8 | Execution protocol number 7 | Specify the number of the protocol to be executed seventh. | 0, 1 to 128 | User |
| +9 | Execution protocol number 8 | Specify the number of the protocol to be executed eighth. | 0, 1 to 128 | User |
| +10 | Collation match Receive packet number 1 | If receiving is included in the communication type of the protocol that has been executed first, the receive packet number successful in collation match is stored. If the communication type is "receive only", " 0 " is stored. If an error occurs during execution of the first protocol, " 0 " is stored. | 0, 1 to 16 | System |
| +11 | Collation match Receive packet number 2 | If receiving is included in the communication type of the protocol that has been executed second, the receive packet number successful in collation match is stored. If the communication type is "receive only", "0" is stored. If an error occurs during execution of the second protocol, " 0 " is stored. If the number of protocols executed is less than 2, " 0 " is stored. | 0,1 to 16 | System |
| +12 | Collation match Receive packet number 3 | If receiving is included in the communication type of the protocol that has been executed third, the receive packet number successful in collation match is stored. If the communication type is "receive only", " 0 " is stored. If an error occurs during execution of the third protocol, " 0 " is stored. If the number of protocols executed is less than 3, " 0 " is stored. | 0,1 to 16 | System |
| +13 | Collation match Receive packet number 4 | If receiving is included in the communication type of the protocol that has been executed fourth, the receive packet number successful in collation match is stored. If the communication type is "receive only", " 0 " is stored. If an error occurs during execution of the fourth protocol, " 0 " is stored. If the number of protocols executed is less than 4, " 0 " is stored. | 0, 1 to 16 | System |
| +14 | Collation match Receive packet number 5 | If receiving is included in the communication type of the protocol that has been executed fifth, the receive packet number successful in collation match is stored. If the communication type is "receive only", " 0 " is stored. If an error occurs during execution of the fifth protocol, " 0 " is stored. If the number of protocols executed is less than $5, ~ " 0$ " is stored. | 0, 1 to 16 | System |
| +15 | Collation match Receive packet number 6 | If receiving is included in the communication type of the protocol that has been executed sixth, the receive packet number successful in collation match is stored. If the communication type is "receive only", " 0 " is stored. If an error occurs during execution of the sixth protocol, " 0 " is stored. If the number of protocols executed is less than $6, ~ " 0 "$ is stored. | 0, 1 to 16 | System |
| +16 | Collation match Receive packet number 7 | If receiving is included in the communication type of the protocol that has been executed seventh, the receive packet number successful in collation match is stored. If the communication type is "receive only", "0" is stored. If an error occurs during execution of the seventh protocol, " 0 " is stored. If the number of protocols executed is less than $7, " 0$ " is stored. | 0, 1 to 16 | System |


| Operand: (s3) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +17 | Collation match Receive packet number 8 | If receiving is included in the communication type of the protocol that has been executed eighth, the receive packet number successful in collation match is stored. If the communication type is "receive only", " 0 " is stored. If an error occurs during execution of the eighth protocol, " 0 " is stored. If the number of protocols executed is less than $8, " 0$ " is stored. | 0, 1 to 16 | System |

## Processing details

- This instruction executes the protocol registered using the engineering tool. Using the connection specified by (s1), the instruction executes the protocol in accordance with the control data stored in the device specified by (s3) and later.
- The instruction continuously executes as many protocols as specified by (s2) (a maximum of 8 protocols) at one time.
- The number of executed protocols is stored in the device specified by $(\mathrm{s} 3)+0$.
- The protocol execution status can be checked with the predefined protocol support function execution status check area (Un\G350 to UnlG669). (■] MELSEC iQ-R Ethernet User's Manual (Application))
- The execution of the SP.ECPRTCL instruction and whether it has been completed normally or with an error can be checked with the completion device (d) or completion status indication device (d) +1 .
- Completion device (d)

The completion device turns on in END processing of the scan performed upon completion of the SP.ECPRTCL instruction and turns off in the next END processing.

- Completion status indication device (d)+1

The completion device turns on or off depending on the completion status of the SP.ECPRTCL instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on in END processing of the scan performed upon completion of the SP.ECPRTCL instruction and turns off in the next END processing. When completed with an error, an error code is stored in the device (completion status) specified by ( s 3 ) +1 .

- The following figure shows the SP.ECPRTCL instruction execution timing.

- Protocol execution can be canceled by setting a protocol cancel request. The protocol cancel request is specified in the predefined protocol support function execution status check area (UnlG350 to UnlG669). ([D] MELSEC iQ-R Ethernet User's Manual (Application))

- The following figure shows the protocol cancel operations from time to time.
- If a cancel request is issued before transmission

The following figure shows the operation when the protocol execution status is "1: Waiting for transmission".


- If a cancel request is issued before completion of transmission

The following figure shows the operation when transmission has not been completed while the protocol execution status is "2: Sending".


- If a cancel request is issued upon completion of transmission

The following figure shows the operation when transmission has been completed while the protocol execution status is "2: Sending".


- If a cancel request is issued while waiting for reception

The following figure shows the operation when the protocol execution status is " 3 : Waiting for data reception".


External device

- If a cancel request is issued during receiving

The following figure shows the operation when the protocol execution status is "4: Receiving".


| Error code (SDO) | Description |
| :--- | :--- |
| 3405 H | (s1) is not a value in the range from 1 to 16. |

Upon completion with an error, the completion status indication device (d)+1 is turned on and an error code is stored in the completion status (s3)+1.
For the error code stored in the completion status (s3)+1, refer to the following.
[] MELSEC iQ-R Ethernet User's Manual (Application)

## Precautions

- If an error occurs in the mth protocol while multiple protocols are being executed, the instruction does not execute the " $\mathrm{m}+1$ "th protocol and after and is completed with an error.
- When a protocol including no-conversion variables is executed, the total data length of the variables used in one packet may exceed 1920 bytes. In this case, the instruction may obtain CPU device values over several scans. Therefore, do not change the CPU device values specified in non-conversion variables from the start of the instruction to the end of execution.
- Protocol cancellation
- The SP.ECPRTCL instruction is completed with an error and stores the protocol cancel request error (C404H) in the device (completion status) specified by (s3)+1.
- If a cancel request is issued while no protocol is being executed, the CPU module completes the cancel request without performing any processing.
- While no communication protocol is used, any cancel request is ignored if issued.
- When multiple protocols are executed continuously, a cancel request may be issued during execution of the nth protocol. In this case, the CPU module forcibly terminates the $n$th protocol and does not execute the subsequent protocols. Protocol number $n$ being executed is stored in the device specified by $((\mathrm{s} 3)+0)$, the receive packet number successful in comparison match is stored in the device specified by 1 to ( $\mathrm{n}-1$ ), and the protocol cancel request error $(\mathrm{C} 404 \mathrm{H})$ is stored in the device specified by $((\mathrm{s} 3)+1)$.
- The CPU module periodically checks for a cancel request. For this reason, it may take time until cancel processing is performed after a cancel request is issued.
- The SP.ECPRTCL instruction itself does not open/close a connection and therefore the SP.SOCOPEN/SOCCLOSE instructions need to be used to open/close the connection.
$\longmapsto$ Page 928 SP.SOCOPEN, Page 931 SP.SOCCLOSE
- If same instructions are executed for the same connection, the subsequent instruction is ignored and is not executed until the preceding instruction is completed.
- If the receive waiting time is set to " 0 : Infinite wait", the SP.ECPRTCL instruction is not completed until the data specified in the protocol setting is received.


## 9．1 PID Control Instructions（Inexact Differential）

## Registering the PID control data to the CPU module

## S（P）．PIDINIT

These instructions store the PID control data by the number of loops used that is set in the specified device number and later altogether in the CPU module．

| Ladder | ST |
| :---: | :---: |
|  | $\begin{aligned} & \text { ENO:=S_PIDINIT(EN,s); } \\ & \text { ENO:=SP_PIDINIT(EN,s); } \end{aligned}$ |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．PIDINIT | - |
|  | $\boxed{ }$ |
| SP．PIDINIT | $\boxed{ }$ |

Setting data
■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| $(\mathrm{s})$ | Start device where the PID control data is set | - | Word | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3EDI（H）G口 | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions store the PID control data by the number of loops used that is set in the device number specified by (s) and later altogether in the CPU module to enable PID control.
- The PID control data are assigned as follows.

(1) Fixed to "0". An error results if a value other than "0" is specified.
- The number of device points used for PID control data setting is calculated by the following formula.

Number of device points $=2+14 \times \mathrm{n}$ ( n : number of loops used)

- Specify each data in binary.
- If the $S(P)$.PIDINIT instruction is executed at two or more locations during a single scan, the setting value of the S(P).PIDINIT instruction executed nearest to the $S(P)$.PIDCONT instruction will be valid.
- Execute the S(P).PIDINIT instruction before execution of the S(P).PIDCONT instruction. To perform PID control, the $S(P)$.PIDINIT instruction must be executed.


## Operation error

| Error code (SDO) | Description |
| :---: | :---: |
| 3405H | Out-of-range data is set in the device specified by (s). <br> - The value set for the PID control data is out of the setting range. <br> - (Number of loops used) < (number of execution loops in one scan) <br> - (Upper limit of manipulated value) < (lower limit of manipulated value) <br> - The area fixed to 0 in the PID control data is not 0 . |

## Performing PID operation

## S(P).PIDCONT

These instructions measure the sampling cycle and perform PID operation when the execution command turns on.


## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S.PIDCONT | - |
|  | $\boxed{ }$ |
| SP.PIDCONT | - |

Setting data

## ■Description, range, data type

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (s) | Head device allocated to the I/O data area | - | Word | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ\|ロ | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | U밈, J밈, U3EDl(H)GD | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (s) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |

## Processing details

- The S(P).PIDCONT instructions measure the sampling cycle and perform PID operation.
- Based on the setting value (SV) and process value (PV) in the I/O data area allocated to the device number specified by (s) and later, these instructions perform PID operation and store the operation result in the automatic manipulated value (MV) area in the I/O data area.
- The $S(P)$.PIDCONT instructions perform PID operation when it is executed for the first time after a lapse of the specified sampling cycle.
- During PID control, be sure to turn on the control command to allow the $S(P)$. PIDCONT instruction to be executed every scan. Failure to execute the instruction every scan disables PID operation in normal sampling cycles. The S(P).PIDCONT instruction cannot be executed more than once in a single scan. Executing the instruction more than once in a single scan disables PID operation in normal sampling cycles.
- The $S(P)$.PIDCONT instruction cannot be written and used in interrupt programs. Writing an S(P).PIDCONT instruction in the interrupt program disables PID operation in normal sampling cycles.
- In (s), specify the head of the device number specified in the I/O data area.
- If a file register is specified as an I/O data area, do not apply memory protection for the file register. If memory protection is applied, normal PID operation is disabled although no error results.
- The I/O data are assigned as follows.

- The number of device points used for I/O data setting is calculated by the following formula.

Number of device points $=10++23 \times n$ ( n : number of loops used)

- Specify each data in binary.
- The initial processing flag sets the processing to be performed at the start of PID operation.
- Initial operation processing is performed assuming that the sampling cycle that has been set has been reached.
- If the initial processing flag is 0 , PID operations for the number of loops used are performed altogether in a single scan. If it is not 0 , PID operations for the number of loops used are divided and processed in several scans, and sampling is started sequentially from the loop that has completed initial processing. The number of processing loops per scan is the number of execution loops per scan that has been set.
- Write data to the I/O data "Write" area by users with the program. Users can read data from the I/O data "Read" area with the program. Never attempt to write data to the area indicated by "Read/write disabled" or "Read"; otherwise, normal operation can no longer be performed. Note that, when starting control from the initial status, the data areas must be cleared by the program.
- Even when the manual manipulated value ( $\mathrm{MV}_{\text {MAN }}$ ) is output in manual mode, execute the $\mathrm{S}(\mathrm{P})$.PIDCONT instruction every scan. Unless the S(P).PIDCONT instruction is executed, the bumpless function cannot be performed.
- Apply an interlock using the READY signal of each module so that the $S(P)$.PIDCONT instruction is executed only when the A/D converter module used to obtain the process value (PV) and the D/A converter module used to output the manipulated value (MV) are normal.


If the instruction is executed when these modules are not normal, PID operation cannot be performed normally as the result of failure in normal acquisition of process values (PV) or in normal output of manipulated values (MV).

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3405 H | The value of the data set in the I/O data area specified by (s) is out of the setting range. |
| 3422 H | The S(P).PIDINIT instruction is not executed before the S(P).PIDCONT instruction. |

## Stopping the operation of specified loop number

## S（P）．PIDSTOP

These instructions stop the PID operation of the specified loop number．

| Ladder | ST |
| :---: | :---: |
|  | $\begin{aligned} & \text { ENO:=S_PIDSTOP(EN,s); } \\ & \text { ENO:=SP_PIDSTOP(EN,s); } \end{aligned}$ |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．PIDSTOP | - |
| SP．PIDSTOP | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| $(\mathbf{s})$ | Loop number to be stopped | 1 to 32 | 16－bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3E미（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

－These instructions stop the PID operation of the loop number in the device specified by（ s ）．The loop stopped by the S（P）．PIDSTOP instruction does not restart PID operation even if the S（P）．PIDINIT instruction is executed．
－Each instruction holds operation data while the loop is stopped．

## Operation error

| Error code（SDO） | Description |
| :--- | :--- |
| 3405 H | Out－of－range data is set in the device specified by（s）． <br> －The specified loop number does not exist． <br> • The specified value is other than 1 to 32. |
| 3422 H | The S（P）．PIDINIT and S（P）．PIDCONT instructions are not executed before the S（P）．PIDSTOP instruction． |

## Starting the operation of specified loop number

## S（P）．PIDRUN

These instructions start the PID operation of the specified loop number．

| Ladder | ST |  |
| :--- | :--- | :--- |
|  |  | ENO：＝S＿PIDRUN（EN，s）； |
| $\square-\square$ | ENO：＝SP＿PIDRUN（EN，s）； |  |
|  |  |  |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．PIDRUN | - |
| SP．PIDRUN | $\boxed{ }$ |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| $(\mathrm{s})$ | Loop number to be stopped | 1 to 32 | 16－bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3E미（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

－These instructions start the PID operation of the loop number in the device specified by（s）．These instructions are used to re－execute the PID operation of the loop number that has been stopped by the S（P）．PIDSTOP instruction．
－The S（P）．PIDRUN instruction，if executed for a loop number already in progress of PID operation，performs no processing．

## Operation error

| Error code（SDO） | Description |
| :--- | :--- |
| 3405 H | The loop number specified by（s）does not exist． |
|  | （s）is outside the range from 1 to 32. |
| 3422 H | The S（P）．PIDINIT and S（P）．PIDCONT instructions are not executed before the S（P）．PIDRUN instruction． |

## Changing the parameters of specified loop number

## S（P）．PIDPRMW

These instructions change the operation parameter of the specified loop number to the PID control data stored in the specified device number and later．


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．PIDPRMW | - |
|  | - |
| SP．PIDPRMW | - |

Setting data
Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| $(\mathrm{s} 1)$ | Loop number to be changed | 1 to 32 | 16－bit unsigned binary | ANY16 |
| $(\mathrm{s} 2)$ | Head device containing the PID control data to be changed | - | Word | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & X, Y, M, L, S M, \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロום | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3E미（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- Changes the operation parameter of the loop number in the device specified by (s1) to the PID control data stored in the device number specified by (s2) and later.
- The following figure shows the configuration of the PID control data in the device specified by (s2) and later.

| (s2) +0 | Operational expression selection |
| :---: | :---: |
| (s2) +1 | Sampling cycle ( $\mathrm{T}_{\mathrm{S}}$ ) |
| (s2)+2 | Proportional constant ( $\mathrm{K}_{\mathrm{P}}$ ) |
| (s2) +3 | Integral constant ( $\mathrm{T}_{\mathrm{l}}$ ) |
| (s2) +4 | Derivative constant ( $\mathrm{T}_{\mathrm{D}}$ ) |
| (s2)+5 | Filter coefficient ( $\alpha$ ) |
| (s2)+6 | Manipulated value lower limit (MVLL) |
| (s2)+7 | Manipulated value upper limit (MVHL) |
| (s2)+8 | Manipulated value change rate limit ( $\triangle \mathrm{MVL}$ ) |
| (s2) +9 | Process value change rate limit ( $\triangle \mathrm{PVV}$ ) |
| (s2)+10 | 0 |
| (s2)+11 | Derivative gain ( $\mathrm{K}_{\mathrm{D}}$ ) |
| (s2)+12 | 0 |
| (s2)+13 | 0 |

## Operation error

| Error code (SD0) | Description |
| :--- | :--- |
| 3405 H | Out-of-range data is set in the device specified by (s1). <br> • The specified loop number does not exist. <br> • The specified value is other than 1 to 32. |
|  | Out-of-range data is set in the device specified by (s2). <br> The PID control data is out of the setting range. <br> The PID control data in the devices specified by (s2)+10, (s2)+12, and (s2)+13 is not 0. |
| 3422 H | The S(P).PIDINIT instruction is not executed before the S(P).PIDPRMW instruction. |

## 9．2 PID Control Instructions（Exact Differential）

## Registering the PID control data to the CPU module

## PIDINIT（P）

These instructions store the PID control data by the number of loops used that is set in the specified device number and later altogether in the CPU module．

| Ladder | ST |  |  |  |
| :--- | :--- | :---: | :---: | :---: |
| $-\square-\square$ （s） <br>   |  |  |  | ENO：＝PIDINIT（EN，s）； <br> ENO：＝PIDINITP（EN，s）； |

FBD／LD


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| PIDINIT | - |
|  | - |
| PIDINITP |  |

Setting data
Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| $(\mathrm{s})$ | Start device where the PID control data is set | - | Word | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロID， U3E미（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- These instructions store the PID control data by the number of loops used that is set in the device number specified by (s) and later altogether in the CPU module to enable PID control.
- The PID control data are assigned as follows.

- The number of device points used for PID control data setting is calculated by the following formula.

Number of device points $=2+10 \times n$ ( n : number of loops used)

- Specify each data in binary.
- If the PIDINIT(P) instruction is executed at two or more locations during a single scan, the setting value of the PIDINIT(P) instruction executed nearest to the PIDCONT $(P)$ instruction will be valid.
- Execute the PIDINIT(P) instruction before execution of the PIDCONT $(P)$ instruction. PID control is disabled unless the PIDINIT(P) instruction has been executed.

Operation error

| Error code (SDO) | Description |
| :---: | :---: |
| 3405H | Out-of-range data is set in the device specified by (s). <br> - The value set for the PID control data is out of the setting range. <br> - (Number of loops used) < (number of execution loops in one scan) <br> - (Upper limit of manipulated value) < (lower limit of manipulated value) |

## Performing PID operation

## PIDCONT(P)

These instructions measure the sampling cycle and perform PID operation when the execution command turns on.

| Ladder | ST |
| :---: | :---: |
|  | $\begin{aligned} & \text { ENO:=PIDCONT(EN,s); } \\ & \text { ENO:=PIDCONTP(EN,s); } \end{aligned}$ |

FBD/LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| PIDCONT | - |
|  | $\boxed{ }$ |
| PIDCONTP |  |

## Setting data

■Description, range, data type

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| $(\mathrm{s})$ | Head device allocated to the I/O data area | - | Word | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | J밈 | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | U밈, J밈, U3EDI(H)Gロ | Z | LT, LST, LC | LZ |  | K, H | E | \$ |  |
| (s) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |

## Processing details

- The PIDCONT $(P)$ instructions measure the sampling cycle and perform PID operation.
- Based on the setting value (SV) and process value (PV) in the I/O data area allocated to the device number specified by (s) and later, these instructions perform PID operation and store the operation result in the automatic manipulated value (MV) area in the I/O data area.
- The PIDCONT(P) instructions perform PID operation when it is executed for the first time after a lapse of the specified sampling cycle.
- During PID control, be sure to turn on the control command to allow the PIDCONT(P) instruction to be executed every scan. Failure to execute the instruction every scan disables PID operation in normal sampling cycles. The PIDCONT(P) instruction cannot be executed more than once in a single scan. Executing the instruction more than once in a single scan disables PID operation in normal sampling cycles.
- The PIDCONT $(P)$ instruction cannot be written and used in interrupt programs. Writing a PIDCONT $(P)$ instruction in the interrupt program disables PID operation in normal sampling cycles.
- In (s), specify the head of the device number specified in the I/O data area.
- If a file register is specified as an I/O data area, do not apply memory protection for the file register. If memory protection is applied, normal PID operation is disabled although no error results.
- The I/O data are assigned as follows.

- The number of device points used for I/O data setting is calculated by the following formula.

Number of device points $=10++18 \times \mathrm{n}$ ( n : number of loops used)

- Specify each data in binary.
- The initial processing flag sets the processing to be performed at the start of PID operation.
- Initial operation processing is performed assuming that the sampling cycle that has been set has been reached.
- If the initial processing flag is 0 , PID operations for the number of loops used are performed altogether in a single scan. If it is not 0 , PID operations for the number of loops used are divided and processed in several scans, and sampling is started sequentially from the loop that has completed initial processing. The number of processing loops per scan is the number of execution loops per scan that has been set.
- Write data to the I/O data "Write" area by users with the program. Users can read data from the I/O data "Read" area with the program. Never attempt to write data to the area indicated by "Read/write disabled" or "Read"; otherwise, normal operation can no longer be performed. Note that, when starting control from the initial status, the data areas must be cleared by the program.
- Even when the manual manipulated value $\left(\mathrm{MV}_{\text {MAN }}\right)$ is output in manual mode, execute the PIDCONT(P) instruction every scan. Unless the PIDCONT(P) instruction is executed, the bumpless function cannot be performed.
- Apply an interlock using the READY signal of each module so that the PIDCONT(P) instruction is executed only when the A/D converter module used to obtain the process value (PV) and the D/A converter module used to output the manipulated value (MV) are normal. If the instruction is executed when these modules are not normal, PID operation cannot be performed normally as the result of failure in normal acquisition of process values (PV) or in normal output of manipulated values (MV).



## Operation error

| Error code (SD0) | Description |
| :--- | :--- |
| 3405 H | The value of the data set in the I/O data area specified by (s) is out of the setting range. |
| 3422 H | The PIDINIT(P) instruction is not executed before the PIDCONT(P) instruction. |

## Stopping the operation of specified loop number

## PIDSTOP（P）

These instructions stop the PID operation of the loop number in the device specified by（s）．

| Ladder | ST |
| :---: | :---: |
|  | $\begin{aligned} & \text { ENO:=PIDSTOP(EN,s); } \\ & \text { ENO:=PIDSTOPP(EN,s); } \end{aligned}$ |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| PIDSTOP | - |
|  | $\boxed{ }$ |
| PIDSTOPP | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| $(\mathbf{s})$ | Loop number to be stopped | 1 to 32 | 16－bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3E미（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

－These instructions stop the PID operation of the loop number in the device specified by（s）．The loop stopped by the PIDSTOP（P）instruction does not restart PID operation even if the PIDINIT（P）instruction is executed．
－Each instruction holds operation data while the loop is stopped．

## Operation error

| Error code（SDO） | Description |
| :--- | :--- |
| 3405 H | Out－of－range data is set in the device specified by（s）． <br> • The specified loop number does not exist． <br> • The specified value is other than 1 to 32. |
| 3422 H | The PIDINIT（P）and PIDCONT（P）instructions are not executed before the PIDSTOP（P）instruction． |

## Starting the operation of specified loop number

## PIDRUN（P）

These instructions start the PID operation of the specified loop number．

| Ladder | ST |
| :---: | :---: |
|  | $\begin{aligned} & \text { ENO:=PIDRUN(EN,s); } \\ & \text { ENO:=PIDRUNP(EN,s); } \end{aligned}$ |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| PIDRUN | - |
|  | $\boxed{ }$ |
| PIDRUNP | $\boxed{ }$ |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| $(\mathbf{s})$ | Loop number to be stopped | 1 to 32 | 16－bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDI（H）Gロ | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

## Processing details

－These instructions start the PID operation of the loop number in the device specified by（s）．These instructions are used to re－execute the PID operation of the loop number that has been stopped by the PIDSTOP（P）instruction．
－The PIDRUN $(P)$ instruction，if executed for a loop number already in progress of PID operation，performs no processing．

## Operation error

| Error code（SDO） | Description |
| :--- | :--- |
| 3405 H | Out－of－range data is set in the device specified by（s）． <br> －The specified loop number does not exist． <br> • The specified value is other than 1 to 32. |
| 3422 H | The PIDINIT（P）and PIDCONT（P）instructions are not executed before the PIDRUN（P）instruction． |

## Changing the parameters of specified loop number

## PIDPRMW（P）

These instructions change the operation parameter of the specified loop number to the PID control data stored in the specified device number and later．


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| PIDPRMW | - |
|  | - |
| PIDPRMWP | - |

Setting data
Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| $(\mathrm{s} 1)$ | Loop number to be changed | 1 to 32 | 16－bit unsigned binary | ANY16 |
| $(\mathrm{s} 2)$ | Head device containing the PID control data to be changed | - | Word | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & X, Y, M, L, S M, \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロום | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3E미（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

- Changes the operation parameter of the loop number in the device specified by (s1) to the PID control data stored in the device number specified by ( s 2 ) and later.
- The following figure shows the configuration of the PID control data in the device specified by (s2) and later.

| (s2)+0 | Operational expression selection |
| :---: | :---: |
| (s2)+1 | Sampling cycle ( $\mathrm{T}_{\mathrm{S}}$ ) |
| (s2)+2 | Proportional constant ( $\mathrm{K}_{\mathrm{P}}$ ) |
| (s2)+3 | Integral constant ( $\mathrm{T}_{\mathrm{I}}$ ) |
| (s2)+4 | Derivative constant ( $\mathrm{T}_{\mathrm{D}}$ ) |
| (s2)+5 | Filter coefficient ( $\alpha$ ) |
| (s2)+6 | Manipulated value lower limit (MVLL) |
| (s2)+7 | Manipulated value upper limit (MVHL) |
| (s2)+8 | Manipulated value change rate limit ( $\triangle \mathrm{MVL}$ ) |
| (s2)+9 | Process value change rate limit ( $\triangle \mathrm{PVL}$ ) |

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3405 H | Out-of-range data is set in the device specified by (s1). <br> The specified loop number does not exist. <br> The specified value is other than 1 to 32. |
|  | The PID control data in the device specified by (s2) is out of the setting range. |
| 3422 H | The PIDINIT(P) instruction is not executed before the PIDPRMW(P) instruction. |

## 10 PROCESS CONTROL INSTRUCTIONS

### 10.1 Overview

This section describes the loop type that can be configured by process control instructions, data configurations of the instructions, instruction execution methods, and precautions.

## Basic loop types

The following table summarizes basic loop types configured by combinations of process control instructions.

| Loop type | Configuration | Application |
| :---: | :---: | :---: |
| Two-degree-of-freedom PID control (S2PID) |  | Used for general PID control (two degrees of freedom). (Speed type) <br> PID operation is performed every control cycle. |
| PID control (SPID) |  | Used for general PID control. (Speed type) PID operation is performed every control cycle. |
| PIDP control (SPIDP) |  | Used for general PID control. (Position type) PID operation is performed every control cycle. |
| Sample PI control (SSPI) |  | Used for processes which involve much dead time. PI control is executed only for the control execution time every control cycle, and later output is held constant. |
| I-PD control (SIPD) |  | Used to make a slow response so as not to give a shock to the operation terminal and process when the set value is changed. |
| Blend PI control (SBPI) |  | Used for processes which allow a constant manipulated value in the long run even if it varies in the short term. |
| Ratio control (SR) |  | Performs control so that a given manipulated value keeps a constant ratio with other rates of change. |
| Two-position (on/off) control (SONF2) |  | Performs control so that the manipulated value is turned on or off depending on whether the deviation is positive or negative. |
| Three-position (on/off) control (SONF3) |  | Performs control by outputting three-area signals for process values. <br> This control can suppress rapid changes in the manipulated value. |
| Batch counter (SBC) |  | Performs valve on/off control in the process of batch charging to the tank. |


| Loop type | Configuration |  | Application |
| :---: | :---: | :---: | :---: |
| Program setter (SPGS) | S.PGS | MV <br> $\longrightarrow$ OUTPUT | Outputs according to the temporal change of the value that has been set in advance. |
| Manual output (SMOUT) | S.MOUT | MV <br> $\longrightarrow$ OUTPUT | Operates the operation terminal for manual output. |
| Monitor (SMON) | $\mathrm{INPUT} \rightarrow \text { S.IN }$ | PV <br> $\longrightarrow$ OUTPUT | Inputs process values to detect process errors such as upper/ lower limit alarms. |
| Manual output with monitor (SMWM) | $\text { INPUT } \rightarrow \text { S.IN }$ | $\xrightarrow{\text { MV }} \text { OUTPUT }$ | Performs manual operation while inputting process values to check that no error is caused. |
| Selector (SSEL) | $\begin{aligned} & \text { INPUT1 } \rightarrow \text { S.SEL } \\ & \text { INPUT2 } \rightarrow \text { S } \end{aligned}$ | $\longrightarrow$ OUTPUT | Used for signal selection. |

## Point/ ${ }^{\circ}$

For the loop type processing time, refer to the following.
L] MELSEC iQ-R CPU Module User's Manual (Application)

## Process control instructions and data configuration

This section describes the data configurations used by process control instructions.

## Data configuration in which loop tags are used

The process control instructions use control information common to each loop by storing it in common memory. The group of this common information is called a loop tag, and the storage memory is called loop tag memory. Loop monitoring and tuning can be performed by monitoring the loop tag.

Ex.
Block diagram of two-degree-of-freedom PID control (S2PID)



The signs in the ladder diagram mean as follows.


| Item | Instruction name |  |  |  |
| :--- | :--- | :--- | :--- | :--- |
|  | S.IN | S.PHPL | S.2PID | S.OUT1 |
| (1) Input data start device | R0 | R20 | R40 | R60 |
| (2) Block memory start device | R100 | R120 | R140 | R160 |
| (3) Operation constant start device | R200 | Null character string (" ") | R240 | R260 |
| (4) Loop tag memory start device | R1000 |  | R300 | - |
| (5) Set value start device | - | - |  |  |

## Data used by process control instructions

The data used by process control instructions includes the following.

- Loop memory ( $\mathfrak{F}$ Page 982 Loop memory)
- Input data (
- Block memory (
- Operation constant ( $\longmapsto$ Page 983 Operation constant)
- Local work memory ( $\longmapsto$ Page 984 Local work memory)


## Loop memory

The loop memory is an area in which the data used commonly by the process control instructions specified by the loop type is stored. The loop memory has also another area in which the data used by the CPU module system during execution of process control instructions is stored.
The loop memory consists of loop tag memory and loop tag past value memory.

- $\mathfrak{F}$ Page 982 Loop tag memory
- $\longmapsto$ Page 982 Loop tag past value memory



## Point ${ }^{\circ}$

The loop memory is configured with 128 words, and therefore a device which has 128 consecutive words should be specified when the loop memory area is allocated.

## Loop tag memory

The loop tag memory is an area ( 96 words) in which the control information used commonly by the process control instructions specified by a loop type among the basic loop types ( $\leftrightarrows$ Page 978 Basic loop types) is stored.

## Point/ $\rho$

For the applications of the area used by process control instructions in the loop tag memory, refer to the following.
W Page 1736 List of Loop Tag Memory Areas Used by Process Control Instructions

## ■Loop tag past value memory

The loop tag past value memory is an area ( 32 words) used by the system during execution of process control instructions. No data can be written during operation. If data is written to the loop tag past value memory during operation, normal operation is disabled.

When starting a process control instruction, write 0 to the loop tag past value memory.

## Input data

Input data is variable data given to each process control instruction. The block word (BW) in the block memory in which the operation result of the previous process control instruction is stored is used as input data.

*1 For the block memory, refer to the following
W Page 983 Block memory
The application of input data varies depending on the instruction used. Refer to the descriptions of individual instructions.

## Block memory

The block memory is an area in which the output information of each process control instruction is stored.
The block memory consists of a block word (BW) and a block bit (BB).

- $\leqslant$ Page 983 Block word (BW)
- $\longmapsto$ Page 983 Block bit (BB)


The application of block memory varies depending on the instruction used. Refer to the descriptions of individual instructions.

## —Block word (BW)

The block word (BW) is an area in which the operation result of each process control instruction is stored. The data stored in the block word (BW) is specified for the input data for the next process control instruction connected by the loop.


## Block bit (BB)

The block bit ( BB ) is an area in which alarm data during execution of each process control instruction is stored. Sixteen bits from b0 to b15 are represented as BB1 to BB16. If an alarm occurs in any of b1 to b15 (BB2 to BB16) used by instructions, 1 is stored in b0 (BB1).

The 16 bits are all cleared to zero at the time instruction execution. However, for an instruction which uses the last value of the block bit (BB), only the bits that will not be used are cleared to zero.

| $\left\lvert\, \begin{aligned} & \stackrel{e}{\infty} \\ & \underset{\sim}{\infty} \end{aligned}\right.$ | $\begin{array}{\|l\|} \hline \infty \\ \stackrel{\infty}{m} \\ \hline \end{array}$ | $\frac{\underset{\sim}{\infty}}{\stackrel{\rightharpoonup}{\infty}}$ | $\frac{m}{\underset{\sim}{m}}$ | $\left\lvert\, \begin{aligned} & \stackrel{N}{\infty} \\ & \underset{\sim}{n} \end{aligned}\right.$ | $\frac{\stackrel{r}{m}}{\stackrel{\rightharpoonup}{\infty}}$ | $\frac{\stackrel{o}{\infty}}{\stackrel{\infty}{\infty}}$ | $\begin{aligned} & \mathbf{o} \\ & \mathbf{\infty} \\ & \infty \end{aligned}$ | $\left\|\begin{array}{l} \infty \\ \infty \\ \infty \\ \infty \end{array}\right\|$ | $\begin{aligned} & \hat{\infty} \\ & \infty \\ & \infty \end{aligned}$ | $\begin{aligned} & \infty \\ & \underset{\infty}{\infty} \\ & \hline \end{aligned}$ | $\left\lvert\, \begin{aligned} & \infty \\ & \infty \\ & \infty \\ & 0 \end{aligned}\right.$ | $\begin{aligned} & \ddagger \\ & \infty \\ & \infty \end{aligned}$ | $\begin{aligned} & \text { n } \\ & \text { m } \end{aligned}$ | $\begin{aligned} & \underset{\sim}{N} \\ & \underset{\sim}{2} \end{aligned}$ | ¢ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |

## Operation constant

The operation constant is an area in which the data used by only one process control instruction is stored.
The application of the operation constant varies depending on the instruction used. Refer to the descriptions of individual instructions.

## Local work memory

The local work memory is an area in which data is temporarily stored during operation of process control instructions.


The application and storage area of the local work memory vary depending on the instruction used. Refer to the descriptions of individual instructions.

## Loop tag memory assignments

Loop tag memory assignments are explained below.
Ex.
Loop tag memory assignments for two-degree-of-freedom PID control (S2PID)


For details on the loop tag memory assignments, refer to the following.
$\longmapsto$ Page 1736 List of Loop Tag Memory Areas Used by Process Control Instructions

## Common items

This section describes the common items in loop types.
-Alarm detection (ALM)
Alarm detection (ALM) indicates loop alarm information. The default setting is 4000 H which indicates manual operation in loop stop state. To enable auto alarm, set it to 0000 H .


Details of Alarm detection (ALM) are shown below.

| Item | Name | Status | Set by |
| :---: | :---: | :---: | :---: |
| SPA | Stop alarm | Loop STOP state <br> - Loop mode MAN is entered. <br> - Loop stop processing is performed for the output value (BW) and alarm signal. | User |
| DMLA | Output variation rate limit alarm | As the result of checking input data using the variation rate limiter, the output variation rate limit has been exceeded. | System |
| OOPA | Output open alarm | The operation output signal is in open state due to disconnection. | System |
| SEA | Sensor alarm | Sensor error alarm | System |
| HHA | Upper upper limit alarm | The process value exceeds the upper limit defined for the process equipment. | System |
| LLA | Lower lower limit alarm | The process value underruns the lower limit defined for the process equipment. | System |
| PHA | Upper limit alarm | As the result of upper limit checking, the process value exceeds the upper limit. | System |
| PLA | Lower limit alarm | As the result of lower limit checking, the process value underruns the lower limit. | System |
| DPPA | Positive direction variation rate alarm | The variation rate exceeds the variation rate range on an upward trend. | System |
| DPNA | Negative direction variation rate alarm | The variation rate underruns the variation rate range on a downward trend. | System |
| DVLA | Large deviation alarm | The result of a deviation check shows that the deviation limit is exceeded. (The deviation check determines whether the deviation has been reduced completely underrunning the alarm value. To do so, when the deviation has been reduced to a certain value range from the alarm value, the large deviation alarm is released.) | System |
| MHA | Output upper limit alarm | As the result of checking with the upper/lower limiter, the value output by the limiter exceeds the output upper limit. | System |
| MLA | Output lower limit alarm | As the result of checking with the upper/lower limiter, the value output by the limiter underruns the output lower limit. | System |

## Disable alarm detection (INH)

This disable alarm detection of each item. The alarms whose detection is disabled by INH are not detected. INH bits b0 to b11 correspond to ALM bits b0 to b11.

(1) Tracking flag (Do not rewrite the tracking flag.)
2) Alarm detection disabled

## Operation mode (MODE)

The process control instructions have the following operation modes that satisfy the following operations in a system connected to the operator station, programmable controller, host computer, and machine side operation panel. Set only 1 bit of flag for the operation mode (MODE).


Details of the operation mode (MODE) are given below.

| Operation mode | Description | Application |
| :--- | :--- | :--- |
| MAN (MANUAL) | - Manual operation from operator station <br> - SV and MV can be set. | Used for monitoring and control from the <br> operator station. |
| AUT (AUTOMATIC) | - Automatic operation <br> - SV can be set. <br> - MV cannot be set. | - Cascade operation <br> - SV nor MV cannot be set. |
| CAS (CASCADE) | Automatic MV setting from host computer | Loop control from the host computer is possible. <br> Used to operate and monitor the operation mode <br> on the operator station. |
| CMV (COMPUTER MV) | Automatic SV setting from host computer | When the computer fails during loop control by <br> the host computer, backup is performed by the <br> predetermined operator station. |
| CMB (COMPUTER MANUAL BACK UP) | Manual operation backup when the host computer is <br> abnormal | Automatic operation backup performed when the host <br> computer is abnormal |
| CCB (COMPUTER CASCADE BACK UP) | Cascade operation backup performed when the host <br> computer is abnormal | When the plant is started, the operation and <br> start-up are performed by using such as the <br> machine side operation panel away from the <br> operator station, and the operation mode is |
| LCM (LOCAL MANIPULATED) | Local manual operation | Local automatic operation |

## How to execute process control instructions

## Execution cycle and control cycle

## Execution cycle

The execution cycle is an interval at which process control instructions are executed.
The following methods can be used to execute process control instructions in each execution cycle.

| Execution method | Description |
| :--- | :--- |
| Timer | A timer is used to measure the execution cycle and a process control instruction is executed when the <br> time of the timer is up. |
| Interrupt program | Interrupt programs I28 to I31 are each executed every execution cycle. |
| Fixed scan execution type program | Fixed scan execution type programs are each executed every execution cycle. |

## Point $\rho$

Specify the execution cycle value used by process control instructions in SD816 and SD817 in a singleprecision real number.

## © Control cycle

The control cycle is a cycle in which PID control is performed by instructions such as S.2PID. For the control cycle, specify an integral multiple of the execution cycle. Execution cycles are counted during execution, and PID operation is performed when the specified control cycle is reached.
Specify the control cycle used in the loop tag memory. Instructions such as S.2PID perform PID control based on the control cycle value specified in the loop tag memory.

Ex.
The S.2PID instruction performs monitoring every second and implements PID control every 5 seconds.


Point $P$
Setting the control cycle to an integral multiple of the execution cycle enables monitoring such as for checking the process value range every execution cycle.

## Concept of program

This section describes the concept of programs using process control instructions.

## Ex.

Program example using the S.2PID instruction in an execution cycle of 1 second

(1) Setting loop tag memory
(2) Setting operation constants

- Setting data for S.IN, S.PHPL, S.2PID, and S.OUT1
(3) Measuring the execution cycle
(4) Setting input data (PV)
- Reading PV from such as the A/D conversion module
(5) MV output
- Outputting MV from such as the D/A conversion module
(6) Specifying process control instructions
- S.IN instruction
- S.PHPL instruction
- S.2PID instruction
- S.OUT1 instruction

For specific program examples using process control instructions, refer to the following.
$\longmapsto$ Page 1731 Process Control Program Examples

## Execution condition switching

## Loop RUN/STOP

If any loop component such as a detector or operation terminal other than the programmable controller fails, each loop can be run and stopped independently for the purpose of maintenance. SPA of the alarm detection (ALM) is used to run/stop the applicable loop.

## Basic operation to stop a loop

- Output status is retained. (Example: Output of S.2PID instruction = 0)
- No alarm is detected.
- The operation mode is MAN.


## Tracking

Tracking refers to making a certain signal follow and match another signal.

## Tracking function

The tracking function used by process control instructions includes the bumpless function and output limiter processing function.

## Bumpless function

The bumpless function prevents manipulated value (MV) output stepping changes when switching from the automatic mode to manual mode, and continuously and smoothly controls MV output.

## Output limiter processing function

The output limiter processing function limits the upper or lower limit of the manipulated value (MV) output by the PID operation in automatic mode. This output limiter processing function is only valid in automatic mode and is not executed in manual mode. The output limiter processing function is not executed either even in automatic mode if the tracking bit (TRK) is set to 0 (Disable tracking).

## Cascade loop tracking

The process control loops making up a cascade loop use the manipulated value (MV) of a primary loop (Loop 0) as the set value (SV) of a secondary loop (Loop 1). Tracking is performed to prevent a sudden change in the set value (SV) when the operation mode of the secondary loop (Loop1) is changed.
Cascade loop tracking processing is outlined below.


In cascade operation, the manipulated value (MV) of Loop 0 is transferred to the set value (SV) of Loop 1. When cascade operation is not performed, the set value (SV) of Loop 1 is transferred to the manipulated value (MV) of Loop 0 . (Tracking to the source specified as the input terminal of the set value (SV) of Loop 1)
Tracking is performed when the operation mode is switched to one other than CAS, CSV, or CCB.
For S.2PID (two-degree-of-freedom PID control), set the following operation constant items to specify tracking.

| Setting item |  | Setting |
| :--- | :--- | :--- |
| Tracking bit (TRK) |  | 1 (Tracking performed) |
| Set value pattern (SVPTN) | Set value pattern | 0 (Set value is upper loop MV.) |
|  | Set value used | 0 (E2 is specified.) |

## Loop selector tracking

Tracking processing is performed under the following conditions.

- The operation mode is MAN, CMB, CMV, or LCM, and the tracking bit (TRK) is 1 .
- The operation mode is AUT, CAS, CAB, CCB, CSV, LCA, or LCC, and the tracking bit (TRK) is 1 and BB1 of the block bit $(B B)$ is 1 .



## Ex.

When the input value (E1) of the S.SEL instruction uses the manipulated value (MV) of the upper loop (Loop 0), the manipulated value (MV) of the S.SEL instruction is tracked to the manipulated value (MV) of Loop 0. The S.SEL instruction specifies tracking according to the following operation constant items.

Operation constant
Tracking bit
Set value pattern


## Precautions

## Overlapping of specified data areas

The process control instructions check for area overlapping of input and output data. Specify input and output data after checking their areas are not overlapped.

Ex.
Example of area overlapping caused by a process control instruction


In the above example, the areas of (d1) and (d2) specified in the setting data are overlapping and an error results.

## Operand specification

Regardless of the program language used, specify devices for the operations of process control instructions. Do not specify labels.

## Errors of process control instructions

When an error occurs in process control instructions, the detailed information of the error is stored in SD81 to SD111 (detailed information 1) and SD113 to SD143 (detailed information 2). For the information to be stored, refer to the following. []] MELSEC iQ-R CPU Module User's Manual (Application)

## 10．2 I／O Control Instructions

## Analog input processing

## S．IN

This instruction performs following processing to the input data（PV）：range check，input limiter，engineering value transformation，and digital filter．


## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．IN | $-\square$ |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| （s1） | Input data start device | Refer to＂Input data＂． | Single－precision real <br> number |
| （d1） | Block memory start device | Refer to＂Block memory＂． | Word |
| （s2） | Operation constant start device | Refer to＂Operation constant＂． | Word |
| （d2） | Loop tag memory start device | Refer to＂Loop tag memory＂． | Word |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，R，ZR，RD | U밈，JロIロ， U3EDI（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Input data

| Operand：（s1） |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| $\begin{aligned} & +0 \\ & +1 \end{aligned}$ | E1 | Input value | －999999 to 999999 | Single－precision real number | － | User |

Block memory


Operation constant

| Operand: (s2) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| $\begin{aligned} & +0 \\ & +1 \end{aligned}$ | EMAX | Engineering value transformation upper limit | -999999 to 999999 [\%] | Single-precision real number | 100.0 | User |
| $\begin{aligned} & +2 \\ & +3 \end{aligned}$ | EMIN | Engineering value transformation lower limit | -999999 to 999999 [\%] | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +4 \\ & +5 \end{aligned}$ | NMAX | Input upper limit | -999999 to 999999 | Single-precision real number | 100.0 | User |
| $\begin{aligned} & +6 \\ & +7 \end{aligned}$ | NMIN | Input lower limit | -999999 to 999999 | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +8 \\ & +9 \end{aligned}$ | HH | Upper limit range error occurrence | -999999 to 999999 | Single-precision real number | 110.0 | User |
| $\begin{aligned} & +10 \\ & +11 \end{aligned}$ | H | Upper limit range error return | -999999 to 999999 | Single-precision real number | 100.0 | User |
| $\begin{aligned} & +12 \\ & +13 \end{aligned}$ | L | Lower limit range error return | -999999 to 999999 | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +14 \\ & +15 \end{aligned}$ | LL | Lower limit range error occurrence | -999999 to 999999 | Single-precision real number | -10.0 | User |

## ■Loop tag memory

The loop tag memory occupies 96 words from the specified start device.


## Processing details

This instruction performs engineering value transformation of the input value (E1) in the device specified by ( s 1 ), and stores the result in the device specified by (d1). The instruction also performs input value (E1) range check, input limiter, and digital filter processing.
The following is the processing block diagram of the S.IN instruction. (The numbers (1) to (5) in the diagram indicate the order of the processing.)


## Range check (1)

- The instruction checks the range of the input value (E1).

If the input value (E1) goes beyond the upper or lower limits, an alarm is output.

| Range check | Condition | Range check result (alarm output) |  |  |
| :---: | :---: | :---: | :---: | :---: |
|  |  | BB2 | BB3 | BB1, SEA |
| Upper limit check | $\mathrm{E} 1 \geq \mathrm{HH}$ | $1^{* 1}$ | - | $1^{* 1}$ |
|  | $\mathrm{E} 1 \leq \mathrm{H}$ | 0 | - | 0 |
|  | $\mathrm{H}<\mathrm{E} 1<\mathrm{HH}$ | Last value | - | Last value |
| Lower limit check | $\mathrm{E} 1 \leq \mathrm{LL}$ | - | $1^{* 1}$ | $1^{* 1}$ |
|  | $\mathrm{E} 1 \geq \mathrm{L}$ | - | 0 | 0 |
|  | LL < E1 < L | - | Last value | Last value |

*1 When ERRI or SEI of Disable alarm detection (INH) is set to 1, alarm output is disabled and therefore ALM SEA, BB2, and BB3 are set to 0 .

- Last value hold processing

When a range excess occurs ( $\mathrm{BB} 1=1$ ) in the range check, whether to continue operation or terminate the $\mathrm{S} . \mathrm{IN}$ instruction is determined by whether SM816 is on or off.

| Condition | Description |
| :--- | :--- |
| SM816 is off (not in hold mode) | "Input limiter (2)" is performed even if a range excess occurs (BB1 = 1). |
| SM816 is on (in hold mode) | If a range excess occurs $($ BB1 $=1)$, the following processing is performed to terminate the S.IN instruction. <br>  <br>  <br> • The last output value $($ BW $)$ is held. <br> • Error information is stored in BB. |

## ■Input limiter (2)

The instruction sets the upper and lower limits for the input value (E1).

| Condition | Result (T1) |
| :--- | :--- |
| E1 $\geq$ NMAX | NMAX |
| E1 $\leq$ NMIN | NMIN |
| NMIN $<$ E1 < NMAX | E1 |

Engineering value inverse transformation (3)
The instruction performs engineering value inverse transformation of the input limiter result (T1) according to the following expression.

T2 $=($ EMAX-EMIN $) \times \frac{\text { T1-NMIN }}{\text { NMAX-NMIN }}+$ EMIN


## Digital filter (4)

The instruction applies a digital filter to the input value (E1) according to the following expression. The digital filter is used to reduce the effect of noise.
$B W=T 2+\alpha \times($ last BW value-T2)

## Loop stop processing (5)

The following processing is performed according to the SPA status of the Alarm detection (ALM).

| SPA status | Processing details |
| :--- | :--- |
| 1 | Performs the following operations and terminates the S.IN instruction. <br>  <br>  <br>  <br>  <br>  <br>  <br>  <br>  <br> • The last output value (BW) is held. <br> • The operation mode (MODE) is set to MAN. <br> • ALM SEA is set to 0. |
| 0 | Performs "range check (1)". |

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3400 H | An invalid operation (such as division by zero) is performed. |
| 3402 H | The value specified by (s1), (d1), (s2), or (d2) is a subnormal number or NaN (not a number). |
| 3403 H | An overflow has occurred. |
| 3405 H | Upper limit range error occurrence (HH) < upper limit range error return (H), lower limit range error return (L) < lower limit range error <br> occurrence (LL), or input upper limit (NMAX) < input lower limit (NMIN) |

## Output processing 1 with mode switching

## S.OUT1

This instruction calculates MV (0 to 100\%) from input data ( $\Delta \mathrm{MV}$ ), performs variation rate \& upper/lower limiter processing, and output conversion.


FBD/LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S.OUT1 | $\square$ |

## Setting data

■Description, range, data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| (s1) | Input data start device | Refer to "Input data". | Single-precision real <br> number |
| (d1) | Block memory start device | Refer to "Block memory". | Word |
| (s2) | Operation constant start device | Refer to "Operation constant". | Word |
| (d2) | Loop tag memory start device | Refer to "Loop tag memory". | Word |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## -Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | J밈 | T, ST, C, D, W, SD, SW, R, ZR, RD | U미미, J밈, U3Eपl(H)G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (s1) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (d1) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (s2) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (d2) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |

## Input data

| Operand: (s1) | Recommended range | Data type | Standard <br> value | Set by |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Position | Symbol | Name | Input value <br> $(\Delta \mathrm{MV})$ | -999999 to $999999[\%]$ | Single-precision <br> real number | - |
| +0 | E1 |  |  |  |  |  |

Block memory


## Operation constant

| Operand: (s2) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| $\begin{aligned} & +0 \\ & +1 \end{aligned}$ | NMAX | Output conversion upper limit | -999999 to 999999 | Single-precision real number | 100.0 | User |
| $\begin{aligned} & +2 \\ & +3 \end{aligned}$ | NMIN | Output conversion lower limit | -999999 to 999999 | Single-precision real number | 0.0 | User |

## Loop tag memory

The loop tag memory occupies 96 words from the specified start device.

| Operand: (d2) |  |  |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range |  |  |  |  |  |  |  | Data type | Standard | Set by |
| +1 | MODE | Operation mode | 0 to FFFFH |  |  |  |  |  |  |  | 16-bit unsigned binary | 8H | User/ system |
| +3 | ALM | Alarm detection | 0 to FFFFH <br> SPA <br> 0: Loop RUN <br> 1: Loop STOP <br> DMLA, SEA, MHA, MLA <br> 0 : No alarm <br> 1: Alarm |  |  |  |  |  |  |  | 16-bit unsigned binary | 4000H | User/ system |
| +4 | INH | Disable alarm detection | 0 to FFFFFH <br> TRKF <br> 0 : Tracking not set <br> 1: Tracking set <br> ERRI, DMLI, MHI, MLI <br> 0 : Alarm detection enabled <br> 1: Alarm detection disabled |  |  |  |  |  |  |  | 16-bit unsigned binary | 4000H | User/ system |
| $\begin{aligned} & +12 \\ & +13 \end{aligned}$ | MV | Manipulated value | -10 to 110 [\%] |  |  |  |  |  |  |  | Single-precision real number | 0.0 | User/ system |
| $\begin{aligned} & +18 \\ & +19 \end{aligned}$ | MH | Output upper limit value | -10 to 110 [\%] |  |  |  |  |  |  |  | Single-precision real number | 100.0 | User |
| $\begin{aligned} & +20 \\ & +21 \end{aligned}$ | ML | Output lower limit value | -10 to 110 [\%] |  |  |  |  |  |  |  | Single-precision real number | 0.0 | User |


| Operand: (d2) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| $\begin{aligned} & +48 \\ & +49 \end{aligned}$ | DML | Output variation rate limit value | 0 to 100 [\%] | Single-precision real number | 100.0 | User |
| $\begin{aligned} & +54 \\ & +55 \end{aligned}$ | I | Integral constant | 0 to 999999 [s] | Single-precision real number | 10.0 | User |
| $\begin{aligned} & +62 \\ & +63 \end{aligned}$ | MVP | MV internal operation value | -999999 to 999999 [\%] | Single-precision real number | 0.0 | System |

## Loop tag past value memory

The system uses this area as a work area.
To start the control from the initial status, clear data by using a sequence program.
The loop tag past value memory occupies 32 words after the loop tag memory.


Execution cycle ( $\Delta T$ )
Set the execution cycle ( $\Delta \mathrm{T}$ ) in SD816 and SD817 in single-precision real number.

## Processing details

This instruction calculates the manipulated value (MV) from the input value ( $\mathrm{E} 1=\Delta \mathrm{MV}$ ) in the device specified by ( $s$ 1), and stores the result in the device specified by (d1). The instruction also performs variation rate \& upper/lower limiter, reset windup, and output conversion processing of the calculated manipulated value (MV).
The following is the processing block diagram of the S.OUT1 instruction. (The numbers (1) to (6) in the diagram indicate the order of the processing.)


## ■Mode determination (1)

The following processing is performed depending on the operation mode (MODE).

| Operation mode (MODE) | Processing details |
| :--- | :--- |
| MAN, CMB, CMV, LCM <br> (alarm clear processing) | • The MHA, MLA, and DMLA of the alarm detection (ALM) are set to 0. <br> • The MHA2 and MLA2 of the alarm detection 2 (ALM2) are set to 0. <br> • The alarm bits (BB1, BB2, BB3, and BB4) are set to 0. <br> • TRKF of INH is set to 1. <br> • "Output conversion processing (5)" is performed and the instruction ends. |
| AUT, CAB, CAS, CCB, CSV, LCA, LCC | "Input addition processing (2)" is performed. <br> However, when ALM SEA is 1 and SM817 is on, alarm bits BB1, BB2, BB3, and BB4 are set to 0 and the <br> S.OUT1 instruction is terminated. |

## Input addition processing (2)

A temporary $\mathrm{MV}(\mathrm{T})$ is calculated on the basis of the input value ( $\mathrm{E} 1=\Delta \mathrm{MV}$ ). The following processing is performed depending on the TRKF of INH

| Tracking flag (TRKF) | Processing details |
| :---: | :---: |
| 1 | - The manipulated value (MV) is stored in the MV internal operation value (MVP). <br> - The input value (E1) is set to 0 . $(\Delta \mathrm{MV}=0)$ <br> - TRKF of INH is set to 0 . <br> - A temporary $\mathrm{MV}(\mathrm{T})$ is calculated according to the following expression. $\mathrm{T}=\mathrm{E} 1+\mathrm{MVP}$ $M V P=T$ |
| 0 | A temporary $\mathrm{MV}(\mathrm{T})$ is calculated according to the following expression. $\mathrm{T}=\mathrm{E} 1+\mathrm{MVP}$ MVP=T |

## Variation rate \& upper/lower limiter (3)

The variation rate and upper/lower limits of the input value ( $\mathrm{E} 1=\Delta \mathrm{MV}$ ) are checked, and the data after the processing and an alarm are output

- Variation rate limiter processing performs the following operations, and outputs the result to the output variation rate alarm (BB4) and the DMLA of the Alarm detection (ALM).

| Condition | BB4, DMLA | Result (T1) |
| :--- | :--- | :--- |
| $\|T-M V\| \leq D M L$ | 0 | $T$ |
| $(T-M V)>D M L$ | $1^{* 1}$ | MV+DML |
| $(T-M V)<-D M L$ | $1^{* 1}$ | $M V-D M L$ |

*1 If the DMLI or ERRI of the disable alarm detection (INH) is set to 1, the output variation rate alarm (BB4) and the DMLA of the alarm detection (ALM) are set to 0 .

- Upper/lower limiter processing performs the following operations, and outputs the result to the output upper limit alarm (BB2); output lower limit alarm (BB3); MHA and MLA of the Alarm detection (ALM); and MHA2 and MLA2 of the Alarm detection (ALM2).

| Condition | BB3, MLA, MLA2 | BB2, MHA, MHA2 | MV |
| :--- | :--- | :--- | :--- |
| $\mathrm{T} 1>\mathrm{MH}$ | 0 | $1^{* 2}$ | MH |
| $\mathrm{T} 1<\mathrm{ML}$ | $1^{* 3}$ | 0 | ML |
| $\mathrm{ML} \leq \mathrm{T} 1 \leq \mathrm{MH}$ | 0 | 0 | T 1 |

*2 If the MHI or ERRI of the disable alarm detection (INH) is set to 1, the output upper limit alarm (BB2) and the MHA of the alarm detection (ALM) are set to 0 .
Note that the MHA2 of the alarm detection 2 (ALM2) remains 1.
*3 If the MLI or ERRI of the disable alarm detection (INH) is set to 1 , the output lower limit alarm (BB3) and the MLA of the alarm detection (ALM) are set to 0 .
Note that the MHA2 of the alarm detection 2 (ALM2) remains 1.

## Reset windup (4)

If the manipulated value (MV) goes beyond the upper or lower limit, the following operation is performed to return it to the upper or lower limit and enable immediate response when the deviation is inverted. However, when the integral constant (I) is 0 , reset windup processing is not performed.

| Condition | Operational expression |
| :--- | :--- |
| $T 1>M H, \frac{\triangle T}{I}<=1$ | $M V P=\left(\frac{\Delta T}{I}\right)(M H-T)+T$ |
| $T 1<M H, \frac{\Delta T}{I}<=1$ | $M V P=\left(\frac{\Delta T}{I}\right)(M L-T)+T$ |

## Output conversion processing (5)

The output value (BW) is calculated from the following expression.
$B W=\frac{\text { NMAX-NMIN }}{100} \times M V+$ NMIN

## Loop stop processing (6)

The following processing is performed according to the SPA status of the alarm detection (ALM).

| SPA status | Processing details |
| :--- | :--- |
| 1 | The loop stops. When the loop stops, the following operations are performed and the S.OUT1 instruction ends. <br> • The last output value (BW) is held. <br> • The DMLA, MHA, and DLA of the alarm detection (ALM) are set to 0. |
|  | • The MHA2 and MLA2 of the alarm detection 2 (ALM2) are set to 0. <br> • The operation mode (MODE) is set to MAN. <br> • The alarm bits (BB1, BB2, BB3, and BB4) are set to 0. |
| 0 | The loop runs and "mode determination (1)" is performed. |

## ■Hold processing (7)

This processing specifies whether to hold the output value (BW) by the S.OUT1 instruction when a sensor error occurs (detected by the S.IN instruction). The hold processing is performed when the value is determined as RUN by "Loop Stop Determination". SM817 is used to specify whether to hold the manipulated value (MV) when a sensor error occurs.

- SM817 = OFF: Do not hold the manipulated value (MV).
- SM817 = ON: Hold the manipulated value (MV)


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3400 H | An invalid operation (such as division by zero) is performed. |
| 3402 H | The value specified by (s1), (s2), or (d2) is a subnormal number or NaN (not a number). |
| 3403 H | An overflow has occurred. |

## Output processing 2 with mode switching

## S.OUT2

This instruction performs variation rate \& upper/lower limiter processing and output conversion on the basis of input data (MV).


FBD/LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S.OUT2 | $\square$ |

## Setting data

Description, range, data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| (s1) | Input data start device | Refer to "Input data". | Single-precision real <br> number |
| (d1) | Block memory start device | Refer to "Block memory". | Word |
| (s2) | Operation constant start device | Refer to "Operation constant". | Word |
| (d2) | Loop tag memory start device | Refer to "Loop tag memory". | Word |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | J밈 | T, ST, C, D, W, SD, SW, R, ZR, RD | U미미, J밈, U3Eपl(H)G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (s1) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (d1) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (s2) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (d2) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |

## Input data

| Operand: (s1) | Recommended range | Data type | Standard <br> value | Set by |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Position | Symbol | Name | Input value (MV) | -999999 to $999999[\%]$ | Single-precision <br> real number | - |
| +0 | E1 |  |  | User |  |  |

Block memory


## Operation constant

| Operand: (s2) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| $\begin{aligned} & +0 \\ & +1 \end{aligned}$ | NMAX | Output conversion upper limit | -999999 to 999999 | Single-precision real number | 100.0 | User |
| $\begin{aligned} & +2 \\ & +3 \end{aligned}$ | NMIN | Output conversion lower limit | -999999 to 999999 | Single-precision real number | 0.0 | User |

## Loop tag memory

The loop tag memory occupies 96 words from the specified start device.

| Operand: (d2) |  |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range |  |  |  |  |  |  | Data type | Standard | Set by |
| +1 | MODE | Operation mode | 0 to FFFFH |  |  |  |  |  |  | 16-bit unsigned binary | 8H | User/ system |
| +3 | ALM | Alarm detection | 0 to FFFFH <br> SPA <br> 0: Loop RUN <br> 1: Loop STOP <br> DMLA, SEA, MHA, MLA <br> 0 : No alarm <br> 1: Alarm |  |  |  |  |  |  | 16-bit unsigned binary | 4000H | User/ system |
| +4 | INH | Disable alarm detection | 0 to FFFFH <br> 0 : Alarm detection enabled <br> 1: Alarm detection disabled |  |  |  |  |  |  | 16-bit unsigned binary | 4000H | User/ system |
| $\begin{aligned} & +12 \\ & +13 \end{aligned}$ | MV | Manipulated value | -10 to 110 [\%] |  |  |  |  |  |  | Single-precision real number | 0.0 | User/ system |
| $\begin{aligned} & +18 \\ & +19 \end{aligned}$ | MH | Output upper limit value | -10 to 110 [\%] |  |  |  |  |  |  | Single-precision real number | 100.0 | User |
| $\begin{aligned} & +20 \\ & +21 \end{aligned}$ | ML | Output lower limit value | -10 to 110 [\%] |  |  |  |  |  |  | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +48 \\ & +49 \end{aligned}$ | DML | Output variation rate limit value | 0 to 100 [\%] |  |  |  |  |  |  | Single-precision real number | 100.0 | User |

## Processing details

This instruction performs output conversion of the input value ( $\mathrm{E} 1=\mathrm{MV}$ ) in the device specified by ( s 1 ), and stores the result in the device specified by (d1). The instruction also performs variation rate \& upper/lower limiter and output conversion processing of the input value at that time.
The following is the processing block diagram of the S.OUT2 instruction. (The numbers (1) to (4) in the diagram indicate the order of the processing.)


## ■Mode determination (1)

The following processing is performed depending on the operation mode (MODE).

| Operation mode (MODE) | Processing details |
| :--- | :--- |
| MAN, CMB, CMV, LCM | • The MHA, MLA, and DMLA of the alarm detection (ALM) are set to 0. |
| (alarm clear processing) | •The alarm bits (BB1, BB2, BB3, and BB4) are set to 0. |
|  | • "Output conversion processing (3)" is performed and the instruction ends. |
| AUT, CAB, CAS, CCB, CSV, LCA, LCC | "Variation rate \& upper/lower limiter processing (2)" is performed. |
|  | However, when ALM SEA is 1 and SM817 is on, alarm bits BB1, BB2, BB3, and BB4 are set to 0 and the <br> S.OUT2 instruction is terminated. |

## Variation rate \& upper/lower limiter (2)

The variation rate and upper/lower limits of the input value ( $\mathrm{E} 1=\Delta \mathrm{MV}$ ) are checked, and the data after the processing and an alarm are output.

- Variation rate limiter processing performs the following operations, and outputs the result to the output variation rate alarm (BB4) and the DMLA of the alarm detection (ALM).

| Condition | BB4, DMLA | Result (T1) |
| :--- | :--- | :--- |
| $\mid$ E1-MV $\mid \leq D M L ~$ | 0 | E1 |
| $($ E1-MV) $>$ DML | $1^{* 1}$ | MV + DML |
| $(E 1-M V)<-$ DML | $1^{* 1}$ | MV - DML |

*1 If the DMLI or ERRI of the disable alarm detection (INH) is set to 1, the output variation rate alarm (BB4) and the DMLA of the alarm detection (ALM) are set to 0 .

- Upper/lower limiter processing performs the following operations, and outputs the result to the output upper limit alarm (BB2); output lower limit alarm (BB3); and MHA and MLA of the alarm detection (ALM).

| Condition | BB3, MLA | BB2, MHA | MV |
| :--- | :--- | :--- | :--- |
| $\mathrm{T} 1>$ MH | 0 | $1^{* 2}$ | MH |
| $\mathrm{T} 1<\mathrm{ML}$ | $1^{* 3}$ | 0 | ML |
| $\mathrm{ML} \leq \mathrm{T} 1 \leq \mathrm{MH}$ | 0 | 0 | T 1 |

*2 If the MHI or ERRI of the disable alarm detection (INH) is set to 1, the output upper limit alarm (BB2) and the MHA of the alarm detection (ALM) are set to 0 .
*3 If the MLI or ERRI of the disable alarm detection (INH) is set to 1, the output lower limit alarm (BB3) and the MLA of the alarm detection (ALM) are set to 0 .

## Output conversion processing (3)

The output value (BW) is calculated from the following expression.
$\mathrm{BW}=\frac{\text { NMAX-NMIN }}{100} \times \mathrm{MV}+\mathrm{NMIN}$

## Loop stop processing (4)

The following processing is performed according to the SPA status of the alarm detection (ALM).

| SPA status | Processing details |
| :--- | :--- |
| 1 | The loop stops. When the loop stops, the following operations are performed and the S.OUT2 instruction ends. <br> • The last output value (BW) is held. |
|  | • The DMLA, MHA, and DLA of the alarm detection (ALM) are set to 0. <br> • The operation mode (MODE) is set to MAN. <br> • The alarm bits (BB1, BB2, BB3, and BB4) are set to 0. |
| 0 | The loop runs and "mode determination (1)" is performed. |

## Hold processing (5)

This processing specifies whether to hold the output value (BW) by the S.OUT2 instruction when a sensor error occurs (detected by the S.IN instruction). The hold processing is performed when the value is determined as RUN by "Loop Stop Determination". SM817 is used to specify whether to hold the manipulated value (MV) when a sensor error occurs.

- SM817 = OFF: Do not hold the manipulated value (MV).
- SM817 = ON: Hold the manipulated value (MV)


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3400 H | An invalid operation (such as division by zero) is performed. |
| 3402 H | The value specified by (s1), (s2), or (d2) is a subnormal number or NaN (not a number). |
| 3403 H | An overflow has occurred. |

## Manual output

## S．MOUT

This instruction reads the manipulated value（MV）from the loop tag memory，and performs output conversion and alarm clear processing．

| Ladder |  |  |  |  | ST <br> ENO：＝S＿MOUT2（EN，s1，s2，d1，d2）； |
| :---: | :---: | :---: | :---: | :---: | :---: |
| ■－－－ |  |  |  |  |  |
|  | （s1） | （d1） | （s2） | （d2） |  |

FBD／LD

| ［－－－］ |  |
| :---: | :---: |
| EN | ENO |
| s1 | d1 |
| s2 | d2 |

Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．MOUT | $\square$ |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| （s1） | Empty string specification <br> （Specify＂＂in ladder．Specify＂in ST or FBD／LD．） | - | String |
| （d1） | Block memory start device | Refer to＂Block memory＂． | Single－precision real <br> number |
| （s2） | Operation constant start device | Refer to＂Operation constant＂． | Word |
| （d2） | Loop tag memory start device | Refer to＂Loop tag memory＂． | Word |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，R，ZR，RD | U미미，J밈， U3EDI（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － |
| （d1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

Block memory

| Operand：（d1） |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Position | Symbol | Name | Recommended range | Sata type <br> value | Set by |  |
| +0 | BW | Output value | -999999 to 999999 | Single－precision <br> real number | - |  |

Operation constant

| Operand: (s2) | Recommended range | Data type | Standard <br> value | Set by |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Position | Symbol | Name | NMAX <br> conversion <br> upper limit | Output |  |  |
| +1 | NMIN | Output <br> conversion <br> lower limit | -999999 to 999999 | Single-precision <br> real number | 100.0 | User |
| +2 | +3 |  |  | Single-precision <br> real number | 0.0 | User |

## Loop tag memory

The loop tag memory occupies 96 words from the specified start device.

| Operand: (d2) |  |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range |  |  |  |  |  |  | Data type | Standard | Set by |
| +1 | MODE | Operation mode | 0 to FFFFH |  |  |  |  |  |  | 16-bit unsigned binary | 8H | User/ system |
| +3 | ALM | Alarm detection | $\begin{aligned} & 0 \text { to FFF } \\ & \begin{array}{l} \text { b15b14 } \\ \hline \begin{array}{\|c\|c} \frac{\alpha}{\omega} \\ \omega \end{array} \\ \hline \text { SPA } \\ \text { 0: Loop I } \\ \text { 1: Loop } \end{array} \end{aligned}$ |  |  |  |  |  |  | 16-bit unsigned binary | 4000H | User/ system |
| $\begin{aligned} & +12 \\ & +13 \end{aligned}$ | MV | Manipulated value | -10 to 110 [\%] |  |  |  |  |  |  | Single-precision real number | 0.0 | User |

## Processing details

This instruction performs output conversion of the manipulated value (MV) in the device specified by (d2), and stores the result in the device specified by (d1).
The following is the processing block diagram of the S.MOUT instruction. (The numbers (1) to (3) in the diagram indicate the order of the processing.)


## ■Mode determination (1)

The following processing is performed depending on the operation mode (MODE).

| Operation mode (MODE) | Processing details |
| :--- | :--- |
| MAN, CMB, CMV, LCM | $\bullet$ •The manipulated value $(M V)$ is used for the output value (BW). <br>  <br> • "Output conversion processing $(2)$ " is performed. |
| AUT, CAB, CAS, CCB, CSV, LCA, LCC | The last output value (BW) is held. |

©Output conversion processing (2)
The output value (BW) is calculated from the following expression.
$B W=\frac{\text { NMAX-NMIN }}{100} \times M V+$ NMIN

## ■Loop stop processing (3)

The following processing is performed according to the SPA status of the alarm detection (ALM).

| SPA status | Processing details |
| :--- | :--- |
| 0 | Performs "Mode determination (1)". |
| 1 | Performs the following operations and terminates the S.MOUT instruction. <br> • The last output value (BW) is held. <br> • The operation mode (MODE) is set to MAN. |

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3400 H | An invalid operation (such as division by zero) is performed. |
| 3402 H | The value specified by (s2) or (d2) is a subnormal number or NaN (not a number). |
| 3403 H | An overflow has occurred. |

## Time proportioning

## S．DUTY

This instruction outputs ON and OFF by changing the ON／OFF ratio in a given cycle in proportion to the input data（ 0 to 100\％）．

| Ladder |  |  |  |  | $\begin{aligned} & \text { ST } \\ & \text { ENO:=S_DUTY(EN,s1,s2,d1,d2); } \end{aligned}$ |
| :---: | :---: | :---: | :---: | :---: | :---: |
| －－－－－ |  |  |  |  |  |
|  | （s1） | （d1） | （s2） | （d2） |  |

FBD／LD

| ■－－－］ |  |
| :---: | :---: |
| EN | ENo |
| s1 | d1 |
| s2 | d2 |

Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．DUTY | $\square$ |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| （s1） | Input data start device | Refer to＂Input data＂． | Single－precision real <br> number |
| （d1） | Block memory start device | Refer to＂Block memory＂． | Word |
| （s2） | Empty string specification <br> （Specify＂＂in ladder．Specify＂in ST or FBD／LD．） | - | String |
| （d2） | Loop tag memory start device | Refer to＂Loop tag memory＂． | Word |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，R，ZR，RD | UロIGロ，JロIロ， U3EDI（H）G口 | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － |
| （d2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Input data

| Operand：（s1） |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Position | Symbol | Name | Recommended range | Data type | Standard <br> value | Set by |
| +0 | E1 | Input value <br> $(\Delta \mathrm{MV})$ | -999999 to $999999[\%]$ | Single－precision <br> real number | - |  |

Block memory


Loop tag memory
The loop tag memory occupies 96 words from the specified start device.

| Operand: (d2) |  |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range |  |  |  |  |  |  | Data type | Standard | Set by |
| +1 | MODE | Operation mode | 0 to FFFFH |  |  |  |  |  |  | 16-bit unsigned binary | 8H | User/ system |
| +3 | ALM | Alarm detection | 0 to FFFFH <br> SPA <br> 0: Loop RUN <br> 1: Loop STOP <br> DMLA, SEA, MHA, MLA <br> 0 : No alarm <br> 1: Alarm |  |  |  |  |  |  | 16-bit unsigned binary | 4000H | User/ system |
| +4 | INH | Disable alarm detection | TRKF <br> 0 : Tracking not set <br> 1: Tracking set <br> ERRI, DMLI, MHI, MLI <br> 0 : Alarm detection enabled <br> 1: Alarm detection disabled |  |  |  |  |  |  | 16-bit unsigned binary | 4000H | User/ system |
| $\begin{aligned} & +12 \\ & +13 \end{aligned}$ | MV | Manipulated value | -10 to 110 [\%] |  |  |  |  |  |  | Single-precision real number | 0.0 | User/ system |
| $\begin{aligned} & +18 \\ & +19 \end{aligned}$ | MH | Output upper limit value | -10 to 110 [\%] |  |  |  |  |  |  | Single-precision real number | 100.0 | User |
| $\begin{aligned} & +20 \\ & +21 \end{aligned}$ | ML | Output lower limit value | -10 to 110 [\%] |  |  |  |  |  |  | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +48 \\ & +49 \end{aligned}$ | DML | Output variation rate limit value | 0 to 100 [\%] |  |  |  |  |  |  | Single-precision real number | 100.0 | User |
| $\begin{aligned} & +54 \\ & +55 \end{aligned}$ | 1 | Integral constant | 0 to 999999 [s] |  |  |  |  |  |  | Single-precision real number | 10.0 | User |
| $\begin{aligned} & +62 \\ & +63 \end{aligned}$ | MVP | MV internal operation value | -999999 to 999999 [\%] |  |  |  |  |  |  | Single-precision real number | 0.0 | System |


| Operand: (d2) |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Position | Symbol | Name | Recommended range | Data type | Standard <br> value | Set by |
| +68 | CTDUTY | Control output <br> cycle | 0 to $999999[\mathrm{~s}]$ |  |  |  |
| Provided that $\frac{C T D U T Y}{\triangle T}<=32767$ |  |  |  |  |  |  |

## Loop tag past value memory

The system uses this area as a work area.
To start the control from the initial status, clear data by using a sequence program.
The loop tag past value memory occupies 32 words after the loop tag memory.


## Execution cycle ( $\Delta T$ )

Set the execution cycle ( $\Delta \mathrm{T}$ ) in SD816 and SD817 in single-precision real number.

## Processing details

This instruction calculates the manipulated value (MV) from the input value ( $\mathrm{E} 1=\Delta \mathrm{MV}$ ) in the device specified by ( s 1 ) by performing input addition processing.
The instruction also turns ON or OFF the device specified by (d1) in proportion to the manipulated value (MV).
The ON/OFF time is a value determined by assuming the time specified by the control output cycle (CTDUTY) as $100 \%$. The ON/OFF time is switched every execution cycle.
The instruction also performs variation rate \& upper/lower limiter and reset windup of the calculated manipulated value (MV).


The following is the processing block diagram of the S.DUTY instruction. (The numbers (1) to (7) in the diagram indicate the order of the processing.)


## ■Mode determination (1)

The following processing is performed depending on the operation mode (MODE).

| Operation mode (MODE) | Processing details |
| :---: | :---: |
| MAN, CMB, CMV, LCM <br> (alarm clear processing) | - The MHA, MLA, and DMLA of the alarm detection (ALM) are set to 0 . <br> - The MHA2 and MLA2 of the alarm detection 2 (ALM2) are set to 0 . <br> - The alarm bits (BB1, BB2, BB3, and BB4) are set to 0 . <br> - TRKF of INH is set to 1 . <br> - "Output ON time conversion processing (5)" is performed. |
| AUT, CAB, CAS, CCB, CSV, LCA, LCC | "Input addition processing (2)" is performed. <br> However, when ALM SEA is 1 and SM817 is on, alarm bits BB1, BB2, BB3, and BB4 are set to 0 and the S.DUTY instruction is terminated. |

## Input addition processing (2)

A temporary $\mathrm{MV}(\mathrm{T})$ is calculated on the basis of the input value ( $\mathrm{E} 1=\Delta \mathrm{MV}$ ). The following processing is performed depending on the TRKF of INH

| Tracking flag (TRKF) status | Processing details |
| :---: | :---: |
| 1 | - The manipulated value (MV) is stored in the MV internal operation value (MVP). <br> - The input value (E1) is set to $0 .(\Delta \mathrm{MV}=0)$ <br> - TRKF of INH is set to 0 . <br> - A temporary $\mathrm{MV}(\mathrm{T})$ is calculated according to the following expression. <br> T=E1+MVP <br> MVP=T |
| 0 | A temporary $\mathrm{MV}(\mathrm{T})$ is calculated according to the following expression. $\begin{aligned} & \mathrm{T}=\mathrm{E} 1+\mathrm{MVP} \\ & \mathrm{MVP}=\mathrm{T} \end{aligned}$ |

## Variation rate \& upper/lower limiter (3)

Variation rates and upper/lower limits are checked for the difference between the temporary MV(T) and manipulated value (MV), and the data after limiter processing and an alarm are output.

- Variation rate limiter processing performs the following operations, and outputs the result to the output variation rate alarm (BB4) and the DMLA of the alarm detection (ALM).

| Condition | BB4, DMLA | Result (T1) |
| :--- | :--- | :--- |
| $\|T-M V\| \leq D M L$ | 0 | $T$ |
| $(T-M V)>D M L$ | $1^{* 1}$ | MV+DML |
| $(T-M V)<-D M L$ | $1^{* 1}$ | $M V-D M L$ |

*1 If the DMLI or ERRI of the disable alarm detection (INH) is set to 1, the output variation rate alarm (BB4) and the DMLA of the alarm detection (ALM) are set to 0 .

- Upper/lower limiter processing performs the following operations, and outputs the result to the output upper limit alarm (BB2); output lower limit alarm (BB3); MHA and MLA of the alarm detection (ALM); and MHA2 and MLA2 of the alarm detection (ALM2).

| Condition | BB3, MLA, MLA2 | BB2, MHA, MHA2 | MV |
| :--- | :--- | :--- | :--- |
| $\mathrm{T} 1>\mathrm{MH}$ | 0 | $1^{* 2}$ | MH |
| $\mathrm{T} 1<\mathrm{ML}$ | $1^{* 3}$ | 0 | ML |
| $\mathrm{ML} \leq \mathrm{T} 1 \leq \mathrm{MH}$ | 0 | 0 | T 1 |

*2 If the MHI or ERRI of the disable alarm detection (INH) is set to 1, the output upper limit alarm (BB2) and the MHA of the alarm detection (ALM) are set to 0 .
Note that the MHA2 of the alarm detection 2 (ALM2) remains 1.
*3 If the MLI or ERRI of the disable alarm detection (INH) is set to 1 , the output lower limit alarm (BB3) and the MLA of the alarm detection (ALM) are set to 0 .
Note that the MLA2 of the alarm detection 2 (ALM2) remains 1.

## Reset windup (4)

If the manipulated value (MV) goes beyond the upper or lower limit, the following operation is performed to return it to the upper or lower limit and enable immediate response when the deviation is inverted. However, when the integral constant (I) is 0 , reset windup processing is not performed.

| Condition | Operational expression |
| :--- | :--- |
| $T 1>M H, \frac{\triangle T}{I}<=1$ | $M V P=\left(\frac{\Delta T}{1}\right)(M H-T)+T$ |
| $T 1<M H, \frac{\Delta T}{I}<=1$ | $M V P=\left(\frac{\Delta T}{1}\right)(M L-T)+T$ |

## Output ON time conversion processing (5)

The following processing is performed by output ON time conversion processing.

| Condition | Processing details |
| :--- | :--- |
| The control output cycle (CTDUTY) has been reached. | The output ON counter is calculated using the following expression. The output counter is <br> cleared to 0 at this time. <br>  |
| OutputON Counter $=\frac{\mathrm{CTDUTY}}{\triangle \mathrm{T}} \times \mathrm{MV} \times \frac{1}{100}$ |  |
| The control output cycle (CTDUTY) has not been reached. | The output counter is incremented by, 1 and "output conversion processing (6)" is performed. |

Output conversion processing (6)
The following processing is performed by output conversion processing

| Condition | BW1 |
| :--- | :--- |
| Output counter < Output ON counter | 1 |
| Output counter $\geq$ Output ON counter | 0 |

Loop stop processing (7)
The following processing is performed according to the SPA status of the alarm detection (ALM).

| SPA status | Processing details |
| :---: | :---: |
| 1 | The loop stops. When the loop stops, the following operations are performed and the S.DUTY instruction ends. <br> - The output bit (BW1) is output at the last ON/OFF rate. <br> - The DMLA, MHA, and DLA of the alarm detection (ALM) are set to 0 . <br> - The MHA2 and MLA2 of the alarm detection 2 (ALM2) are set to 0 . <br> - The operation mode (MODE) is set to MAN. <br> - The alarm bits (BB1, BB2, BB3, and BB4) are set to 0 . |
| 0 | The loop runs and "mode determination (1)" is performed. |

## ■Hold processing (8)

This processing specifies whether to hold the output value by the S.DUTY instruction when a sensor error occurs (detected by the S.IN instruction). The hold processing is performed when the value is determined as RUN by "Loop Stop Determination".
SM817 is used to specify whether to hold the manipulated value (MV) when a sensor error occurs.

- SM817 = OFF: Do not hold the manipulated value (MV).
- SM817 = ON: Hold the manipulated value (MV).


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3400 H | An invalid operation (such as division by zero) is performed. |
| 3402 H | The value specified by (s1) or (d2) is a subnormal number or NaN (not a number). |
| 3403 H | An overflow has occurred. |
| 3405 H | Control output cycle (CTDUTY) $<0$ |
|  | The execution cycle ( $\Delta \mathrm{T})$ setting is less than 0. |
|  | (Control output cycle (CTDUTY) execution cycle $(\Delta \mathrm{T}))>32767$ |

## Batch counter

## S．BC

This instruction compares the input data with the set value，and outputs bit data when it reaches the set value．


## FBD／LD

| ■－－－$]$ |  |
| :---: | :---: |
| EN | ENO |
| s1 | d1 |
| s2 | d2 |

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．BC | $-\square$ |

## Setting data

DDescription，range，data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| （s1） | Input data start device | Refer to＂Input data＂． | 32－bit unsigned binary |
| （d1） | Block memory start device | Refer to＂Block memory＂． | Word |
| （s2） | Empty string specification <br> （Specify＂＂in ladder．Specify＂in ST or FBD／LD．） | - | String |
| （d2） | Loop tag memory start device | Refer to＂Loop tag memory＂． | Word |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，R，ZR，RD | UपIGロ，JロID， U3EDl（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － |
| （d2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## －Input data

| Operand：（s1） |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| $\begin{aligned} & +0 \\ & +1 \\ & \hline \end{aligned}$ | E1 | Input value | 0 to 2147483647 | 32－bit unsigned binary | － | User |

Block memory


■Loop tag memory
The loop tag memory occupies 96 words from the specified start device.


## ■Loop tag past value memory

The system uses this area as a work area.
To start the control from the initial status, clear data by using a sequence program.
The loop tag past value memory occupies 32 words after the loop tag memory.

| Operand: (d2) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| +124 | - | Variation rate monitoring counter initialization completion flag | - | - | - | System |
| +125 |  | Variation rate monitoring counter (rounded off to the nearest whole number) |  |  |  |  |
| $\begin{aligned} & +126 \\ & +127 \end{aligned}$ | $x_{n-m}$ | - |  |  |  |  |

Execution cycle ( $\Delta T$ )
Set the execution cycle $(\Delta T)$ in SD816 and SD817 in single-precision real number.

## Processing details

This instruction compares the input value (E1) with SV1/SV2 and outputs bit data when E1 reaches SV1/SV2.
The instruction also performs upper limit check, variation rate check, and output conversion processing of the input value (E1) at that time.

## ■Upper limit check (1)

The upper limit check performs the following operations, and outputs the result to the upper limit alarm (BB2) and PHA of the alarm detection (ALM).

| Condition | BB2, PHA |
| :--- | :--- |
| E1 $>$ PH | $1^{* 1}$ |
| Others | 0 |

*1 If PHI or ERRI of the disable alarm detection (INH) is set to 1, the upper limit alarm (BB2) and the PHA of the alarm detection (ALM) are set to 0 .

## Variation rate check processing (2)

A variation rate alarm check is performed during the variation rate alarm check time (CTIM) in the device specified by (d2).
For the variation rate alarm check, the variation of the input value (E1) is compared with the variation rate alarm value (DPL) every execution cycle $\Delta \mathrm{T}$ ).

| Condition | BB3, DPPA |
| :--- | :--- |
| $\left(X_{n}-X_{n-m}\right) \geq$ DPL | $1^{* 1}$ |
| Others | 0 |

[^27]The variation rate alarm counter is calculated using the following expression.
$m=\frac{C T I M}{\Delta T}$
The variation rate alarm counter ( m ) varies from 1 to m . However, no processing is performed when variation rate alarm counter $(m)=0$.

Ex.
When variation rate alarm counter $(m)=4$, operations are performed as shown below.


Output conversion processing (3)
In output conversion processing, the following operations are performed and the result is stored in output 1 (BW1)/output 2 (BW2).

| Condition | BW1 | BW2 |
| :--- | :--- | :--- |
| E1 <0 | 0 | 0 |
| $0 \leq E 1<$ SV1 | 0 | - |
| E1 $\geq$ SV1 | 1 | - |
| $0 \leq E 1<$ SV2 | - | 0 |
| E1 $\geq$ SV2 | - | 1 |

## Operation error

| Error code (SD0) | Description |
| :--- | :--- |
| 3400 H | An invalid operation (such as division by zero) is performed. |
| 3402 H | The value specified by (s1) or (d2) is a subnormal number or NaN (not a number). |
| 3403 H | An overflow has occurred. |
| 3405 H | Variation rate alarm check time (CTIM) <0 |
|  | The execution cycle $(\Delta \mathrm{T})$ setting is less than 0. |
|  | (Variation rate alarm check time $(\mathrm{CTIM}) \div$ execution cycle $(\Delta \mathrm{T}))>32767$ |

## Pulse integration

## S．PSUM

This instruction integrates and outputs the number of input pulses．

| Ladder |  |  |  |  |  | ST |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| ■－－－- |  |  |  |  |  |  |
|  |  | （s1） | （d1） |  | (d2) |  |
| FBD／LD |  |  |  |  |  |  |



## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．PSUM | $\square$ |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| （s1） | Input data start device | Refer to＂Input data＂． | Word |
| （d1） | Block memory start device | Refer to＂Block memory＂． | Word |
| （s2） | Operation constant start device | Refer to＂Operation constant＂． | Word |
| （d2） | Loop tag memory start device | Refer to＂Loop tag memory＂． | Word |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | J미민 | T，ST，C，D，W，SD， SW，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

Input data

| Operand: (s1) |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol |  | Name | Recommended range |  | Data type | Standard value | Set by |
| $\begin{aligned} & +0 \\ & +1 \end{aligned}$ | E1 |  | Input value | Use a ring counter consisting of 16 bits or more. <br> - 16-bit ring counter $00000000 \mathrm{H} \rightarrow 0000$ FFFFH $\rightarrow 00000000 \mathrm{H}$ [pulse] <br> - 24-bit ring counter $00000000 \mathrm{H} \rightarrow 00 F F F F F F H \rightarrow 00000000 \mathrm{H}$ [pulse] <br> - 32-bit ring counter $00000000 \mathrm{H} \rightarrow$ FFFFFFFFFH $\rightarrow 00000000 \mathrm{H}$ [pulse] However, set 32767 (7FFFH) or less for the pulse increment in each instruction execution. |  | 32-bit unsigned binary | - | User |
| +2 | e | e1 <br> e2 | Integration start signal <br> Integration hold signal |      | $$ | 16-bit unsigned binary | - | User |

Block memory

| Operand: (d1) |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Position | Symbol | Name | Recommended range | Data type | Standard <br> value | Set by |
| +0 | BW1 | Output value <br> (integral part) | 0 to 2147483647 | 32 -bit unsigned <br> binary | - | System |
| +2 | BW2 | Output value <br> (decimal part) | 0 to 2147483647 |  |  |  |
| +3 |  |  |  |  |  |  |

## Operation constant

| Operand: (d2) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| +0 | W | Weight per pulse | 1 to 999 | 16-bit unsigned binary | 1 | User |
| +1 | U | Unit conversion constant | 1,10,100, 1000 | 16-bit unsigned binary | 1 | User |
| $\begin{aligned} & +2 \\ & +3 \end{aligned}$ | HILMT | Integration upper limit | 1 to 2147483647 | 32-bit unsigned binary | $\begin{aligned} & 2147483 \\ & 647 \end{aligned}$ | User |
| +4 | SUMPTN | Integration pattern | - 0: Return to 0 when the integration upper limit (HILMIT) is exceeded. <br> - 1: Holds the integration upper limit (HILIMT) when it is exceeded. | 16-bit unsigned binary | 0 | User |

## Loop tag memory

The loop tag memory occupies 96 words from the specified start device.

| Operand: (d2) |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Position | Symbol | Name | Recommended range | Data type | Standard <br> value | Set by |
| +10 | SUM1 | Integrated value <br> (integral part) | 0 to 2147483647 | 32-bit unsigned <br> binary | 0 |  |
| +11 | SUM2 | Integrated value <br> (decimal part) | 0 to 2147483647 | 32-bit unsigned <br> binary | 0 | System |
| +12 | (12 |  |  |  | System |  |

## ■Loop tag past value memory

The system uses this area as a work area.
To start the control from the initial status, clear data by using a sequence program.
The loop tag past value memory occupies 32 words after the loop tag memory.

| Operand: (d2) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| $\begin{aligned} & +116 \\ & +117 \end{aligned}$ | $E 1_{n-1}$ | Last input value | - | - | - | System |

## Processing details

This instruction integrates the input value (E1) in the device specified by (s1), and stores the result in the device specified by (d1).
HILMT and SUMPTN can be used to specify whether to return SUM1/SUM2 to 0 or hold the HILMT value when BW1/BW2 exceeds HILMT.
e1 and e2 can be used to start or stop integration of E1.

- Operation performed when the integration pattern is set to "Return to 0 when HILMT is exceeded"

- Operation performed when the integration pattern is set to "Hold the HILMT value when HILMT is exceeded"



## ■Input value increment operation processing

In input value increment operation processing, the following processing is performed for the input value (E1).

| e1 | e2 | Input value increment (T1) |
| :--- | :--- | :--- |
| 0 | 0 | - |
| 0 | 1 | - |
| 1 | 0 | $E 1-E 1_{n-1}$ |
| 1 | 1 | - |

Integration value calculation processing
In integrated value calculation processing, the following processing is performed for the input value increment (T1).

| e1 | e2 | Integrated value (decimal part) (T2), integrated value (decimal part) (T3) |
| :--- | :--- | :--- |
| 0 | 0 | $\mathrm{~T} 2=0$ |
| $\mathrm{~T} 3=0$ |  |  |

*1 In the case of integration stop/reset $(\mathrm{e} 1=0)$, processing is performed by assuming it as integration hold clear $(\mathrm{e} 2=0)$.

## ■Output conversion processing

In output conversion processing, the following processing is performed for the integrated values (T2, T3).

| SUMPTN | Condition | BW1, SUM1 | BW2, SUM2 |
| :--- | :--- | :--- | :--- |
|  | T2 2 HILMT | BW1 $=$ T2 $\div$ remainder of HILMT <br> SUM1 $=$ T2 $\div$ remainder of HILMT | BW2 $=$ T3 <br> SUM2 $=$ T3 |
|  | Others | BW1 $=$ T2 <br> SUM1 $=$ T2 | BW2 $=$ T3 <br> SUM2 $=$ T3 |
|  | T2 2 HILMT | BW1 $=$ HILMT <br> SUM1 $=$ HILMT | BW2 $=0$ <br> SUM2 $=0$ |
|  | Others | BW1 $=$ T2 <br> SUM1 $=$ T2 | BW2 $=$ T3 <br> SUM2 $=$ T3 |
|  |  |  |  |

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3400 H | An invalid operation (such as division by zero) is performed. |

### 10.3 Control Operation Instructions

## Basic PID control

## S.PID

This instruction performs process value differential type (inexact differential) PID operation. The instruction performs the following processing steps: SV setting, tracking, gain (Kp) operation, PID operation, and deviation check.


## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S.PID | $-\square$ |

## Setting data

■Description, range, data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| (s1) | Input data start device | Refer to "Input data". | Single-precision real <br> number |
| (d1) | Block memory start device | Refer to "Block memory". | Word |
| (s2) | Operation constant start device | Refer to "Operation constant". | Word |
| (d2) | Loop tag memory start device | Refer to "Loop tag memory". | Word |
| (s3) | When E2 is used: Set value start device <br> When E2 is not used: Dummy device | Refer to "Set value". | Single-precision real <br> number |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | J밈 | T, ST, C, D, W, SD, SW, R, ZR, RD | U밈, J밈, U3E $\square 1(H) G \square$ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (s1) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (d1) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (s2) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (d2) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (s3) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |

## Input data

| Operand: (s1) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| $\begin{aligned} & +0 \\ & +1 \end{aligned}$ | E1 | Input value | -999999 to 999999 | Single-precision real number | - | User |

## Block memory



Operation constant


## ■Loop tag memory

The loop tag memory occupies 96 words from the specified start device.

| Operand: (d2) |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range |  |  |  |  |  | Data type | Standard value | Set by |
| +1 | MODE | Operation mode | 0 to FFFFH |  |  |  |  |  | 16-bit unsigned binary | 8H | User/ system |
| +3 | ALM | Alarm detection | 0 to FFFFH <br> SPA <br> 0: Loop RUN <br> 1: Loop STOP <br> DVLA, MHA, MLA <br> 0 : No alarm <br> 1: Alarm |  |  |  |  |  | 16-bit unsigned binary | 4000H | User/ system |
| +4 | INH | Disable alarm detection | 0 to FFFFH <br> TRKF <br> 0 : Tracking not set <br> 1: Tracking set <br> ERRI, DVLI, MHI, MLI <br> 0 : Alarm detection enabled <br> 1: Alarm detection disabled |  |  |  |  |  | 16-bit unsigned binary | 4000H | User/ system |
| $\begin{aligned} & +14 \\ & +15 \end{aligned}$ | SV | Set value | RL to RH |  |  |  |  |  | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +16 \\ & +17 \end{aligned}$ | DV | Deviation | -110 to 110 [\%] |  |  |  |  |  | Single-precision real number | 0.0 | System |
| $\begin{aligned} & +22 \\ & +23 \end{aligned}$ | RH | Engineering value upper limit | -999999 to 999999 |  |  |  |  |  | Single-precision real number | 100.0 | User |
| $\begin{aligned} & +24 \\ & +25 \end{aligned}$ | RL | Engineering value lower limit | -999999 to 999999 |  |  |  |  |  | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +46 \\ & +47 \end{aligned}$ | CT | Control cycle | 0 to 999999 [s] <br> Provided that $\frac{C T}{\triangle T}<=32767$ |  |  |  |  |  | Single-precision real number | 1.0 | User |
| $\begin{aligned} & +50 \\ & +51 \end{aligned}$ | DVL | Deviation limit value | 0 to 100 [\%] |  |  |  |  |  | Single-precision real number | 100.0 | User |
| $\begin{aligned} & +52 \\ & +53 \end{aligned}$ | P | Gain | 0 to 999999 |  |  |  |  |  | Single-precision real number | 1.0 | User |
| $\begin{aligned} & +54 \\ & +55 \end{aligned}$ | I | Integral constant | 0 to 999999 [s] |  |  |  |  |  | Single-precision real number | 10.0 | User |
| $\begin{aligned} & +56 \\ & +57 \end{aligned}$ | D | Derivative constant | 0 to 999999 [s] |  |  |  |  |  | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +58 \\ & +59 \end{aligned}$ | GW | Gap width | 0 to 100 [\%] |  |  |  |  |  | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +60 \\ & +61 \end{aligned}$ | GG | Gap gain | 0 to 999999 |  |  |  |  |  | Single-precision real number | 1.0 | User |
| $\begin{aligned} & +62 \\ & +63 \end{aligned}$ | MVP | MV internal operation value | -999999 to 999999 [\%] |  |  |  |  |  | Single-precision real number | 0.0 | System |

## ■Loop tag past value memory

The system uses this area as a work area.
To start the control from the initial status, clear data by using a sequence program.
The loop tag past value memory occupies 32 words after the loop tag memory.


## Set value

The set value (E2) is valid only when b0 of the set value pattern (SVPTN) is set to 0 (Used). To use the upper loop MV as the set value (E2), specify the device (offset +12 ) where the manipulated value (MV) of the upper loop is set.
If E2 is not used, specify a dummy device (SD820).

| Operand: (s3) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| $\begin{aligned} & +0 \\ & +1 \end{aligned}$ | E2 | Set value | -10 to 110 [\%] | Single-precision real number | 0.0 | User |

Execution cycle ( $\Delta \mathrm{T}$ )
Set the execution cycle ( $\Delta \mathrm{T}$ ) in SD816 and SD817 in single-precision real number.

## Processing details

This instruction performs PID operation when the specified control cycle is reached. (The PID operation is of the velocity type/ process value differential type (inexact differential).)
At this time, the instruction also performs the following processing steps: SV setting, tracking, gain (Kp) operation, and deviation check.
The following is the processing block diagram of the S.PID instruction. (The numbers (1) to (7) in the diagram indicate the order of the processing.)


## SV setting processing (1)

The following processing is performed according to the operation mode (MODE) setting.

| MODE setting | Processing details |
| :--- | :--- |
| CAS, CCB, CSV | •If the set value (E2) is specified, engineering value transformation processing (refer to the <br> following expression) is performed, and then "tracking processing (2)" is performed. |
|  | $\mathrm{SV}_{\mathrm{n}}=\frac{R \mathrm{RH}-\mathrm{RL}}{100} \times \mathrm{E} 2+\mathrm{RL}$ <br> - If the set value (E2) is not specified, "tracking processing (2)" is performed without <br> performing engineering value transformation processing. |
| MAN, AUT, CMV, CMB, CAB, LCM, LCA, LCC | "Tracking processing (2)" is performed. |

## Tracking processing (2)

- The set value (SV) is inversely transformed from the engineering value and $\mathrm{SV}_{\mathrm{n}}$ ' is calculated (refer to the following expression).
$S V_{n}^{\prime}=\frac{100}{R H-R L} \times\left(S V_{n}-R L\right)$
- Tracking processing is performed when all of the following conditions are satisfied.
- The tracking bit (TRK) is set to 1 .
- The set value (E2) is used.
- The operation mode (MODE) is set to any of the following: MAN, AUT, CMV, CMB, CAB, LCM, LCA, or LCC.
$\mathrm{E} 2=\mathrm{SV}_{\mathrm{n}}{ }^{\prime}$
- If the set value (E2) is the upper loop MV, the TRKF of the disable alarm detection (INH) of the upper loop is set to 1 .


## Gain (Kp) operation processing (3)

- The deviation (DV) is calculated under the following conditions.

| Condition | Operational expression |
| :--- | :--- |
| Direct action $(\mathrm{PN}=1)$ | $\mathrm{DV}=\mathrm{E} 1-\mathrm{SV}_{\mathrm{n}}{ }^{\prime}$ |
| Reserve action $(\mathrm{PN}=0)$ | $\mathrm{DV}=\mathrm{SV}_{\mathrm{n}}{ }^{\prime}-\mathrm{E} 1$ |

- The output gain $(\mathrm{K})$ is calculated under the following conditions.

| Condition | Operational expression |
| :--- | :--- |
| $\|D V\| \leq G W$ | $K=G G$ |
| $\|D V\|>G W$ | $K=1-\frac{(1-G G) \times G W}{\|D V\|}$ |

IPID operation (4)
The PID operation is performed with the following operational expression.

| Item | Operational expression |  |
| :--- | :--- | :--- |
| $B_{n}$ | Direct action $(P N=1)$ | $B_{n-1}+\frac{M_{D} \times T_{D}}{M_{D} \times C T+T_{D}} \times\left\{\left(P V_{n}-2 P V_{n-1}+P V_{n-2}\right)-\frac{C T \times B_{n-1}}{T_{D}}\right\}$ |
|  | Reserve action $(P N=$ <br> $0)$ | $B_{n-1}+\frac{M_{D} \times T_{D}}{M_{D} \times C T+T_{D}} \times\left\{-\left(P V_{n}-2 P V_{n-1}+P V_{n-2}\right)-\frac{C T \times B_{n-1}}{T_{D}}\right\}$ |
| $B W(\Delta M V)$ | $K_{P} \times\left\{\left(D V_{n}-D V_{n-1}\right)+\frac{C T}{T_{1}} \times D V_{n}+B_{n}\right\}$ |  |

$K_{p}: K \times$ Gain (P), $M_{D}$ : Derivative gain (MTD), $T_{I}$ : Integral constant (I), $T_{D}$ : Derivative constant ( D )
Note that special processing is performed in the following cases.

| Condition | Processing |
| :--- | :--- |
| In either of the following cases: <br> 1. Derivative constant $(\mathrm{D})=0\left(T_{\mathrm{D}}=0\right)$ <br> 2. Operation mode (MODE $)=$ MAN, LCM, or CMV | $\mathrm{B}_{\mathrm{n}}=0$ <br> $($ Note that the loop tag past value memory is set.) |
| In any of the following cases: <br> 1. Integral constant $(I)=0\left(T_{1}=0\right)$ <br> 2. MHA2 or MLA2 of alarm detection 2 (ALM2) is 1. <br> (MVP>MH) and $\left(\frac{C T}{T_{1}} \times D V_{n}>0\right)$ <br> 3. MHA2 or MLA2 of alarm detection 2 (ALM2) is 1. <br> (MVP<ML) and $\left(\frac{C T}{T_{1}} \times D V_{n}<0\right)$ |  |

## Deviation check (5)

A deviation is checked under the following conditions, and the result is output to the DVLA of the alarm detection (ALM) and the large deviation alarm (BB1).

| Condition | Result |
| :--- | :--- |
| DVL < $\|\mathrm{DV}\|$ | DVLA $=$ BB1 $=1^{* 1}$ |
| $(\mathrm{DVL}-\mathrm{DVLS})<\|\mathrm{DV}\| \leq$ DVL | DVLA $=$ BB1 $=$ Last value status hold ${ }^{* 1}$ |
| $\|\mathrm{DV}\| \leq(\mathrm{DVL}-\mathrm{DVLS})$ | DVLA $=$ BB1 $=0$ |

*1 If the DMLI or ERRI of the disable alarm detection (INH) is set to 1 , the DVLA of the alarm detection (ALM) and the large deviation alarm (BB1) are set to 0.

## ■Loop stop processing (6)

The following processing is performed according to the SPA status of the alarm detection (ALM).

| SPA status | Processing details |
| :--- | :--- |
| 1 | The loop stops. When the loop stops, the following operations are performed and the S.PID instruction ends. <br> • The output value $(B W(\Delta M V))$ is set to 0. <br> - The DVLA of alarm detection $(A L M)$ is set to 0. |
|  | - MODE is set to MAN. <br> • BB1 of BB is set to 0. |
| 0 | The loop runs and "control cycle determination processing (7)" is performed. |

## Control cycle determination (7)

If the specified control cycle is not reached, BW $(\Delta \mathrm{MV})$ is set to 0 and the S.PID instruction is terminated.
If the specified control cycle is reached, "SV setting processing (1)" is performed.
Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3400 H | An invalid operation (such as division by zero) is performed. |
| 3402 H | The value specified by (s1), (s2), or (d2) is a subnormal number or NaN (not a number). |
| 3403 H | An overflow has occurred. |
| 3405 H | The control cycle (CT) setting is less than 0. |
|  | The execution cycle ( $\Delta \mathrm{T}$ ) setting is less than 0. |
|  | The value divided the control cycle (CT) by the execution cycle ( $\Delta \mathrm{T}$ ) exceeds 32767. |

## Two－degree－of－freedom PID control

## S．2PID

This instruction performs two－degree－of－freedom PID control operation（inexact differential）．The instruction performs the following processing steps：SV setting，tracking，gain（Kp）operation，two－degree－of－freedom PID control operation，and deviation check．


FBD／LD

| ■－－－${ }^{-}$ |  |
| :---: | :---: |
| EN | ENO |
| s1 | d1 |
| s2 | d2 |
| s3 |  |

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．2PID | $\square$ |
|  | $\square$ |

## Setting data

## Description，range，data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| （s1） | Input data start device | Refer to＂Input data＂． | Single－precision real <br> number |
| （d1） | Block memory start device | Refer to＂Block memory＂． | Word |
| （s2） | Operation constant start device | Refer to＂Operation constant＂． | Word |
| （d2） | Loop tag memory start device | Refer to＂Loop tag memory＂． | Word |
| （s3） | When E2 is used：Set value start device <br> When E2 is not used：Dummy device | Refer to＂Set value＂． | Single－precision real <br> number |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）G口 | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s3） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Input data

| Operand：（s1） |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| $\begin{aligned} & +0 \\ & +1 \end{aligned}$ | E1 | Input value | －999999 to 999999 ［\％］ | Single－precision real number | － | User |

Block memory


Operation constant


Loop tag memory
The loop tag memory occupies 96 words from the specified start device.

| Operand: (d2) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| +1 | MODE | Operation mode | 0 to FFFFH | 16-bit unsigned binary | 8H | User/ system |
| +3 | ALM | Alarm detection | 0 to FFFFFH <br> SPA <br> 0: Loop RUN <br> 1: Loop STOP <br> DVLA, MHA, MLA <br> 0: No alarm <br> 1: Alarm | 16-bit unsigned binary | 4000 H | User/ system |


| Operand: (d2) |  |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range |  |  |  |  |  |  | Data type | Standard value | Set by |
| +4 | INH | Disable alarm detection | 0 to FFFFH |  |  |  |  |  |  | 16-bit unsigned binary | 4000H | User/ system |
| $\begin{aligned} & +14 \\ & +15 \end{aligned}$ | SV | Set value | RL to RH |  |  |  |  |  |  | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +16 \\ & +17 \end{aligned}$ | DV | Deviation | -110 to 110 [\%] |  |  |  |  |  |  | Single-precision real number | 0.0 | System |
| $\begin{aligned} & +22 \\ & +23 \end{aligned}$ | RH | Engineering value upper limit | -999999 to 999999 |  |  |  |  |  |  | Single-precision real number | 100.0 | User |
| $\begin{aligned} & +24 \\ & +25 \end{aligned}$ | RL | Engineering value lower limit | -999999 to 9999999 |  |  |  |  |  |  | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +46 \\ & +47 \end{aligned}$ | CT | Control cycle | 0 to 999999 [s] <br> Provided that $\frac{C T}{\triangle T}<=32767$ |  |  |  |  |  |  | Single-precision real number | 1.0 | User |
| $\begin{aligned} & +50 \\ & +51 \end{aligned}$ | DVL | Deviation limit value | 0 to 100 [\%] |  |  |  |  |  |  | Single-precision real number | 100.0 | User |
| $\begin{aligned} & +52 \\ & +53 \end{aligned}$ | P | Gain | 0 to 999999 |  |  |  |  |  |  | Single-precision real number | 1.0 | User |
| $\begin{aligned} & +54 \\ & +55 \end{aligned}$ | I | Integral constant | 0 to 999999 [s] |  |  |  |  |  |  | Single-precision real number | 10.0 | User |
| $\begin{aligned} & +56 \\ & +57 \end{aligned}$ | D | Derivative constant | 0 to 999999 [s] |  |  |  |  |  |  | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +58 \\ & +59 \end{aligned}$ | GW | Gap width | 0 to 100 [\%] |  |  |  |  |  |  | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +60 \\ & +61 \end{aligned}$ | GG | Gap gain | 0 to 999999 |  |  |  |  |  |  | Single-precision real number | 1.0 | User |
| $\begin{aligned} & +62 \\ & +63 \end{aligned}$ | MVP | MV internal operation value | -999999 to 999999 [\%] |  |  |  |  |  |  | Single-precision real number | 0.0 | System |
| $\begin{aligned} & +64 \\ & +65 \end{aligned}$ | $\alpha$ | Two-degree-offreedom parameter $\alpha$ | 0 to 1 <br> Increasing $\alpha$ decreases the manipulated value variation relative to the set value change. (It will take time to stabilize.) <br> Decreasing $\alpha$ increases the manipulated value variation relative to the set value change. However, it strengthens the compensation operation and accordingly makes hunting greater. |  |  |  |  |  |  | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +66 \\ & +67 \end{aligned}$ | $\beta$ | Two-degree-offreedom parameter $\beta$ | 0 to 1 <br> Increasing $\beta$ decreases the effect of derivative control on the set value change. <br> Decreasing $\beta$ increases the effect of derivative control on the set value change. |  |  |  |  |  |  | Single-precision real number | 1.0 | User |

## ■Loop tag past value memory

The system uses this area as a work area.
To start the control from the initial status, clear data by using a sequence program.
The loop tag past value memory occupies 32 words after the loop tag memory.


## Set value

The set value (E2) is valid only when b0 of the set value pattern (SVPTN) is set to 0 (Used). To use the upper loop MV as the set value (E2), specify the device (offset +12 ) where the manipulated value (MV) of the upper loop is set.
If E 2 is not used, specify a dummy device (SD820).

| Operand: (s3) |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Position | Symbol | Name | Recommended range | Data type | Standard <br> value | Set by |
| +0 <br> +1 | E2 | Set value | -10 to $110[\%]$ | Single-precision <br> real number | 0.0 |  |

## Execution cycle ( $\Delta T$ )

Set the execution cycle ( $\Delta \mathrm{T}$ ) in SD816 and SD817 in single-precision real number.

## Processing details

This instruction performs two-degree-of-freedom PID control operation when the specified control cycle is reached. At this time, the instruction also performs the following processing steps: SV setting, tracking, gain (Kp) operation, and deviation check.
The following is the processing block diagram of the S.2PID instruction. (The numbers (1) to (7) in the diagram indicate the order of the processing.)


## ISV setting processing (1)

The following processing is performed according to the operation mode (MODE) setting.

| MODE setting | Processing details |
| :--- | :--- |
| CAS, CCB, CSV | •If the set value (E2) is specified, engineering value transformation processing (refer to the <br> following expression) is performed, and then "tracking processing (2)" is performed. |
|  | $\mathrm{SV}_{\mathrm{n}}=\frac{R H-R L}{100} \times \mathrm{E} 2+\mathrm{RL}$ <br> - If the set value (E2) is not specified, "tracking processing (2)" is performed without <br> performing engineering value transformation processing. |
| MAN, AUT, CMV, CMB, CAB, LCM, LCA, LCC | "Tracking processing (2)" is performed. |

## Tracking processing (2)

- The set value (SV) is inversely transformed from the engineering value and $\mathrm{SV}_{\mathrm{n}}$ ' is calculated (refer to the following expression).
$S V_{n}^{\prime \prime}=\frac{100}{R H-R L} \times\left(S V_{n}-R L\right)$
- Tracking processing is performed when all of the following conditions are satisfied.
- The tracking bit (TRK) is set to 1 .
- The set value (E2) is used.
- The operation mode (MODE) is set to any of the following: MAN, AUT, CMV, CMB, CAB, LCM, LCA, or LCC.
$\mathrm{E} 2=\mathrm{SV} \mathrm{V}_{\mathrm{n}}{ }^{\prime}$
- If the set value (E2) is the upper loop MV, the TRKF of the disable alarm detection (INH) of the upper loop is set to 1 .


## Gain (Kp) operation processing (3)

- The deviation (DV) is calculated under the following conditions.

| Condition | Operational expression |
| :--- | :--- |
| Direct action $(\mathrm{PN}=1)$ | $\mathrm{DV}=\mathrm{E} 1-\mathrm{SV}_{\mathrm{n}}{ }^{\prime}$ |
| Reserve action $(\mathrm{PN}=0)$ | $\mathrm{DV}=\mathrm{SV}_{\mathrm{n}}{ }^{\prime}-\mathrm{E} 1$ |

- The output gain $(\mathrm{K})$ is calculated under the following conditions.

| Condition | Operational expression |
| :--- | :--- |
| $\|D V\| \leq G W$ | $K=G G$ |
| $\|D V\|>G W$ | $K=1-\frac{(1-G G) \times G W}{\|D V\|}$ |

Two-degree-of-freedom PID control (4)
The two-degree-of-freedom PID control operation is performed with the following operational expression.

$K_{p}: K \times$ Gain (P), $M_{D}$ : Derivative gain (MTD), $T_{I}$ : Integral constant (I), $T_{D}$ : Derivative constant (D)
Note that special processing is performed in the following cases.

| Condition | Processing |
| :--- | :--- |
| In either of the following cases: <br> 1. Derivative constant $(\mathrm{D})=0\left(T_{\mathrm{D}}=0\right)$ <br> 2. Operation mode (MODE $)=$ MAN, LCM, or CMV | $\mathrm{B}_{n}=0, \mathrm{D}_{\mathrm{n}}=0$ <br> (Note that the loop tag past value memory is set.) |
| In any of the following cases: <br> 1. Integral constant $(\mathrm{I})=0\left(\mathrm{~T}_{1}=0\right)$ <br> 2. MHA2 or MLA2 of alarm detection 2 (ALM2) is 1. <br> (MVP>MH) and $\left(\frac{C T}{T_{1}} \times D V_{n}>0\right)$ | $\frac{C T}{T_{1}} \times D V_{n}=0$ |
| 3. MHA2 or MLA2 of alarm detection 2 (ALM2) is 1. |  |
| (MVP<ML) and $\left(\frac{C T}{T_{1}} \times D V_{n}<0\right)$ |  |

## Deviation check (5)

A deviation is checked under the following conditions, and the result is output to the DVLA of the alarm detection (ALM) and the large deviation alarm (BB1) in the block memory.

| Condition | Result |
| :--- | :--- |
| DVL $<\|D V\|$ | DVLA $=$ BB1 $=1^{* 1}$ |
| $(D V L-D V L S)<\|D V\| \leq D V L$ | DVLA $=$ BB1 $=$ Last value status hold ${ }^{* 1}$ |
| $\|D V\| \leq(D V L-D V L S)$ | DVLA $=$ BB1 $=0$ |
| $* 1 \quad$ If the DMLI or ERRI of the disable alarm detection (INH) is set to 1, the DVLA of the alarm detection (ALM) and the large deviation alarm |  |
|  |  |
| $\quad$ (BB1) are set to 0. |  |

## -Loop stop processing (6)

The following processing is performed according to the SPA status of the alarm detection (ALM).

| SPA status | Processing details |
| :--- | :--- |
| 1 | The loop stops. When the loop stops, the following operations are performed and the S.2PID instruction ends. <br> • The output value (BW) is set to 0. |
|  | • The DVLA of alarm detection (ALM) is set to 0. <br> • The operation mode (MODE) is set to MAN. <br> • BB1 of BB is set to 0. |
| 0 | The loop runs and "control cycle determination processing (7)" is performed. |

## Control cycle determination (7)

If the specified control cycle is not reached, output value BW ( $\Delta \mathrm{MV}$ ) is set to 0 and the S.2PID instruction is terminated. If the specified control cycle is reached, "SV setting processing (1)" is performed.

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3400 H | An invalid operation (such as division by zero) is performed. |
| 3402 H | The value specified by (s1), (s2), or (d2) is a subnormal number or NaN (not a number). |
| 3403 H | An overflow has occurred. |
| 3405 H | The control cycle (CT) setting is less than 0. |
|  | The execution cycle ( $\Delta \mathrm{T}$ ) setting is less than 0. |
|  | The value divided the control cycle (CT) by the execution cycle $(\Delta \mathrm{T})$ exceeds 32767. |

## Position type PID control

## S．PIDP

Performs position type PID operation．The instruction performs the following processing steps：SV setting，tracking，gain（Kp） operation，PID operation，deviation check，and operation mode determination．Depending on the operation result up to the mode determination processing，the instruction decides next processing：variation rate \＆upper／lower limiter and output conversion，or alarm clear and output conversion．


FBD／LD


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．PIDP | $\square$ |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| （s1） | Input data start device | Refer to＂Input data＂． | Single－precision real <br> number |
| （d1） | Block memory start device | Refer to＂Block memory＂． | Word |
| （s2） | Operation constant start device | Refer to＂Operation constant＂． | Word |
| （d2） | Loop tag memory start device | Refer to＂Loop tag memory＂． | Word |
| （s3） | When E2 is used：Set value start device <br> When E2 is not used：Dummy device | Refer to＂Set value＂． | Single－precision real <br> number |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，R，ZR，RD | UपIGㅁ，J밈， U3EDl（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s3） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Input data

| Operand：（s1） |  | Recommended range | Data type | Standard <br> value | Set by |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Position | Symbol | Name | Input value | -999999 to $999999[\%]$ | Single－precision <br> real number | - |
| +0 | E1 |  |  | User |  |  |

Block memory


## Operation constant



## ■Loop tag memory

The loop tag memory occupies 96 words from the specified start device.


## ■Loop tag past value memory

The system uses this area as a work area.
To start the control from the initial status, clear data by using a sequence program.
The loop tag past value memory occupies 32 words after the loop tag memory.


## Set value

The set value (E2) is valid only when b0 of the set value pattern (SVPTN) is set to 0 (Used). To use the upper loop MV as the set value (E2), specify the device (offset +12 ) where the manipulated value (MV) of the upper loop is set.
If E 2 is not used, specify a dummy device (SD820).

| Operand: (s3) | Recommended range | Data type | Standard <br> value | Set by |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Position | Symbol | Name | -10 to $110[\%]$ | Single-precision <br> real number | 0.0 |
| +0 | E2 | Set value |  | User |  |
| +1 |  |  |  |  |  |

## Execution cycle ( $\Delta T$ )

Set the execution cycle $(\Delta T)$ in SD816 and SD817 in single-precision real number.

## Processing details

This instruction performs position type PID operation when the specified control cycle is reached.
At this time, the instruction also performs the following processing steps: SV setting, tracking, gain (Kp) operation, deviation check, and operation mode determination.
Depending on the operation result up to the mode determination processing, the instruction performs either variation rate \& upper/lower limiter and output conversion processing, or alarm clear and output conversion processing.
The following is the processing block diagram of the S.PIDP instruction. (The numbers (1) to (10) in the diagram indicate the order of the processing.)


SV setting processing (1)
The following processing is performed according to the operation mode (MODE) setting.

| MODE setting | Processing details |
| :--- | :--- |
| CAS, CCB, CSV | •If the set value (E2) is specified, engineering value transformation processing (refer to the <br> following expression) is performed, and then "tracking processing (2)" is performed. |
|  | $\mathrm{SV}_{\mathrm{n}}=\frac{R H-R L}{100} \times \mathrm{E} 2+\mathrm{RL}$ <br> - If the set value (E2) is not specified, "tracking processing (2)" is performed without <br> performing engineering value transformation processing. |
| MAN, AUT, CMV, CMB, CAB, LCM, LCA, LCC | "Tracking processing (2)" is performed. |

Tracking processing (2)

- The set value (SV) is inversely transformed from the engineering value and $\mathrm{SV}_{\mathrm{n}}$ ' is calculated (refer to the following expression).
$S V_{n}^{\prime}=\frac{100}{R H-R L} \times\left(S V_{n}-R L\right)$
- Tracking processing is performed when all of the following conditions are satisfied.
- The tracking bit (TRK) is set to 1.
- The set value (E2) is used.
- The operation mode (MODE) is set to any of the following: MAN, AUT, CMV, CMB, CAB, LCM, LCA, or LCC.
$\mathrm{E} 2=\mathrm{SV} \mathrm{V}_{\mathrm{n}}$,
- If the set value (E2) is the upper loop MV, the TRKF of the disable alarm detection (INH) of the upper loop is set to 1 .


## Gain (Kp) operation processing (3)

- The deviation (DV) is calculated under the following conditions.

| Condition | Operational expression |
| :--- | :--- |
| Direct action $(\mathrm{PN}=1)$ | $\mathrm{DV}=\mathrm{E} 1-\mathrm{SV}_{\mathrm{n}}{ }^{\prime}$ |
| Reserve action $(\mathrm{PN}=0)$ | $\mathrm{DV}=\mathrm{SV}_{\mathrm{n}}{ }^{\prime}-\mathrm{E} 1$ |

- The output gain $(\mathrm{K})$ is calculated under the following conditions.

| Condition | Operational expression |
| :--- | :--- |
| $\|D V\| \leq G W$ | $K=G G$ |
| $\|D V\|>G W$ | $K=1-\frac{(1-G G) \times G W}{\|D V\|}$ |

## IPID operation (4)

The PID operation is performed with the following operational expression.

| Item |  | Operational expression |
| :--- | :--- | :--- |
| $B_{n}$ | Direct action $(P N=1)$ | $B_{n-1}+\frac{M_{D} \times T_{D}}{M_{D} \times C T+T_{D}} \times\left\{\left(P V_{n}-P V_{n-1}\right)-\frac{C T \times B_{n-1}}{T_{D}}\right\}$ |
|  | Reserve action $(P N=$ <br> $0)$ | $B_{n-1}+\frac{M_{D} \times T_{D}}{M_{D} \times C T+T_{D}} \times\left\{-\left(P V-P V_{n-1}\right)-\frac{C T \times B_{n-1}}{T_{D}}\right\}$ |
| $I_{n}$ | $I_{n-1}+\frac{C T}{T_{1}} \times D V_{n}$ |  |
| $T$ | $K_{p} \times\left(D V_{n}+I_{n}+B_{n}\right)$ |  |

$K_{P}: K \times$ Gain ( $P$ ), $M_{D}$ : Derivative gain (MTD), $T_{I}$ : Integral constant (I), $T_{D}$ : Derivative constant ( $D$ )
Note that special processing is performed in the following cases.

| Condition | Processing |
| :---: | :---: |
| In either of the following cases: <br> 1. Derivative constant $(D)=0\left(T_{D}=0\right)$ <br> 2. Operation mode (MODE) = MAN, LCM, or CMV | $\mathrm{B}_{\mathrm{n}}=0$ <br> (Note that the loop tag past value memory is set.) |
| In any of the following cases: <br> 1. Integral constant $(I)=0\left(T_{I}=0\right)$ <br> 2. MHA2 of the alarm detection $2($ ALM2 $)=1$ $\frac{C T}{T_{1}} \times D V_{n}>0$ <br> 3. MLA2 of the alarm detection $2($ ALM2 $)=1$ $\frac{C T}{T_{1}} \times D V_{n}<0$ <br> 4. Operation mode $($ MODE $)=$ MAN, LCM, or CMV | $\frac{C T}{T_{1}} \times D V_{n}=0$ |
| When all of the following conditions are satisfied: <br> 1. b0 of SD818 (bumpless switching function of S.PIDP control) $=1$. <br> 2. TRKF of the disable alarm detection $($ INH $)=1$ <br> 3. Operation mode $(M O D E)=$ Other than MAN, LCM, and CMV | $\begin{aligned} & \mathrm{I}_{\mathrm{n}-1}=\frac{\mathrm{MV}}{\mathrm{~K}_{\mathrm{p}}}-\left(\mathrm{DV} \mathrm{~V}_{\mathrm{n}}+\mathrm{B}_{\mathrm{n}}\right) \\ & \text { TRKF }=0 \end{aligned}$ |

## Deviation check (5)

A deviation is checked under the following conditions, and the result is output to the DVLA of the alarm detection (ALM) and the large deviation alarm (BB2).

| Condition | Result |
| :--- | :--- |
| $D V L<\|D V\|$ | DVLA $=\mathrm{BB} 2=1^{* 1}$ |
| $(D V L-D V L S)<\|D V\| \leq D V L$ | DVLA $=$ BB2 $=$ Last value*1 $^{*}$ |
| $\|D V\| \leq(D V L-D V L S)$ | $D V L A=B B 2=0$ |

*1 If the DMLI or ERRI of the disable alarm detection (INH) is set to 1 , the DVLA of the alarm detection (ALM) and the large deviation alarm (BB2) are set to 0 .

## ■Mode determination (6)

The following processing is performed according to the operation mode (MODE) setting.

| Operation mode (MODE) | Processing details |
| :--- | :--- |
| MAN, CMB, CMV, LCM | • The MHA, MLA, and DMLA of the alarm detection (ALM) are set to 0. |
| (alarm clear processing) | - The MHA2 and MLA2 of the alarm detection 2 (ALM2) are set to 0. |
|  | - The alarm bits (BB3, BB4, and BB5) are set to 0. |
|  | - The data of BB2 is transferred to BB1. (BB1 = BB2) <br> - When b0 of SD818 (bumpless switching function of S.PIDP control) is 1, TRKF of INH is set to 1. <br> - "Output conversion processing (8)" is performed and the instruction ends. |
| AUT, CAB, CAS, CCB, CSV, LCA, LCC | "Variation rate \& upper/lower limiter processing (7)" is performed. |

## Variation rate \& upper/lower limiter (7)

The variation rate and upper/lower limits of the input value (E1) are checked, and the data after the processing and an alarm are output.
Variation rate limiter processing performs the following operations, and outputs the result to the output variation rate alarm (BB5) of (d1) and the DMLA of the alarm detection (ALM).

| Condition | BB5, DMLA | T1 |
| :--- | :--- | :--- |
| $\|T-M V\| \leq D M L$ | 0 | T |
| $(T-M V)>D M L$ | $1^{* 1}$ | $\mathrm{MV}+\mathrm{DML}$ |
| $(\mathrm{T}-\mathrm{MV})<-\mathrm{DML}$ | $1^{* 1}$ | $\mathrm{MV}-\mathrm{DML}$ |

*1 If the DMLI or ERRI of the disable alarm detection (INH) is set to 1, the output variation rate alarm (BB5) and the DMLA of the alarm detection (ALM) are set to 0 .
Upper/lower limiter processing performs the following operations, and outputs the result to the output upper limit alarm (BB3); output lower limit alarm (BB4); MHA and MLA of the alarm detection (ALM); and MHA2 and MLA2 of the alarm detection (ALM2).

| Condition | BB4, MLA, MLA2 ${ }^{* 4}$ | BB3, MHA, MHA2*4 | MV |
| :--- | :--- | :--- | :--- |
| $\mathrm{T} 1>$ MH | 0 | $1^{* 2}$ | MH |
| $\mathrm{T} 1<\mathrm{ML}$ | $1^{* 3}$ | 0 | ML |
| $\mathrm{ML} \leq \mathrm{T} 1 \leq \mathrm{MH}$ | 0 | 0 | T 1 |

*2 If the MHI or ERRI of the disable alarm detection (INH) is set to 1, the output upper limit alarm (BB3) and the MHA of the alarm detection (ALM) are set to 0 .
Note that the MHA2 of the alarm detection 2 (ALM2) remains 1.
*3 If the MLI or ERRI of the disable alarm detection (INH) is set to 1, the output lower limit alarm (BB4) and the MLA of the alarm detection (ALM) are set to 0 .
Note that the MLA2 of the alarm detection 2 (ALM2) remains 1.
*4 If the specified control cycle is not reached, the MHA2 and MLA2 status of the alarm detection 2 (ALM2) are held.

## ■Output conversion processing (8)

The output value (BW) is calculated from the following expression.
$B W=\frac{\text { NMAX-NMIN }}{100} \times$ MV + NMIN

## Loop stop processing (9)

The following processing is performed according to the SPA status of the alarm detection (ALM).

| SPA status | Processing details |
| :--- | :--- |
| 1 | The loop stops. The following operations are performed and the instruction ends. <br> • The last output value (BW) is held. <br> • The DVLA, MHA, MLA, and DMLA of the alarm detection (ALM) are set to 0. <br> • The MHA2 and MLA2 of the alarm detection 2 (ALM2) are set to 0. <br> • The operation mode (MODE) is set to MAN. <br> • All of the alarm bits (BB1, BB2, BB3, BB4, and BB5) are set to 0. |
| The loop runs and "control cycle determination processing (10)" is performed. |  |
| 0 |  |

## Control cycle determination (10)

If the specified control cycle is not reached, "mode determination processing (6)" is performed regarding T as MV. If the specified control cycle is reached, "SV setting processing (1)" is performed.

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3400 H | An invalid operation (such as division by zero) is performed. |
| 3402 H | The value specified by (s1), (s2), or (d2) is a subnormal number or NaN (not a number). |
| 3403 H | An overflow has occurred. |
| 3405 H | The control cycle (CT) setting is less than 0. |
|  | The execution cycle ( $\Delta \mathrm{T}$ ) setting is less than 0. |
|  | The value divided the control cycle (CT) by the execution cycle $(\Delta \mathrm{T})$ exceeds 32767. |

## Sample PI control

## S．SPI

The instruction checks whether ST or HT is applicable and，if ST is applicable，performs the following processing steps：SV setting，tracking，gain（Kp）operation，SPI operation，and deviation check．


FBD／LD

| ■－－－ |  |
| :---: | :---: |
| EN | Eno |
| s1 | d1 |
| s2 | d2 |
| s3 |  |

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．SPI | $-\square$ |

## Setting data

## Description，range，data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| （s1） | Input data start device | Refer to＂Input data＂． | Single－precision real <br> number |
| （d1） | Block memory start device | Refer to＂Block memory＂． | Word |
| （s2） | Operation constant start device | Refer to＂Operation constant＂． | Word |
| （d2） | Loop tag memory start device | Refer to＂Loop tag memory＂． | Word |
| （s3） | When E2 is used：Set value start device <br> When E2 is not used：Dummy device | Refer to＂Set value＂． | Single－precision real <br> number |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，R，ZR，RD | UपIGㅁ，J밈， U3EDl（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s3） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Input data

| Operand：（s1） |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Position | Symbol | Name | Recommended range | Data type | Standar <br> d value | Set by |
| +0 | E1 | Input value | -999999 to $999999[\%]$ | Single－precision <br> real number | - |  |

Block memory


Operation constant


Loop tag memory
The loop tag memory occupies 96 words from the specified start device.

| Operand: (d2) |  |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range |  |  |  |  |  |  | Data type | Standard | Set by |
| +1 | MODE | Operation mode | 0 to FFFFH |  |  |  |  |  |  | 16-bit unsigned binary | 8H | User/ system |
| +3 | ALM | Alarm detection | SPA <br> 0: Loop RUN <br> 1: Loop STOP <br> DVLA, MHA, MLA <br> 0: No alarm <br> 1: Alarm |  |  |  |  |  |  | 16-bit unsigned binary | 4000H | User/ system |


| Operand: (d2) |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range |  |  |  |  |  | Data type | Standard value | Set by |
| +4 | INH | Disable alarm detection | TRKF <br> 0 : Tracking not set <br> 1: Tracking set <br> ERRI, DVLI, MHI, MLI <br> 0: Alarm detection enabled <br> 1: Alarm detection disabled |  |  |  |  |  | 16-bit unsigned binary | 4000H | User/ system |
| $\begin{aligned} & +14 \\ & +15 \end{aligned}$ | SV | Set value | RL to RH |  |  |  |  |  | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +16 \\ & +17 \end{aligned}$ | DV | Deviation | -110 to 110 [\%] |  |  |  |  |  | Single-precision real number | 0.0 | System |
| $\begin{aligned} & +22 \\ & +23 \end{aligned}$ | RH | Engineering value upper limit | -999999 to 999999 |  |  |  |  |  | Single-precision real number | 100.0 | User |
| $\begin{aligned} & +24 \\ & +25 \end{aligned}$ | RL | Engineering value lower limit | -999999 to 999999 |  |  |  |  |  | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +46 \\ & +47 \end{aligned}$ | ST | Operating time | 0 to 999999 [s] <br> Provided that $\frac{S T}{\Delta T}<=32767$ |  |  |  |  |  | Single-precision real number | 1.0 | User |
| $\begin{aligned} & +50 \\ & +51 \end{aligned}$ | DVL | Deviation limit value | 0 to 100 [\%] |  |  |  |  |  | Single-precision real number | 100.0 | User |
| $\begin{aligned} & +52 \\ & +53 \end{aligned}$ | P | Gain | 0 to 999999 |  |  |  |  |  | Single-precision real number | 1.0 | User |
| $\begin{aligned} & +54 \\ & +55 \end{aligned}$ | I | Integral constant | 0 to 999999 [s] |  |  |  |  |  | Single-precision real number | 10.0 | User |
| $\begin{aligned} & +56 \\ & +57 \end{aligned}$ | STHT | Sampling cycle | 0 to 999999 [s] <br> Provided that $\frac{\text { STHT }}{\triangle T}<=32767$ |  |  |  |  |  | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +58 \\ & +59 \end{aligned}$ | GW | Gap width | 0 to 100 [\%] |  |  |  |  |  | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +60 \\ & +61 \end{aligned}$ | GG | Gap gain | 0 to 999999 |  |  |  |  |  | Single-precision real number | 1.0 | User |
| $\begin{aligned} & +62 \\ & +63 \end{aligned}$ | MVP | MV internal operation value | -999999 to 999999 [\%] |  |  |  |  |  | Single-precision real number | 0.0 | System |

## ■Loop tag past value memory

The system uses this area as a work area.
To start the control from the initial status, clear data by using a sequence program.
The loop tag past value memory occupies 32 words after the loop tag memory.


## Set value

The set value (E2) is valid only when b0 of the set value pattern (SVPTN) is set to 0 (Used). To use the upper loop MV as the set value (E2), specify the device (offset +12 ) where the manipulated value (MV) of the upper loop is set.
If E2 is not used, specify a dummy device (SD820).

| Operand: (s3) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| $\begin{aligned} & +0 \\ & +1 \end{aligned}$ | E2 | Set value | -10 to 110 [\%] | Single-precision real number | 0.0 | User |

## Execution cycle ( $\Delta \mathrm{T}$ )

Set the execution cycle $(\Delta T)$ in SD816 and SD817 in single-precision real number.

## Processing details

This instruction performs normal PI operation during the operating time (ST).
The instruction checks whether ST or HT is applicable and, if ST is applicable, performs the following processing steps: SV setting, tracking, gain (Kp) operation, SPI operation, and deviation check.


The following is the processing block diagram of the S.SPI instruction. (The numbers (1) to (7) in the diagram indicate the order of the processing.)


## ISV setting processing (1)

The following processing is performed according to the operation mode (MODE) setting.

| Operation mode (MODE) | Processing details |
| :--- | :--- |
| CAS, CCB, CSV | •If the set value (E2) is specified, engineering value transformation processing (refer to the <br> following expression) is performed, and then "tracking processing (2)" is performed. <br> $S V_{n}=\frac{R H-R L}{100} \times E 2+R L$ <br> - If the set value (E2) is not specified, "tracking processing (2)" is performed without <br> performing engineering value transformation processing. |
| MAN, AUT, CMV, CMB, CAB, LCM, LCA, LCC | "Tracking processing (2)" is performed. |

## Tracking processing (2)

- The set value (SV) is inversely transformed from the engineering value and $\mathrm{SV}_{\mathrm{n}}$ ' is calculated (refer to the following expression).
$S V_{n}^{\prime}=\frac{100}{R H-R L} \times\left(S V_{n}-R L\right)$
- Tracking processing is performed when all of the following conditions are satisfied.
- The tracking bit (TRK) is set to 1 .
- The set value (E2) is used.
- The operation mode (MODE) is set to any of the following: MAN, AUT, CMV, CMB, CAB, LCM, LCA, or LCC.
$\mathrm{E} 2=\mathrm{SV} \mathrm{V}_{\mathrm{n}}$,
- If the set value (E2) is the upper loop MV, the TRKF of the disable alarm detection (INH) of the upper loop is set to 1 .


## Gain (Kp) operation processing (3)

- The deviation (DV) is calculated under the following conditions.

| Condition | Operational expression |
| :--- | :--- |
| Direct action $(\mathrm{PN}=1)$ | $\mathrm{DV}=\mathrm{E} 1-\mathrm{SV}_{\mathrm{n}}{ }^{\prime}$ |
| Reserve action $(\mathrm{PN}=0)$ | $\mathrm{DV}=\mathrm{SV}{ }_{\mathrm{n}}{ }^{\prime}-\mathrm{E} 1$ |

- The output gain $(\mathrm{K})$ is calculated under the following conditions.

| Condition | Operational expression |
| :--- | :--- |
| $\|D V\| \leq G W$ | $K=G G$ |
| $\|D V\|>G W$ | $K=1-\frac{(1-G G) \times G W}{\|D V\|}$ |

ISPI operation (4)
The SPI operation is performed with the following operational expression.

| Item | Operational expression |
| :--- | :--- |
| During operating time (ST) | $\mathrm{BW}=\mathrm{K}_{\mathrm{P}} \times\left\{\left(\mathrm{DV} \mathrm{V}_{\mathrm{n}}-\mathrm{DV}_{n-1}\right)+\frac{\mathrm{BT}}{\mathrm{T}_{1}} \times D V_{n}\right\}$ |
| During hold time (sample cycle <br> $(\mathrm{STHT})$ - operating time (ST)) | $\mathrm{BW}=0$ (Note that the loop tag past value memory is not set.) |

$\mathrm{K}_{\mathrm{p}}$ : $\mathrm{K} \times$ gain $(\mathrm{P}), \mathrm{T}_{\mathrm{l}}$ : integral constant (I),BT: execution cycle( $\Delta \mathrm{T}$ )
Note that special processing is performed in the following cases.

| Condition | Processing |
| :--- | :--- |
| In any of the following cases: | $\frac{B T}{T_{1}} \times D V_{n}=0$ |
| 1. Integral constant $(I)=0\left(T_{1}=0\right)$ |  |
| 2. MHA2 or MLA2 of alarm detection 2 (ALM2) is 1. |  |
| (MVP>MH) and $\left(\frac{B T}{T_{1}} \times D V_{n}>0\right)$ |  |
| 3. MHA2 or MLA2 of alarm detection 2 (ALM2) is 1. |  |
| $(M V P<M L)$ and $\left(\frac{B T}{T_{1}} \times D V_{n}<0\right)$ |  |

## Deviation check (5)

A deviation is checked under the following conditions, and the result is output to the DVLA of the alarm detection (ALM) and the large deviation alarm (BB1).

| Condition | Result |
| :--- | :--- |
| DVL $<\|D V\|$ | DVLA $=$ BB1 $=1^{* 1}$ |
| $(D V L-D V L S)<\|D V\| \leq D V L$ | DVLA $=$ BB1 $=$ Last value status hold*1 |
| $\|D V\| \leq(D V L-D V L S)$ | DVLA $=$ BB1 $=0$ |

*1 If the DMLI or ERRI of the disable alarm detection (INH) is set to 1, the DVLA of the alarm detection (ALM) and the large deviation alarm (BB1) are set to 0.

## Loop stop processing (6)

The following processing is performed according to the SPA status of the alarm detection (ALM).

| SPA status | Processing details |
| :--- | :--- |
| 1 | The loop stops. When the loop stops, the following operations are performed and the S.SPI instruction ends. <br> • The output value (BW) is set to 0. <br> • The DVLA of alarm detection (ALM) is set to 0. |
|  | • The operation mode (MODE) is set to MAN. <br> • BB1 of BB is set to 0. |
| 0 | The loop runs and "operating time/hold time determination (7)" is performed. |

Operating time monitoring (7)
The instruction checks whether ST or HT (= STHT - ST) is applicable and performs the following processing.

| Condition | Processing details |
| :--- | :--- |
| Operating time (ST) | The instruction performs the following processing steps: SV setting, tracking, gain (Kp) operation, PI operation <br> (operating time), and deviation check. |
| Hold time (HT) (= STHT - ST) | The instruction performs the following processing steps: tracking, SPI operation (hold time), and deviation <br> check. <br> Under the following conditions, however, PI control is performed continuously with the hold time set to 0. <br> $\frac{S T H T}{\triangle T}<=\frac{S T}{\triangle T}$ <br> If the integral part of the left side of the above expression is 0, no processing is performed. (BW also remains <br> unchanged.) |

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3400 H | An invalid operation (such as division by zero) is performed. |
| 3402 H | The value specified by (s1), (s2), or (d2) is a subnormal number or NaN (not a number). |
| 3403 H | An overflow has occurred. |
| }{} | Operating time $(\mathrm{ST})<0$ |
|  | The execution cycle $(\Delta \mathrm{T})$ setting is less than 0. |
|  | Sampling cycle $(\mathrm{STHT})<0$ |
|  | (Operating time $(\mathrm{ST}) \div$ execution cycle $(\Delta \mathrm{T}))>32767$ |
|  | (Sample cycle $(\mathrm{STHT}) \div$ execution cycle $(\Delta \mathrm{T}))>32767$ |

## I－PD control

## S．IPD

This instruction performs I－PD operation．The instruction performs the following processing steps：SV setting，tracking，gain $K_{P}$ operation，IPD operation，and deviation check．


FBD／LD


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．IPD | $\square$ |

## Setting data

## Description，range，data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| （s1） | Input data start device | Refer to＂Input data＂． | Single－precision real <br> number |
| （d1） | Block memory start device | Refer to＂Block memory＂． | Word |
| （s2） | Operation constant start device | Refer to＂Operation constant＂． | Word |
| （d2） | Loop tag memory start device | Refer to＂Loop tag memory＂． | Word |
| （s3） | When E2 is used：Set value start device <br> When E2 is not used：Dummy data | Refer to＂Set value＂． | Single－precision real <br> number |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，R，ZR，RD | U밈，J밈， U3EDI（H）Gロ | Z | LT，LST， LC | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s3） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Input data

| Operand：（s1） |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Position | Symbol | Name | Recommended range | Data type | Standard <br> value | Set by |
| +0 | E1 | Input value | -999999 to $999999[\%]$ | Single－precision <br> real number | - |  |

Block memory


Operation constant


Loop tag memory
The loop tag memory occupies 96 words from the specified start device.

| Operand: (d2) |  |  |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range |  |  |  |  |  |  |  | Data type | Standard value | Set by |
| +1 | MODE | Operation mode | 0 to FFFFH |  |  |  |  |  |  |  | 16-bit unsigned binary | 8H | User/ system |
| +3 | ALM | Alarm detection | SPA <br> 0: Loop RUN <br> 1: Loop STOP <br> DVLA, MHA, MLA <br> 0: No alarm <br> 1: Alarm |  |  |  |  |  |  |  | 16-bit unsigned binary | 4000H | User/ system |


| Operand: (d2) |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range |  |  |  |  |  | Data type | Standard value | Set by |
| +4 | INH | Disable alarm detection | TRKF <br> 0 : Tracking not set <br> 1: Tracking set <br> ERRI, DVLI, MHI, MLI <br> 0 : Alarm detection enabled <br> 1: Alarm detection disabled |  |  |  |  |  | 16-bit unsigned binary | 4000 H | User/ system |
| $\begin{aligned} & +14 \\ & +15 \end{aligned}$ | SV | Set value | RL to RH |  |  |  |  |  | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +16 \\ & +17 \end{aligned}$ | DV | Deviation | -110 to 110 [\%] |  |  |  |  |  | Single-precision real number | 0.0 | System |
| $\begin{aligned} & +22 \\ & +23 \end{aligned}$ | RH | Engineering value upper limit | -999999 to 999999 |  |  |  |  |  | Single-precision real number | 100.0 | User |
| $\begin{aligned} & +24 \\ & +25 \end{aligned}$ | RL | Engineering value lower limit | -999999 to 999999 |  |  |  |  |  | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +46 \\ & +47 \end{aligned}$ | CT | Control cycle | 0 to 999999 [s] <br> Provided that $\frac{C T}{\triangle T}<=32767$ |  |  |  |  |  | Single-precision real number | 1.0 | User |
| $\begin{aligned} & +50 \\ & +51 \end{aligned}$ | DVL | Deviation limit value | 0 to 100 [\%] |  |  |  |  |  | Single-precision real number | 100.0 | User |
| $\begin{aligned} & +52 \\ & +53 \end{aligned}$ | P | Gain | 0 to 999999 |  |  |  |  |  | Single-precision real number | 1.0 | User |
| $\begin{aligned} & +54 \\ & +55 \end{aligned}$ | I | Integral constant | 0 to 999999 [s] |  |  |  |  |  | Single-precision real number | 10.0 | User |
| $\begin{aligned} & +56 \\ & +57 \end{aligned}$ | D | Derivative constant | 0 to 999999 [s] |  |  |  |  |  | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +58 \\ & +59 \end{aligned}$ | GW | Gap width | 0 to 100 [\%] |  |  |  |  |  | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +60 \\ & +61 \end{aligned}$ | GG | Gap gain | 0 to 999999 |  |  |  |  |  | Single-precision real number | 1.0 | User |
| $\begin{aligned} & +62 \\ & +63 \end{aligned}$ | MVP | MV internal operation value | -999999 to 999999 [\%] |  |  |  |  |  | Single-precision real number | 0.0 | System |

## ■Loop tag past value memory

The system uses this area as a work area.
To start the control from the initial status, clear data by using a sequence program.
The loop tag past value memory occupies 32 words after the loop tag memory.


## Set value

The set value (E2) is valid only when b0 of the set value pattern (SVPTN) is set to 0 (Used). To use the upper loop MV as the set value (E2), specify the device (offset +12 ) where the manipulated value (MV) of the upper loop is set.
If E 2 is not used, specify a dummy device (SD820).

| Operand: (s3) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| $\begin{aligned} & +0 \\ & +1 \end{aligned}$ | E2 | Set value | -10 to 110 [\%] | Single-precision real number | 0.0 | User |

Execution cycle ( $\Delta T$ )
Set the execution cycle $(\Delta T)$ in SD816 and SD817 in single-precision real number.

## Processing details

This instruction performs I-PD control when the specified control cycle is reached.
At this time, the instruction also performs the following processing steps: SV setting, tracking, gain (Kp) operation, and deviation check.
The following is the processing block diagram of the S.IPD instruction. (The numbers (1) to (7) in the diagram indicate the order of the processing.)


ISV setting processing (1)
The following processing is performed according to the operation mode (MODE) setting.

| MODE setting | Processing details |
| :--- | :--- |
| CAS, CCB, CSV | •If the set value (E2) is specified, engineering value transformation processing (refer to the <br> following expression) is performed, and then "tracking processing (2)" is performed. |
|  | $\mathrm{SV}_{\mathrm{n}}=\frac{R \mathrm{RH}-\mathrm{RL}}{100} \times \mathrm{E} 2+\mathrm{RL}$ <br> - If the set value (E2) is not specified, "tracking processing (2)" is performed without <br> performing engineering value transformation processing. |
| MAN, AUT, CMV, CMB, CAB, LCM, LCA, LCC | "Tracking processing (2)" is performed. |

## Tracking processing (2)

- The set value (SV) is inversely transformed from the engineering value and $\mathrm{SV}_{\mathrm{n}}$ ' is calculated (refer to the following expression).
$S V_{n}^{\prime}=\frac{100}{R H-R L} \times\left(S V_{n}-R L\right)$
- Tracking processing is performed when all of the following conditions are satisfied.
- The tracking bit (TRK) is set to 1 .
- The set value (E2) is used.
- The operation mode (MODE) is set to any of the following: MAN, AUT, CMV, CMB, CAB, LCM, LCA, or LCC.
$\mathrm{E} 2=\mathrm{SV}_{\mathrm{n}}{ }^{\prime}$
- If the set value (E2) is the upper loop MV, the TRKF of the alarm inhibition (INH) of the upper loop is set to 1 .


## Gain (Kp) operation processing (3)

- The deviation (DV) is calculated under the following conditions.

| Condition | Operational expression |
| :--- | :--- |
| Direct action $(\mathrm{PN}=1)$ | $\mathrm{DV}=\mathrm{E} 1-\mathrm{SV}_{\mathrm{n}}{ }^{\prime}$ |
| Reserve action $(\mathrm{PN}=0)$ | $\mathrm{DV}=\mathrm{SV}_{\mathrm{n}}{ }^{\prime}-\mathrm{E} 1$ |

- The output gain $(\mathrm{K})$ is calculated under the following conditions.

| Condition | Operational expression |
| :--- | :--- |
| $\|D V\| \leq G W$ | $K=G G$ |
| $\|D V\|>G W$ | $K=1-\frac{(1-G G) \times G W}{\|D V\|}$ |

I-PD operation (4)
The PID operation is performed with the following operational expression.

| Item | Operational expression |  |
| :--- | :--- | :--- |
| $B_{n}$ | Direct action $(P N=1)$ | $B_{n-1}+\frac{M_{D} \times T_{D}}{M_{D} \times C T+T_{D}} \times\left\{\left(P V_{n}-2 P V_{n-1}+P V_{n-2}\right)-\frac{C T \times B_{n-1}}{T_{D}}\right\}$ |
|  | Reserve action $(P N=$ <br> $0)$ | $B_{n-1}+\frac{M_{D} \times T_{D}}{M_{D} \times C T+T_{D}} \times\left\{-\left(P V_{n}-2 P V_{n-1}+P V_{n-2}\right)-\frac{C T \times B_{n-1}}{T_{D}}\right\}$ |
| $B W(\Delta M V)$ | Direct action $(P N=1)$ | $K_{P} \times\left\{\frac{C T}{T_{1}} \times D V_{n}+\left(P V_{n}-P V_{n-1}\right)+B_{n}\right\}$ |
|  | Reserve action $(P N=$ <br> $0)$ | $K_{P}+\left\{\frac{C T}{T_{1}} \times D V_{n}-\left(P V_{n}-P V_{n-1}\right)+B_{n}\right\}$ |

$K_{p}: K \times$ Gain (P), $M_{D}$ : Derivative gain (MTD), $T_{I}$ : Integral constant (I), $T_{D}$ : Derivative constant (D)
Note that special processing is performed in the following cases.

| Condition | Processing |
| :--- | :--- |
| In either of the following cases: <br> 1. Derivative constant $(\mathrm{D})=0\left(T_{\mathrm{D}}=0\right)$ <br> 2. Operation mode (MODE $)=$ MAN, LCM, or CMV | $\mathrm{B}_{n}=0$ <br> $($ Note that the loop tag past value memory is set.) |
| In any of the following cases: <br> 1. Integral constant $(\mathrm{I})=0\left(\mathrm{~T}_{1}=0\right)$ <br> 2. MHA2 or MLA2 of alarm detection 2 (ALM2) is 1. <br> (MVP>MH) and $\left(\frac{C T}{T_{1}} \times D V_{n}>0\right)$ | $\frac{C T}{T_{1}} \times D V_{n}=0$ |
| 3. MHA2 or MLA2 of alarm detection 2 (ALM2) is 1. |  |
| (MVP<ML) and $\left(\frac{C T}{T_{1}} \times D V_{n}<0\right)$ |  |

## Deviation check (5)

A deviation is checked under the following conditions, and the result is output to the DVLA of the alarm detection (ALM) and the large deviation alarm (BB1),

| Condition | Result |
| :--- | :--- |
| DVL $<\|D V\|$ | DVLA $=$ BB1 $=1^{* 1}$ |
| $(D V L-D V L S)<\|D V\| \leq D V L$ | DVLA $=$ BB1 $=$ Last value status hold ${ }^{* 1}$ |
| $\|D V\| \leq(D V L-D V L S)$ | DVLA $=$ BB1 $=0$ |

*1 If the DMLI or ERRI of the disable alarm detection (INH) is set to 1 , the DVLA of the alarm detection (ALM) and the large deviation alarm (BB1) are set to 0 .

## -Loop stop processing (6)

The following processing is performed according to the SPA status of the alarm detection (ALM).

| SPA status | Processing details |
| :--- | :--- |
| 1 | The loop stops. When the loop stops, the following operations are performed and the S.IPD instruction ends. <br> • The output value (BW) is set to 0. |
|  | • The DVLA of alarm detection (ALM) is set to 0. <br> • The operation mode (MODE) is set to MAN. <br> • BB1 of BB is set to 0. |
| 0 | The loop runs and "control cycle determination processing (7)" is performed. |

## Control cycle determination (7)

If the specified control cycle is not reached, output value (BW) is set to 0 and the S.IPD instruction is terminated. If the specified control cycle is reached, "SV setting processing (1)" is performed.

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3400 H | An invalid operation (such as division by zero) is performed. |
| 3402 H | The value specified by (s1), (s2), or (d2) is a subnormal number or NaN (not a number). |
| 3403 H | An overflow has occurred. |
| 3405 H | The control cycle (CT) setting is less than 0. |
|  | The execution cycle ( $\Delta \mathrm{T}$ ) setting is less than 0. |
|  | The value divided the control cycle (CT) by the execution cycle $(\Delta \mathrm{T})$ exceeds 32767. |

## Blend PI control

## S．BPI

This instruction performs blend PI operation．The instruction performs the following processing steps：SV setting，tracking， gain $K_{p}$ operation，BPI operation，and deviation check．

| Ladder |  |  |  |  |  | ```ST ENO:=S_BPI(EN,s1,s2,s3,d1,d2);``` |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| －－－－］ |  |  |  |  |  |  |
|  | （s1） | （d1） | （s2） | （d2） | （s3） |  |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．BPI | $-\square$ |

## Setting data

## Description，range，data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| （s1） | Input data start device | Refer to＂Input data＂． | Single－precision real <br> number |
| （d1） | Block memory start device | Refer to＂Block memory＂． | Word |
| （s2） | Operation constant start device | Refer to＂Operation constant＂． | Word |
| （d2） | Loop tag memory start device | Refer to＂Loop tag memory＂． | Word |
| （s3） | When E2 is used：Set value start device <br> When E2 is not used：Dummy device | Refer to＂Set value＂． | Single－precision real <br> number |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，R，ZR，RD | U밈，J밈， U3EDI（H）Gロ | Z | LT，LST， LC | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s3） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Input data

| Operand：（s1） |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Position | Symbol | Name | Recommended range | Data type | Standard <br> value | Set by |
| +0 | E1 | Input value | -999999 to $999999[\%]$ | Single－precision <br> real number | - |  |

Block memory


Operation constant


Loop tag memory
The loop tag memory occupies 96 words from the specified start device.

| Operand: (d2) |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range |  |  |  |  |  |  |  |  | Data type | Standard value | Set by |
| +1 | MODE | Operation mode | 0 to FFFFH |  |  |  |  |  |  |  |  | 16-bit unsigned binary | 8H | User/ system |
| +3 | ALM | Alarm detection | SPA <br> 0: Loop RUN <br> 1: Loop STOP <br> DVLA, MHA, MLA <br> 0 : No alarm <br> 1: Alarm |  |  |  |  |  |  |  | 0 to FFFFH | 16-bit unsigned binary | 4000 H | User/ system |


| Operand: (d2) |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range |  |  |  |  | Data type | Standard | Set by |
| +4 | INH | Disable alarm detection | TRKF <br> 0 : Tracking not set <br> 1: Tracking set <br> ERRI, DVLI, MHI, MLI <br> 0 : Alarm detection enabled <br> 1: Alarm detection disabled |  |  |  |  | 16-bit unsigned binary | 4000H | User/ system |
| $\begin{aligned} & +14 \\ & +15 \end{aligned}$ | SV | Set value | RL to RH |  |  |  |  | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +16 \\ & +17 \end{aligned}$ | DV | Deviation | -110 to 110 [\%] |  |  |  |  | Single-precision real number | 0.0 | System |
| $\begin{aligned} & +22 \\ & +23 \end{aligned}$ | RH | Engineering value upper limit | -999999 to 999999 |  |  |  |  | Single-precision real number | 100.0 | User |
| $\begin{aligned} & +24 \\ & +25 \end{aligned}$ | RL | Engineering value lower limit | -999999 to 999999 |  |  |  |  | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +46 \\ & +47 \end{aligned}$ | CT | Control cycle | 0 to 999999 [s] <br> Provided that $\frac{C T}{\triangle T}<=32767$ |  |  |  |  | Single-precision real number | 1.0 | User |
| $\begin{aligned} & +50 \\ & +51 \end{aligned}$ | DVL | Deviation limit value | 0 to 100 [\%] |  |  |  |  | Single-precision real number | 100.0 | User |
| $\begin{aligned} & +52 \\ & +53 \end{aligned}$ | P | Gain | 0 to 999999 |  |  |  |  | Single-precision real number | 1.0 | User |
| $\begin{aligned} & +54 \\ & +55 \end{aligned}$ | I | Integral constant | 0 to 999999 [s] |  |  |  |  | Single-precision real number | 10.0 | User |
| $\begin{aligned} & +56 \\ & +57 \end{aligned}$ | SDV | DV cumulative total ( $\Sigma$ DV) | -999999 to 999999 [\%] |  |  |  |  | Single-precision real number | 0.0 | System |
| $\begin{aligned} & +58 \\ & +59 \end{aligned}$ | GW | Gap width | 0 to 100 [\%] |  |  |  |  | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +60 \\ & +61 \end{aligned}$ | GG | Gap gain | 0 to 999999 |  |  |  |  | Single-precision real number | 1.0 | User |

## Loop tag past value memory

The system uses this area as a work area.
To start the control from the initial status, clear data by using a sequence program.
The loop tag past value memory occupies 32 words after the loop tag memory.

| Operand: (d2) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| +96 | - | Control cycle counter initialization completion flag | - | - | - | System |
| +97 |  | Control cycle counter (The value is rounded off to the nearest whole number.) |  |  |  |  |
| $\begin{aligned} & +98 \\ & +99 \end{aligned}$ |  | - | $\frac{C T}{T_{1}} \times \Sigma D V_{1}$ |  |  |  |

## Set value

The set value (E2) is valid only when b0 of the set value pattern (SVPTN) is set to 0 (Used). To use the upper loop MV as the set value (E2), specify the device (offset +12 ) where the manipulated value (MV) of the upper loop is set.
If E2 is not used, specify a dummy device (SD820).

| Operand: (s3) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| $\begin{aligned} & +0 \\ & +1 \end{aligned}$ | E2 | Set value | -10 to 110 [\%] | Single-precision real number | 0.0 | User |

## Execution cycle ( $\Delta T$ )

Set the execution cycle ( $\Delta \mathrm{T}$ ) in SD816 and SD817 in single-precision real number.

## Processing details

This instruction performs BPI operation when the specified control cycle is reached.
At this time, the instruction also performs the following processing steps: SV setting, tracking, gain (Kp) operation, and deviation check.
The following is the processing block diagram of the S.BPI instruction. (The numbers (1) to (7) in the diagram indicate the order of the processing.)


## SV setting processing (1)

The following processing is performed according to the operation mode (MODE) setting.

| MODE setting | Processing details |
| :--- | :--- |
| CAS, CCB, CSV | •If the set value (E2) is specified, engineering value transformation processing (refer to the <br> following expression) is performed, and then "tracking processing (2)" is performed. |
|  | $\mathrm{SV}_{\mathrm{n}}=\frac{\mathrm{RH}-\mathrm{RL}}{100} \times \mathrm{E} 2+\mathrm{RL}$ <br> •If the set value (E2) is not specified, "tracking processing (2)" is performed without <br> performing engineering value transformation processing. |
| MAN, AUT, CMV, CMB, CAB, LCM, LCA, LCC | "Tracking processing (2)" is performed. |

## Tracking processing (2)

- The set value (SV) is inversely transformed from the engineering value and $\mathrm{SV}_{\mathrm{n}}$ ' is calculated (refer to the following expression).
$S V_{n}^{\prime}=\frac{100}{R H-R L} \times\left(S V_{n}-R L\right)$
- Tracking processing is performed when all of the following conditions are satisfied.
- The tracking bit (TRK) is set to 1 .
- The set value (E2) is used.
- The operation mode (MODE) is set to any of the following: MAN, AUT, CMV, CMB, CAB, LCM, LCA, or LCC. $\mathrm{E} 2=\mathrm{SV}_{\mathrm{n}}{ }^{\prime}$
- If the set value (E2) is the upper loop MV, the TRKF of the disable alarm detection (INH) of the upper loop is set to 1 .


## Gain (Kp) operation processing (3)

- The deviation (DV) is calculated under the following conditions.

| Condition | Operational expression |
| :--- | :--- |
| Direct action $(\mathrm{PN}=1)$ | $\mathrm{DV}=\mathrm{E} 1-\mathrm{SV}_{\mathrm{n}}{ }^{\prime}$ |
| Reserve action $(\mathrm{PN}=0)$ | $\mathrm{DV}=\mathrm{SV}_{\mathrm{n}}{ }^{\prime}-\mathrm{E} 1$ |

- The output gain $(\mathrm{K})$ is calculated under the following conditions.

| Condition | Operational expression |
| :--- | :--- |
| $\|D V\| \leq G W$ | $K=G G$ |
| $\|D V\|>G W$ | $K=1-\frac{(1-G G) \times G W}{\|D V\|}$ |

BPI operation (4)
The BPI operation is performed with the following operational expression.

| Item | Operational expression |
| :--- | :--- |
| $B W(\Delta M V)$ | $\mathrm{K}_{\mathrm{p}} \times \mathrm{BT} \times\left(\mathrm{DV}_{\mathrm{n}}+\frac{\mathrm{CT}}{\mathrm{T}_{1}} \times \Sigma \mathrm{DV}_{1}\right)$ |

$\mathrm{K}_{\mathrm{p}}: \mathrm{K} \times$ gain ( P ), BT : execution cycle, $\mathrm{T}_{\mathrm{I}}$ : integral constant (I), $\Sigma \mathrm{DV}_{\mathrm{I}}$ : $\mathrm{DV}_{\mathrm{n}}$ cumulative value, $\mathrm{DV}_{\mathrm{n}}$ : deviation Note that special processing is performed in the following cases.

| Condition | Processing |
| :--- | :--- |
| In either of the following cases: <br> 1. Integral constant $(I)=0\left(T_{1}=0\right)$ <br> 2. MHA or MLA of alarm detection $(A L M)$ is 1. | $\frac{C T}{T_{1}} \times \sum D V_{1}=$ Last value |
| Integral constant $(I) \neq 0\left(T_{1} \neq 0\right)$ | $\frac{C T}{T_{1}} \times \sum D V_{1}=\frac{C T}{T_{1}} \times\left(\sum D V_{1}+D V_{n}\right)$ |

Deviation check (5)
A deviation is checked under the following conditions, and the result is output to the DVLA of the alarm detection (ALM) and the large deviation alarm (BB1) in the device specified by (d2).

| Condition | Result |
| :--- | :--- |
| DVL $<\|D V\|$ | DVLA $=$ BB1 $=1^{* 1}$ |
| $(D V L-D V L S)<\|D V\| \leq$ DVL | DVLA $=$ BB1 $=$ Last value status hold ${ }^{* 1}$ |
| $\|D V\| \leq(D V L-D V L S)$ | DVLA $=$ BB1 $=0$ |

[^28]
## ■Loop stop processing (6)

The following processing is performed according to the SPA status of the alarm detection (ALM).

| SPA status | Processing details |
| :--- | :--- |
| 1 | The loop stops. When the loop stops, the following operations are performed and the S.BPI instruction ends. <br> • The output value (BW) is set to 0. <br> • The DVLA of alarm detection (ALM) is set to 0. |
|  | • The operation mode (MODE) is set to MAN. <br> • BB1 of BB is set to 0. |
| 0 | The loop runs and "control cycle determination processing (7)" is performed. |

## Control cycle determination (7)

If the specified control cycle is not reached, output value (BW) is set to 0 and the S.BPI instruction is terminated. If the specified control cycle is reached, "SV setting processing (1)" is performed.

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3400 H | An invalid operation (such as division by zero) is performed. |
| 3402 H | The value specified by (s1), (s2), or (d2) is a subnormal number or NaN (not a number). |
| 3403 H | An overflow has occurred. |
| 3405 H | The control cycle (CT) setting is less than 0. |
|  | The execution cycle ( $\Delta \mathrm{T}$ ) setting is less than 0. |
|  | The value divided the control cycle (CT) by the execution cycle $(\Delta \mathrm{T})$ exceeds 32767. |

## Ratio calculation

## S．R

This instruction performs the following steps for the input data：engineering value transformation，tracking，variation rate limiter，and ratio calculation．


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## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．R | $\square$ |

## Setting data

## Description，range，data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| （s1） | Input data start device | Refer to＂Input data＂． | Single－precision real <br> number |
| （d1） | Block memory start device | Refer to＂Block memory＂． | Single－precision real <br> number |
| （s2） | Operation constant start device | Refer to＂Operation constant＂． | Word |
| （d2） | Loop tag memory start device | Refer to＂Loop tag memory＂． | Word |
| （s3） | When E2 is used：Set value start device <br> When E2 is not used：Dummy device | Refer to＂Set value＂． | Single－precision real <br> number |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロロロ | T，ST，C，D，W，SD， SW，R，ZR，RD | U밈，J미， U3EDl（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s3） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Input data

| Operand：（s1） |  | Recommended range | Data type | Standard <br> value | Set by |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Position | Symbol | Name | -999999 to $999999[\%]$ | Single－precision <br> real number | - |
| +0 | E1 | Input value | User |  |  |

Block memory

| Operand: (d1) |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Position | Symbol | Name | Recommended range | Data type | Standard <br> value | Set by |
| +0 | BW | Output value | -999999 to $999999[\%]$ | Single-precision <br> real number | - |  |

Operation constant

| Operand: (s2) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| +0 | TRK | Tracking bit | 0 : Tracking not performed <br> 1: Tracking performed | 16-bit unsigned binary | 0 | User |
| +1 | SVPTN | Set value pattern | 0 to 3 <br> (2) (1) <br> (1) Use of set value <br> Specify whether to use the set value (E2) or not. <br> 0 : Used <br> 1: Not used <br> (2) Set value pattern <br> Specify whether to use the upper loop MV as the set value (E2) or not. <br> 0 : E2 is the upper loop MV. <br> 1: E2 is not the upper loop MV. | 16-bit unsigned binary | 3 | User |

## Loop tag memory

The loop tag memory occupies 96 words from the specified start device.

| Operand: (d2) |  |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range |  |  |  |  |  |  | Data type | Standard | Set by |
| +1 | MODE | Operation mode | 0 to FFFFH |  |  |  |  |  |  | 16-bit unsigned binary | 8H | User/ system |
| +3 | ALM | Alarm detection | 0 to FF <br> b15b14  <br>  $\begin{array}{l}\mathbb{a} \\ \infty \\ \infty\end{array}$ <br> SPA <br> 0: Loop <br> 1: Loop |  |  |  |  |  | b0 | 16-bit unsigned binary | 4000H | User/ system |
| $\begin{aligned} & +14 \\ & +15 \end{aligned}$ | SPR | Set value | -999999 to 999999 |  |  |  |  |  |  | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +16 \\ & +17 \end{aligned}$ | BIAS | Bias | -999999 to 999999 [\%] |  |  |  |  |  |  | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +46 \\ & +47 \end{aligned}$ | CT | Control cycle | 0 to 999999 [s] <br> Provided that $\frac{C T}{\triangle T}<=32767$ |  |  |  |  |  |  | Single-precision real number | 1.0 | User |
| $\begin{aligned} & +50 \\ & +51 \end{aligned}$ | DR | Variation rate limit value | 0 to 999999 |  |  |  |  |  |  | Single-precision real number | 100.0 | User |
| $\begin{aligned} & +52 \\ & +53 \end{aligned}$ | RMAX | Ratio upper limit value | -999999 to 999999 |  |  |  |  |  |  | Single-precision real number | 100.0 | User |
| $\begin{aligned} & +54 \\ & +55 \end{aligned}$ | RMIN | Ratio lower limit value | -999999 to 999999 |  |  |  |  |  |  | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +56 \\ & +57 \end{aligned}$ | $\mathrm{R}_{\mathrm{n}}$ | Ratio current value | -999999 to 999999 |  |  |  |  |  |  | Single-precision real number | 0.0 | System |

## ■Loop tag past value memory

The system uses this area as a work area.
To start the control from the initial status, clear data by using a sequence program.
The loop tag past value memory occupies 32 words after the loop tag memory.

| Operand: (d2) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| +96 | - | Control cycle counter initialization completion flag | - | - | - | System |
| +97 |  | Control cycle counter (The value is rounded off to the nearest whole number.) |  |  |  |  |
| $\begin{aligned} & +98 \\ & +99 \end{aligned}$ | $\mathrm{R}_{\mathrm{n}-1}$ | Last value |  |  |  |  |

## Set value

The set value (E2) is valid only when b0 of the set value pattern (SVPTN) is set to 0 (Used). To use the upper loop MV as the set value (E2), specify the device (offset +12 ) where the manipulated value (MV) of the upper loop is set. If E2 is not used, specify a dummy device (SD820).

| Operand: (s3) |  | Recommended range | Data type | Standard <br> value | Set by |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Position | Symbol | Name | Set value | -10 to $110[\%]$ | Single-precision <br> real number | 0.0 |
| +0 | E2 |  |  | User |  |  |

## Execution cycle ( $\Delta T$ )

Set the execution cycle $(\Delta T)$ in SD816 and SD817 in single-precision real number.

## Processing details

This instruction performs ratio calculation when the specified control cycle is reached.
At this time, the instruction also performs the following steps: engineering value transformation, tracking, and variation rate limiter.


SPR: Set value, $R_{n}$ : Current ratio value, DR: Variation rate limit value
The following is the processing block diagram of the S.R instruction. (The numbers (1) to (6) in the diagram indicate the order of the processing.)


## Tracking processing (1)

- Tracking processing is performed when all of the following conditions are satisfied.
- The tracking bit (TRK) is set to 1
- The set value (E2) is used.
- The operation mode (MODE) is set to any of the following: MAN, AUT, CMV, CMB, CAB, LCM, LCA, or LCC.
$\mathrm{E} 2=\frac{100}{\text { RMAX }- \text { RMIN }} \times($ SPR - RMIN $)$
- If the set value (E2) is the upper loop MV, the TRKF of the disable alarm detection (INH) of the upper loop is set to 1 .


## Variation rate limiter (2)

Variation rate limiter processing performs the following operations, and stores the result in the current ratio value $\left(R_{n}\right)$.

| Condition | Operational expression |
| :--- | :--- |
| $\left(S^{\prime} R-R_{n}\right) \geq D R$ | $R_{n}=R_{n-1}+D R$ |
| $\left(S P R-R_{n}\right) \leq-D R$ | $R_{n}=R_{n-1}-D R$ |
| $\left\|S P R-R_{n}\right\|<D R$ | $R_{n}=S P R$ |

## Ratio calculation (3)

The ratio calculation is performed with the following operational expression.
$B W=\frac{R_{n}-R M I N}{R M A X-R M I N} \times E 1+B I A S$

## ■Loop stop processing (4)

The following processing is performed according to the SPA status of the alarm detection (ALM).

| SPA status | Processing details |
| :--- | :--- |
| 1 | The loop stops. When the loop stops, the following operations are performed and the S.R instruction ends. <br> • The last output value (BW) is held. <br> • The operation mode (MODE) is set to MAN. |
| 0 | The loop runs and "control cycle determination processing (5)" is performed. |

## Control cycle determination (5)

If the specified control cycle is not reached, output value (BW) is set to 0 and the S.R instruction is terminated. If the specified control cycle is reached, "mode determination (6)" is performed.

■Mode determination (6)
The following processing is performed depending on the operation mode (MODE).

| Operation mode (MODE) | Processing details |
| :--- | :--- |
| CAS, CCB, CSV | • If the set value (E2) is specified, engineering value transformation processing (refer to the following <br> expression) is performed, and then "variation rate limiter (2)" is performed. <br> SPR $=\frac{R M A X-R M I N ~}{100} \times E 2+R M I N$ |
|  | - If the set value (E2) is not specified, "variation rate limiter (2)" is performed without performing <br> engineering value transformation processing. |
| MAN, AUT, CMV, CMB, CAB, LCM, LCA, LCC | "Tracking processing (1)" is performed. |

Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3400 H | An invalid operation (such as division by zero) is performed. |
| 3402 H | The value specified by (s1) or (d2) is a subnormal number or NaN (not a number). |
| 3403 H | An overflow has occurred. |
| 3405 H | The control cycle (CT) setting is less than 0. |
|  | The execution cycle ( $\Delta \mathrm{T}$ ) setting is less than 0. |
|  | The value divided the control cycle (CT) by the execution cycle $(\Delta \mathrm{T})$ exceeds 32767. |

## Lower／upper limit alarm

## S．PHPL

Checks whether the PV that has been output by the S．IN instruction exceeds the upper limit or underruns the lower limit．


FBD／LD

| ［－二－］ |  |
| :---: | :---: |
| EN | ENO |
| s1 | d1 |
| s2 | d2 |

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．PHPL | $\square$ |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| （s1） | Input data start device | Refer to＂Input data＂． | Single－precision real <br> number |
| （d1） | Block memory start device | Refer to＂Block memory＂． | Word |
| （s2） | Empty string specification <br> （Specify＂＂in ladder．Specify＂in ST or FBD／LD．） | - | String |
| （d2） | Loop tag memory start device | Refer to＂Loop tag memory＂． | Word |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，R，ZR，RD | U밈，J밈， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － |
| （d2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Input data

| Operand：（s1） | Recommended range |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Position | Symbol | Name | Input value | -999999 to $999999[\%]$ | Single－precision <br> real number | - |
| +0 | E1 |  |  | User |  |  |

Block memory

| Operand: (d1) |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Sym |  | Name | Recommended range |  |  |  | Data type | Standard value | Set by |
| $\begin{aligned} & +0 \\ & +1 \end{aligned}$ | BW |  | Output value | -999999 to 999999 [\%] |  |  |  | Single-precision real number | - | System |
| +2 | BB | BB1 <br> BB2 <br> BB3 <br> BB4 <br> BB5 | Alarm <br> Upper limit alarm <br> Lower limit alarm <br> Positive direction variation rate alarm <br> Negative direction variation rate alarm | 0 : No alarm <br> 1: Alarm |  |  |  | 16-bit unsigned binary | - | System |

## Loop tag memory

The loop tag memory occupies 96 words from the specified start device.

| Operand: (d2) |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range |  |  |  |  |  | Data type | Standard | Set by |
| +1 | MODE | Operation mode | 0 to FFFFH |  |  |  |  |  | 16-bit unsigned binary | 8H | User/ system |
| +3 | ALM | Alarm detection | 0 to FF <br> b15 b14 <br> SPA <br> 0: Loop <br> 1: Loop <br> HHA, <br> 0 : No a <br> 1: Alarm | SPA <br> 0: Loop RUN <br> 1: Loop STOP <br> HHA, LLA, PHA, PLA, DPPA, DPNA <br> 0 : No alarm <br> 1: Alarm |  |  |  | 0 to FFFFH | 16-bit unsigned binary | 4000H | User/ system |
| +4 | INH | Disable alarm detection | 0 to FFFFH |  |  |  |  |  | 16-bit unsigned binary | 4000H | User/ system |
| $\begin{aligned} & +10 \\ & +11 \end{aligned}$ | PV | Process value | RL to RH |  |  |  |  |  | Single-precision real number | 0.0 | System |
| $\begin{aligned} & +22 \\ & +23 \end{aligned}$ | RH | Engineering value upper limit | -999999 to 999999 |  |  |  |  |  | Single-precision real number | 100.0 | User |
| $\begin{aligned} & +24 \\ & +25 \end{aligned}$ | RL | Engineering value lower limit | -999999 to 999999 |  |  |  |  |  | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +26 \\ & +27 \end{aligned}$ | PH | Upper limit alarm value | RL to RH |  |  |  |  |  | Single-precision real number | 100.0 | User |
| $\begin{aligned} & +28 \\ & +29 \end{aligned}$ | PL | Lower limit alarm value | RL to RH |  |  |  |  |  | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +30 \\ & +31 \end{aligned}$ | HH | Upper upper limit alarm value | RL to RH |  |  |  |  |  | Single-precision real number | 100.0 | User |
| $\begin{aligned} & +32 \\ & +33 \end{aligned}$ | LL | Lower lower limit alarm value | RL to RH |  |  |  |  |  | Single-precision real number | 0.0 | User |


| Operand: (d2) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| $\begin{aligned} & +40 \\ & +41 \end{aligned}$ | HS | Upper/lower limit alarm hysteresis | 0 to 999999 [\%] | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +42 \\ & +43 \end{aligned}$ | CTIM | Variation rate alarm check time | 0 to 999999 [s] <br> Provided that $\frac{\text { CTIM }}{\triangle T}<=32767$ | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +44 \\ & +45 \end{aligned}$ | DPL | Variation rate alarm value | 0 to 100 [\%] | Single-precision real number | 100.0 | User |

## Loop tag past value memory

The system uses this area as a work area.
To start the control from the initial status, clear data by using a sequence program.
The loop tag past value memory occupies 32 words after the loop tag memory.

| Operand: (d2) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| +124 | - | Variation rate monitoring counter initialization completion flag | - | - | - | System |
| +125 |  | Variation rate monitoring counter (rounded off to the nearest whole number) |  |  |  |  |
| $\begin{aligned} & +126 \\ & +127 \end{aligned}$ | $E 1_{n-m}$ | - |  |  |  |  |

## Execution cycle ( $\Delta \mathrm{T}$ )

Set the execution cycle $(\Delta T)$ in SD816 and SD817 in single-precision real number.

## Processing details

This instruction checks whether the input value (E1) exceeds the upper limit or underruns the lower limit to output an alarm. The following is the processing block diagram of the S.PHPL instruction. (The numbers (1) to (5) in the diagram indicate the order of the processing.)


## Engineering value inverse transformation (1)

The instruction performs the following operations to match the ranges of $\mathrm{PH}, \mathrm{PL}, \mathrm{HH}$, and LL to the input values (E1).
$P H^{\prime}=\frac{100}{R H-R L} \times(P H-R L)$
$P L^{\prime}=\frac{100}{R H-R L} \times(P L-R L)$
$H^{\prime}=\frac{100}{R H-R L} \times(H H-R L)$
$L L^{\prime}=\frac{100}{R H-R L} \times(L L-R L)$

## ■Upper/lower limit check (2)

The upper and lower limits of the input value (E1) are checked under the following conditions.

| Check item | Condition | ALM | BB2 | BB3 |
| :---: | :---: | :---: | :---: | :---: |
| Upper limit check | E1 > PH' | PHA $=1{ }^{* 1}$ | $1^{* 1}$ | - |
|  | E1欰H' - HS | PHA $=0$ | 0 | - |
|  | Others | PHA: The last value is held. ${ }^{1}$ | Hold*1 | - |
| Lower limit check | $\mathrm{E} 1<\mathrm{PL}{ }^{\prime}$ | PLA $=1{ }^{*}$ | - | $1^{*}{ }^{2}$ |
|  | E1 $\geq$ PL' + HS | PLA $=0$ | - | 0 |
|  | Others | PLA: The last value is held. ${ }^{*}{ }^{2}$ | - | Hold ${ }^{*}$ |
| Upper upper limit check | $\mathrm{E} 1>\mathrm{HH}^{\prime}$ | $\mathrm{HHA}=1{ }^{*}$ | - | - |
|  |  | HHA $=0$ | - | - |
|  | Others | HHA: The last value is held. ${ }^{*}$ | - | - |
| Lower lower limit check | E1 < LL' | LLA $=1^{*} 4$ | - | - |
|  | E1 2 LL'+HS | LLA $=0$ | - | - |
|  | Others | LLA: The last value is held. ${ }^{*}{ }^{4}$ | - | - |

*1 If ERRI or PHI of disable alarm detection (INH) is set to 1, PHA of ALM and BB2 are set to 0 .
*2 If ERRI or PLI of disable alarm detection (INH) is set to 1 , the PLA of ALM and BB3 are set to 0 .
*3 If ERRI or HHI of disable alarm detection (INH) is set to 1 , HHA of ALM is set to 0 .
*4 If ERRI or LLI of disable alarm detection (INH) is set to 1 , LLA of ALM is set to 0 .

## Variation rate check (3)

- A variation rate check is performed for the duration specified by the variation rate alarm check time (CTIM). The number of executions of a variation rate check is determined by the following expression.
$m=\frac{C T I M}{\Delta T}$
m varies from 1 to m .
However, no processing is performed when $\mathrm{m}=0$ (integral part).
Ex.
When $m=4$, operations are performed as shown below.

(1) Oth time: $E 1_{n}-E 1_{n-4}$
(2) 1 st time: $E 1_{n}-E 1_{n-4}$
(3) 2nd time: E1n $-E 1_{n-4}$
(4) 3rd time: $E 1_{n}-E 1_{n-4}$
(5) 4th time: $\mathrm{E} 1_{\mathrm{n}+4}-\mathrm{E} 1_{\mathrm{n}}$
- A change in the input data and variation rate alarm value (DPL) are compared every execution cycle ( $\Delta T$ ).

| Check item | Condition | ALM | BB4 | BB5 |
| :--- | :--- | :--- | :--- | :--- |
| Variation rate check | $\mathrm{E} 1_{\mathrm{n}+\mathrm{m}-\mathrm{E} 1_{\mathrm{n}} \geq \mathrm{DPL}}$ | DPPA $=1^{* 1}$ | $1^{* 1}$ | - |
|  | Others | DPPA $=0$ | 0 | - |
|  | $\mathrm{E} 1_{\mathrm{n}+\mathrm{m}-\mathrm{E} 1_{\mathrm{n}} \leq- \text { DPL }}$ | DPNA $=1^{* 2}$ | - | $1^{* 2}$ |
|  | Others | DPNA $=0$ | - | 0 |

*1 If ERRI or DPPI of disable alarm detection (INH) is set to 1 , the DPPA of ALM and BB4 are set to 0 .
*2 If ERRI or DPNI of disable alarm detection (INH) is set to 1 , the DPNA of ALM and BB5 are set to 0 .

## Engineering value transformation (4)

The instruction performs engineering value transformation using the following expression.
$P V=\frac{R H-R L}{100} \times E 1+R L$

## ■Loop stop processing (5)

The following processing is performed according to the SPA status of the alarm detection (ALM).

| SPA status | Processing details |
| :--- | :--- |
| 1 | The loop stops. When the loop stops, the following operations are performed and the S.PHPL instruction ends. <br> • The instruction performs engineering value inverse transformation using the following expression. <br> BW $=\frac{100}{R H-R L} \times(P V-R L)$ |
|  | • The alarm bits (BB1, BB2, BB3, BB4, and BB5) are set to 0. <br> • The DMLA, MHA, and DLA of alarm detection (ALM) are set to 0. |
| 0 | The loop runs and "engineering value inverse transformation (1)" is performed. |

Operation error

| Error code (SD0) | Description |
| :---: | :---: |
| 3400 H | An invalid operation (such as division by zero) is performed. |
| 3402H | The value specified by ( s 1 ) or ( d 2 ) is a subnormal number or NaN (not a number). |
| 3403H | An overflow has occurred. |
| 3405 H | Variation rate alarm value (DPL) <- Variation rate alarm value (DPL) |
|  | The execution cycle ( $\Delta \mathrm{T}$ ) setting is less than 0 . |
|  | Variation rate alarm check time (CTIM) < 0 |
|  | (Variation rate alarm check time (CTIM) $\div$ execution cycle $(\Delta T))>32767$ |

## Lead－lag compensation

## S．LLAG

This instruction performs lead－lag compensation for the input data and outputs the operation result．


FBD／LD

| ᄃ：二｣ |  |
| :---: | :---: |
| En | eno |
| s1 | d1 |
| s2 | d2 |

Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．LLAG | $-\square$ |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| （s1） | Input data start device | Refer to＂Input data＂． | Word |
| （d1） | Block memory start device | Refer to＂Block memory＂． | Single－precision real <br> number |
| （s2） | Operation constant start device | Refer to＂Operation constant＂． | Word |
| （d2） | Local work memory start device | Refer to＂Local work memory＂． | Single－precision real <br> number |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，R，ZR，RD | UロIGロ，JロIロ， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## ©lnput data



Block memory

| Operand: (d1) |  | Recommended range | Data type | Standard <br> value | Set by |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Position | Symbol | Name | Output value | -999999 to $999999[\%]$ | Single-precision <br> real number | - |
| +0 | BW |  |  |  |  |  |
| +1 |  |  |  |  |  |  |

Operation constant

| Operand: (s2) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| $\begin{aligned} & +0 \\ & +1 \end{aligned}$ | $\mathrm{T}_{1}$ | Lag time | 0 to 999999 [s] | Single-precision real number | 1.0 | User |
| $\begin{aligned} & \hline+2 \\ & +3 \end{aligned}$ | $\mathrm{T}_{2}$ | Lead time | 0 to 999999 [s] | Single-precision real number | 1.0 | User |

## ■ Local work memory

The system uses this area as a work area.
To start the control from the initial status, clear data by using a sequence program.

| Operand: (d2) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| $\begin{aligned} & +0 \\ & +1 \end{aligned}$ | $E 1_{n-1}$ | Last input value | - | Single-precision real number | - | System |

## Execution cycle ( $\Delta \mathrm{T}$ )

Set the execution cycle ( $\Delta \mathrm{T}$ ) in SD816 and SD817 in single-precision real number.

## Processing details

This instruction sets the lag time $\left(T_{1}\right)$ of (s2) and the lead time $\left(T_{2}\right)$ and performs lead-lag compensation according to the actuating signal (e1).


The S.LLAG instruction performs the following operations.

| Condition | BW (output value) |
| :--- | :--- |
| $\mathrm{e} 1=0$ | $\mathrm{BW}=\frac{1}{T_{1}+\triangle T} \times\left\{\mathrm{T}_{2} \times\left(\mathrm{E} 1-\mathrm{E} 1_{\mathrm{n}-1}\right)+\mathrm{T}_{1} \times\right.$ Last BW value $\left.+\triangle \mathrm{T} \times \mathrm{E} 1\right\}$ |
|  | However, $\mathrm{BW}=0$ when $\mathrm{T}+\Delta \mathrm{T}=0$. |
| $\mathrm{e} 1=1$ | $\mathrm{BW}=\mathrm{E} 1$ (The input value is output as is.) |

Operation error

| Error code (SD0) | Description |
| :--- | :--- |
| 3400 H | An invalid operation (such as division by zero) is performed. |
| 3402 H | The value specified by $(\mathrm{s} 1),(\mathrm{d} 1),(\mathrm{s} 2)$, or $(\mathrm{d} 2)$ is a subnormal number or NaN (not a number). |
| 3403 H | An overflow has occurred. |
| 3405 H | Lag time $\left(\mathrm{T}_{1}\right)<0$ or lead time $\left(\mathrm{T}_{2}\right)<0$ |
|  | The execution cycle $(\Delta \mathrm{T})$ setting is less than 0. |

## Integral control

## S．I

This instruction performs lead－lag compensation for the input data and outputs the operation result．


FBD／LD

| ■－－－$\square$ |  |
| :---: | :---: |
| EN | ENO |
| s1 | d1 |
| s2 |  |
| s3 |  |

Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．I | $\square$ |

Setting data
Description，range，data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| （s1） | Input data start device | Refer to＂Input data＂． | Word |
| （d1） | Block memory start device | Refer to＂Block memory＂． | Single－precision real <br> number |
| （s2） | Operation constant start device | Refer to＂Operation constant＂． | Word |
| （s3） | Empty string specification <br> （Specify＂＂in ladder．Specify＂in ST or FBD／LD．） | - | String |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s3） | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － |

## Input data



Block memory

| Operand: (d1) |  | Recommended range | Data type | Standard <br> value | Set by |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Symbol | Name | Output value | -999999 to 999999 | Single-precision <br> real number | - |
| $+0+1$ | BW |  |  | System |  |  |

## Operation constant

| Operand: (s2) |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Symbol | Name | Recommended range | Data type | Standard <br> value | Set by |
| +0 <br> +1 | T | Integral time | 0 to $999999[\mathrm{~s}]$ | Single-precision <br> real number | 1.0 |  |
| $+2+3$ | Ys | Output initial <br> value | -999999 to 999999 | Single-precision <br> real number | 0.0 | User |

Execution cycle ( $\Delta \mathrm{T}$ )
Set the execution cycle ( $\Delta \mathrm{T}$ ) in SD816 and SD817 in single-precision real number.

## Processing details

This instruction performs integral operation according to the operation control signal (e1).


The S.I instruction performs the following operations.

| e1 | $\mathbf{T}$ | $\mathbf{B W}$ |
| :--- | :--- | :--- |
| 0 | $\neq 0$ | $B W=Y_{n}=\frac{\Delta T}{T} \times E 1+Y_{n-1}$ |
| 0 | 0 | $B W=Y n-1$ |
| 1 | - | $B W=Y s$ |

E1: Input value of this time, $\Delta \mathrm{T}$ : Execution cycle, Yn: Output value of this time, Yn-1: Last output value

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3400 H | An invalid operation (such as division by zero) is performed. |
| 3402 H | The value specified by (s1), (d1), or (s2) is a subnormal number or NaN (not a number). |
| 3403 H | An overflow has occurred. |

## Derivative control

## S．D

This instruction performs differentiation operation for the input data，and outputs the operation result．


FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．D | $\square \square$ |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| （s1） | Input data start device | Refer to＂Input data＂． | Word |
| （d1） | Block memory start device | Refer to＂Block memory＂． | Single－precision real <br> number |
| （s2） | Operation constant start device | Refer to＂Operation constant＂． | Word |
| （d2） | Local work memory start device | Refer to＂Local work memory＂． | Single－precision real <br> number |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）GD | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Mlnput data



Block memory

| Operand: (d1) |  | Recommended range | Data type | Standard <br> value | Set by |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Symbol | Name | Output value | -999999 to 999999 | Single-precision <br> real number | - |
| $+0+1$ | BW | System |  |  |  |  |

Operation constant

| Operand: (s2) |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Symbol | Name | Recommended range | Data type | Standard <br> value | Set by |
| +0 <br> +1 | T | Derivative time | 0 to $999999[\mathrm{~s}]$ | Single-precision <br> real number | 1.0 |  |
| $+2+3$ | Ys | Output initial <br> relue | -999999 to 999999 | Single-precision <br> real number | 0.0 | User |

## ■Local work memory

The system uses this area as a work area.
To start the control from the initial status, clear data by using a sequence program.

| Operand: (d2) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| $\begin{aligned} & +0 \\ & +1 \end{aligned}$ | $E 1_{n-1}$ | Last input value | - | Single-precision real number | - | System |

## Execution cycle ( $\Delta \mathrm{T}$ )

Set the execution cycle ( $\Delta \mathrm{T}$ ) in SD816 and SD817 in single-precision real number.

## Processing details

This instruction performs differentiation operation according to the operation control signal (e1).


The S.D instruction performs the following operations.

| e1 | BW |
| :--- | :--- |
| 0 | $B W=\frac{T}{T+\triangle T} \times\left(Y_{n-1}-E 1_{n-1}+E 1\right)$ |
|  | However, $B W=0$ when $T+\Delta T=0$. |
| 1 | $B W=Y s$ |

E 1 : Input value of this time, $\Delta \mathrm{T}$ : Execution cycle, $\mathrm{E} 1_{\mathrm{n}-1}$ : Last input value, $\mathrm{Y}_{\mathrm{n}-1}$ : Last output value

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3400 H | An invalid operation (such as division by zero) is performed. |
| 3402 H | The value specified by (s1), (d1), (s2), or (d2) is a subnormal number or NaN (not a number). |
| 3403 H | An overflow has occurred. |

## Dead time

## S．DED

This instruction outputs the input data with the delay by the specified dead time．


FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．DED | $\square$ |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| （s1） | Input data start device | Refer to＂Input data＂． | Word |
| （d1） | Block memory start device | Refer to＂Block memory＂． | Word |
| （s2） | Operation constant start device | Refer to＂Operation constant＂． | Word |
| （d2） | Local work memory start device | Refer to＂Local work memory＂． | Word |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，R，ZR，RD | UपIGロ，J밈， U3EDI（H）Gロ | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

Input data


Block memory


Operation constant


## Local work memory

The system uses this area as a work area.
To start the control from the initial status, clear data by using a sequence program.

| Operand: (d2) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| +0 | - | Last value input (e1') | - | - | - | System |
| +1 |  | Cycle counter |  |  |  |  |
| +2 |  | Number of data blocks stored in the dead time table |  |  |  |  |
| +3+4 |  | Dead time table 1 |  |  |  |  |
| +5+6 |  | Dead time table 2 |  |  |  |  |
| ! |  | ! |  |  |  |  |
| $\begin{aligned} & +2 \mathrm{SN}+1+2 \\ & \mathrm{SN}+2 \end{aligned}$ |  | Dead time table SN |  |  |  |  |

*1 The cycle counter value is rounded off to the nearest whole number.

## Execution cycle ( $\Delta \mathrm{T}$ )

Set the execution cycle ( $\Delta \mathrm{T}$ ) in SD816 and SD817 in single-precision real number.

## Processing details

The input value (E1) is output with a delay by the dead time according to the content of the operation control signal (e1).


SN: Sampling count, ST: Data collection interval, E1: Input value, YS: Initial output value The S.DED instruction performs the following operations.

| e1 | OCHG | Dead time | BW |  |
| :---: | :---: | :---: | :---: | :---: |
| 1 | 0/1 | - | E1 |  |
| $1 \rightarrow 0$ | 0 | ST×SN | Up to SN | E1 when e1 changed from 1 to 0 |
|  |  |  | After SN | Least recent data *1 |
|  | 1 |  | Up to SN | YS |
|  |  |  | After SN | Least recent data*1 |
| $0 \rightarrow 0$ | 0/1 | ST $\times$ SN | Least recent data ${ }^{* 1}$ |  |

*1 Least recent data is an input value (E1) after SN.

- When the dead time table does not have sufficient data, the data sufficiency bit (BB1) is set to 1 .
- When the sampling count (SN) is 0 , the data sufficiency bit (BB1) is 0 and the output value (BW) equals the input value (E1).


## Operation error

| Error code (SDO) | Error content |
| :--- | :--- |
| 3400 H | An invalid operation (such as division by zero) is performed. |
| 3402 H | The value specified by (s1) or (s2) is a subnormal number or NaN (not a number). |
| 3403 H | An overflow has occurred. |
| 3405 H | The execution cycle ( $\Delta \mathrm{T}$ ) setting is less than 0. |
|  | The sampling count (SN) is less than 0 or greater than 48. |
|  | The data sampling interval (ST) is less than 0. |
|  | (Data sampling interval (ST) $\div$ execution cycle $(\Delta \mathrm{T})$ ) is greater than 32767. |

## High selector

## S．HS

This instruction outputs only the maximum value among the input data．

| Ladder |  |  |  |  | STEN |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  |  |
|  |  |  |  |  |  |

FBD／LD

| ■－二－］ |  |
| :---: | :---: |
| EN | ENO |
| s1 | d1 |
| s2 |  |
| s3 |  |

Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．HS | $-\square$ |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| （s1） | Input data start device | Refer to＂Input data＂． | Word |
| （d1） | Block memory start device | Refer to＂Block memory＂． | Word |
| （s2） | Empty string specification <br> （Specify＂＂in ladder．Specify＂in ST or FBD／LD．） | - | String |
| （s3） | Empty string specification <br> （Specify＂＂in ladder．Specify＂in ST or FBD／LD．） | - | String |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，R，ZR，RD | UपIGㅁ，J밈， U3EDl（H）GD | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － |
| （s3） | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － |

## Input data

| Operand: (s1) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| +0 | n | Number of inputs | 1 to 16 | 16-bit unsigned binary | - | User |
|  | E1 | Input value 1 | -999999 to 999999 | Single-precision real number | - | User |
| $\begin{aligned} & +3 \\ & +4 \end{aligned}$ | E2 | Input value 2 |  |  |  |  |
| ! | $\vdots$ | : |  |  |  |  |
| $\begin{aligned} & +2 n-1 \\ & +2 n \end{aligned}$ | En | Input value n |  |  |  |  |

Block memory


## Processing details

This instruction outputs the maximum value out of input values E1 to En.

## -High selector processing

The maximum value out of input values E1 to En is stored in the output value (BW)
In addition, the BB output selection (BB1 to BB16) corresponding to the maximum value is set to 1 .
The correspondence between input values 1 (E1) to 16 (E16) and BB output selections (BB1 to BB16) is shown below.

| Input value |  | E15 | E14 | $\sim$ | E2 | E1 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Bit to be set to 1 at the maximum value |  | BB15 | BB14 | $\sim$ | BB2 | BB1 |
| Condition | Processing |  |  |  |  |  |
| Two or more maximum values exist. | The bits corresponding to the maximum values are all set to 1 . |  |  |  |  |  |
| Only one input | Only input value 1 (E1) is used as the input value. |  |  | - The input value 1 (E1) is stored in the output value (BW). <br> - BB output selection BB 1 is set to 1 . <br> - BB output selections BB2 to BB16 are set to 0 . |  |  |
|  | Only one of input values 2 (E2) to 16 (E16) is used as the input value. |  |  | A value out of input values 2 (E2) to 16 (E16) and the value of input value 1 (E1) are used for data processing. |  |  |

Operation error

| Error code | Error content |
| :--- | :--- |
| 3402 H | (s1) is a subnormal number or NaN (not a number). |
| 3405 H | The number of inputs $(\mathrm{n})$ is less than 1 or greater than 16. |

## Low selector

## S．LS

This instruction outputs only the minimum value among the input data．


FBD／LD

| ■－－－ |  |
| :---: | :---: |
| EN | ENO |
| s1 | d1 |
| s2 |  |
| s3 |  |

Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．LS | $-\square$ |

Description，range，data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| （s1） | Input data start device | Refer to＂Input data＂． | Word |
| （d1） | Block memory start device | Refer to＂Block memory＂． | Word |
| （s2） | Empty string specification <br> （Specify＂＂in ladder．Specify＂in ST or FBD／LD．） | - | String |
| （s3） | Empty string specification <br> （Specify＂＂in ladder．Specify＂in ST or FBD／LD．） | - | String |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，R，ZR，RD | UपIGロ，J밈， U3E미（H）Gㅁ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － |
| （s3） | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － |

Input data

| Operand：（s1） |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| $\begin{aligned} & \hline+0 \\ & +1 \end{aligned}$ | n | Number of inputs | 1 to 16 | 16－bit unsigned binary | － | User |
| $\begin{aligned} & +1 \\ & +2 \end{aligned}$ | E1 | Input value 1 | －999999 to 999999 | Single－precision real number | － | User |
| $\begin{aligned} & +3 \\ & +4 \end{aligned}$ | E2 | Input value 2 |  |  |  |  |
| ！ | ！ | $\vdots$ |  |  |  |  |
| $\begin{aligned} & +2 n-1 \\ & +2 n \end{aligned}$ | En | Input value n |  |  |  |  |

Block memory

| Operand: (d1) |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Symbol |  | Name | Rec | comm | ende | d ran | nge |  |  |  |  |  | Data type | Standard value | Set by |
| $\begin{aligned} & +0 \\ & +1 \end{aligned}$ | BW |  | Output value | Minimum value in E1 to En |  |  |  |  |  |  |  |  |  | Single-precision real number | - | System |
| +2 | BB | $\begin{array}{\|l} \hline \text { BB1 } \\ \text { to } \\ \text { BB1 } \\ 6 \end{array}$ | Output selection | $\begin{aligned} & \begin{array}{l} \text { b15b } \\ \begin{array}{l} \frac{\infty}{\infty} \\ \infty \\ \hline \end{array} \\ \text { 0: C } \\ 1: C \end{array} \end{aligned}$ | Corres Corres | pondi pondi | ing in ing in |  <br> nput $v$ input v |  |  | the mini | 2 | 1 | 16-bit unsigned binary | - | System |

## Processing details

This instruction outputs the minimum value out of input values E1 to En.

## Low selector processing

The minimum value out of input values E 1 to En is stored in the output value (BW).
In addition, the BB output selection (BB1 to BB16) corresponding to the minimum value is set to 1 .
The correspondence between input values 1 (E1) to 16 (E16) and BB output selections (BB1 to BB16) is shown below.

| Input value |  | E16 | E15 | E14 | $\sim$ | E2 | E1 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Bit to be set to 1 at the minimum value |  | BB16 | BB15 | BB14 | $\sim$ | BB2 | BB1 |
| Condition | Processing |  |  |  |  |  |  |
| Two or more minimum values exist. | The bits corresponding to the minimum values are all set to 1 . |  |  |  |  |  |  |
| Only one input | Only input value 1 (E1) is used as the input value. |  |  |  | - The input value 1 ( E 1 ) is stored in the output value (BW). <br> - BB output selection BB1 is set to 1 . <br> - BB output selections BB2 to BB16 are set to 0 . |  |  |
|  | Only one of input values 2 (E2) to 16 (E16) is used as the input value. |  |  |  | A value out of input values 2 (E2) to 16 (E16) and the value of input value 1 (E1) are used for data processing. |  |  |

## Operation error

| Error code | Error content |
| :--- | :--- |
| 3402 H | $(\mathrm{s} 1)$ is a subnormal number or NaN (not a number). |
| 3405 H | The number of inputs $(\mathrm{n})$ is less than 1 or greater than 16. |

## Middle value selector

## S.MID

This instruction outputs the intermediate values between the maximum and minimum values among the input data.


FBD/LD

| ■--- |  |
| :---: | :---: |
| EN | ENO |
| s1 | d1 |
| s2 |  |
| s3 |  |

Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S.MID | $\boxed{\square}$ |

Description, range, data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| (s1) | Input data start device | Refer to "Input data". | Word |
| (d1) | Block memory start device | Refer to "Block memory". | Word |
| (s2) | Empty string specification <br> (Specify "" in ladder. Specify " in ST or FBD/LD.) | - | String |
| (s3) | Empty string specification <br> (Specify "" in ladder. Specify " in ST or FBD/LD.) | - | String |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | J미민 | T, ST, C, D, W, SD, SW, R, ZR, RD | UपIGロ, JロID, U3E미(H)Gㅁ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (s1) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (d1) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (s2) | - | - | - | - | - | - | - | - | - | - | $\bigcirc$ | - |
| (s3) | - | - | - | - | - | - | - | - | - | - | $\bigcirc$ | - |

Input data

| Operand: (s1) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| +0 | n | Number of inputs | 1 to 16 | 16-bit unsigned binary | - | User |
| $\begin{aligned} & +1 \\ & +2 \end{aligned}$ | E1 | Input value 1 | -999999 to 999999 | Single-precision real number | - | User |
| $\begin{aligned} & +3 \\ & +4 \end{aligned}$ | E2 | Input value 2 |  |  |  |  |
| ! | : | ! |  |  |  |  |
| $\begin{aligned} & +2 n-1 \\ & +2 n \end{aligned}$ | En | Input value n |  |  |  |  |

Block memory


## Processing details

This instruction outputs an intermediate value between the maximum and minimum values among input values E1 to En.

## IMiddle value selector processing

An intermediate value among input values E1 to En is stored in the output value (BW).
In addition, the BB output selection (BB1 to BB16) corresponding to the intermediate value is set to 1 .
The correspondence between input values 1 (E1) to 16 ( E 16 ) and BB output selections (BB1 to BB16) is shown below.

| Input value | E16 | E15 | E14 | $\sim$ | E1 |
| :--- | :--- | :--- | :--- | :--- | :--- |
| Bit to be set to 1 at an intermediate value | BB16 | BB15 | BB14 | $\sim$ | BB2 |

- When the number of inputs is an even number, the smaller value among the intermediate values is stored.
- When two or more intermediate values exist, the bits corresponding to the intermediate values are all set to 1 .


## ■Remarks

Intermediate values are selected as follows:

- Input values 1 (E1) to $n(E n)$ are sorted in ascending order. (When there are same input values, they are sorted in ascending order of their input numbers.)
- The intermediate value in the sorted values is selected.


## Ex.

When the input data are $2,5,1,4$, and 3 , operations are performed as shown below.
In this example, 3 is the intermediate value and accordingly the output select (BB5) is set to 1 .

| Input data |
| :--- |
| E1 E2 E 3 E 4 <br> E 5    <br> 2 5 1 4 3 |

## Operation error

| Error code | Error content |
| :--- | :--- |
| 3402 H | (s1) is a subnormal number or NaN (not a number). |
| 3405 H | The number of inputs $(\mathrm{n})$ is less than 1 or greater than 16. |

## Average value calculation

## S．AVE

Calculates and outputs the mean value of the input data．
■Execution condition


FBD／LD


## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．AVE | $-\square$ |

－Description，range，data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| （s1） | Input data start device | Refer to＂Input data＂． | Word |
| （d1） | Block memory start device | Refer to＂Block memory＂． | Single－precision real <br> number |
| （s2） | Empty string specification <br> （Specify＂＂in ladder．Specify＂in ST or FBD／LD．） | String |  |
| （s3） | Empty string specification <br> （Specify＂＂in ladder．Specify＂in ST or FBD／LD．） | - | String |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － |
| （s3） | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － |

## ■Input data

| Operand: (s1) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| +0 | n | Number of inputs | 1 to 16 | 16-bit unsigned binary | - | User |
| $\begin{aligned} & +1 \\ & +2 \end{aligned}$ | E1 | Input value 1 | -999999 to 999999 | Single-precision real number | - | User |
| $\begin{aligned} & +3 \\ & +4 \end{aligned}$ | E2 | Input value 2 |  |  |  |  |
| ! | ! | ! |  |  |  |  |
| $\begin{aligned} & +2 n-1 \\ & +2 n \end{aligned}$ | En | Input value n |  |  |  |  |

Block memory

| Operand: (d1) | Recommended range | Data type | Standard <br> value | Set by |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Symbol | Name | Output value | Mean value of E1 to En | Single-precision <br> real number | - |
| +0 | BW |  |  | System |  |  |
| 1 |  |  |  |  |  |  |

## Processing details

This instruction calculates and outputs the mean value of input values E1 to En.

## ■Average value calculation

The instruction calculates the mean value of input values E1 to En.
The numerical value specified by the number of inputs $(n)$ is used as the denominator.
$B W=\frac{E 1+E 2+E 3 \cdots E n}{N}$

## Operation error

| Error code | Error content |
| :--- | :--- |
| 3400 H | An invalid operation (such as division by zero) is performed. |
| 3402 H | (s1) is a subnormal number or NaN (not a number). |
| 3403 H | An overflow has occurred. |
| 3405 H | The number of inputs $(\mathrm{n})$ is less than 1 or greater than 16. |

## Upper／lower limiter

## S．LIMT

This instruction applies a limiter with hysteresis to the output value．


FBD／LD

| ■－－－$]$ |  |
| :---: | :---: |
| EN | ENO |
| s1 | d1 |
| s2 |  |
| s3 |  |

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．LIMT | $\square$ |

Description，range，data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| （s1） | Input data start device | Refer to＂Input data＂． | Single－precision real <br> number |
| （d1） | Block memory start device | Refer to＂Block memory＂． | Word |
| （s2） | Operation constant start device | Refer to＂Operation constant＂． | Word |
| （s3） | Empty string specification <br> （Specify＂＂in ladder．Specify＂in ST or FBD／LD．） | - | String |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s3） | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － |

## Input data

| Operand：（s1） | Recommended range | Data type | Standard <br> value | Set by |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Symbol | Name | -999999 to $999999[\%]$ | Single－precision <br> real number | - |
| +0 | E1 | Input value |  | User |  |

Block memory


Operation constant

| Operand: (s2) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| $\begin{aligned} & +0 \\ & +1 \end{aligned}$ | HILMT | Upper limit value ${ }^{* 1}$ | -999999 to 999999 [\%] | Single-precision real number | 100.0 | User |
| $\begin{aligned} & +2 \\ & +3 \end{aligned}$ | LOLMT | Lower limit value ${ }^{* 1}$ | -999999 to 999999 [\%] | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +4 \\ & +5 \end{aligned}$ | HS1 | Upper limit hysteresis | 0 to 999999 [\%] | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +6 \\ & +7 \end{aligned}$ | HS2 | Lower limit hysteresis | 0 to 999999 [\%] | Single-precision real number | 0.0 | User |

*1 Set the upper limit (HILMT) and lower limit values (LOLMT) in such a way that HILMT equals to or exceeds LOLMT.

## Processing details

This instruction applies upper and lower limiters with hysteresis to the output value.


The S.LIMT instruction performs the following operations.

| Condition | BW | BB1 | BB2 |
| :--- | :--- | :--- | :--- |
| E1 $\geq$ HILMT | HILMT | 1 | 0 |
| (LOLMT+HS2)<E1<(HILMT-HS1) | E1 | 0 | 0 |
| E1 $\leq$ LOLMT | LOLMT | 0 | 1 |
| Other than the above (hysteresis part) | E1 | Last value | Last value |

## Operation error

| Error code | Error content |
| :--- | :--- |
| 3402 H | The value specified by (s1) or (s2) is a subnormal number or NaN (not a number). |
| 3403 H | An overflow has occurred. |
| 3405 H | Upper hysteresis (HS1) is greater than 0 or the lower hysteresis (HS2) is smaller than 0. |
| 3405 H | Lower limit value (LOLMT) is greater than upper limit value (HILMT). |

## Variation rate limiter 1

## S.VLMT1

This instruction limits the varying speed and outputs it when the variation rate of input (E1) exceeds the limit.

| Ladder |  |  |  |  | ST |
| :---: | :---: | :---: | :---: | :---: | :---: |
| ---- |  |  |  |  |  |
|  | (s1) | (d1) | (s2) | (s3) |  |

FBD/LD

| ■--- |  |
| :---: | :---: |
| EN | ENO |
| s1 | d1 |
| s2 |  |
| s3 |  |

Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S.VLMT1 | $-\square$ |

## Setting data

DDescription, range, data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| (s1) | Input data start device | Refer to "Input data". | Single-precision real <br> number |
| (d1) | Block memory start device | Refer to "Block memory". | Word |
| (s2) | Operation constant start device | Refer to "Operation constant". | Word |
| (s3) | Empty string specification <br> (Specify "" in ladder. Specify " in ST or FBD/LD.) | String |  |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | J밈 | T, ST, C, D, W, SD, SW, R, ZR, RD | UपIGロ, JपIロ, U3EDI(H)GD | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (s1) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (d1) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (s2) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (s3) | - | - | - | - | - | - | - | - | - | - | $\bigcirc$ | - |

## Input data

| Operand: (s1) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| $\begin{aligned} & +0 \\ & +1 \end{aligned}$ | E1 | Input value | -999999 to 999999 [\%] | Single-precision real number | - | User |

Block memory


Operation constant

| Operand: (s2) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| $\begin{aligned} & +0 \\ & +1 \end{aligned}$ | V1 | Positive direction limit value | 0 to 999999 [\%/s] | Single-precision real number | 100.0 | User |
| $\begin{aligned} & +2 \\ & +3 \end{aligned}$ | V2 | Negative direction limit value | 0 to 999999 [\%/s] | Single-precision real number | 100.0 | User |
| $\begin{aligned} & +4 \\ & +5 \end{aligned}$ | HS1 | Positive direction hysteresis | 0 to 999999 [\%] | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +6 \\ & +7 \end{aligned}$ | HS2 | Negative direction hysteresis | 0 to 999999 [\%] | Single-precision real number | 0.0 | User |

Execution cycle ( $\Delta T$ )
Set the execution cycle ( $\Delta \mathrm{T}$ ) in SD816 and SD817 in single-precision real number.

## Processing details

This instruction limits the varying speed and outputs it when the variation rate of input (E1) exceeds the limit.



The S.VLIMT1 instruction performs the following operations.

| Condition | Input (E1-BW) | BW | BB1 | BB2 |
| :---: | :---: | :---: | :---: | :---: |
| Positive direction E1 2 BW | $(\mathrm{E} 1-\mathrm{BW}) \geq(\mathrm{V} 1 \times \Delta \mathrm{T})$ | $B W=B W+V 1 \times \Delta T$ | 1 | 0 |
|  | $(\mathrm{E} 1-\mathrm{BW})<(\mathrm{V} 1 \times \Delta \mathrm{T}-\mathrm{HS} 1)$ | BW=E1 | 0 | 0 |
|  | Others | BW=E1 | Last value | Last value |
| Negative direction E1<BW | (BW-E1) $\geq$ (V2× 2 T) | $B W=B W-V 2 \times \Delta T$ | 0 | 1 |
|  | (BW-E1)<(V2× $\times$ T-HS2) | BW=E1 | 0 | 0 |
|  | Others | $B W=E 1$ | Last value | Last value |

## Operation error

| Error code | Error content |
| :--- | :--- |
| 3402 H | The value specified by (s1) or (s2) is a subnormal number or NaN (not a number). |
| 3403 H | An overflow has occurred. |
| 3405 H | Positive direction hysteresis (HS1) is less than 0 or the negative direction hysteresis (HS2) is less than 0. |

## Variation rate limiter 2

## S.VLMT2

This instruction holds the last value and outputs it when the variation rate of input (E1) exceeds the limit.


FBD/LD

| ■--- |  |
| :---: | :---: |
| EN | ENO |
| s1 | d1 |
| s2 |  |
| s3 |  |

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S.VLMT2 | $\square$ |

## Setting data

DDescription, range, data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| (s1) | Input data start device | Refer to "Input data". | Single-precision real <br> number |
| (d1) | Block memory start device | Refer to "Block memory". | Word |
| (s2) | Operation constant start device | Refer to "Operation constant". | Word |
| (s3) | Empty string specification <br> (Specify "" in ladder. Specify " in ST or FBD/LD.) | String |  |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | J밈 | T, ST, C, D, W, SD, SW, R, ZR, RD | UपIGロ, JपIロ, U3EDI(H)GD | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (s1) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (d1) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (s2) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (s3) | - | - | - | - | - | - | - | - | - | - | $\bigcirc$ | - |

## Input data

| Operand: (s1) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| $\begin{aligned} & +0 \\ & +1 \end{aligned}$ | E1 | Input value | -999999 to 999999 [\%] | Single-precision real number | - | User |

Block memory


Operation constant

| Operand: (s2) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| $\begin{aligned} & +0 \\ & +1 \end{aligned}$ | V1 | Positive direction limit value | 0 to 999999 [\%/s] | Single-precision real number | 100.0 | User |
| $\begin{aligned} & +2 \\ & +3 \end{aligned}$ | V2 | Negative direction limit value | 0 to 999999 [\%/s] | Single-precision real number | 100.0 | User |
| $\begin{aligned} & +4 \\ & +5 \end{aligned}$ | HS1 | Positive direction hysteresis | 0 to 999999 [\%] | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +6 \\ & +7 \end{aligned}$ | HS2 | Negative direction hysteresis | 0 to 999999 [\%] | Single-precision real number | 0.0 | User |

Execution cycle ( $\Delta T$ )
Set the execution cycle ( $\Delta \mathrm{T}$ ) in SD816 and SD817 in single-precision real number.

## Processing details

This instruction holds the last value and outputs it when the variation rate of input (E1) exceeds the limit.


E1: Input value, BW: Output value
The S.VLMT2 instruction performs the following operations.

| Condition | Input $(\mathrm{E} 1-\mathrm{BW})$ | BW | BB1 | BB2 |
| :--- | :--- | :--- | :--- | :--- |
| Positive direction $\mathrm{E} 1 \geq \mathrm{BW}$ | $(\mathrm{E} 1-\mathrm{BW}) \geq(\mathrm{V} 1 \times \Delta \mathrm{T})$ | $\mathrm{BW}=\mathrm{BW}$ | 1 | 0 |
|  | $(\mathrm{E} 1-\mathrm{BW})<(\mathrm{V} 1 \times \Delta \mathrm{T}-\mathrm{HS} 1)$ | $\mathrm{BW}=\mathrm{E} 1$ | 0 |  |
|  | Others | $\mathrm{BW}=\mathrm{BW}$ | Last value | Last value |
| Negative direction $\mathrm{E} 1<\mathrm{BW}$ | $(\mathrm{BW}-\mathrm{E} 1) \geq(\mathrm{V} 2 \times \Delta \mathrm{T})$ | $\mathrm{BW}=\mathrm{BW}$ | 0 | 1 |
|  | $(\mathrm{BW}-\mathrm{E} 1)<(\mathrm{V} 2 \times \Delta \mathrm{T}-\mathrm{HS} 2)$ | $\mathrm{BW}=\mathrm{E} 1$ | 0 | 0 |
|  | Others | Last value | Last value |  |

## Operation error

| Error code | Error content |
| :--- | :--- |
| 3402 H | The value specified by (s1) or (s2) is a subnormal number or NaN (not a number). |
| 3403 H | An overflow has occurred. |
| 3405 H | Positive direction hysteresis (HS1) is less than 0 or the negative direction hysteresis (HS2) is less than 0. |

## Two－position（on／off）control

## S．ONF2

The instruction performs the following steps：SV setting，tracking，MV correction，MV output，and two－position（on／off）control．

| Ladder |  |  |  |  |  | ST |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  |  |  | ENO：＝S＿ONF2（EN，s1，s2，s3，d1，d2）； |
|  | （s1） | （d1） | （s2） | （d2） |  |  |  |

FBD／LD

| ■－－－】 |  |
| :---: | :---: |
| EN | ENO |
| s1 | d1 |
| s2 | d2 |
| s3 |  |

Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．ONF2 | $\square$ |

Setting data
Description，range，data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| （s1） | Input data start device | Refer to＂Input data＂． | Single－precision real <br> number |
| （d1） | Block memory start device | Refer to＂Block memory＂． | Word |
| （s2） | Operation constant start device | Refer to＂Operation constant＂． | Word |
| （d2） | Loop tag memory start device | Refer to＂Loop tag memory＂． | Word |
| （s3） | Set value start device when E2 is used <br> Dummy device when E2 is not used | Refer to＂Set value＂． | Single－precision real <br> number |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s3） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## ■Input data

| Operand：（s1） | Recommended range | Data type | Standard <br> value | Set by |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Symbol | Name | Input value | -999999 to $999999[\%]$ | Single－precision <br> real number | - |
| +0 | E1 |  | User／ <br> system |  |  |  |

Block memory


Operation constant


Loop tag memory
The loop tag memory occupies 96 words from the specified start device.


| Operand: (d2) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| $\begin{aligned} & +12 \\ & +13 \end{aligned}$ | MV | Manipulated value | -10 to 110 [\%] | Single-precision real number | 0.0 | User/ system |
| $\begin{aligned} & +14 \\ & +15 \end{aligned}$ | SV | Set value | RL to RH | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +16 \\ & +17 \end{aligned}$ | DV | Deviation | -110 to 110 [\%] | Single-precision real number | 0.0 | System |
| $\begin{aligned} & +18 \\ & +19 \end{aligned}$ | HSO | Hysteresis | 0 to 999999 | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +22 \\ & +23 \end{aligned}$ | RH | Engineering value upper limit | -999999 to 999999 | Single-precision real number | 100.0 | User |
| $\begin{aligned} & +24 \\ & +25 \end{aligned}$ | RL | Engineering value lower limit | -999999 to 999999 | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +46 \\ & +47 \end{aligned}$ | CT | Control cycle | 0 to 999999 [s] <br> Provided that $\frac{C T}{\triangle T}<=32767$ | Single-precision real number | 1.0 | User |

## Loop tag past value memory

The system uses this area as a work area.
To start the control from the initial status, clear data by using a sequence program.
The loop tag past value memory occupies 32 words after the loop tag memory.

| Operand: (d2) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| +96 | - | Control cycle counter initialization completion flag | - | - | - | System |
| +97 |  | Control cycle counter (The value is rounded off to the nearest whole number.) |  |  |  |  |

## Set value

The set value (E2) is valid only when b0 of the set value pattern (SVPTN) is set to 0 (Used). To use the upper loop MV as the set value (E2), specify the device (offset +12 ) where the manipulated value (MV) of the upper loop is set.
If E 2 is not used, specify a dummy device (SD820).

| Operand: (s3) |  | Recommended range | Data type | Standard <br> value | Set by |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Symbol | Name | Set value | -10 to $110[\%]$ | Single-precision <br> real number | 0.0 |
| +0 | E2 |  | User/ <br> system |  |  |  |

Execution cycle ( $\Delta \mathrm{T}$ )
Set the execution cycle ( $\Delta \mathrm{T}$ ) in SD816 and SD817 in single-precision real number.

## Processing details

This instruction performs two-position (on/off) control (1-contact ON/OFF) when the specified control cycle is reached. The instruction also performs the following steps: SV setting, tracking, MV correction, and MV output processing. The following is the processing block diagram of the S.ONF2 instruction. (The numbers (1) to (7) in the diagram indicate the order of the processing.)


## SV setting processing (1)

The following processing is performed according to the operation mode (MODE) setting.

| MODE setting | Processing details |
| :--- | :--- |
| CAS, CCB, CSV | If the set value (E2) is specified, engineering value transformation processing (refer to the <br> following expression) is performed, and then "tracking processing (2)" is performed. <br> $S V_{n}=\frac{R H-R L}{100} \times E 2+R L$ <br> If the set value (E2) is not specified, "tracking processing (2)" is performed without performing <br> engineering value transformation processing. |
| MAN, AUT, CMV, CMB, CAB, LCM, LCA, LCC | "Tracking processing (2)" is performed. |

## Tracking processing (2)

- The set value (SV) is inversely transformed from the engineering value and SVn' is calculated (refer to the following expression).
$S V_{n}^{\prime}=\frac{100}{R H-R L} \times\left(S V_{n}-R L\right)$
- Tracking processing is performed when all of the following conditions are satisfied.
- The tracking bit (TRK) is set to 1 .
- The set value (E2) is used.
- The operation mode (MODE) is set to any of the following: MAN, AUT, CMV, CMB, CAB, LCM, LCA, or LCC.
$\mathrm{E} 2=\mathrm{SV}{ }_{\mathrm{n}}{ }^{\prime}$
- If the set value (E2) is the upper loop MV, the TRKF of the disable alarm detection (INH) of the upper loop is set to 1 .


## MV correction (3)

The instruction calculates the deviation (DV) based on the input value ( E 1 ) and the set value ( $\mathrm{SVn} \mathrm{n}^{\prime}$ ) after tracking, and then calculates the MV correction value (MV').

- The deviation (DV) is calculated under the following conditions.

| Condition | DV |
| :--- | :--- |
| Direct action $(\mathrm{PN}=1)$ | $\mathrm{E} 1-\mathrm{SV}_{\mathrm{n}}{ }^{\prime}$ |
| Reserve action $(\mathrm{PN}=0)$ | $\mathrm{SV}_{\mathrm{n}}{ }^{\prime}-\mathrm{E} 1$ |

- The MV correction value ( $\mathrm{MV}^{\prime}$ ) is calculated under the following conditions.

| Condition | MV' |
| :--- | :--- |
| DV $\geq$ HS0 | $100 \%$ |
| DV $\leq-H S 0$ | $0 \%$ |
| $-H S O<$ DV $<H S 0$ | Last value (BW value) |

IMV output (4)
The manipulated value (MV (BW)) is calculated under the following conditions.

| Condition | $B W$ |
| :--- | :--- |
| CMV, MAN, CMB, LCM | $B W=M_{n}$ |
| CSV, CCB, CAB, CAS, AUT, LCC, LCA | $B W=M V^{\prime}$ |
|  | $\mathrm{MV}_{\mathrm{n}}=\mathrm{BW}$ |

Two-position (on/off) control (5)
The BB operation result (BB1) is output under the following conditions.

| Condition | BB1 |
| :--- | :--- |
| $\|B W\| \geq 50 \%$ | 1 |
| $\|B W\|<50 \%$ | 0 |

■Loop stop processing (6)
The following processing is performed according to the SPA status of the alarm detection (ALM).

| SPA status | Processing details |
| :--- | :--- |
| 1 | The loop stops. When the loop stops, the following operations are performed and the S.ONF2 instruction ends. <br> The output value (BW) is held. <br> The operation mode (MODE) is set to MAN. <br> The output result (BB1) value is set to 0. |
| 0 | The loop runs and "control cycle determination processing (7)" is performed. |

Control cycle determination (7)

- If the specified control cycle is not reached, the following processing is performed.

| Operation mode (MODE) | Processing |
| :--- | :--- |
| CSV, CCB, CAB, CAS, AUT, LCC, <br> or LCA | The output value (BW) is held and the S.ONF2 instruction ends. |
| MAN, CMB, CMV, or LCM | "Two-position (on/off) control (5) is processed assuming that the output value (BW) equals the manipulated value (MV). |

- If the specified control cycle is reached, "SV setting processing (1)" is performed.

Operation error

| Error code | Error content |
| :--- | :--- |
| 3400 H | An invalid operation (such as division by zero) is performed. |
| 3402 H | The value specified by (s1) or (d2) is a subnormal number or NaN (not a number). |
| 3403 H | An overflow has occurred. |
| 3405 H | The control cycle (CT) setting is less than 0. |
|  | The execution cycle ( $\Delta \mathrm{T}$ ) setting is less than 0. |
|  | Hysteresis (HSO) <- hysteresis (HSO) |
|  | The value divided the control cycle (CT) by the execution cycle ( $\Delta \mathrm{T}$ ) exceeds 32767. |

## Three－position（on／off）control

## S．ONF3

The instruction performs the following steps：SV setting，tracking，MV correction，MV output，and three－position（on／off） control．

| Ladder |  |  |  |  |  | $\begin{aligned} & \text { ST } \\ & \text { ENO:=S_ONF3(EN,s1,s2,s3,d1,d2); } \end{aligned}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\square-\square-\square$ （s1） （d1） （s2） （d2） （s3） |  |  |  |  |  |  |
|  |  |  |  |  |  |  |

FBD／LD

| ［－二－］ |  |
| :---: | :---: |
| EN | Eno |
| s1 | d1 |
| s2 | d2 |
| s3 |  |

■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．ONF3 | $-\square$ |

## Setting data

## Description，range，data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| （s1） | Input data start device | Refer to＂Input data＂． | Single－precision real <br> number |
| （d1） | Block memory start device | Refer to＂Block memory＂． | Word |
| （s2） | Operation constant start device | Refer to＂Operation constant＂． | Word |
| （d2） | Loop tag memory start device | Refer to＂Loop tag memory＂． | Word |
| （s3） | Set value start device when E2 is used <br> Dummy device when E2 is not used | Refer to＂Set value＂． | Single－precision real <br> number |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，R，ZR，RD | U밈，J밈， U3EDI（H）Gロ | Z | LT，LST， LC | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s3） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Input data

| Operand：（s1） |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| $\begin{aligned} & +0 \\ & +1 \end{aligned}$ | E1 | Input value | －999999 to 999999 ［\％］ | Single－precision real number | － | User |

Block memory


## ©Operation constant

| Operand: (s2) |  |  |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Symbol | Name | Recommended range |  |  |  |  |  |  |  | Data type | Standard value | Set by |
| +0 | PN | Operation mode | 0: Reverse action <br> 1: Direct action |  |  |  |  |  |  |  | 16-bit unsigned binary | 0 | User |
| +1 | TRK | Tracking bit | 0 : Tracking not performed <br> 1: Tracking performed |  |  |  |  |  |  |  | 16-bit unsigned binary | 0 | User |
| +2 | SVPTN | Set value pattern |  | to 3 <br> b15 <br> 1) Us <br> pecify <br> : Use <br> : Not <br> (2) Se <br> pecify <br> ot. <br> : E2 <br> : E2 | se of set <br> ify wheth <br> ed <br> t used <br> et value <br> ify wheth <br> is the u <br> is not th | value <br> er to <br> pattern er to <br> per lo e upp | 正 <br> use the set <br> use the upp <br> oop MV. <br> er loop MV. | value <br> er loop | (E2) or <br> MV a | (2) (1) <br> not. <br> s the set value (E2) or | 16-bit unsigned binary | 3 | User |

## ■Loop tag memory

The loop tag memory occupies 96 words from the specified start device.


## ■Loop tag past value memory

The system uses this area as a work area.
To start the control from the initial status, clear data by using a sequence program.
The loop tag past value memory occupies 32 words after the loop tag memory.

| Operand: (d2) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| +96 | - | Control cycle counter initialization completion flag | - | - | - | System |
| +97 |  | Control cycle counter (The value is rounded off to the nearest whole number.) |  |  |  |  |

## Set value

The set value (E2) is valid only when b0 of the set value pattern (SVPTN) is set to 0 (Used). To use the upper loop MV as the set value (E2), specify the device (offset +12 ) where the manipulated value (MV) of the upper loop is set.
If E 2 is not used, specify a dummy device (SD820).

| Operand: (s3) | Recommended range | Data type | Standard <br> value | Set by |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Symbol | Name | Set value | -10 to $110[\%]$ | Single-precision <br> real number | 0.0 |
| +0 | E2 |  | User/ <br> system |  |  |  |

## Execution cycle ( $\Delta T$ )

Set the execution cycle $(\Delta T)$ in SD816 and SD817 in single-precision real number.

## Processing details

This instruction performs two-position (on/off) control (2-contact ON/OFF) every control cycle.
The instruction also performs the following steps: SV setting, tracking, MV correction, and MV output processing.
The following is the processing block diagram of the S.ONF3 instruction. (The numbers (1) to (7) in the diagram indicate the order of the processing.)


## ISV setting processing (1)

The following processing is performed according to the operation mode (MODE) setting.

| MODE setting | Processing details |
| :--- | :--- |
| CAS, CCB, CSV | If the set value (E2) is specified, engineering value transformation processing (refer to the <br> following expression) is performed, and then "tracking processing (2)" is performed. <br> $S V_{n}=\frac{R H-R L}{100} \times E 2+R L$ <br> If the set value (E2) is not specified, "tracking processing (2)" is performed without performing <br> engineering value transformation processing. |
| MAN, AUT, CMV, CMB, CAB, LCM, LCA, LCC | "Tracking processing (2)" is performed. |

## Tracking processing (2)

The set value (SV) is inversely transformed from the engineering value and SVn' is calculated (refer to the following expression).
$S V_{n}^{\prime}=\frac{100}{R H-R L} \times\left(S V_{n}-R L\right)$
Tracking processing is performed when all of the following conditions are satisfied.

- The tracking bit (TRK) is set to 1 .
- The set value (E2) is used.
- The operation mode (MODE) is set to any of the following: MAN, AUT, CMV, CMB, CAB, LCM, LCA, or LCC.
$\mathrm{E} 2=\mathrm{SV}_{\mathrm{n}}{ }^{\prime}$
- If the set value (E2) is the upper loop MV, the TRKF of the disable alarm detection (INH) of the upper loop is set to 1 .

MV correction (3)
The instruction calculates the deviation (DV) based on the input value (E1) and the set value ( SVn ') after tracking, and then calculates the MV correction value (MV').

- The deviation (DV) is calculated under the following conditions.

| Condition | DV |
| :--- | :--- |
| Direct action (PN = 1) | $\mathrm{E1}^{\prime}-\mathrm{SV}_{\mathrm{n}}{ }^{\prime}$ |
| Reserve action (PN = 0) | $\mathrm{SV}_{\mathrm{n}}{ }^{\prime}-\mathrm{E} 1$ |

- The MV correction value (MV') is calculated under the following conditions.

| Condition | MV' |
| :--- | :--- |
| DV $\geq(H S 1+H S 0)$ | $100 \%$ |
| DV $\leq-(H S 1+H S 0)$ | $0 \%$ |
| $(-H S 1+H S 0)<\mathrm{DV}<(\mathrm{HS1} 1-\mathrm{HSO})$ | $50 \%$ |
| Others | Last value (BW value) |

MV output (4)
The manipulated value (MV (BW)) is calculated under the following conditions.

| Condition | $B W$ |
| :--- | :--- |
| CMV, MAN, CMB, LCM | $B W=M V_{n}$ |
| CSV, CCB, CAB, CAS, AUT, LCC, LCA | $\mathrm{BW}=\mathrm{MV}$ <br> $M V_{n}=B W$ |

Three-position (on/off) control (5)
The BB operation results (BB1, BB2) are output under the following conditions.

| Condition | BB1 | BB2 |
| :--- | :--- | :--- |
| BW $\geq 75 \%$ | 1 | 0 |
| $25 \% \leq B W<75 \%$ | 0 | 0 |
| $B W<25 \%$ | 0 | 1 |

Loop stop processing (6)
The following processing is performed according to the SPA status of the alarm detection (ALM).

| SPA status | Processing details |
| :--- | :--- |
| 1 | The loop stops. When the loop stops, the following operations are performed and the S.ONF3 instruction ends. <br> • The output value (BW) is held. |
|  | • The operation mode (MODE) is set to MAN. <br> • The output result (BB1, BB2) values are set to 0. |
| 0 | The loop runs and "control cycle determination processing (7)" is performed. |

## Control cycle determination (7)

- If the specified control cycle is not reached, the following processing is performed.

| Operation mode (MODE) | Processing |
| :--- | :--- |
| CSV, CCB, CAB, CAS, AUT, LCC, <br> or LCA | The output value (BW) is held and the S.ONF3 instruction ends. |
| MAN, CMB, CMV, or LCM | "Three-position (on/off) control (5) is processed assuming that the output value (BW) equals the manipulated value <br> (MV). |

- If the control cycle (CT) is reached, "SV setting processing (1)" is performed.


## Operation error

| Error code | Error content |
| :---: | :---: |
| 3400 H | An invalid operation (such as division by zero) is performed. |
| 3402H | The value specified by ( s 1 ) or (d2) is a subnormal number or NaN (not a number). |
| 3403H | An overflow has occurred. |
| 3405 H | Hysteresis 0 (HSO) is less than 0 . |
|  | The control cycle (CT) setting is less than 0 . |
|  | The execution cycle ( $\Delta \mathrm{T}$ ) setting is less than 0 . |
|  | (Hysteresis 1 (HS1) + hysteresis 0 (HSO)) is less than 0 . |
|  | Hysteresis 1 (HS1) is less than 0. |
|  | The value divided the control cycle (CT) by the execution cycle ( $\Delta \mathrm{T}$ ) exceeds 32767 . |

## Dead band

## S.DBND

This instruction provides a dead band and performs output processing.


FBD/LD

| ■--- |  |
| :---: | :---: |
| EN | ENO |
| s1 | d1 |
| s2 |  |
| s3 |  |

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S.DBND | $\square$ |

## Setting data

Description, range, data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| (s1) | Input data start device | Refer to "Input data". | Single-precision real <br> number |
| (d1) | Block memory start device | Refer to "Block memory". | Word |
| (s2) | Operation constant start device | Refer to "Operation constant". | Word |
| (s3) | Empty string specification <br> (Specify "" in ladder. Specify " in ST or FBD/LD.) | - | String |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | J밈 | T, ST, C, D, W, SD, SW, R, ZR, RD | UपIGロ, JपIロ, U3EDI(H)GD | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (s1) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (d1) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (s2) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (s3) | - | - | - | - | - | - | - | - | - | - | $\bigcirc$ | - |

## Input data

| Operand: (s1) |  | Recommended range | Data type | Standard <br> value | Set by |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Symbol | Name | Input value | -999999 to $999999[\%]$ | Single-precision <br> real number | - |
| +0 | E1 |  |  | User |  |  |

Block memory


## Operation constant

| Operand: (s2) | Recommended range | Data type | Standard <br> value | Set by |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Symbol | Name | Dead band <br> upper limit | -999999 to 999999 | Single-precision <br> real number | 100.0 |
| +0 | D1 | Dead band <br> lower limit | -999999 to 999999 | Single-precision <br> real number | 0.0 |  |
| +2 | D2 |  |  |  | User |  |

## Processing details

This instruction provides a dead band and performs output processing.


BB1 1
$\qquad$
D1: Dead band upper limit, D2: Dead band lower limit
The S.DBND instruction performs the following operations.

| Condition | BW | BB1 |
| :--- | :--- | :--- |
| D2 $\leq 1 \leq D 1$ | $\frac{\text { D2+D1 }}{2}$ | 1 |
| (E1<D2) or (E1>D1) | E1 | 0 |

## Operation error

| Error code | Error content |
| :--- | :--- |
| 3402 H | The value specified by (s1) or (s2) is a subnormal number or NaN (not a number). |
| 3403 H | An overflow has occurred. |

## Program setter

## S.PGS

This instruction provides control output according to the SV and MV patterns.


FBD/LD

|  |  |
| :---: | :---: |
| En | Eno |
| s1 | ${ }^{11}$ |
| s2 |  |

Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S.PGS | - |

## Setting data

Description, range, data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| (s1) | Empty string specification <br> (Specify "" in ladder. Specify " in ST or FBD/LD.) | - | String |
| (d1) | Block memory start device | Refer to "Block memory". | Word |
| (s2) | Empty string specification <br> (Specify "" in ladder. Specify " in ST or FBD/LD.) | String |  |
| (d2) | Loop tag memory start device | Refer to "Loop tag memory". | Word |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T, ST, C, D, W, SD, SW, R, ZR, RD | U밈, J밈, U3EDI(H)GD | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (s1) | - | - | - | - | - | - | - | - | - | - | $\bigcirc$ | - |
| (d1) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (s2) | - | - | - | - | - | - | - | - | - | - | $\bigcirc$ | - |
| (d2) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |

Block memory


## ■Loop tag memory

The loop tag memory occupies 96 words from the specified start device.

| Operand: (d2) |  |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range |  |  |  |  |  |  | Data type | Standard | Set by |
| +1 | MODE | Operation mode | 0 to FFFFH |  |  |  |  |  |  | 16-bit unsigned binary | 8H | User/ system |
| +3 | ALM | Alarm detection | 0 to FFFFH <br> SPA <br> 0: Loop RUN <br> 1: Loop STOP <br> MHA, MLA <br> 0: No alarm <br> 1: Alarm |  |  |  |  |  |  | 16-bit unsigned binary | 4000H | User/ system |
| +4 | INH | Disable alarm detection | 0 to FF <br> b15 <br>  <br> 0: Alar <br> 1: Alar | ion en ion dis | abled <br> sabled |  |  |  |  | 16-bit unsigned binary | 4000H | User/ system |
| +10 | PTNO | Number of operation constant break points | 0 to 16 |  |  |  |  |  |  | 16-bit unsigned binary | 0 | User |
| $\begin{aligned} & +12 \\ & +13 \end{aligned}$ | MV | Manipulated value | -10 to 110 [\%] |  |  |  |  |  |  | Single-precision real number | 0.0 | User/ system |
| $\begin{aligned} & +14 \\ & +15 \end{aligned}$ | SV | Set value | 0 to 999999 [s] |  |  |  |  |  |  | Single-precision real number | 0.0 | User/ system |
| +16 | TYPE | Operation type | Operation mode AUT or CAB <br> 0: Hold type operation <br> 1: Return type operation |  |  |  |  |  |  | 16-bit unsigned binary | 0 | User |
| $\begin{aligned} & +18 \\ & +19 \end{aligned}$ | MH | Output upper limit value | -10 to 110 [\%] |  |  |  |  |  |  | Single-precision real number | 100.0 | User |
| $\begin{aligned} & +20 \\ & +21 \end{aligned}$ | ML | Output lower limit value | -10 to 110 [\%] |  |  |  |  |  |  | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +22 \\ & +23 \end{aligned}$ | SV1 | Setting time 1 | 0 to 999999 [s] |  |  |  |  |  |  | Single-precision real number | 0.0 | User |
| ! | ! | ! |  |  |  |  |  |  |  |  |  |  |
| $\begin{aligned} & +52 \\ & +53 \end{aligned}$ | SV16 | Setting time 16 |  |  |  |  |  |  |  |  |  |  |
| $\begin{aligned} & +54 \\ & +55 \end{aligned}$ | MV1 | Set output 1 | -10 to 110 [\%] |  |  |  |  |  |  | Single-precision real number | 0.0 | User |
| ! | ! | ! |  |  |  |  |  |  |  |  |  |  |
| $\begin{aligned} & +84 \\ & +85 \end{aligned}$ | MV16 | Set output 16 |  |  |  |  |  |  |  |  |  |  |

## Execution cycle ( $\Delta \mathrm{T}$ )

Set the execution cycle ( $\Delta \mathrm{T}$ ) in SD816 and SD817 in single-precision real number.

## Processing details

This instruction provides control output according to the SV and MV patterns.
The S.PGS instruction has the following three output types.

| Output type | Description |
| :--- | :--- |
| Hold type | Outputs data while holding the value of setting time 10 (SV10). |
| Return type | Sets the set value (SV) to 0 and outputs the last value of the manipulated value (MV). |
| Cyclic type | Processes based on the setting time 1 (SV1) to 10 (SV10) and then restarts processing from setting time 1 (SV1). |



The following is the processing block diagram of the S.PGS instruction. (The numbers (1) to (5) in the diagram indicate the order of the processing.)


## Output type (1)

The output type is determined by the combination of operation mode (MODE) and operation type (TYPE) as follows.

| Operation mode (MODE) | Operation type (TYPE) | Operation |
| :--- | :--- | :--- |
| MAN, CMB, CMV, LCM, LCA, LCC | - | Operation stop with the current SV and MV |
| AUT, CAB | 0 | Hold type operation |
|  | 1 | Return type operation |
| CAS, CCB, CSV | - | Cyclic type operation |

## Loop stop processing (2)

The following processing is performed according to the SPA status of the alarm detection (ALM).

| SPA status | Processing details |
| :--- | :--- |
| 1 | The loop stops. <br> When the loop stops or the number of operation constant break points is 0, the following operations are performed and the S.PGS <br> instruction ends. <br> • The output value (BW) is held. <br> - All of the output alarm bits (BB1, BB2, and BB3) are set to 0. <br> - The MHA and MLA of alarm detection (ALM) are set to 0. <br> - The operation mode (MODE) is set to MAN. |
| 0 | The loop runs. When the loop runs, "SV count-up processing (3)" is performed. |

## SV count-up processing (3)

SV count-up is performed every execution cycle ( $\Delta \mathrm{T}$ ) according to the following expression.
$S V^{\prime}=S V+\Delta T$
MMVPGS operation (4)
$M V_{\text {PGS }}$ operation is shown below.

| Output type |  | Hold type | Return type | Cyclic type |
| :---: | :---: | :---: | :---: | :---: |
| Operation mode (MODE) |  | AUT, CAB |  | CAS, CCB, CSV |
| MV ${ }_{\text {PGS }}$ operation | SV<SV1 | MV1 |  |  |
|  | $\mathrm{SV}_{\mathrm{n}-1} \leq \mathrm{SV}<\mathrm{SV}_{\mathrm{n}}$ | $\frac{M V_{n}-M V_{n-1}}{S V_{n}-S V_{n-1}} \times\left(S V^{\prime}-S V_{n-1}\right)+M V_{n-1}$ |  |  |
| Processing at $\mathrm{SV}^{\prime}>\mathrm{SV}_{\mathrm{n}}$ | 0: Mode shift | MAN | MAN | No mode shift |
|  | SV | Last value | 0 | 0 |
|  | MV | Last value | Last value | MV1 |
|  | Restart method | After SV setting, change to the MAN $\rightarrow$ AUT mode. | Change to the MAN $\rightarrow$ AUT mode. | Automatic restart |

Output processing (5)
The output processing conditions are shown below.

| Condition | Manual |  |  | Automatic |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | MAN, CMB, CMV, LCM, LCA, LCC |  |  | AUT, CAB, CAS, CCB, CSV |  |  |
|  | BW | BB2, MHA | BB3, MLA | BW | BB2, MHA | BB3, MLA |
| $M V_{\text {PGS }}>M H$ | $M V_{n}$ | 0 | 0 | $M V_{n}=M H$ | $1{ }^{* 1}$ | 0 |
| MV $\mathrm{PGGS}<\mathrm{ML}$ | $M V_{n}$ | 0 | 0 | $M V_{\mathrm{n}}=\mathrm{ML}$ | 0 | $1{ }^{*}$ |
| Others | $M V_{n}$ | 0 | 0 | $M V_{n}=M V_{\text {PGS }}$ | 0 | 0 |

*1 If the MHI or ERRI of the disable alarm detection (INH) is set to 1 , the output upper limit alarm (BB2) and the MHA of the alarm detection (ALM) are set to 0 .
*2 If the MLI or ERRI of the disable alarm detection (INH) is set to 1, the output lower limit alarm (BB3) and the MLA of the alarm detection (ALM) are set to 0.

## Operation error

| Error code | Error content |
| :--- | :--- |
| 3400 H | An invalid operation (such as division by zero) is performed. |
| 3402 H | The value specified by (d2) is a subnormal number or NaN (not a number). |
| 3403 H | An overflow has occurred. |
| 3405 H | The number of operation constant break points (PTNO) is less than 0 or greater than 16. |

## Loop selector

## S．SEL

In automatic mode，outputs the value selected by the selection signal from the input data，and in manual mode，outputs the manipulated value（MV）in the loop tag memory．


FBD／LD

| ■－－－$\square$ |  |
| :---: | :---: |
| EN | ENO |
| s1 | d1 |
| s2 | d2 |
| s3 |  |

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．SEL | $\boxed{\square}$ |
|  | $\square$ |

## Setting data

## Description，range，data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| （s1） | Input data 1 start device | Refer to＂Input data 1＂． | Single－precision real <br> number |
| （d1） | Block memory start device | Refer to＂Block memory＂． | Word |
| （s2） | Operation constant start device | Refer to＂Operation constant＂． | Word |
| （d2） | Loop tag memory start device | Refer to＂Loop tag memory＂． | Word |
| （s3） | Input data 2 start device | Refer to＂Input data 2＂． | Single－precision real <br> number |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，R，ZR，RD | UपIGロ，JロIロ， U3EDl（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s3） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Input data 1

| Operand：（s1） |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| +0 +1 | E1 | Input value 1 | －999999 to 999999 ［\％］ | Single－precision real number | － | User |

Block memory


## Operation constant

| Operand: (s2) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| $\begin{aligned} & +0 \\ & +1 \end{aligned}$ | NMAX | Output conversion upper limit | -999999 to 999999 | Single-precision real number | 100.0 | User |
| $\begin{aligned} & +2 \\ & +3 \end{aligned}$ | NMIN | Output conversion lower limit | -999999 to 999999 | Single-precision real number | 0.0 | User |
| +4 | TRK | Tracking bit | 0 : Tracking not performed <br> 1: Tracking performed | 16-bit unsigned binary | 0 | User |
| +5 | SVPTN | Set value pattern | $(5)(4)(3)(2)(1)$ <br> (1) Input value selection (e1) <br> Specify whether to use E1 or E2 for the input value. <br> 0: E1 <br> 1: E2 <br> (2) Use of input value 1 (E1) <br> Specify whether to use the input value 1 (E1) or not. <br> 0: Use <br> 1: Not use <br> (3) Use of input value 2 (E2) <br> Specify whether to use the input value 2 (E2) or not. <br> 0: Use <br> 1: Not use <br> (4) Input value 1 (E1) pattern <br> Specify whether to use the upper loop MV as the input value 1 (E1) or not. <br> 0 : E1 is the upper loop MV. <br> 1: E1 is not the upper loop MV. <br> (5) Input value 2 (E2) pattern <br> Specify whether to use the upper loop MV as the input value 2 (E2) or not. <br> 0: E2 is the upper loop MV. <br> 1: E2 is not the upper loop MV. | 16-bit unsigned binary | 1EH | User |

## ■Loop tag memory

The loop tag memory occupies 96 words from the specified start device.

| Operand: (d2) |  |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range |  |  |  |  |  |  | Data type | Standard value | Set by |
| +1 | MODE | Operation mode | 0 to FFFFH |  |  |  |  |  |  | 16-bit unsigned binary | 8H | User/ system |
| +3 | ALM | Alarm detection | 0 to FFFFH <br> SPA <br> 0: Loop RUN <br> 1: Loop STOP <br> DMLA, MHA, MLA <br> 0: No alarm <br> 1: Alarm |  |  |  |  |  |  | 16-bit unsigned binary | 4000 H | User/ system |
| +4 | INH | Disable alarm detection | 0 to FFFFH <br> 0 : Alarm detection enabled <br> 1: Alarm detection disabled |  |  |  |  |  |  | 16-bit unsigned binary | 4000H | User/ system |
| $\begin{aligned} & +10 \\ & +11 \end{aligned}$ | PV | Selecting a Value | RL to RH |  |  |  |  |  |  | Single-precision real number | 0.0 | System |
| $\begin{aligned} & +12 \\ & +13 \end{aligned}$ | MV | Manipulated value | -10 to 110 [\%] |  |  |  |  |  |  | Single-precision real number | 0.0 | User/ system |
| $\begin{aligned} & +14 \\ & +15 \end{aligned}$ | PV1 | Process value 1 | RL to RH |  |  |  |  |  |  | Single-precision real number | 0.0 | System |
| $\begin{aligned} & +16 \\ & +17 \end{aligned}$ | PV2 | Process value 2 | RL to RH |  |  |  |  |  |  | Single-precision real number | 0.0 | System |
| $\begin{aligned} & +18 \\ & +19 \end{aligned}$ | MH | Output upper limit value | -10 to 110 [\%] |  |  |  |  |  |  | Single-precision real number | 100.0 | User |
| $\begin{aligned} & +20 \\ & +21 \end{aligned}$ | ML | Output lower limit value | -10 to 110 [\%] |  |  |  |  |  |  | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +22 \\ & +23 \end{aligned}$ | RH | Engineering value upper limit | -999999 to 999999 |  |  |  |  |  |  | Single-precision real number | 100.0 | User |
| $\begin{aligned} & +24 \\ & +25 \end{aligned}$ | RL | Engineering value lower limit | -999999 to 999999 |  |  |  |  |  |  | Single-precision real number | 0.0 | User |
| +26 | SLNO | Selected No. | (1) Selection of input value 1 (E1) <br> 0 : Not selected <br> 1: Selected <br> (2) Selection of input value 2 (E2) <br> 0 : Not selected <br> 1: Selected |  |  |  |  |  |  | 16-bit unsigned binary | 0 | System |
| +48+49 | DML | Output variation rate limit value | 0 to 100 [\%] |  |  |  |  |  |  | Single-precision real number | 100.0 | User |

## ■Input data 2

| Operand: (s3) |  | Recommended range | Data type | Standard <br> value | Set by |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Position | Symbol | Name | Input value 2 | -999999 to $999999[\%]$ | Single-precision <br> real number | 0.0 |
| +0 | E2 |  |  | User |  |  |

## Processing details

This instruction outputs data in the specified mode (automatic or manual mode).

- In automatic mode, the instruction outputs the value selected by the selection signal (e1) from the input values 1 (E1) and 2 (E2).
- In manual mode, it outputs the manipulated value (MV).

The following is the processing block diagram of the S.SEL instruction. (The numbers (1) to (7) in the diagram indicate the order of the processing.)


## Engineering value transformation (1)

The instruction performs engineering value transformation using the following expression.
$P V_{n}=\frac{R H-R L}{100} \times E_{n}+R L$

## ■Input value 1 (E1), 2 (E2) selection processing (2)

Specify the input value selection (e1) of the set value pattern (SVPTN) to specify which input value is to be used, 1 (E1) or 2 (E2).

- Input value selection (e1) = 0: Input value $1(E 1)$ is used. Selected value $(P V)=$ process value $1(P V 1)$
- Input value selection (e1) = 1: Input value 2 ( E 2 ) is used. Selected value (PV) = process value $2(\mathrm{PV} 2)$

Selected No. (SLNO): The bit corresponding to the input value E1 or E2 is set to 1.

## ■Mode check (3)

The following processing is performed depending on the operation mode (MODE).

| Operation mode (MODE) | Processing details |
| :--- | :--- |
| MAN, CMB, CMV, LCM | "Output conversion processing (5)" is performed. <br> The MHA, MLA, and DMLA of the alarm detection (ALM) are set to 0. <br> The alarm bits (BB1, BB2, BB3, and BB4) are set to 0. |
| AUT, CAB, CAS, CCB, CSV, LCA, LCC | The instruction performs engineering value inverse transformation using the following expression. |
|  | $\mathrm{T}=\frac{100}{\mathrm{RH}-\mathrm{RL}} \times(\mathrm{PV}-\mathrm{RL}$ ) |
|  | "Variation rate \& upper/lower limiter processing (4)" is performed. |

## Variation rate \& upper/lower limiter (4)

The instruction checks the variation rate and upper/lower limit values for the input value 1 (E1) or 2 (E2).
The variation rate limiter conditions are shown below.

| Condition | $\mathrm{T}^{\prime}$ | BB4, DMLA |
| :--- | :--- | :--- |
| $\left\|T-M V_{n}\right\| \leq D M L$ | $\mathrm{~T}^{\prime}=\mathrm{T}$ | 0 |
| $\left(T-M V_{n}\right)>D M L$ | $\mathrm{~T}^{\prime}=\mathrm{MV}_{\mathrm{n}}+D M L$ | $1^{* 1}$ |
| $\left(\mathrm{~T}-\mathrm{MV} \mathrm{V}_{\mathrm{n}}\right)<-\mathrm{DML}$ | $\mathrm{T}^{\prime}=\mathrm{MV}_{\mathrm{n}}-\mathrm{DML}$ | $1^{* 1}$ |

*1 If the DMLI or ERRI of the disable alarm detection (INH) is set to 1, the output variation rate alarm (BB4) and the DMLA of the alarm detection (ALM) are set to 0 .
The upper/lower limiter conditions are shown below.

| Condition | MV | BB2, MHA | BB3, MLA |
| :--- | :--- | :--- | :--- |
| $\mathrm{T}^{\prime}>\mathrm{MH}$ | $\mathrm{MV}_{\mathrm{n}}=\mathrm{MH}$ | $1^{* 2}$ | 0 |
| $\mathrm{~T}<\mathrm{ML}$ | $\mathrm{MV}_{\mathrm{n}}=\mathrm{ML}$ | 0 | $1^{* 3}$ |
| $\mathrm{ML} \leq \mathrm{T}^{\prime} \leq \mathrm{MH}$ | $\mathrm{MV}_{\mathrm{n}}=\mathrm{T}^{\prime}$ | 0 | 0 |

*2 If the MHI or ERRI of the disable alarm detection (INH) is set to 1, the output upper limit alarm (BB2) and the MHA of the alarm detection (ALM) are set to 0 .
*3 If the MLI or ERRI of the disable alarm detection (INH) is set to 1, the output lower limit alarm (BB3) and the MLA of the alarm detection (ALM) are set to 0.

## Output conversion processing (5)

The instruction performs engineering value transformation using the following expression.
$\mathrm{BW}=\frac{\text { NMAX-NMIN }}{100} \times \mathrm{MV}_{\mathrm{n}}+\mathrm{NMIN}$

## Tracking processing (6)

Processing is performed when the following conditions are satisfied.

| Condition | Processing |
| :--- | :--- |
| When all of the following conditions are satisfied: | Operation result is output to the input value 1 (E1) or 2 <br> - The operation mode (MODE) is set to any of the following: MAN, CMB, CMV, or LCM. <br> - Tracking bit (TRK) is 1. <br> $E_{n}=M V_{n}$ |
| When all of the following conditions are satisfied: |  |
| - The operation mode (MODE) is set to any of the following: AUT, CAS, CAB, CCB, CSV, LCA, or |  |
| LCC. |  |
| - Tracking bit (TRK) is 1. |  |
| - BB alarm (BB1) =1 |  |
| $E_{n}=M V_{n}$ |  |

## Loop stop processing (7)

The following processing is performed according to the SPA status of the alarm detection (ALM).

| SPA status | Processing details |
| :--- | :--- |
| 1 | The loop stops. When the loop stops, the following operations are performed and the S.SEL instruction ends. <br> - The output value (BW) is held. |
|  | - The alarm bits (BB1, BB2, BB3, and BB4) are set to 0. <br> • The MHA, MLA, and DMLA of the alarm detection (ALM) are set to 0. <br> - The operation mode (MODE) is set to MAN. |
| 0 | Processing from "Engineering value transformation (1)" through to "Tracking processing (6)" is performed. |

## Operation error

| Error code | Error content |
| :--- | :--- |
| 3400 H | An invalid operation (such as division by zero) is performed. |
| 3402 H | The value specified by (s1), (s2), (d2), or (s3) is a subnormal number or NaN (not a number). |
| 3403 H | An overflow has occurred. |

## Bumpless transfer

## S.BUMP

This instruction gradually brings the output value (BW) closer to the output set value (E1) from the output control value (E2) when the mode switching signal (e1) changes from manual to automatic.


FBD/LD

| [---] |  |
| :---: | :---: |
| EN | ENo |
| s1 | d1 |
| s2 | d2 |

## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S.BUMP | $\square$ |

## Setting data

## Description, range, data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| (s1) | Input data start device | Refer to "Input data". | Word |
| (d1) | Block memory start device | Refer to "Block memory". | Single-precision real <br> number |
| (s2) | Operation constant start device | Refer to "Operation constant". | Word |
| (d2) | Local work memory start device | Refer to "Local work memory". | Word |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T, ST, C, D, W, SD, SW, R, ZR, RD | U밈, J밈, U3EDI(H)GD | Z | LT, LST, LC | LZ |  | K, H | E | \$ |  |
| (s1) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (d1) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (s2) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (d2) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |

## Input data

| Operand (s1) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| $\begin{aligned} & +0 \\ & +1 \end{aligned}$ | E1 | Output set value | -999999 to 999999 [\%] | Single-precision real number | - | User |
| $\begin{aligned} & +2 \\ & +3 \end{aligned}$ | E2 | Output control value | -999999 to 999999 [\%] | Single-precision real number | - | User |
| +4 | e1 | Mode switching signal | 0: Manual mode <br> 1: Automatic mode | 16-bit unsigned binary | - | User |

Block memory

| Operand (d1) |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Position | Symbol | Name | Recommended range | Data type | Standard <br> value | Set by |
| +0 <br> +1 | BW | Output value | -999999 to $999999[\%]$ | Single-precision <br> real number | - |  |

OPeration constant

| Operand: (s2) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| $\begin{aligned} & +0 \\ & +1 \end{aligned}$ | T | Lag time | 0 to 999999 [s] | Single-precision real number | 1.0 | User |
| $\begin{aligned} & +2 \\ & +3 \end{aligned}$ | a | Lag band | 0 to 999999 [\%] | Single-precision real number | 1.0 | User |

## Local work memory

The system uses this area as a work area.
To start the control from the initial status, clear data by using a sequence program.

| Operand: (d2) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| $\begin{aligned} & +0 \\ & +1 \end{aligned}$ | Xq | Initial deviation value | - | Single-precision real number | 1.0 | System |
| $\begin{aligned} & +2 \\ & +3 \end{aligned}$ | Xp | Deviation |  |  |  |  |

## Execution cycle ( $\Delta \mathrm{T}$ )

Set the execution cycle ( $\Delta \mathrm{T}$ ) in SD816 and SD817 in single-precision real number.

## Processing details

This instruction gradually brings the output value (BW) closer to the output set value (E1) from the output control value (E2) when the operation mode changes from manual to automatic.
When the output value (BW) enters the range specified by the lag band (a) on the basis of the output set value (E1), the instruction brings it closer to the output set value (E1) with a primary lag.
In manual mode (mode switching signal $(e 1)=0$ ), the instruction calculates the output value $(B W)$, initial deviation value $(X q)$, and deviation (Xp) from the following expressions.

- Output value (BW) = output control value (E2)
- Initial deviation value (Xq) = output control value (E2) - output set value (E1)
- Deviation $(\mathrm{Xp})=$ output control value (E2) - output set value (E1)

In automatic mode (mode switching signal $(e 1)=1$ ), the instruction calculates the output value (BW) from the following expressions.

| Condition | \|Xp|>a | \|Xp|<a |
| :---: | :---: | :---: |
| Xp | $X p=X p^{\prime}-\frac{\Delta T}{T} X q$ | $X p=\frac{T}{T+\triangle T} X p^{\prime}$ |
| BW | $\mathrm{BW}=\mathrm{E} 1+\mathrm{Xp}$ <br> Provided that $\|X p\|<=\frac{\Delta T}{T}\|X q\|$ <br> - $\mathrm{BW}=\mathrm{E} 1$ <br> - $\mathrm{Xp}=\mathrm{Xp}{ }^{\prime}$ | $B W=E 1+X p$ <br> provided that $\|\mathrm{Xp}\| \leq 10^{-4}$ <br> - BW=E1 <br> - $\mathrm{Xp}=\mathrm{Xp}$ ' |

 $(X p)=X p$.

## Operation error

| Error code | Error content |
| :--- | :--- |
| 3400 H | An invalid operation (such as division by zero) is performed. |
| 3402 H | The value specified by (s1), (s2), or (d2) is a subnormal number or NaN (not a number). |
| 3403 H | An overflow has occurred. |

## Analog memory

## S．AMR

This instruction increases or decreases the output value（BW）at a fixed rate．


FBD／LD

| ■－－－ |  |
| :---: | :---: |
| EN | ENO |
| s1 | d1 |
| s2 |  |
| s3 |  |

Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．AMR | $\square$ |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| （s1） | Input data start device | Refer to＂Input data＂． | Word |
| （d1） | Block memory start device | Refer to＂Block memory＂． | Single－precision real <br> number |
| （s2） | Operation constant start device | Refer to＂Operation constant＂． | Word |
| （s3） | Empty string specification <br> （Specify＂＂in ladder．Specify＂in ST or FBD／LD．） | - | String |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，R，ZR，RD | UロIGロ，JロIロ， U3E미（H）Gロ | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s3） | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － |

## Input data

| Operand: (s1) |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Symbol | Name | Recommended range |  |  |  |  |  | Data type | Standard value | Set by |
| $\begin{aligned} & +0 \\ & +1 \end{aligned}$ | E1 | Output addition value | -999999 to 999999 |  |  |  |  |  | Single-precision real number | - | User |
| $\begin{aligned} & +2 \\ & +3 \end{aligned}$ | E2 | Output subtraction value | -999999 to 999999 |  |  |  |  |  | Single-precision real number | - | User |
| $\begin{aligned} & +4 \\ & +5 \end{aligned}$ | E3 | Output set value | -999999 to 999999 |  |  |  |  |  | Single-precision real number | - | User |
| +6 | e1 e2 e3 | Operation output signal <br> Output addition signal <br> Output subtraction signal | e1 <br> 0 : Manual mode <br> 1: Automatic mode <br> e2 <br> 0: Do not add <br> 1: Add <br> e3 <br> 0 : Do not subtract <br> 1: Subtract |  |  |  |  |  | 16-bit unsigned binary | - | User |

## Block memory

| Operand: (d1) | Recommended range | Data type | Standard <br> value | Set by |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Symbol | Name | Output value | -999999 to 999999 | Single-precision <br> real number | - |
| +0 | BW |  |  | System |  |  |

## Operation constant

| Operand: (s2) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| $\begin{aligned} & +0 \\ & +1 \end{aligned}$ | d1 | Output upper limit value | 0 to 999999 | Single-precision real number | 1.0 | User |
| $\begin{aligned} & +2 \\ & +3 \end{aligned}$ | d2 | Output lower limit value | 0 to 999999 | Single-precision real number | 1.0 | User |

## Execution cycle ( $\Delta \mathrm{T}$ )

Set the execution cycle $(\Delta T)$ in SD816 and SD817 in single-precision real number.

## Processing details

This instruction increases or decreases the output value (BW) at a fixed rate.


The instruction performs the following processing as specified by operation output signal (e1), output addition signal (e2), and output subtraction signal (e3).

- In manual mode (operation output signal $(\mathrm{e} 1)=0$ ), the output value $(B W)$ equals the output set value (E3).
- In automatic mode (operation output signal (e1)=1), the instruction performs the following operations as specified by the output addition signal (e2) and output subtraction signal (e3).

| e2 | e3 | BW |
| :--- | :--- | :--- |
| 1 | 0 | $\mathrm{BW}=\mathrm{BW}+\|\mathrm{E} 1\| \times \Delta \mathrm{T}$ <br> provided that when $\mathrm{d} 1 \leq \mathrm{BW}, \mathrm{BW}$ equals d 1. |
| 0 | 1 | $\mathrm{BW}=\mathrm{BW}-\|\mathrm{E} 2\| \times \Delta \mathrm{T}$ <br> provided that when $\mathrm{BW} \leq \mathrm{d} 2, \mathrm{BW}$ equals d 2. |
| 1 | 0 | $B W=B W$ |
| 0 | 1 |  |

## Operation error

| Error code | Error content |
| :--- | :--- |
| 3402 H | The value specified by (s1) or (s2) is a subnormal number or NaN (not a number). |
| 3403 H | An overflow has occurred. |

## 10．4 Correction Operation Instructions

## Function generator

## S．FG

This instruction outputs the input data values following the specified function generator pattern．


■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．FG | $-\square$ |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| （s1） | Input data start device | Refer to＂Input data＂． | Single－precision real <br> number |
| （d1） | Block memory start device | Refer to＂Block memory＂． | Single－precision real <br> number |
| （s2） | Operation constant start device | Refer to＂Operation constant＂． | 16－bit unsigned binary |
| （d2） | Local work memory start device | Refer to＂Local work memory＂． | Word |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## ■Input data

| Operand：（s1） | Recommended range |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Symbol | Name | Data type | Standard <br> value | Set by |
| +0 | E1 | Input value | -999999 to 999999 | Single－precision <br> real number | - |

Block memory

| Operand: (d1) |  | Recommended range | Data type | Standard <br> value | Set by |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Symbol | Name | Output value | -999999 to 999999 | Single-precision <br> real number | - |
| +0 | BW |  |  |  |  |  |
| +1 |  |  |  |  |  |  |

OPeration constant

| Operand: (s2) | Recommended range | Data type | Standard <br> value | Set by |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Symbol | Name | 16 -bit unsigned <br> binary | 0 |
| +0 | SN | Number of <br> break points | 0 to 48 | User |

Local work memory

| Operand: (d2) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| $\begin{aligned} & +0 \\ & +1 \end{aligned}$ | X1 | Break point coordinates | -999999 to 999999 | Single-precision real number | - | User |
| $\begin{aligned} & +2 \\ & +3 \end{aligned}$ | Y1 | Break point coordinates |  |  |  |  |
| $\begin{aligned} & +4 \\ & +5 \end{aligned}$ | X2 | Break point coordinates |  |  |  |  |
| $\begin{aligned} & +6 \\ & +7 \end{aligned}$ | Y2 | Break point coordinates |  |  |  |  |
| ! | ! | ! |  |  |  |  |
| $\begin{aligned} & +4 \mathrm{SN}-4 \\ & +4 \mathrm{SN}-3 \end{aligned}$ | Xn | Break point coordinates |  |  |  |  |
| $\begin{aligned} & +4 \mathrm{SN}-2 \\ & +4 \mathrm{SN}-1 \end{aligned}$ | Yn | Break point coordinates |  |  |  |  |

## Processing details

This instruction outputs values according to the function generator pattern consisting of $n$ break points as specified by (s2) with regard to the input value (E1).


The S.FG instruction performs the following operations.

| Condition | Output value (BW) |
| :--- | :--- |
| $E 1 \leq X 1$ | $B W=Y 1$ |
| $X_{i-1}<E 1 \leq X_{i}(i=2$ to $n)$ | $B W=\frac{Y_{i}-Y_{i-1}}{X_{i}-X_{i-1}} \times\left(E 1-X_{i-1}\right)+Y_{i-1}$ |
| $X_{n}<E 1$ | $B W=Y_{n}$ |

- If the value specified in ( n ) is 0 , no processing is performed.
- If $X_{i-1}>X_{i}$, processing is stopped when $n=i-1$. (The subsequent data is ignored.)
- When there are two or more $X_{i}$ for the same $Y_{i}$, the $Y$ with smaller $i$ is selected.


## Operation error

| Error code | Error content |
| :--- | :--- |
| 3400 H | An invalid operation (such as division by zero) is performed. |
| 3402 H | The value specified by (s1) or (d2) is a subnormal number or NaN (not a number). |
| 3403 H | An overflow has occurred. |
| 3405 H | The number of break points (SN) is less than 0 or greater than 48. |

## Inverse function generator

## S．IFG

This instruction outputs the input data values following the specified inverse function generator pattern．


FBD／LD

| ■－二－$]$ |  |
| :---: | :---: |
| En | eno |
| s1 | d1 |
| s2 | d2 |

Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．IFG | $\square$ |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| （s1） | Input data start device | Refer to＂Input data＂． | Single－precision real <br> number |
| （d1） | Block memory start device | Refer to＂Block memory＂． | Single－precision real <br> number |
| （s2） | Operation constant start device | Refer to＂Operation constant＂． | 16－bit unsigned binary |
| （d2） | Local work memory start device | Refer to＂Local work memory＂． | Word |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，R，ZR，RD | UロIGロ，JロIロ， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## －lnput data

| Operand：（s1） | Recommended range | Data type | Standard <br> value | Set by |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Symbol | Name | -999999 to 999999 | Single－precision <br> real number | - |
| +0 | E1 | Input value | User |  |  |
| 1 |  |  |  |  |  |

## Block memory

| Operand：（d1） |  |  |  |  |  |  | Recommended range |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Symbol | Name | Data type | Standard <br> value | Set by |  |  |
| +0 | BW | Output value | -999999 to 999999 | Single－precision <br> real number | - |  |  |

Operation constant

| Operand: (s2) | Recommended range | Data type | Standard <br> value | Set by |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Symbol | Name | 16 -bit unsigned <br> binary | 0 |
| +0 | SN | Number of <br> break points | 0 to 48 | User |

## Local work memory

| Operand: (d2) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| $\begin{aligned} & \hline+0 \\ & +1 \end{aligned}$ | X1 | Break point coordinates | -999999 to 999999 | Single-precision real number | - | User |
| $\begin{aligned} & +2 \\ & +3 \end{aligned}$ | Y1 | Break point coordinates |  |  |  |  |
| $\begin{aligned} & +4 \\ & +5 \end{aligned}$ | X2 | Break point coordinates |  |  |  |  |
| $\begin{aligned} & +6 \\ & +7 \end{aligned}$ | Y2 | Break point coordinates |  |  |  |  |
| ! | : | ! |  |  |  |  |
| $\begin{aligned} & +4 \mathrm{SN}-4 \\ & +4 \mathrm{SN}-3 \end{aligned}$ | Xn | Break point coordinates |  |  |  |  |
| $\begin{aligned} & +4 \mathrm{SN}-2 \\ & +4 \mathrm{SN}-1 \end{aligned}$ | Yn | Break point coordinates |  |  |  |  |

## Processing details

This instruction outputs values according to the inverse function generator pattern consisting of $n$ break points as specified by (s2) with regard to the input value (E1).


The S.IFG instruction performs the following operations.

| Condition | Output value (BW) |
| :--- | :--- |
| $E 1 \leq Y 1$ | $B W=X 1$ |
| $Y_{i-1}<E 1 \leq Y_{i}(i=2$ to $n)$ | $B W=\frac{X_{i}-X_{i-1}}{Y_{i}-Y_{i-1}} \times\left(E 1-Y_{i-1}\right)+X_{i-1}$ |
| $Y_{n}<E 1$ | $B W=X_{n}$ |

- If the value specified in ( $n$ ) is 0 , no processing is performed.
- If $X_{i-1}>X_{i}$, processing is stopped when $n=i-1$. (The subsequent data is ignored.)
- When there are two or more $X_{i}$ for the same $Y_{i}$, the $X$ with smaller $i$ is selected.


## Operation error

| Error code | Error content |
| :--- | :--- |
| 3400 H | An invalid operation (such as division by zero) is performed. |
| 3402 H | The value specified by (s1) or (d2) is a subnormal number or NaN (not a number). |
| 3403 H | An overflow has occurred. |
| 3405 H | The number of break points (SN) is less than 0 or greater than 48. |

## Standard filter

## S．FLT

This function outputs the mean value of the n pieces of data sampled at the specified data collection intervals（ST）．


FBD／LD

| ［－－－］ |  |
| :---: | :---: |
| EN | ENO |
| s1 | d1 |
| s2 | d2 |

Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．FLT | $\square$ |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| （s1） | Input data start device | Refer to＂Input data＂． | Single－precision real <br> number |
| （d1） | Block memory start device | Refer to＂Block memory＂． | Word |
| （s2） | Operation constant start device | Refer to＂Operation constant＂． | Word |
| （d2） | Local work memory start device | Refer to＂Local work memory＂． | Word |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，R，ZR，RD | U밈，J밈， U3EDl（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Input data

| Operand：（s1） | Recommended range | Data type | Standard <br> value | Set by |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Symbol | Name | Input value | -999999 to 999999 | Single－precision <br> real number | - |
| +0 | E1 |  |  | User |  |  |

Block memory


Operation constant

| Operand: (s2) | Recommended range | Data type | Standard <br> value | Set by |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Symbol | Name | Data sampling <br> interval <br> +1 | ST | 0 to $999999[\mathrm{~s}]$ | Single-precision <br> real number | 1.0 |
| +2 | Provided that $\frac{\mathrm{ST}}{\triangle T}<=32767$ | User |  |  |  |  |  |
| +2 | Sampling count | 0 to 48 | 16 -bit unsigned <br> binary | 0 |  |  |  |

## Local work memory

The system uses this area as a work area.
To start the control from the initial status, clear data by using a sequence program.

| Operand: (d2) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| $\begin{aligned} & +0 \\ & +1 \end{aligned}$ | ST' | Last data sampling interval | - | Single-precision real number | - | System |
| +2 | SN' | Last sampling count |  | 16-bit unsigned binary |  | System |
| +3 | i | Cycle counter*1 |  | 16-bit unsigned binary |  | System |
| +4 | n1 | Number of data blocks stored |  | 16-bit unsigned binary |  | System |
| +5 | n2 | Storage address |  | 16-bit unsigned binary |  | System |
| $\begin{aligned} & +6 \\ & +7 \end{aligned}$ | - | - | - | - | - | - |
| $\begin{aligned} & +8 \\ & +9 \end{aligned}$ | 1 | Dead time table 1 | - | Single-precision real number | - | System |
| $\begin{aligned} & +10 \\ & +11 \end{aligned}$ | 2 | Dead time table 2 |  |  |  |  |
| ! | $\vdots$ | $\vdots$ |  |  |  |  |
| $\begin{aligned} & +2 \mathrm{SN}+6 \\ & +2 \mathrm{SN}+7 \end{aligned}$ | SN | Dead time table SN |  |  |  |  |

*1 The cycle counter value is rounded off to the nearest whole number.

## Execution cycle ( $\Delta \mathrm{T}$ )

Set the execution cycle ( $\Delta \mathrm{T}$ ) in SD816 and SD817 in single-precision real number.

## Processing details

This instruction samples input values (E1) at data sampling intervals (ST) by the number of samples (SN), stores them in the dead time table, averages the number of collected data blocks (SN), and outputs the result.

- The data refreshing cycle is "data sampling interval (ST) $\div$ execution cycle $(\Delta T)$ ". (The resultant value is rounded off to the nearest whole number.)
- When the dead time table is filled with the number of sampled data blocks (SN), the data sufficiency bit (BB1) is set to 0 . When the dead time table does not have sufficient data, BB1 is set to 1 .


## Point ${ }^{\rho}$

- Until the dead time table is filled with data, the past data is averaged and output.
- Processing is performed by $\mathrm{ST}=\mathrm{n} \times \Delta \mathrm{T}$ ( n is an integer).


## Operation error

| Error code | Error content |
| :--- | :--- |
| 3400 H | An invalid operation (such as division by zero) is performed. |
| 3402 H | The value specified by (s1), (s2), or (d2) is a subnormal number or NaN (not a number). |
| 3403 H | An overflow has occurred. |
|  | The sampling count $(\mathrm{SN})$ is less than 0 or greater than 48. |
|  | The execution cycle ( $\Delta \mathrm{T})$ setting is less than 0. |
|  | The data sampling interval (ST) is less than 0. |
|  | (Data sampling interval $(\mathrm{ST}) \div$ execution cycle $(\Delta \mathrm{T})$ ) is greater than 32767. |

## Integration

## S.SUM

This instruction integrates and outputs the input data.


FBD/LD

| ■--- |  |
| :---: | :---: |
| EN | ENO |
| s1 | d1 |
| s2 |  |
| s3 |  |

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S.SUM | - |

## Setting data

■Description, range, data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| (s1) | Input data start device | Refer to "Input data". | Word |
| (d1) | Block memory start device | Refer to "Block memory". | Single-precision real <br> number |
| (s2) | Operation constant start device | Refer to "Operation constant". | Word |
| (s3) | Empty string specification <br> (Specify "" in ladder. Specify " in ST or FBD/LD.) | - | String |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | J밈 | T, ST, C, D, W, SD, SW, R, ZR, RD | UपIGロ, JपIロ, U3EDI(H)GD | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (s1) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (d1) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (s2) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (s3) | - | - | - | - | - | - | - | - | - | - | $\bigcirc$ | - |

## Input data



Block memory

| Operand: (d1) |  | Recommended range | Data type | Standard <br> value | Set by |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Symbol | Name | Output value | -999999 to 999999 | Single-precision <br> real number | - |
| $+0+1$ | BW | System |  |  |  |  |

## Operation constant

| Operand: (s2) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| $\begin{aligned} & +0 \\ & +1 \end{aligned}$ | ILC | Input low-cut value | -999999 to 999999 | Single-precision real number | 0.0 | User |
| $\begin{aligned} & \hline+2 \\ & +3 \end{aligned}$ | A | Initial value | -999999 to 999999 | Single-precision real number | 0.0 | User |
| +4 | RANGE | Input range | 1:/second <br> 2: /minute <br> 3:/hour | 16-bit unsigned binary | 1 | User |

## Execution cycle ( $\Delta \mathrm{T}$ )

Set the execution cycle ( $\Delta \mathrm{T}$ ) in SD816 and SD817 in single-precision real number.

## Processing details

When the integration start signal (e1) changes from 0 to 1 , this instruction integrates the input values (E1) and outputs the result.
The S.SUM instruction performs the following operations.

| e1 | E1 | Output (BW) |
| :--- | :--- | :--- |
| 0 | - | Outputs the initial value $(A)$ of the operation constant. |
| 1 | E1 ILC | The last value is output as is. |
|  | E1>ILC | BW=E1 $\times \frac{\Delta T}{T}+$ Last value |

The value of $T$ used for operation varies depending on the setting of the input range (RANGE).

- Input range (RANGE) $=1: \mathrm{T}=1$
- Input range (RANGE) $=2: T=60$
- Input range (RANGE) = 3: T = 3600


## Operation error

| Error code | Error content |
| :--- | :--- |
| 3400 H | An invalid operation (such as division by zero) is performed. |
| 3402 H | The value specified by (s1), (d1), or (s2) is a subnormal number or NaN (not a number). |
| 3403 H | An overflow has occurred. |
| 3405 H | Input range (RANGE) is less than 1 or greater than 3. |

## Temperature／pressure correction

## S．TPC

This instruction outputs input data after temperature／pressure correction．


FBD／LD

| ■－－－ |  |
| :---: | :---: |
| EN | ENO |
| s1 | d1 |
| s2 |  |
| s3 |  |

Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．TPC | $\square$ |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| （s1） | Input data start device | Refer to＂Input data＂． | Word |
| （d1） | Block memory start device | Refer to＂Block memory＂． | Single－precision real <br> number |
| （s2） | Operation constant start device | Refer to＂Operation constant＂． | Word |
| （s3） | Empty string specification <br> （Specify＂＂in ladder．Specify＂in ST or FBD／LD．） | - | String |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，R，ZR，RD | UपIGロ，JロID， U3EDl（H）GD | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s3） | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － |

## Input data



Block memory

| Operand: (d1) |  | Recommended range | Data type | Standard <br> value | Set by |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Symbol | Name | Output value | -999999 to 999999 | Single-precision <br> real number | - |
| +0 | BW |  |  |  |  |  |
| +1 |  |  |  |  |  |  |

Operation constant

| Operand: (s2) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| $\begin{aligned} & +0 \\ & +1 \end{aligned}$ | TEMP | Designed temperature $\mathrm{T}^{\prime}$ (engineering value) | -999999 to $999999\left[{ }^{\circ} \mathrm{C}\right]$ | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +2 \\ & +3 \end{aligned}$ | B1 | Bias <br> (temperature) | -999999 to $999999\left[{ }^{\circ} \mathrm{C}\right]$ | Single-precision real number | 273.15 | User |
| $\begin{aligned} & +4 \\ & +5 \end{aligned}$ | PRES | Designed pressure $\mathrm{P}^{\prime}$ (engineering value) | -999999 to 999999 | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +6 \\ & +7 \end{aligned}$ | B2 | Bias (pressure) | -999999 to 999999 | Single-precision real number | 10332.0 | User |

## Processing details

This instruction outputs the input value (E1) after performing temperature or pressure correction over it.
The S.TPC instruction calculates the temperature/pressure correction value using the following expression.
$\mathrm{BW}=\mathrm{E} 1 \times \mathrm{A} 1 \times \mathrm{A} 2$
The values of A1 and A2 are calculated by the following expressions.

| Input |  | A1 | A2 |
| :--- | :--- | :--- | :--- |
| e1 | e2 |  |  |
| Used | Used | $\frac{T^{\prime}+\mathrm{B} 1}{\mathrm{E} 2+\mathrm{B} 1}$ | $\frac{\mathrm{E} 3+\mathrm{B} 2}{\mathrm{P}^{\prime}+\mathrm{B} 2}$ |
| Not used | Used | 1.0 | $\frac{\mathrm{E} 3+\mathrm{B} 2}{\mathrm{P}^{\prime}+\mathrm{B} 2}$ |
| Used | Not used | $\frac{\mathrm{T}^{\prime}+\mathrm{B} 1}{\mathrm{E} 2+\mathrm{B} 1}$ | 1.0 |
| Not used | Not used | 1.0 | 1.0 |

## Operation error

| Error code | Error content |
| :--- | :--- |
| 3400 H | An invalid operation (such as division by zero) is performed. |
| 3402 H | The value specified by (s1) or (s2) is a subnormal number or NaN (not a number). |
| 3403 H | An overflow has occurred. |

$1144^{10}$ PROCESS CONTROL NSTRUCTIONS
10.4 Correction Operation Instructions

## Engineering value transformation

## S．ENG

This instruction performs engineering value inverse transformation of the input data．


FBD／LD

| EN | ENO |
| :---: | :---: |
|  |  |
| s1 | d1 |
| s2 |  |
| s3 |  |

Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．ENG | $\square$ |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| （s1） | Input data start device | Refer to＂Input data＂． | Single－precision real <br> number |
| （d1） | Block memory start device | Refer to＂Block memory＂． | Single－precision real <br> number |
| （s2） | Operation constant start device | Refer to＂Operation constant＂． | Word |
| （s3） | Empty string specification <br> （Specify＂＂in ladder．Specify＂in ST or FBD／LD．） | - | String |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，R，ZR，RD | U밈，J밈， U3Eपl（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s3） | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － |

Input data

| Operand：（s1） |  | Recommended range | Data type | Standard <br> value | Set by |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Symbol | Name | -999999 to $999999[\%]$ | Single－precision <br> real number | - |  |
| +0 | E1 | Input value |  | User <br> +1 |  |  |

## Block memory

| Operand: (d1) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| +0 +1 | BW | Output value | -999999 to 999999 | Single-precision real number | - | System |

©Operation constant

| Operand: (s2) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| $\begin{aligned} & \hline+0 \\ & +1 \end{aligned}$ | RH | Engineering value upper limit | -999999 to 999999 | Single-precision real number | 100.0 | User |
| $\begin{aligned} & +2 \\ & +3 \end{aligned}$ | RL | Engineering value lower limit | -999999 to 999999 | Single-precision real number | 0.0 | User |

## Processing details

This instruction performs engineering value transformation of the input data (E1) and outputs it.



The S.ENG instruction performs the following operations.
$B W=\frac{R H-R L}{100} \times E 1+R L(E 1=0 \sim 100 \%)$

## Operation error

| Error code | Error content |
| :--- | :--- |
| 3400 H | An invalid operation (such as division by zero) is performed. |
| 3402 H | The value specified by (s1) or (s2) is a subnormal number or NaN (not a number). |
| 3403 H | An overflow has occurred. |

## Engineering value inverse transformation

## S．IENG

The instruction performs engineering value inverse transformation processing to the input data．


FBD／LD

| ■－－－ |  |
| :---: | :---: |
| EN | ENO |
| s1 | d1 |
| s2 |  |
| s3 |  |

Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．IENG | - |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| （s1） | Input data start device | Refer to＂Input data＂． | Single－precision real <br> number |
| （d1） | Block memory start device | Refer to＂Block memory＂． | Single－precision real <br> number |
| （s2） | Operation constant start device | Refer to＂Operation constant＂． | Word |
| （s3） | Empty string specification <br> （Specify＂＂in ladder．Specify＂in ST or FBD／LD．） | - | String |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，R，ZR，RD | UपIGロ，Jロ\ロ， U3EDl（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s3） | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － |

## Input data

| Operand：（s1） | Recommended range | Data type | Standard <br> value | Set by |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Symbol | Name | Input value | -999999 to 999999 | Single－precision <br> real number | - |
| $+0+1$ | E1 |  | User |  |  |  |

Block memory

| Operand: (d1) |  | Recommended range | Data type | Standard <br> value | Set by |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Symbol | Name | Output value | -999999 to $999999[\%]$ | Single-precision <br> real number | - |
| +0 | BW |  |  |  |  |  |
| +1 |  |  |  |  |  |  |

Operation constant

| Operand: (s2) |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Symbol | Name | Recommended range | Data type | Standard <br> value | Set by |
| +0 <br> +1 | RH | Engineering <br> value upper limit | -999999 to 999999 | Single-precision <br> real number | 100.0 | User |
| +2 | RL | Engineering <br> value lower limit | -999999 to 999999 | Single-precision <br> real number | 0.0 | User |

## Processing details

This instruction converts the input value (E1) to the \% value and outputs it.


The S.IENG instruction performs the following operations.

$$
B W=\frac{100}{R H-R L} \times(E 1-R L)[\%]
$$

Set the engineering value upper limit (RH) and lower limit (RL) so that RH is greater than RL.
Even when RH equals or less than RL, processing is executed accordingly but does not result in engineering value inverse transformation.
When RH equals RL, the output value (BW) becomes 0 .

## Operation error

| Error code | Error content |
| :--- | :--- |
| 3400 H | An invalid operation (such as division by zero) is performed. |
| 3402 H | The value specified by (s1) or (s2) is a subnormal number or NaN (not a number). |
| 3403 H | An overflow has occurred. |

## 10．5 Arithmetic Operation Instructions

## Addition

## S．ADD

This instruction adds input data with a coefficient．


FBD／LD

| ■－－－$\square$ |  |
| :---: | :---: |
| EN | ENO |
| s1 | d1 |
| s2 |  |
| s3 |  |

## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．ADD | $-\square$ |

## Setting data

DDescription，range，data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| （s1） | Input data start device | Refer to＂Input data＂． | Word |
| （d1） | Block memory start device | Refer to＂Block memory＂． | Single－precision real <br> number |
| （s2） | Operation constant start device | Refer to＂Operation constant＂． | Word |
| （s3） | Empty string specification <br> （Specify＂＂in ladder．Specify＂in ST or FBD／LD．） | - | String |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロロロ | T，ST，C，D，W，SD， SW，R，ZR，RD | UपIGロ，J미， U3EDl（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s3） | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － |

## Input data

| Operand: (s1) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| +0 | n | Number of inputs | 0 to 5 | 16-bit unsigned binary | - | User |
| $\begin{aligned} & +1 \\ & +2 \end{aligned}$ | E1 | Input value 1 | -999999 to 999999 | Single-precision real number | - | User |
| $\begin{aligned} & +3 \\ & +4 \end{aligned}$ | E2 | Input value 2 |  |  |  |  |
| ! | ! | ! |  |  |  |  |
| $\begin{aligned} & +2 n-1 \\ & +2 n \end{aligned}$ | En | Input value n |  |  |  |  |

## Block memory

| Operand: (d1) |  | Recommended range | Data type | Standard <br> value | Set by |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Symbol | Name | Output value | -999999 to 999999 | Single-precision <br> real number | - |
| +0 | BW |  |  |  |  |  |

## Operation constant

| Operand: (s2) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| +0 | n | Number of coefficients | 0 to 5 | 16-bit unsigned binary | 0 | User |
| $\begin{aligned} & +1 \\ & +2 \end{aligned}$ | K1 | Coefficient 1 | -999999 to 999999 | Single-precision real number | 1.0 | User |
| $\begin{aligned} & +3 \\ & +4 \end{aligned}$ | K2 | Coefficient 2 |  |  |  |  |
| ! | ! | $\vdots$ |  |  |  |  |
| $\begin{aligned} & +2 n-1 \\ & +2 n \end{aligned}$ | Kn | Coefficient n |  |  |  |  |
| $\begin{aligned} & +2 n+1 \\ & +2 n+2 \end{aligned}$ | B | Bias | -999999 to 999999 | Single-precision real number | 0.0 | User |

## Processing details

This instruction adds the data of input values (E1 to En) with a coefficient.
The S.ADD instruction performs the following operations.
$B W=(K 1 \times E 1)+(K 2 \times E 2) \ldots+(K n \times E n)+B$
When the number of inputs $(n)$ is 0 , the output value (BW) becomes bias (B).

## Operation error

| Error code | Error content |
| :--- | :--- |
| 3402 H | The value specified by $(\mathrm{s} 1)$ or $(\mathrm{s} 2)$ is a subnormal number or NaN (not a number). |
| 3403 H | An overflow has occurred. |
| 3405 H | The number of inputs $(\mathrm{n})$ is less than 0 or greater than 5. |
|  | The number of coefficients $(\mathrm{n})$ is less than 0 or greater than 5. |

## Subtraction

## S．SUB

This instruction subtracts input data with a coefficient．


FBD／LD

| ■－－－ |  |
| :---: | :---: |
| EN | ENO |
| s1 | d1 |
| s2 |  |
| s3 |  |

Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．SUB | $\square$ |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| （s1） | Input data start device | Refer to＂Input data＂． | Word |
| （d1） | Block memory start device | Refer to＂Block memory＂． | Single－precision real <br> number |
| （s2） | Operation constant start device | Refer to＂Operation constant＂． | Word |
| （s3） | Empty string specification <br> （Specify＂＂in ladder．Specify＂in ST or FBD／LD．） | - | String |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，R，ZR，RD | U밈，J밈， U3EDl（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s3） | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － |

## Input data

| Operand: (s1) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| +0 | n | Number of inputs | 0 to 5 | 16-bit unsigned binary | - | User |
| $\begin{aligned} & +1 \\ & +2 \end{aligned}$ | E1 | Input value 1 | -999999 to 999999 | Single-precision real number | - | User |
| $\begin{aligned} & +3 \\ & +4 \end{aligned}$ | E2 | Input value 2 |  |  |  |  |
| ! | ! | ! |  |  |  |  |
| $\begin{aligned} & +2 n-1 \\ & +2 n \end{aligned}$ | En | Input value n |  |  |  |  |

## Block memory

| Operand: (d1) |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Symbol | Name | Recommended range | Data type | Standard <br> value | Set by |
| +0 | BW | Output value | -999999 to 999999 | Single-precision <br> real number | - |  |

## Operation constant

| Operand: (s2) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| +0 | n | Number of coefficients | 0 to 5 | 16-bit unsigned binary | 0 | User |
| $\begin{aligned} & +1 \\ & +2 \end{aligned}$ | K1 | Coefficient 1 | -999999 to 999999 | Single-precision real number | 1.0 | User |
| $\begin{aligned} & +3 \\ & +4 \end{aligned}$ | K2 | Coefficient 2 |  |  |  |  |
| ! | ! | $\vdots$ |  |  |  |  |
| $\begin{aligned} & +2 n-1 \\ & +2 n \end{aligned}$ | Kn | Coefficient n |  |  |  |  |
| $\begin{aligned} & +2 n+1 \\ & +2 n+2 \end{aligned}$ | B | Bias | -999999 to 999999 | Single-precision real number | 0.0 | User |

## Processing details

This instruction performs subtraction of the data of input values (E1 to En) with a coefficient.
The S.SUB instruction performs the following operations.
BW=(K1×E1)-(K2×E2)...-(Kn $\times E n$ ) $+B$
When the number of inputs $(\mathrm{n})$ is 0 , the output value (BW) becomes bias (B).

## Operation error

| Error code | Error content |
| :--- | :--- |
| 3402 H | The value specified by $(\mathrm{s} 1)$ or $(\mathrm{s} 2)$ is a subnormal number or NaN (not a number). |
| 3403 H | An overflow has occurred. |
| 3405 H | The number of inputs $(\mathrm{n})$ is less than 0 or greater than 5. |
|  | The number of coefficients $(\mathrm{n})$ is less than 0 or greater than 5. |

## Multiplication

## S.MUL

This instruction multiplies input data with a coefficient.


FBD/LD

| ■--- |  |
| :---: | :---: |
| EN | ENO |
| s1 | d1 |
| s2 |  |
| s3 |  |

Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S.MUL | - |

## Setting data

Description, range, data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| (s1) | Input data start device | Refer to "Input data". | Word |
| (d1) | Block memory start device | Refer to "Block memory". | Single-precision real <br> number |
| (s2) | Operation constant start device | Refer to "Operation constant". | Word |
| (s3) | Empty string specification <br> (Specify "" in ladder. Specify " in ST or FBD/LD.) | - | String |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T, ST, C, D, W, SD, SW, R, ZR, RD | U밈, J밈, U3EDI(H)GD | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (s1) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (d1) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (s2) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (s3) | - | - | - | - | - | - | - | - | - | - | $\bigcirc$ | - |

## Input data

| Operand: (s1) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| +0 | n | Number of inputs | 0 to 5 | 16-bit unsigned binary | - | User |
| $\begin{aligned} & +1 \\ & +2 \end{aligned}$ | E1 | Input value 1 | -999999 to 999999 | Single-precision real number | - | User |
| $\begin{aligned} & +3 \\ & +4 \end{aligned}$ | E2 | Input value 2 |  |  |  |  |
| ! | ! | ! |  |  |  |  |
| $\begin{aligned} & +2 n-1 \\ & +2 n \end{aligned}$ | En | Input value n |  |  |  |  |

## Block memory

| Operand: (d1) |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Symbol | Name | Recommended range | Data type | Standard <br> value | Set by |
| +0 | BW | Output value | -999999 to 999999 | Single-precision <br> real number | - |  |

## Operation constant

| Operand: (s2) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| +0 | n | Number of coefficients | 0 to 5 | 16-bit unsigned binary | 0 | User |
| $\begin{aligned} & +1 \\ & +2 \end{aligned}$ | K1 | Coefficient 1 | -999999 to 999999 | Single-precision real number | 1.0 | User |
| $\begin{aligned} & +3 \\ & +4 \end{aligned}$ | K2 | Coefficient 2 |  |  |  |  |
| ! | ! | $\vdots$ |  |  |  |  |
| $\begin{aligned} & +2 n-1 \\ & +2 n \end{aligned}$ | Kn | Coefficient n |  |  |  |  |
| $\begin{aligned} & +2 n+1 \\ & +2 n+2 \end{aligned}$ | B | Bias | -999999 to 999999 | Single-precision real number | 0.0 | User |

## Processing details

This instruction performs multiplication of the data of input values (E1 to En) with a coefficient.
The S.MUL instruction performs the following operations.
$B W=(\mathrm{K} 1 \times \mathrm{E} 1) \times(\mathrm{K} 2 \times \mathrm{E} 2) \ldots \times(\mathrm{Kn} \times \mathrm{En})+\mathrm{B}$
When the number of inputs $(n)$ is 0 , the output value (BW) becomes bias (B).

## Operation error

| Error code | Error content |
| :--- | :--- |
| 3402 H | The value specified by $(\mathrm{s} 1)$ or $(\mathrm{s} 2)$ is a subnormal number or NaN (not a number). |
| 3403 H | An overflow has occurred. |
| 3405 H | The number of inputs $(\mathrm{n})$ is less than 0 or greater than 5. |
|  | The number of coefficients $(\mathrm{n})$ is less than 0 or greater than 5. |

## Division

## S．DIV

This instruction performs division of the input data with a coefficient．


FBD／LD

| ■－－－${ }^{-}$ |  |
| :---: | :---: |
| EN | ENO |
| s1 | d1 |
| s2 |  |
| s3 |  |

Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．DIV | - |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| （s1） | Input data start device | Refer to＂Input data＂． | Single－precision real <br> number |
| （d1） | Block memory start device | Refer to＂Block memory＂． | Single－precision real <br> number |
| （s2） | Operation constant start device | Refer to＂Operation constant＂． | Word |
| （s3） | Empty string specification <br> （Specify＂＂in ladder．Specify＂in ST or FBD／LD．） | - | String |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s3） | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － |

## Input data

| Operand：（s1） |  | Recommended range |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Symbol | Name | Sata type <br> value | Set by |  |
| +0 | E1 | Input value 1 <br> （numerator） | -999999 to 999999 | Single－precision <br> real number | - |
| +2 | E2 | Input value 2 <br> （denominator） | -999999 to 999999 | Single－precision <br> real number | - |

Block memory

| Operand: (d1) | Recommended range | Data type | Standard <br> value | Set by |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Symbol | Name | Output value | -999999 to 999999 | Single-precision <br> real number | - |
| +0 | BW |  |  | System |  |  |

## Operation constant

| Operand: (s2) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| $\begin{aligned} & +0 \\ & +1 \end{aligned}$ | A | Coefficient 1 | -999999 to 999999 | Single-precision real number | 1.0 | User |
| $\begin{aligned} & \hline+2 \\ & +3 \end{aligned}$ | K1 | Coefficient 2 | -999999 to 999999 | Single-precision real number | 1.0 | User |
| $\begin{aligned} & +4 \\ & +5 \end{aligned}$ | K2 | Coefficient 3 | -999999 to 999999 | Single-precision real number | 1.0 | User |
| $\begin{aligned} & +6 \\ & +7 \end{aligned}$ | B1 | Bias 1 | -999999 to 999999 | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +8 \\ & +9 \end{aligned}$ | B2 | Bias 2 | -999999 to 999999 | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +10 \\ & +11 \end{aligned}$ | B3 | Bias 3 | -999999 to 999999 | Single-precision real number | 0.0 | User |

## Processing details

This instruction divides the input value 1 (E1) by the input value 2 (E2).
The S.DIV instruction performs the following operations.
$B W=A \times \frac{\mathrm{K} 1 \times \mathrm{E} 1+\mathrm{B} 1}{\mathrm{~K} 2 \times \mathrm{E} 2+\mathrm{B} 2}+\mathrm{B} 3$
When the denominator (efficient 2 (K2)×input value 2 (numerator) (E2) + bias 2 (B2)) is 0 , the output value (BW) becomes bias 3 (B3).

## Operation error

| Error code | Error content |
| :--- | :--- |
| 3400 H | An invalid operation (such as division by zero) is performed. |
| 3402 H | The value specified by (s1) or (s2) is a subnormal number or NaN (not a number). |
| 3403 H | An overflow has occurred. |

## Square root

## S．SQR

This instruction outputs the square root $(\sqrt{ })$ of input data．

| Ladder |  |  |  |  | ST |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| ■-—- |  |  |  |  |  | NO：＝S＿SQR（EN，s1，s2，s3，d1）； |
|  | （s1） | （d1） | （s2） | （s |  |  |

FBD／LD

| ■－－－ |  |
| :---: | :---: |
| EN | ENO |
| s1 | d1 |
| s2 |  |
| s3 |  |

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．SQR | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| （s1） | Input data start device | Refer to＂Input data＂． | Single－precision real <br> number |
| （d1） | Block memory start device | Refer to＂Block memory＂． | Single－precision real <br> number |
| （s2） | Operation constant start device | Refer to＂Operation constant＂． | Word |
| （s3） | Empty string specification <br> （Specify＂＂in ladder．Specify＂in ST or FBD／LD．） | - | String |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，R，ZR，RD | UपIGロ，Jロ\ロ， U3EDl（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s3） | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － |

## Input data

| Operand：（s1） | Recommended range | Data type | Standard <br> value | Set by |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Symbol | Name | Single－precision <br> real number | - |
| +0 | E1 | Input value | 0 to 999999 | User |
| 1 |  |  |  |  |

Block memory

| Operand: (d1) | Recommended range | Data type | Standard <br> value | Set by |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Symbol | Name | Output value | 0 to 999999 | Single-precision <br> real number | - |
| $+0+1$ | BW |  |  |  |  |  |

## Operation constant

| Operand: (s2) |  | Recommended range | Data type | Standard <br> value | Set by |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Symbol | Name | Single-precision <br> real number | 0.0 |  |  |
| +0 | OLC | Output low-cut <br> value | 0 to 999999 | User <br> Single-precision <br> real number | 1.0 | User |
| +2 | K | Coefficient | 0 to 999999 |  |  |  |

## Processing details

This instruction outputs $\sqrt{ }$ of the input value (E1). When the input value ( E 1 ) is less than 0,0 is output. The S.SQR instruction performs the following operations.
$B W=K \times \sqrt{(E 1)}$
In the following case, however, the output value (BW) becomes 0 .
Coefficient $(\mathrm{K}) \times \sqrt{\text { Input value ( } \mathrm{E} 1)}<==$ Output low cut value (OLC)

## Operation error

| Error code | Error content |
| :--- | :--- |
| 3402 H | The value specified by (s1) or (s2) is a subnormal number or NaN (not a number). |
| 3403 H | An overflow has occurred. |

## Absolute value

## S．ABS

This instruction outputs the absolute value of input data．


FBD／LD

| ■－－－ |  |
| :---: | :---: |
| EN | ENO |
| s1 | d1 |
| s2 |  |
| s3 |  |

Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．ABS | $\square$ |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| （s1） | Input data start device | Refer to＂Input data＂． | Single－precision real <br> number |
| （d1） | Block memory start device | Refer to＂Block memory＂． | Word |
| （s2） | Empty string specification <br> （Specify＂＂in ladder．Specify＂in ST or FBD／LD．） | String |  |
| （s3） | Empty string specification <br> （Specify＂＂in ladder．Specify＂in ST or FBD／LD．） | - | String |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，R，ZR，RD | UपIGロ，Jロ\ロ， U3Eपl（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － |
| （s3） | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － |

## Input data

| Operand：（s1） | Recommended range |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Symbol | Name | Data type | Standard <br> value | Set by |
| +0 | E1 | Input value | -999999 to 999999 | Single－precision <br> real number | - |

Block memory


## Processing details

This instruction outputs the absolute value of the input value (E1).



The S.ABS instruction performs the following operations.
$B W=|E 1|$
The instruction determines the sign of the input value (E1) and outputs the result to the sign determination bits (BB1 and BB2) of the input value (E1).

| E1 status | BB1 | BB2 |
| :--- | :--- | :--- |
| E1>0 | 1 | 0 |
| E1 $<0$ | 0 | 1 |
| E1 $=0$ | 0 | 0 |

## Operation error

| Error code | Error content |
| :--- | :--- |
| 3402 H | (s1) is a subnormal number or NaN (not a number). |

## 10．6 Comparison Operation Instructions

## Comparing data

## S．＞

This instruction compares input data，and outputs the comparison result．


FBD／LD

| ■－二－$\square$ |  |
| :---: | :---: |
| EN | Eno |
| s1 | d1 |
| s2 |  |
| s3 |  |

（ $\square$ is to be replaced by S＿GT．）

## －Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．＞ | $\square$ |

## Setting data

## ■Description，range，data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| （s1） | Input data start device | Refer to＂Input data＂． | Word |
| （d1） | Block memory start device | Refer to＂Block memory＂． | Word |
| （s2） | Operation constant start device | Refer to＂Operation constant＂． | Word |
| （s3） | Empty string specification <br> （Specify＂＂in ladder．Specify＂in ST or FBD／LD．） | String |  |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | Jロום | T，ST，C，D，W，SD， SW，R，ZR，RD | U밈，J밈， U3EDI（H）Gロ | z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s3） | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － |

## Input data

| Operand：（s1） |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| $\begin{aligned} & +0 \\ & +1 \end{aligned}$ | E1 | Input value 1 | －999999 to 999999 | Single－precision real number | － | User |
| $\begin{aligned} & +2 \\ & +3 \end{aligned}$ | E2 | Input value 2 | －999999 to 999999 | Single－precision real number | － | User |

Block memory


■Operation constant

| Operand: (s2) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| $\begin{aligned} & +0 \\ & +1 \end{aligned}$ | K | Set value | -999999 to 999999 | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +2 \\ & +3 \end{aligned}$ | HS | Hysteresis | 0 to 999999 | Single-precision real number | 0.0 | User |

## Processing details

This instruction compares input values 1 (E1) and 2 (E2) and outputs the result (d1) to the comparison output (BB1).

| Condition | BB1 |
| :--- | :--- |
| $E 1>(E 2+K)$ | 1 |
| $E 1 \leq(E 2+K-H S)$ | 0 |
| $(E 2+K-H S)<E 1 \leq(E 2+K)$ | Last value is output. |

## Operation error

| Error code | Error content |
| :--- | :--- |
| 3402 H | The value specified by (s1) or (s2) is a subnormal number or NaN (not a number). |
| 3403 H | An overflow has occurred. |
| 3405 H | Hysteresis (HS) is less than 0. |

## S.<

This instruction compares input data, and outputs the comparison result.


|  |  |
| :---: | :---: |
| EN | ENO |
| s1 | d1 |
| s2 |  |
| s3 |  |

( $\square$ is to be replaced by S_LT.)

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S. $<$ | - |

Setting data
-Description, range, data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| (s1) | Input data start device | Refer to "Input data". | Word |
| (d1) | Block memory start device | Refer to "Block memory". | Word |
| (s2) | Operation constant start device | Refer to "Operation constant". | Word |
| (s3) | Empty string specification <br> (Specify "" in ladder. Specify " in ST or FBD/LD.) | - | String |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | J밈 | T, ST, C, D, W, SD, SW, R, ZR, RD | U밈, J밈, U3EDI(H)G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (s1) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (d1) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (s2) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (s3) | - | - | - | - | - | - | - | - | - | - | $\bigcirc$ | - |

## Input data

| Operand: (s1) |  | Symbol | Name | Recommended range | Data type | Standard <br> value | Set by |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Device | E1 | Input value 1 | -999999 to 999999 | Single-precision <br> real number | - |  |  |
| +0 |  | E2 | Input value 2 | -999999 to 999999 | Single-precision <br> real number | - |  |
| +2 | +3 |  |  |  | User |  |  |

Block memory


## Operation constant

| Operand: (s2) |  | Symbol | Name | Recommended range | Data type | Standard <br> value |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Set by |  |  |  |  |  |
| +0 | K | Set value | -999999 to 999999 | Single-precision <br> real number | 0.0 | User |
| +2 | HS | Hysteresis | 0 to 999999 | Single-precision <br> real number | 0.0 | User |

## Processing details

This instruction compares input values 1 (E1) and 2 (E2) and outputs the result (d1) to the comparison output (BB1).

| Condition | BB1 |
| :--- | :--- |
| $\mathrm{E} 1<(\mathrm{E} 2+\mathrm{K})$ | 1 |
| $\mathrm{E} 1 \geq(\mathrm{E} 2+\mathrm{K}+\mathrm{HS})$ | 0 |
| $(\mathrm{E} 2+\mathrm{K}) \leq \mathrm{E} 1<(\mathrm{E} 2+\mathrm{K}+\mathrm{HS})$ | Last value is output. |

## Operation error

| Error code | Error content |
| :--- | :--- |
| 3402 H | The value specified by（s1）or（s2）is a subnormal number or NaN（not a number）． |
| 3403 H | An overflow has occurred． |
| 3405 H | Hysteresis（HS）is less than 0. |

## S．＝

This instruction compares input data，and outputs the comparison result．


FBD／LD

（ $\square$ is to be replaced by $S_{-} E Q$ ．）

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．$=$ | $-\square$ |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| （s1） | Input data start device | Refer to＂Input data＂． | Word |
| （d1） | Block memory start device | Refer to＂Block memory＂． | Word |
| （s2） | Operation constant start device | Refer to＂Operation constant＂． | Single－precision real <br> number |
| （s3） | Empty string specification <br> （Specify＂＂in ladder．Specify＂in ST or FBD／LD．） | String |  |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）GD | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s3） | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － |

## Input data

| Operand: (s1) |  | Recommended range | Data type | Standard <br> value | Set by |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Symbol | Name | Input value 1 | -999999 to 999999 | Single-precision <br> real number | - |
| $+0+1$ | E1 | Input value 2 | -999999 to 999999 | Single-precision <br> real number | - | User |
| $+2+3$ | E2 |  |  |  |  |  |

Block memory


## Operation constant

| Operand: (s2) | Recommended range | Data type | Standard <br> value | Set by <br> Device Symbol | Name | -999999 to 999999 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| +0 | K | Set value | Single-precision <br> real number | 0.0 |  |  |
| 1 |  |  |  |  |  |  |

## Processing details

This instruction compares input values 1 (E1) and 2 (E2) and outputs the result (d1) to the comparison output (BB1).

| Condition | BB1 |
| :--- | :--- |
| $E 1=(E 2+K)$ | 1 |
| $E 1 \neq(E 2+K)$ | 0 |

Operation error

| Error code | Error content |  |  |
| :--- | :--- | :---: | :---: |
| 3402 H | The value specified by (s1) or (s2) is a subnormal number or NaN (not a number). |  |  |
| 3403 H | An overflow has occurred. |  |  |
| $>=$ |  |  |  |

This instruction compares input data, and outputs the comparison result.


FBD/LD

| ■--- |  |
| :---: | :---: |
| EN | ENO |
| s1 | d1 |
| s2 |  |
| s3 |  |

( $\square$ is to be replaced by S_GE.)

Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．＞＝ | $\square$ |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| （s1） | Input data start device | Refer to＂Input data＂． | Word |
| （d1） | Block memory start device | Refer to＂Block memory＂． | Word |
| （s2） | Operation constant start device | Refer to＂Operation constant＂． | Word |
| （s3） | Empty string specification <br> （Specify＂＂in ladder．Specify＂in ST or FBD／LD．） | - | String |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，R，ZR，RD | U밈，J미， U3EDI（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s3） | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － |

## Input data

| Operand：（s1） |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| $\begin{aligned} & +0 \\ & +1 \end{aligned}$ | E1 | Input value 1 | －999999 to 999999 | Single－precision real number | － | User |
| $\begin{aligned} & +2 \\ & +3 \end{aligned}$ | E2 | Input value 2 | －999999 to 999999 | Single－precision real number | － | User |

## Block memory



## Operation constant

| Operand：（s2） |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Symbol | Name | Recommended range | Data type | Standard <br> value | Set by |
| +0 | K | Set value | -999999 to 999999 | Single－precision <br> real number | 0.0 |  |
| +2 | HS | Hysteresis | 0 to 999999 | Single－precision <br> real number | 0.0 | User |
| +3 |  |  |  |  |  |  |

## Processing details

This instruction compares input values 1 (E1) and 2 (E2) and outputs the result (d1) to the comparison output (BB1).

| Condition | BB1 |
| :--- | :--- |
| $E 1 \geq(E 2+K)$ | 1 |
| $E 1<(E 2+K-H S)$ | 0 |
| $(E 2+K-H S) \leq E 1<(E 2+K)$ | Last value is output. |

## Operation error

| Error code | Error content |
| :--- | :--- |
| 3402 H | The value specified by (s1) or (s2) is a subnormal number or NaN (not a number). |
| 3403 H | An overflow has occurred. |
| 3405 H | Hysteresis (HS) is less than 0. |

## S.<=

This instruction compares input data, and outputs the comparison result.


FBD/LD

| ■--- $]$ |  |
| :---: | :---: |
| EN | ENO |
| s1 | d1 |
| s2 |  |
| s3 |  |

( $\square$ is to be replaced by S_LE.)

## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S. $<=$ | $-\square$ |

## Setting data

Description, range, data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| (s1) | Input data start device | Refer to "Input data". | Word |
| (d1) | Block memory start device | Refer to "Block memory". | Word |
| (s2) | Operation constant start device | Refer to "Operation constant". | Word |
| (s3) | Empty string specification <br> (Specify "" in ladder. Specify " in ST or FBD/LD.) | String |  |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | J밈 | T, ST, C, D, W, SD, SW, R, ZR, RD | UपIGロ, J밈, U3EDl(H)Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (s1) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (d1) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (s2) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (s3) | - | - | - | - | - | - | - | - | - | - | $\bigcirc$ | - |

## Input data

| Operand: (s1) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| $\begin{aligned} & +0 \\ & +1 \end{aligned}$ | E1 | Input value 1 | -999999 to 999999 | Single-precision real number | - | User |
| $\begin{aligned} & +2 \\ & +3 \end{aligned}$ | E2 | Input value 2 | -999999 to 999999 | Single-precision real number | - | User |

Block memory


■Operation constant

| Operand: (s2) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| $\begin{aligned} & +0 \\ & +1 \end{aligned}$ | K | Set value | -999999 to 999999 | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +2 \\ & +3 \end{aligned}$ | HS | Hysteresis | 0 to 999999 | Single-precision real number | 0.0 | User |

## Processing details

This instruction compares input values 1 (E1) and 2 (E2) and outputs the result (d1) to the comparison output (BB1).

| Condition | BB1 |
| :--- | :--- |
| $\mathrm{E} 1 \leq(\mathrm{E} 2+\mathrm{K})$ | 1 |
| $\mathrm{E} 1>(\mathrm{E} 2+\mathrm{K}+\mathrm{HS})$ | 0 |
| $(\mathrm{E} 2+\mathrm{K})<\mathrm{E} 1 \leq(\mathrm{E} 2+\mathrm{K}+\mathrm{HS})$ | Last value is output. |

## Operation error

| Error code | Error content |
| :--- | :--- |
| 3402 H | The value specified by (s1) or (s2) is a subnormal number or NaN (not a number). |
| 3403 H | An overflow has occurred. |
| 3405 H | Hysteresis (HS) is less than 0. |

### 10.7 Auto Tuning

Auto tuning is designed to make initial setting of PID constants.
Auto tuning can be used for processes that can be approximated with the "primary lag plus dead time" represented by the following expression.

Ex.
Process with relatively slow response such as temperature control
$\frac{K}{1+T s} e^{-L s}$
K: Gain, T: Time constant, L: Dead time, s: Laplace operator
Auto tuning can be used for the loop that uses S.PID or S.2PID instruction.
Auto tuning is performed in the NZ method: step response method of Ziegler and Nichols).

## Outline of step response method

With no control operation being performed, change the manipulated value (MV) in a stepwise manner and look how the process value (PV) varies.

- When MV is changed in a stepwise manner, PV begins to change slowly. Soon, the change speed becomes faster and then becomes slow again, and finally is settled as a fixed value.
- Draw a tangent line at the place where PV varies fastest, and find the points of intersection $A$ and $B$ where the tangent line crosses the horizontal axis corresponding to the first process value $\left(\theta_{0}\right)$ and last process value $\left(\theta_{1}\right)$. This obtains the equivalent dead time ( L ) and equivalent time constant $(T)$ as shown below.
- Determine maximum ramp (response speed) $R=Y / T$ from the equivalent time constant ( $T$ ) and maximum process value width $(\mathrm{Y})$. Apply the equivalent dead time $(\mathrm{L})$ and maximum ramp $(\mathrm{R})$ to the Ziegler and Nichols' adjustment rule to calculate the proportional gain $\mathrm{KP}(\mathrm{P})$, integral constant $\mathrm{TI}(\mathrm{I})$, and derivative constant $\mathrm{TD}(\mathrm{D})$.



## Auto tuning procedure

The following shows the auto tuning procedure.


The auto tuning completion status (BB16) is set to 1 (Completed) at completion of auto tuning


- Time chart from auto tuning start till normal completion

- Time chart from auto tuning start till stop due to alarm occurrence



## Auto tuning instructions

## S．AT1

This instruction performs auto tuning to make initial setting of PID constants．


## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| S．AT1 | $-\square$ |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type |
| :--- | :--- | :--- | :--- |
| （s1） | Input data start device | Refer to＂Input data＂． | Word |
| （d1） | Block memory start device | Refer to＂Block memory＂． | 16－bit unsigned binary |
| （s2） | Operation constant start device | Refer to＂Operation constant＂． | 16－bit unsigned binary |
| （d2） | Loop tag memory start device | Refer to＂Loop tag memory＂． | Word |
| （d3） | Local work memory start device | Refer to＂Local work memory＂． | Word |
| EN | Execution condition | - | Bit |
| ENO | Execution result | - | Bit |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，R，ZR，RD | UロIGロ，JロIロ， U3EDI（H）GD | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d3） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Input data



Block memory


## Operation constant

| Operand: (s2) | Recommended range | Data type | Standard <br> value | Set by |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Symbol | Name | 16 -bit unsigned <br> binary | 0 |
| +0 | PN | Operation mode | 0: Reverse action <br> $1:$ Direct action | User |

Loop tag memory
The loop tag memory occupies 96 words from the specified start device.

| Operand: (d2) |  |  |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Symbol | Name | Recommended range |  |  |  |  |  |  |  | Data type | Standard | Set by |
| +1 | MODE | Operation mode | 0 to FFFFH |  |  |  |  |  |  |  | 16-bit unsigned binary | 8H | User/ system |
| +3 | ALM | Alarm detection | SPA <br> 0: Loop RUN <br> 1: Loop STOP <br> HHA, LLA, PHA, PLA <br> 0 : No alarm <br> 1: Alarm |  |  |  |  |  |  |  |  | 4000H | User/ system |


| Operand: (d2) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| $\begin{aligned} & +12 \\ & +13 \end{aligned}$ | MV | Manipulated value | -10 to 110 [\%] | Single-precision real number | 0.0 | User/ system |
| $\begin{aligned} & +18 \\ & +19 \end{aligned}$ | MH | Output upper limit value | -10 to 110 [\%] | Single-precision real number | 100.0 | User |
| $\begin{aligned} & +20 \\ & +21 \end{aligned}$ | ML | Output lower limit value | -10 to 110 [\%] | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +52 \\ & +53 \end{aligned}$ | P | Gain | 0 to 999999 | Single-precision real number | 1.0 | User/ system |
| $\begin{aligned} & +54 \\ & +55 \end{aligned}$ | I | Integral constant | 0 to 999999 [s] | Single-precision real number | 10.0 | User/ system |
| $\begin{aligned} & +56 \\ & +57 \end{aligned}$ | D | Derivative constant | 0 to 999999 [s] | Single-precision real number | 0.0 | User/ system |
| $\begin{aligned} & +70 \\ & +71 \end{aligned}$ | AT1STEP <br> MV | AT1 step manipulated value | -100 to 100 [\%] | Single-precision real number | 0.0 | User |
| $\begin{aligned} & +72 \\ & +73 \end{aligned}$ | AT1ST | AT1 sampling cycle | 0 to 999999 [s] <br> Provided that $\frac{A T 1 S T}{\Delta T}<=32767$ | Single-precision real number | 1.0 | User |
| $\begin{aligned} & +74 \\ & +75 \end{aligned}$ | AT1TOUT 1 | AT1 timeout time | 0 to 999999 [s] <br> Provided that $\frac{\text { AT1TOUT1 }}{\Delta T}<=32767$ | Single-precision real number | 100.0 | User |
| $\begin{aligned} & +76 \\ & +77 \end{aligned}$ | AT1TOUT $2$ | Timeout time after maximum AT1 ramp | $0 \text { to } 999999 \text { [s] }$ <br> Provided that $\frac{\text { AT1TOUT2 }}{\Delta T}<=32767$ | Single-precision real number | 10.0 | User |

## Local work memory

The system uses this area as a work area.
To start the control from the initial status, clear data by using a sequence program.

| Operand: (d3) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Symbol | Name | Recommended range | Data type | Standard value | Set by |
| +0 | - | Sampling period counter initialization completion flag | - | - | - | System |
| +1 |  | Sampling period counter*1 |  |  |  |  |
| +2 |  | Timeout time counter initialization completion flag |  |  |  |  |
| +3 |  | Timeout time counter ${ }^{* 1}$ |  |  |  |  |
| +4 |  | Timeout time (after maximum ramp) counter initialization completion flag |  |  |  |  |
| +5 |  | Timeout time (after maximum ramp) counter ${ }^{* 1}$ |  |  |  |  |
| +6 |  | Step manipulated value set completion flag |  |  |  |  |
| +7 |  | Auto tuning counter |  |  |  |  |
| $+8+9$ |  | Auto tuning start PV0 |  |  |  |  |
| +10+11 | $P V_{n-1}$ | Last process value |  |  |  |  |
| +12+13 | - | Maximum ramp value |  |  |  |  |
| +14+15 |  | Counter at maximum ramp |  |  |  |  |
| +16+17 |  | PV at maximum ramp |  |  |  |  |
| +18+19 | R | Response speed |  |  |  |  |
| +20+21 | L | Equivalent dead time |  |  |  |  |

*1 The sampling period counter, timeout time counter, and timeout (after maximum ramp) counter are each rounded off to the nearest whole number.

## Execution cycle ( $\Delta \mathrm{T}$ )

Set the execution cycle $(\Delta T)$ in SD816 and SD817 in single-precision real number.

## Processing details

This instruction performs auto tuning to make initial setting of PID constants.

## Start signal determination processing

The instruction performs the following processing according to the auto tuning start signal (e1) and auto tuning completion (BB16).

| e1 | BB16 | Processing |
| :--- | :--- | :--- |
| 0 | 0 | •BB alarms from BB1 to identification alarm BB8 are set to 0. <br> •When the step manipulated value set completion flag is 1 , the following processing is performed. <br> MV=MV-AT1STEPMV <br> •The S.AT1 instruction is terminated. |
| 1 | 0 | • "Loop stop processing" is performed. |
| 0 | 1 | •BB auto tuning completion (BB16) is set to 0. <br> • The S.AT1 instruction is terminated. |
| 1 | 1 | •The S.AT1 instruction is terminated. |

## Loop stop processing

The following processing is performed according to the SPA status of the alarm detection (ALM).

| SPA status | Processing details |
| :--- | :--- |
| 1 | The loop stops. When the loop stops, the following operations are performed and the S.AT1 instruction ends. <br> • Auto tuning completion (BB16) is set to 1. <br> - When the step manipulated value set completion flag is 1, the following processing is performed. <br> MV=MV-AT1STEPMV |
| 0 | The loop runs, and "mode determination processing" is performed. |

## Mode determination processing

The following processing is performed according to the operation mode (MODE) setting.

| MODE setting | Processing details |
| :--- | :--- |
| AUT, CAB, CAS, CCB, CSV, LCA, LCC | The following processing is performed and the S.AT1 instruction is terminated. <br> • The operation mode alarm (BB7) is set to 1. <br> - Auto tuning completion (BB16) is set to 1. <br> - When the step manipulated value set completion flag is 1, the following processing is <br> performed. |
|  | MV=MV-AT1STEPMV |

Input check processing
The following processing is performed according to the alarm detection (ALM).

| Alarm Detection (ALM) | Processing details |
| :--- | :--- |
| PHA or HHA is 1. | The following processing is performed and the S.AT1 instruction is terminated. <br> - The input upper limit alarm (BB2) is set to 1. |
| PLA or LLA is 1. Auto tuning completion (BB16) is set to 1. |  |

Timeout determination processing
This function determines whether the auto tuning processing has reached the AT1 timeout time (AT1TOUT1).

| Auto tuning processing | Processing details |
| :--- | :--- |
| If the AT1 timeout time (AT1TOUT1) is reached | The following processing is performed and the S.AT1 instruction is terminated. <br> - The input upper limit alarm (BB6) is set to 1. |
|  | - Auto tuning completion (BB16) is set to 1. |
| If the AT1 timeout time (AT1TOUT1) has not been reached | "Timeout (after maximum ramp) determination processing" is performed. |

## Timeout (after maximum ramp) determination processing

This function determines whether the auto tuning processing has reached the AT1 timeout time after maximum ramp (AT1TOUT2).
However, if the timeout time (after maximum ramp) counter initialization completion flag is 0 , "step manipulated value set processing" is performed

| Auto tuning processing | Processing details |
| :--- | :--- |
| If the AT1 timeout time after maximum ramp (AT1TOUT2) is <br> reached | "Identification processing" is performed. |
| If the AT1 timeout time after maximum ramp (AT1TOUT2) has <br> not been reached | "Step manipulated value set processing" is performed. |

## Step manipulated value set processing

This function checks the step manipulated value set completion flag to determine whether the step manipulated value has been set (1) or not set (0).

- When the step manipulated value set completion flag is 0 , the following processing is performed.

The AT1 step manipulated value (AT1SETPMV) is added to the manipulated value (MV).
T1=MV+AT1STEPMV
The upper/lower limiter function performs the following operations, and outputs the result to the output upper limit alarm (BB4) and output lower limit alarm (BB5).

| Condition | Result |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  | BB4 | BB5 | BB16 | MV | Processing after upper/lower limiter |
| T1 > MH | 1 | 0 | 1 | Original MV remains unchanged. | The S.AT1 instruction is terminated. |
| T1 < ML | 0 | 1 | 1 |  |  |
| $\mathrm{ML} \leq \mathrm{T} 1 \leq \mathrm{MH}$ | 0 | 0 | 0 | T1 | The following processing is performed. <br> - The step manipulated value set completion flag is set to 1 . <br> - The auto tuning counter is set to 0 . <br> - The input value (E1) is stored in auto tuning start PVO. <br> - The input value (E1) is stored in the last measurement value $\left(\mathrm{PV}_{\mathrm{n}-1}\right)$. <br> - The maximum slope value, maximum ramp counter, maximum ramp PV, response speed $(R)$, and equivalent dead time $(L)$ are set to 0 . |

- When the step manipulated value set completion flag is 1 , "sampling cycle determination processing" is performed.


## Sampling cycle determination processing

This function checks the AT1 sampling period (AT1ST) to determine whether the sampling period has been reached.

- If the sampling period has not been reached, the S.AT1 instruction is terminated.
- If the sampling period has been reached, "response waveform observation processing" is performed.

Response waveform observation processing
The following processing is performed for the input value (E1).

| Item | Processing |  |  |
| :---: | :---: | :---: | :---: |
| Response waveform observation | The auto tuning counter is incremented. |  |  |
|  | The following processing is performed according to the input value ( E 1 ) and last measurement value $\left(\mathrm{PV}_{\mathrm{n}-1}\right)$. $\mathrm{T} 2=\mathrm{E} 1-\mathrm{PV} \mathrm{~V}_{\mathrm{n}-1}$ <br> - Reserve action ( $\mathrm{PN}=0$ ) <br> - Direct action (PN = 1) |  |  |
|  | The input value ( E 1 ) is stored in the last measurement value ( $\mathrm{PV}_{\mathrm{n}-1}$ ). |  |  |
| Maximum ramp value | The following processing is performed according to the ramp (T2) and the S.AT1 instruction is terminated. |  |  |
|  | - Reverse action ( $\mathrm{PN}=0$ ) and AT1 step manipulated value <br> (AT1STEPMV) $\geq 0$ <br> - Direct action ( $\mathrm{PN}=1$ ) and AT1 step manipulated value (AT1STEPMV) < 0 | Maximum ramp value $\leq$ Ramp (T2) | - Maximum ramp value = Ramp (T2) <br> - Maximum ramp counter = Counter from the start of auto tuning <br> - Maximum ramp PV = Input value (E1) <br> - Reset the timeout time (after maximum ramp) counter, and start counting again. |
|  |  | Maximum ramp value > Ramp (T2) | The last maximum ramp value remains unchanged. |
|  | - Direct action (PN=1) and AT1 step manipulated value (AT1STEPMV) $\geq 0$ <br> - Reverse action ( $\mathrm{PN}=0$ ) and AT1 step manipulated value (AT1STEPMV) < 0 | Maximum ramp value $\geq$ Ramp (T2) | - Maximum ramp value = Ramp (T2) <br> - Maximum ramp counter = Counter from the start of auto tuning <br> - Maximum ramp PV = Input value (E1) <br> - Reset the timeout time (after maximum ramp) counter, and start counting again. |
|  |  | Maximum ramp value < Ramp (T2) | The last maximum ramp value remains unchanged. |

## Identification processing

The following processing is performed from the maximum ramp value.

| Processing | Item |
| :---: | :---: |
| Response speed | The response speed for calculation ( $R^{\prime}$ ) and response speed (R) are calculated from the following expressions. $\mathrm{R}^{\prime}=\frac{\text { Maximum ramp value (\%) }}{\mathrm{AT} 1 \mathrm{ST}}, \mathrm{R}=\frac{\left\|\mathrm{R}^{\prime}\right\|}{100}(/ \mathrm{s})$ |
|  | When the response speed (R) is 0 , the following processing is performed and the S.AT1 instruction is terminated. <br> - Identification alarm BB8 is set to 1 . <br> - Auto tuning completion (BB16) is set to 1 . <br> - When the step manipulated value set completion flag is 1 , the following processing is performed. <br> MV=MV-AT1STEPMV |
| Equivalent dead time | The Y -axis intercept (b) when tangent is drawn by the response speed for calculation ( $\mathrm{R}^{\prime}$ ) and the equivalent dead time ( L ) are calculated from the following expressions. <br> $b=(P V$ at maximum ramp $)-R^{\prime} \times($ counter at maximum ramp $) \times A T 1 S T$ <br> $\mathrm{L}=$ $\qquad$ <br> (Auto tuning start-time PV0)-b <br> R' |
|  | When the equivalent dead time $(\mathrm{L})$ is equal to or less than 0 , the following processing is performed and the S.AT1 instruction is terminated. <br> - Identification alarm BB8 is set to 1 . <br> - Auto tuning completion (BB16) is set to 1 . <br> - When the step manipulated value set completion flag is 1 , the following processing is performed. <br> MV=MV-AT1STEPMV |

IPID constants calculation processing
The response speed (R), equivalent dead time (L), and AT1 step manipulated value (AT1STEPMV) are assigned to the adjustment rule to calculate PID constants.

- Control method

The control method is selected according to the integral constant $\mathrm{TI}(\mathrm{I})$ and derivative constant TD (D).

| Integral constant $T_{1}(\mathbf{I})$ | Derivative constant $T_{D}(\mathrm{D})$ | Control method |
| :--- | :--- | :--- |
| $\mathrm{T}_{1} \leq 0$ | - | Proportional control (P operation) only |
| $\mathrm{T}_{1}>0$ | $\mathrm{~T}_{\mathrm{D}} \leq 0$ | PI control (PI operation) |
|  | $\mathrm{T}_{\mathrm{D}}>0$ | PID control (PID operation) |

- Adjustment rule
$N$ method: The adjustment rule using the step response of Ziegler and Nichols is used.

| Control method | Proportional gain $K_{P}(\mathbf{P})$ | Integral constant $T_{\mathbf{I}}(\mathbf{I})$ | Derivative constant $T_{\mathbf{D}}(\mathbf{D})$ |
| :--- | :--- | :--- | :--- |
| P | $\frac{1}{\mathrm{R} \times \mathrm{L}} \times \frac{\|\mathrm{AT} 1 \mathrm{STEPMV}\|}{100}$ | 0 | 0 |
| PI | $\frac{0.9}{\mathrm{R} \times \mathrm{L}} \times \frac{\mathrm{ATT1STEPMV\mid}}{100}$ | $3.33 \times \mathrm{L}$ | 0 |
| PID | $\frac{1.2}{\mathrm{R} \times \mathrm{L}} \times \frac{\mathrm{IAT1STEPMV\mid}}{100}$ | $2 \times \mathrm{L}$ | $0.5 \times \mathrm{L}$ |

- PID constants storing

The following processing is performed and the S.AT1 instruction is terminated.

- PID constants are stored in the gain (P), integral constants (I), and derivative constants (D).
- Auto tuning completion (BB16) is set to 1.
- The AT1 step manipulated value (AT1SETPMV) is subtracted from the manipulated value (MV) and the result is stored in the manipulated value (MV). MV=MV-AT1STEPMV


## Operation error

| Error code | Error content |
| :---: | :---: |
| 3400 H | An invalid operation (such as division by zero) is performed. |
| 3402H | Input data (S1) is a subnormal number or NaN (not a number). |
| 3403H | An overflow has occurred. |
| 3405H | AT1 sampling period (AT1ST) < 0 |
|  | AT1 timeout time (AT1TOUT1) < 0 |
|  | AT1 timeout (after maximum ramp) time (AT1TOUT2) < 0 |
|  | Execution cycle ( $\Delta \mathrm{T}$ ) < 0 |
|  | (AT1 sampling period (AT1ST) - execution cycle ( $\Delta T$ )) > 32767 |
|  | (AT1 timeout time (AT1TOUT1) $\div$ execution cycle ( $\Delta$ T $)$ ) $>32767$ |
|  | (AT1 timeout (after maximum ramp) time (AT1TOUT2) $\div$ Execution cycle ( $\Delta$ T) ) $>32767$ |

## 11 MULTIPLE CPU DEDICATED INSTRUCTIONS

### 11.1 Another CPU Module Access Instructions

## Overview

The host CPU module read or write device data from or to another CPU module by using another CPU module access instructions.

The following figure shows the operation for writing data from CPU No. 1 to CPU No. 2 by using another CPU module access instruction.


The following table lists another CPU module access instructions.

| Instruction <br> symbol | Description | Application |
| :--- | :--- | :--- |
| $\mathrm{D}(\mathrm{P})$. DDRD | Loads the device data of another CPU module to the device of the host CPU module. | Use these instructions to read or write data <br> at the timing set by the fixed scan <br> communication function. |
| $\mathrm{D}(\mathrm{P})$. DDWR | Writes the device data of the host CPU module to the device of another CPU module. | Use these instructions to read or write data <br> at the timing of each CPU module. |
| $M(P) . D D R D$ | Loads the device data of another CPU module to the device of the host CPU module. | Writes the device data of the host CPU module to the device of another CPU module. |

## Setting parameters

To use the $D(P)$.DDRD or $D(P)$.DDWR instruction, the fixed scan communication function of the system parameters needs to be set.

## Readable/writable devices

The following table lists the devices that can be read from or written to another CPU module by using another CPU module access instructions.

| Classification | Type | Device name $^{* 2}$ | Target device <br> setting | Condition |
| :--- | :--- | :--- | :--- | :--- |
| User device <br> System device | Bit device | X, Y, M, L, B, F, SB, <br> SM | Available | Satisfy the following conditions. <br> $\bullet$ Four digits are specified by 16 bits. <br> $\bullet$ The start bit device is a multiple of 16 (10H). |
|  | Word device | T, ST, C, D, W, SW, <br> SD | Available | None |
|  | File register | Word device | R, ZR | Available |
| Indirect specification ${ }^{* 1}$ |  | - | None |  |

*1 When an indirect specification is used for the target device, the device to be written or read is determined from the indirect address stored in the device of the host CPU module.
*2 Index modification ( $Z, Z Z$ representation) using the index register by the string specification can be performed. Another CPU module is accessed with a value which is index-modified by the value of the index register of the host CPU module. For example, "K4M0Z0" with $Z 0=16$ causes $M 0+16=M 16$, causing $K 4 M 16$ to access another CPU module. Similarly, "ZROZZO" with Z0, Z1=100000 causes ZR0+10000=ZR100000, causing ZR100000 to access another CPU module.

## Device specification method and readable/writable ranges

Specify the device of another CPU module with character strings.


The string specification enables writing to or reading from every range of the device in another CPU module. For example, when the data register of the host CPU has 12K points while the data register of another CPU module has 16 K points, 16 K points of data can be written to or read from the head of the data register of another CPU module.


## Point!

- Even if " 0 " is added to higher places of a device number, the device is processed the same as when it is not added. For example, "D1" and "D0001" are each processed as D1.
- Device numbers are not case-sensitive in terms of processing. For example, "D1" and "d1" are each processed as D1.
- Note that if a device not existing in another CPU module is specified by a character string, the instruction will be completed with an error.


## Number of available blocks

Another CPU module access instructions use the system area in minimum units of blocks, each consisting of 16 words. The following table lists the numbers of blocks available for another CPU module access instructions.

| Number of CPU modules | Maximum number of blocks |
| :--- | :--- |
| 2 | 599 |
| 3 | 299 |
| 4 | 199 |

The following figure shows how blocks are used in a multiple CPU system consisting of three CPU modules.


## Maximum number of data points that can be read or written

The maximum number of data points that can be read or written by an instruction depends on the number of CPU modules in a multiple CPU system configuration

| Number of CPU <br> modules | Maximum number of data points that can be read | Maximum number of data points that can be written |
| :--- | :--- | :--- |
| 2 modules | 8192 point | 8192 point |
| 3 modules | 4096 point | 4096 point |
| 4 modules | 2048 point | 2048 point |

## Number of blocks used by instructions

The number of blocks used by instructions depends on the number of read/write data points. The following table lists the numbers of blocks used by instructions.

| Reading/writing | Number of blocks | Example |
| :--- | :--- | :--- |
| Read | Number of blocks used by instructions $=(21+$ number of <br> read data points $) \div 16$ | - When the number of read data points is 100 <br> Number of blocks used by instructions $=(21+100) \div 16=7[$ blocks $]$ |
| Write | Number of blocks used by instructions $=(19+$ number of <br> write data points $) \div 16$ | - When the number of write data points is 100 <br> Number of blocks used by instructions $=(19+100) \div 16=7[$ [blocks $]$ |

## Simultaneous execution of another CPU module access Instructions

Another CPU module access instructions can be executed simultaneously within the range of the following expression.

## Conditions under which another CPU module access instructions can be executed simultaneously <br> [Number of blocks available for each CPU module] $\geq$ [total number of blocks used by concurrently executed instructions

If executing another CPU module access instruction causes the number of blocks used by the CPU module access instructions to exceed the total number of blocks in the system area, the instruction is not executed (no processing) in the relevant scan and is executed in the next scan

Note, however, that this instruction is completed with an error if the number of empty block in the system area is less than the value specified in SD796 to SD799 (maximum number of blocks for multiple CPU dedicated instructions) when the instruction is executed.
The table below shows whether another CPU module access instruction can be executed when the number of empty blocks in the system area is less than the number of blocks used by another CPU module access instructions or the value set in SD796 to SD799.

| Size relationship between the value set in $S D^{* 3}$ and number of empty blocks*2 | Size relationship between the number of blocks used by instruction*1 and number of empty blocks |  |
| :---: | :---: | :---: |
|  | Number of blocks used by instruction $\leq$ and number of empty blocks | Number of blocks used by instruction > number of empty blocks |
| Value set in SD $\leq$ number of empty blocks | Executed | Not executed (non-processing) |
| Value set in SD > number of empty blocks | Completed with an error |  |
| *1 Number of blocks used by another <br> *2 Number of empty blocks in the syst <br> *3 Value set in SD796 to SD799 | module access instructions |  |

## Interlock applied when another CPU module access instructions are used

Special relay SM796 to SM799 is used for interlocking among another CPU module access instructions.
When executing multiple another CPU module access instructions concurrently, use SM796 to SM799 for interlocking among these instructions.

When using SM796 to SM799, specify the maximum numbers of blocks of the instructions used by individual CPU modules in SD796 to SD799. For example, when the maximum number of blocks used by another CPU module access instructions executed for CPU module No. 3 is 5, specify 5 in SD798. When the number of blocks specified in any of SD796 to SD799 is exceeded, the relevant special relay (SM796 to SM799) turns on.

## Precautions

- Execute the $D(P) . D D W R, M(P) . D D W R, D(P) . D D R D$, or $M(P)$.DDRD instruction while the read/write target CPU module is on. If the instruction is executed while the target CPU is not on, the instruction performs no processing.
- After the $D(P)$.DDWR, $M(P)$.DDWR, $D(P)$.DDRD, or $M(P)$.DDRD instruction is executed, do not change the device range specified in the setting data before the completion device is turned on; otherwise, the completion status and completion device data can no longer be stored in the system.
- SB/SW and SM/SD include the system information area. When writing data with the $D(P) D D W R$ or $M(P)$.DDWR instruction, be careful not to overwrite the system information area.
- If the number of blocks used by the instruction to be executed is greater than the value set in SD796 to SD799, the instruction may not be executed (terminated abnormally) even if it is interlocked with SD796 to SD799.
- Set SD796 to SD799 before executing the instruction for the corresponding CPU module. (It is recommended to set them in the first scan after the CPU module runs.)


## Reading device data from another CPU module

## D（P）．DDRD，M（P）．DDRD

These instructions read device data from another CPU module in a multiple CPU system．


FBD／LD


## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| D．DDRD | - |
| M．DDRD | $\boxed{ }$ |
| DP．DDRD | - |
| MP．DDRD |  |

## Setting data

## ■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| $(\mathrm{U} / \mathrm{H})$ | Start I／O number（first three digits in <br> four－digit hexadecimal representation） <br> of another CPU module | 3E0H to 3E3H | 16－bit unsigned binary | ANY16 |
| （s1） | Start device of host CPU module <br> where the control data is stored | Refer to the control data． | Word | ANY16＿ARRAY <br> （Number of elements： <br> 2） |
| （s2） | Start device of another CPU module <br> where the data to be read is stored | - | String | ANYSTRING＿SINGLE |
| （d1） | Start device of host CPU module for <br> storing the data that has been read | - | Word | ANY16 |
| （d2） | Completion device | - | Bit | ANYBIT＿ARRAY <br> （Number of elements： <br> 2） |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | BOOL |  |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3E미（H）Gㅁ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （U／H） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － |
| （d1） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d2） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |


| Operand: (s1) |  | Description | Setting range | Set by |
| :--- | :--- | :--- | :--- | :--- |
| Device | Item | Completion status | The completion status is stored. <br> $\bullet 0000 \mathrm{H}:$ Completed successfully <br> $\bullet$ Other than $0000 \mathrm{H}:$ Completed with an error (error code) | System |
| +0 | Number of read data <br> points | Specify the number of read data points in units of words. | - | 1 to $8192^{* 1}$ |
| +1 | User |  |  |  |

*1 This is the maximum setting range in a multiple CPU system consisting of two CPU modules.
It may be less than 8192 because the number of data points that can be read varies depending on the system configuration. ( $\mathfrak{B}$ Page 1179 Another CPU Module Access Instructions)

## Processing details

- In a multiple CPU system, these instructions read the data in the device specified by (d1) in the host CPU module, by the number of read data points specified by ( s 1 ) +1 , and store it in the device specified by ( d 1 ) and later in another CPU module (U/H).

- The following figure shows an outline of operation of the $D(P)$.DDRD and $M(P)$.DDRD instructions.
- Outline of operation of the $D(P)$.DDRD instructions

- Outline of operation of the M(P).DDRD instructions

- The execution of the $D(P)$.DDRD or $M(P)$.DDRD instruction and whether it has been completed normally or with an error can be checked with the completion device (d2) or completion status indication device (d2)+1.
- Completion device (d2)

The completion device turns on in END processing of the scan performed upon completion of the $D(P)$.DDRD or M(P).DDRD instruction and turns off in the next END processing

- Completion status indication device (d2)+1

The completion device turns on or off depending on the completion status of the $D(P)$.DDRD or $M(P)$.DDRD instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on in END processing of the scan performed upon completion of the $D(P)$.DDRD or M(P).DDRD instruction and turns off in the next END processing.
When completed with an error, an error code is stored in the device (completion status) specified by ( s 1 ) +0 .

- The number of blocks used by instructions depends on the number of read data points. For the number of blocks used by instructions, refer to the following.
$\rightsquigarrow$ Page 1179 Another CPU Module Access Instructions
- For the specifiable target devices in the read target CPU module, refer to the following.
$\longmapsto$ Page 1179 Another CPU Module Access Instructions
- If an instruction is executed while the system area has no empty block, it is completed with an error. Completion with an error can be prevented by setting the number of blocks used by instructions in SD796 to SD799 and using SM796 to SM799 as interlocks.


## Operation error

| Error code (SDO) | Description |
| :---: | :---: |
| 2800H | The start I/O number (first three digits in four-digit hexadecimal representation) of the specified CPU module is out of the range, 3EOH to 3E3H. |
| 2801H | An invalid another CPU module is specified. <br> - A reserved CPU module is specified. <br> - An unmounted CPU module is specified. |
| 2802H | Another CPU module does not support the D(P).DDRD and M(P).DDRD instructions. |
| 2803H | The host CPU module is specified as another CPU module. |
| 2810H | A CPU module which cannot execute the instruction is specified as another CPU module. |
| 3404H | An invalid character string is used to specify a device. |
| 3405H | The number of read data points specified by (s1) +1 is out of the range from 0 to $8192 .{ }^{*}{ }^{2}$ |
| 3440 H | The $D(P)$.DDRD instruction is executed with the inter-CPU fixed-scan communication disabled. |
| 3441H | The specified number of data points exceeds the size of the system area that can be used by each CPU module. |
| *2 This is the maximum setting range in a multiple CPU system consisting of two CPU modules. <br> It may be less than 8192 because the number of data points that can be read varies depending on the system configuration. ( $B$ Page 1179 Another CPU Module Access Instructions) |  |


| Error code <br> $((\mathbf{s} \mathbf{1})+\mathbf{0})$ | Description |
| :--- | :--- |
| 0010 H | The instruction request to the target CPU module exceeds the allowable value. (There is not empty block in the system area.) |
| 1001 H | The device of another CPU module specified by (s2) cannot be used by another CPU module. Alternatively, it is out of the device range. |
| 1080 H | The number of read data points that has been set by the D(P).DDRD or M(P).DDRD instruction is 0. |

## Writing device data to another CPU module

## D（P）．DDWR，M（P）．DDWR

These instructions write device data to another CPU module in a multiple CPU system．

| Ladder |  |  |  |  | ST | T |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | （s1 | （s2） | （d1 | (d2) |  | $\begin{aligned} & \text { ENO:=D_DDWR(EN,U/H,s1,s2,d1,d2); } \\ & \text { ENO:=DP_DDWR(EN,U/H,s1,s2,d1,d2); } \\ & \text { ENO:=M_DDWR(EN,U/H,s1,s2,d1,d2); } \\ & \text { ENO:=MP_DDWR(EN,U/H,s1,s2,d1,d2); } \end{aligned}$ |
| FBD／LD |  |  |  |  |  |  |
|  |  |  |  |  |  |  |

## －Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| D．DDWR | - |
| M．DDWR | - |
| DP．DDWR | - |
| MP．DDWR |  |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （U／H） | Start I／O number（first three digits in <br> four－digit hexadecimal representation） <br> of another CPU module | 3 EOH to 3E3H | 16－bit unsigned binary | ANY16 |
| （s1） | Start device of host CPU module <br> where the control data is stored | Refer to the control data． | Word | ANY16＿ARRAY <br> （Number of elements： <br> 2） |
| （s2） | Start device of host CPU module <br> where the write data is stored | - | Word | ANY16 |
| （d1） | Start device of another CPU module <br> for storing the written data | - | String | ANYSTRING＿SINGLE |
| （d2） | Completion device | - | Bit | ANYBIT＿ARRAY <br> （Number of elements： <br> 2） |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | BOOL |  |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，Jㅁㅁ， U3EDI（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （U／H） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － |
| （d2） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Control data



## Processing details

- In a multiple CPU system, these instructions read the data in the device specified by (s2) in the host CPU module, by the number of write data points specified by ( s 1 ) +1 , and store it in the device specified by ( d 1 ) and later in another CPU module (U/H).

- The following figure shows an outline of operation of the $D(P)$.DDWR and $M(P)$.DDWR instructions.
- Outline of operation of the $D(P)$.DDWR instructions

- Outline of operation of the $M(P)$.DDWR instructions

- The execution of the $D(P)$.DDWR or $M(P)$.DDWR instruction and whether it has been completed normally or with an error can be checked with the completion device (d2) or completion status indication device (d2)+1.
- Completion device (d2)

The completion device turns on in END processing of the scan performed upon completion of the $D(P)$.DDWR or M(P).DDWR instruction and turns off in the next END processing.

- Completion status indication device (d2)+1

The completion device turns on or off depending on the completion status of the $D(P)$.DDWR or $M(P)$.DDWR instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on in END processing of the scan performed upon completion of the $D(P)$.DDWR or M(P).DDWR instruction and turns off in the next END processing.
When completed with an error, an error code is stored in the device (completion status) specified by ( s 1 ) +0 .

- The number of blocks used by instructions depends on the number of write data points. For the number of blocks used by instructions, refer to the following.
W Page 1179 Another CPU Module Access Instructions
- For the specifiable target devices in the write target CPU module, refer to the following.
$\longmapsto$ Page 1179 Another CPU Module Access Instructions
- If an instruction is executed while the system area has no empty block, it is completed with an error. Completion with an error can be prevented by setting the number of blocks used by instructions in SD796 to SD799 and using SM796 to SM799 as interlocks.


## Operation error

| Error code (SDO) | Description |
| :---: | :---: |
| 2800H | The start I/O number (first three digits in four-digit hexadecimal representation) of the specified CPU module is out of the range, 3EOH to 3E3H. |
| 2801H | An invalid another CPU module is specified. <br> - A reserved CPU module is specified. <br> - An unmounted CPU module is specified. |
| 2802H | Another CPU module does not support the D(P).DDWR and M(P).DDWR instructions. |
| 2803H | The host CPU module is specified as another CPU module. |
| 2810H | A CPU module which cannot execute the instruction is specified as another CPU module. |
| 3404H | An invalid character string is used to specify a device. |
| 3405 H | The number of write data points specified by (s1)+1 is out of the range from 0 to $8192 .{ }^{*}$ |
| 3440 H | The $\mathrm{D}(\mathrm{P})$.DDWR instruction is executed with the inter-CPU fixed-scan communication disabled. |
| 3441H | The specified number of data points exceeds the size of the system area that can be used by each CPU module. |
| *2 This is the maximum setting range in a multiple CPU system consisting of two CPU modules. <br> It may be less than 8192 because the number of data points that can be written varies depending on the system configuration. ( Page 1179 Another CPU Module Access Instructions) |  |


| Error code <br> $((\mathbf{s} 1)+\mathbf{0})$ | Description |
| :--- | :--- |
| 0010 H | The instruction request to the target CPU module exceeds the allowable value. (There is not empty block in the system area.) |
| 1001 H | The device of another CPU module specified by (d1) cannot be used by another CPU module. Alternatively, it is out of the device range. |
| 1080 H | The number of write data points that has been set by the $\mathrm{D}(\mathrm{P})$. DDWR or M(P).DDWR instruction is 0. |

## PART 4 MODULE DEDICATED INSTRUCTIONS

Part 4 consists of the following chapters.

12 NETWORK COMMON INSTRUCTIONS

13 ETHERNET INSTRUCTIONS

14 CC-LINK IE CONTROLLER NETWORK INSTRUCTIONS

15 CC-LINK IE FIELD NETWORK INSTRUCTIONS

16 CC-LINK INSTRUCTIONS

17 SERIAL COMMUNICATION INSTRUCTIONS

18 A/D CONVERSION INSTRUCTIONS

19 POSITIONING INSTRUCTIONS

## 12 <br> NETWORK COMMON INSTRUCTIONS

## Availability of each module

The following table summarizes the availability of each module for the instructions explained in this chapter.

| Instruction symbol | Availability |  |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
|  | Ethernet module | CC-Link IE Controller Network module | CC-Link IE Field Network module |  |
| READ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | Page 1195 JP.READ, GP.READ |
| SREAD | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | Page 1202 JP.SREAD, GP.SREAD |
| WRITE | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | Page 1209 JP.WRITE, GP.WRITE |
| SWRITE | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | Page 1217 JP.SWRITE, GP.SWRITE |
| SEND | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | Page 1225 JP.SEND, GP.SEND |
| RECV | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | Page 1232 JP.RECV, GP.RECV |
| RECVS | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | Page 1237 G.RECVS, Z.RECVS |
| $J(P) . Z N R D$ | $\bigcirc$ | $\bigcirc$ | $\times$ | Page $1241 \mathrm{~J}(\mathrm{P}) . \mathrm{ZNRD}$ |
| J(P).ZNWR | $\bigcirc$ | $\bigcirc$ | $\times$ | Page $1245 \mathrm{~J}(\mathrm{P})$.ZNWR |
| REQ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | Page $1250 \mathrm{~J}(\mathrm{P}) . \mathrm{REQ}, \mathrm{G}(\mathrm{P}) . \mathrm{REQ}$ |
| RIRD | $\times$ | $\bigcirc$ | $\bigcirc$ | Page 1264 J(P).RIRD, G(P).RIRD |
| RIWT | $\times$ | $\bigcirc$ | $\bigcirc$ | Page 1269 J(P).RIWT, G(P).RIWT |

## Target networks and target station types

The network common instructions can access networks not only in the own station but also in other stations. (Excluding the RIRD and RIRW instructions)
The following table lists the target stations of individual instructions.

| Instruction symbol | Target station (another station) |  |
| :---: | :---: | :---: |
|  | Target network | Target station type |
| READ <br> SREAD | - Ethernet <br> - CC-Link IE Controller Network <br> - CC-Link IE Field Network <br> - MELSECNET/H <br> - MELSECNET/10 | - RCPU <br> - QCPU <br> - LCPU <br> - QSCPU <br> - QnACPU <br> - Intelligent device station |
| WRITE SWRITE | - Ethernet <br> - CC-Link IE Controller Network <br> - CC-Link IE Field Network <br> - MELSECNET/H <br> - MELSECNET/10 | - RCPU <br> - QCPU <br> - LCPU <br> - QnACPU <br> - Intelligent device station |
| SEND RECV RECVS | - Ethernet <br> - CC-Link IE Controller Network <br> - CC-Link IE Field Network <br> - MELSECNET/H <br> - MELSECNET/10 | - RCPU <br> - QCPU <br> - LCPU <br> - QnACPU <br> - Interface board for personal computer*1 |
| J(P).ZNRD J(P).ZNWR | - Ethernet <br> - CC-Link IE Controller Network <br> - MELSECNET/H <br> - MELSECNET/10 | - QCPU <br> - LCPU <br> - QnACPU <br> - ACPU |
| REQ | - Ethernet <br> - CC-Link IE Controller Network <br> - CC-Link IE Field Network <br> - MELSECNET/H <br> - MELSECNET/10 | - RCPU <br> - QCPU <br> - LCPU <br> - QSCPU <br> - QnACPU <br> - Ethernet adapter module |
| RIRD RIWT | - CC-Link IE Controller Network <br> - CC-Link IE Field Network ${ }^{* 2}$ | - CC-Link IE Controller Network device <br> - CC-Link IE Field Network device |

*1 These instructions can access the following personal computer interface boards having the SEND/RECV functions. CC-Link IE Field Network interface board, CC-Link IE Controller Network interface board, MELSECNET/H interface board, and MELSECNET/10 interface board
*2 These instructions cannot be executed from the local station to the intelligent device station. Execute them in the master station.

## Range of available channel numbers

The range of own station channels that can be specified by the network common instructions varies depending on the module.
The following table summarizes the ranges of channels that can be specified by instructions for individual modules.

## Available channel numbers

| Instruction symbol | RJ71GP21-SX |  |  |  |  | RJ71GF11-T2 |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| READ, SREAD, WRITE, SWRITE | 1 to 10 |  |  |  |  | 1,2 |  |  |  |
| SEND, RECV, RECVS, REQ | 1 to 8 |  |  |  |  | 1,2 |  |  |  |
| RRUN, RSTOP, RTMRD, RTMWR | 1 to 8 |  |  |  |  | - |  |  |  |
| REMFR, REMTO | - |  |  |  |  | 1 to $32 * 1$ |  |  |  |
| Instruction symbol | RJ71EN71*2 |  |  |  |  |  |  |  |  |
|  | RJ71EN71(E+E) |  | RJ71EN71(E+CCIEC) |  | RJ71EN71(CCIEC) PORT1/PORT2 | RJ71EN71(E+CCIEF) |  | RJ71EN71(CCIEF) | RJ71EN71(Q) |
|  | PORT1 | PORT2 | PORT1 | PORT2 |  | PORT1 | PORT2 | PORT1/PORT2 | PORT1 |
| READ, SREAD, WRITE, SWRITE | 1 to 8 | 11 to 18 | 1 to 8 | 11 to 18 | 1 to 8 | 1 to 8 | 11 to 18 | 1 to 8 | 1 to 8 |
| SEND, RECV, RECVS, REQ | 1 to 8 | 11 to 18 | 1 to 8 | 11 to 18 | 1 to 8 | 1 to 8 | 11 to 18 | 1 to 8 | 1 to 8 |
| RRUN, RSTOP, RTMRD, RTMWR | - | - | - | 11 to 18 | 1 to 8 | - | - | - | - |
| REMFR, REMTO | - | - | - | - | - | - | 1 to $32{ }^{* 1}$ | 1 to $32^{* 1}$ | - |

*1 The own station channel numbers specified by REMFR and REMTO instructions can be used in combination with the own station channel numbers specified by link dedicated instructions.
*2 The range of channel numbers of an Ethernet module varies depending on the combination of network types at PORT1 and PORT2.

- RJ71EN71(E+E): PORT1 and PORT2 are "Ethernet".
- RJ71EN71(E+CCIEC): PORT1 is "Ethernet" and PORT2 is "CC-Link IE Controller Network".
- RJ71EN71(CCIEC): PORT1 and PORT2 are "CC-Link IE Controller Network".
- RJ71EN71(E+CCIEF): PORT1 is "Ethernet" and PORT2 is "CC-Link IE Field Network".
- RJ71EN71(CCIEF): PORT1 and PORT2 are "CC-Link IE Field Network".
- RJ71EN71(Q): PORT1 is "Q-compatible Ethernet". (PORT2 cannot be used.)

Corresponding Network type

| Instruction <br> symbol | CC-Link IE Controller <br> Network | CC-Link IE Field Network | Ethernet | Q-compatible Ethernet |
| :--- | :--- | :--- | :--- | :--- |
| READ, SREAD, <br> WRITE, SWRITE | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $O$ |
| SEND, RECV, <br> RECVS, REQ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $O$ |
| RRUN, RSTOP, <br> RTMRD, RTMWR | $\bigcirc$ | - | - | - |
| REMFR, REMTO | - | - | - | - |

## Specifications of character string data specified by link dedicated instructions

Some operands specified in link dedicated instructions are specified in character strings. The following table summarizes the specifications of character string data specified.


## Calculating the value to be set for the arrival monitoring time

The following shows the method for determining the value to be set for the arrival monitoring time specified by link dedicated instructions.

## Outline of communication processing for link dedicated instructions

When a link dedicated instruction is executed in the same network, the communication process at occurrence of retransmission is as follows.

Ex.
The READ instruction is executed in the same network.

(1) The CPU module executes the instruction and the network module accepts the instruction.
(2) The own station network module sends the request to another station.
(3) Another station network module receives the request and relays it to the CPU module
(4) The CPU module processes the request
(5) Another station network module relays the response from the CPU module
(6) Another station network module sends the response to the own station.

7 The own station network module receives the response and processes it.
(8) The own station network module requests retransmission if the response does not arrive within the arrival monitoring time

## Calculating the arrival monitoring time

The value to be set for the arrival monitoring time must be greater than the time taken from (2) to $\mathbf{7}$
[Calculation formula]
Arrival monitoring time>Tc_req+Tt_req+Tcpu+Tt_ans+Tc_ans+Tnet

| Item | Description | Details |
| :---: | :---: | :---: |
| Tc_req | Communication time (request) | In the case of the CC-Link IE Controller Network or CC-Link IE Field Network, calculate based on the link scan time. <br> For concurrent multiple transmissions including other transient transmissions, further increase the time in units of link scans. <br> The link scan time can be checked with the diagnostic window of each network. When Ethernet is used, the time varies depending on the amount of data and network load ratio (line congestion). <br> Calculate the time by adding the delay of the switching hub and the line congestion to 1 ms . |
| Tc_ans | Communication time (response) |  |
| Tt_req | Network module relay time (request) | Set 20 ms in total. |
| Tt_ans | Network module relay time (response) |  |
| Tnet | Network module instruction processing time (response) |  |
| Tcpu | CPU module response time | This time varies depending on the CPU module type and operating status. <br> Calculate the response time by "sequence scan time" $\times$ "number of times the device/ label access service is performed per scan". <br> For the scan time and device/label access service processing, refer to the following. ([]] MELSEC iQ-R CPU Module User's Manual (Application)) |

### 12.1 Link Dedicated Instructions

## Reading data from the programmable controller on another station

## JP.READ, GP.READ

These instructions read data from a device in the programmable controller of another station. (in units of words)


FBD/LD


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| JP.READ | - |
| GP.READ | - |

## Setting data

Description, range, data type

| Operand |  | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- | :--- |
| (J/U) | JP.READ | (J): Own station network number | 1 to 239 | 16 -bit unsigned binary | ANY16 |
|  | GP.READ | (U): Start I/O number (first three digits in four-digit <br> hexadecimal representation) of own station or <br> own node | 00 H to FEH | 16-bit unsigned binary | ANY16 |
| (s1) | Own station start device where control data is <br> stored | Refer to the control data. | Device name | ANY16*1 |  |
| (s2) | Target station start device where the data to be <br> read is stored | - | String*2 | ANYSTRING_SINGLE*2 |  |
| (d1) | Own station start device (a continuous area for <br> the length of the read data) for storing the data <br> that has been read | - | Device name | ANY16 |  |
| (d2) | Own station device to be turned on one scan <br> when the instruction completes. <br> (d2)+1 also turns on when the instruction <br> completes with an error. | - | Bit | ANYBIT_ARRAY <br> (Number of elements: 2) |  |
| EN | Execution condition | Execution result | - | Bit | Bit |
| ENO |  |  | - | BOOL |  |

*1 When specifying data with a label, define the array so that an area required for operation can be secured, and specify the array label element.
*2 For the specifications of the string data to be specified, refer to the following.
$\longmapsto$ Page 1193 Specifications of character string data specified by link dedicated instructions

Applicable devices

| Operand |  | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3E［l（H）Gロ | Z | LT, LST, LC | LZ |  | K，H | E | \＄ | J | U |
| （ $\mathrm{J} / \mathrm{U}$ ） | $\begin{aligned} & \text { JP.RE } \\ & \text { AD } \end{aligned}$ | － | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － |
|  | $\begin{aligned} & \text { GP.R } \\ & \text { EAD } \end{aligned}$ | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － | $\bigcirc$ |
| （s1） |  | － | － | ${ }^{*}{ }^{2}$ | － | － | － | － | $\bigcirc$ | － | － | － | － | － |
| （s2） |  | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － | － |
| （d1） |  | － | － | ${ }^{*}{ }^{2}$ | － | － | － | － | $\bigcirc$ | － | － | － | － | － |
| （d2） |  | O＊1 | － | $0^{* 3}$ | － | － | － | － | － | － | － | － | － | － |

＊1 FX and FY cannot be used．
＊2 FD cannot be used
＊3 T，ST，C，and FD cannot be used．
－The READ instruction cannot be executed when the target station is ACPU．
－Specify the own station start device（d1）for storing the data that has been read，by considering the range in which the data that has been read can be stored．
（Example：When areas D150 and after in the own station CPU module are already in use）



| Operand: (s1) |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description |  | Setting range | Set by |
| +8 | Arrival monitoring time | [CC-Link IE Controller Network or CC-Link IE Field Network] <br> Specify the monitoring time until completion of processing. If processing is not completed within the monitoring time, the request is resent by the number of resends specified in (s1)+7. <br> ■When " 0 " is specified in bit 8 of ( s 1 )+0 <br> - 0: 10s <br> - 1 to 32767: 1 to 32767s <br> -When " 0 " is specified in bit 8 of ( $s 1$ )1 <br> - 0: 10s <br> - 1 to 65535 : 1 to $65535 \times 100 \mathrm{~ms}$ |  | 0 to 65535 | User |
|  |  | [Ethernet] <br> Specify the monitoring time until completion of processing. If processing is not completed within the monitoring time, the request is resent by the number of resends specified in (s1)+7. <br> ■When "0" is specified in bit 8 of ( s 1 ) +0 <br> Specify the TCP resend timer value or greater for the monitoring time till completion of processing. <br> - 0 to (TCP resend timer value): The TCP resend timer value is assumed as the monitoring time. <br> - (TCP resend timer value +1 ) to 16383: (TCP resend timer value +1 ) to 16383 s When " 0 " is specified in bit 8 of ( s 1 ) 1 <br> - 0: 10s <br> - 1 to 65535: 1 to $65535 \times 100 \mathrm{~ms}$ |  | 0 to 65535 | User |
| +9 | Read data length | Specify the number of words to be read. <br> ■When reading data from RCPU, QCPU, or LCPU <br> - Channels 1 to 8 are used: 1 to 960 (words) <br> - Channels 9 and 10 are used: 1 to 8192 (words) <br> ■When reading data from QnACPU <br> - 1 to 480 (words) |  | 1 to 8192 | User |
| +10 | Not used | - |  | - | - |
| +11 | Clock setting flag | The validity status (valid or invalid) of the data in ( s 1 ) +12 and later is stored. Note that the data in ( $s 1$ ) +12 and later is not cleared even when the instruction is completed successfully. <br> - 0: Invalid <br> - 1: Valid |  | - | System |
| +12 | Clock data (Set only in an abnormal state) | Upper 8 bits: Month ( 01 H to 12 H ) <br> Lower 8bits: Year (00H to 99H: Upper two digits of the year) |  | - | System |
| +13 |  | Upper 8 bits: Hour ( 00 H to 23 H ) Lower 8 bits: Day ( 01 H to 31 H ) |  | - | System |
| +14 |  | Upper 8 bits: Second (00H to 59H) <br> Lower 8 bits: Minute ( 00 H to 59 H ) |  | - | System |
| +15 |  | Upper 8 bits: Year ( 00 H to 99 H : Upper two digits of the year) Lower 8 bits: Day of the week (00H (Sun.) to 06H (Sat.)) |  | - | System |
| +16 | Error detection network number | ■When " 0 " is specified in bit 15 of ( $\mathbf{s} 1$ )+0 <br> The network number of the station in which an error was detected is stored. (No information is stored if an error is detected in the own station.) <br> - 1 to 239 (Network number) <br> ■When "1" is specified in bit 15 of ( s 1 )+0 (Ethernet only) <br> The IP address (third and fourth octets) of the station where an error was detected is stored. |  | - | System |


| Operand: (s1) |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description |  |  |  | Setting range | Set by |
| +17 | Error-detected station number | When " 0 " is specified in bit 15 of ( s 1 )+0 <br> The station number of the station where an error was detected is stored. (No information is stored if an error is detected in the own station.) <br> [Ethernet or CC-Link IE Controller Network] <br> - 1 to 120: Station number <br> [CC-Link IE Field Network] <br> - 125: Master station <br> - 1 to 120: Local station, intelligent device station, submaster station <br> ■When "1" is specified in bit 15 of ( s 1 ) +0 (Ethernet only) <br> The IP address (first and second octets) of the station where an error was detected is stored. |  |  |  | - | System |
| - The continuous area (a maximum of 8192 words) specified by ( $s 1$ ) +9 is required in the read data storage device (d1). <br> - The number of resends (s1)+7 must be set every time the instruction is executed. |  |  |  |  |  |  |  |

## Processing details

- These instructions read the data from the specified word device in the target station specified by the target network number and target station number of the control data or the target station specified by the IP address. Upon completion of reading the device data, the completion device specified by (d2) turns on.
- For the target stations that can be specified, refer to the following.
$\longmapsto$ Page 1191 Target networks and target station types

- When "network number" and "station number" are specified (" 0 " is specified in bit 15 of ( s 1 )+0) by the target station address specification method, device data can be read also from stations connected to networks other than the stations connected to the own station network. (If "IP address" is specified ("1" is specified in bit 15 of ( $\mathbf{s} 1$ )+0), device data cannot be read from stations connected via a relay station.)
- When executing multiple link dedicated instructions concurrently, be careful not to overlap the channels of the link dedicated instructions. Multiple link dedicated instructions specifying the same channel cannot be used concurrently.
- The execution of the READ instruction and whether it has been completed normally or completed with an error can be checked with the completion device (d2) or completion status indication device (d2)+1.
- Completion device (d2)

The completion device turns on in END processing of the scan performed upon completion of the READ instruction and turns off in the next END processing.

- Completion status indication device (d2)+1

The completion device turns on or off depending on the completion status of the READ instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on in END processing of the scan performed upon completion of the READ instruction and turns off in the next END processing.

- The following figure shows the execution timing of the READ instruction.
- When completed normally

- When completed with an error

- Read processing is performed only once on the rising edge when the read command turns on.


## Operation error

| Error code $((s 1)+1)$ | Description |
| :---: | :---: |
| 4000H to 4FFFH | [] MELSEC iQ-R CPU Module User's Manual (Application) |
| COOOH to CFFFH | [] MELSEC iQ-R Ethernet User's Manual (Application) |
| D000H to DFFFH | [] MELSEC iQ-R CC-Link IE Field Network User's Manual (Application) |
| E000H to EFFFFH | [] MELSEC iQ-R CC-Link IE Controller Network User's Manual (Application) |

# Reading data from the programmable controller on another station (with notification) 

## JP.SREAD, GP.SREAD

These instructions read data from a device in the programmable controller of another station. (in units of words)
After the data reading is completed by the SREAD instruction, the device of another station is turned on. The other station can recognize that data has been read by the SREAD instruction.


FBD/LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| JP.SREAD | - |
| GP.SREAD | - |

## Setting data

Description, range, data type

| Operand |  | Description | Range | Data type | Data type (label) |
| :---: | :---: | :---: | :---: | :---: | :---: |
| (J/U) | JP.SREA <br> D | (J): Own station network number | 1 to 239 | 16-bit unsigned binary | ANY16 |
|  | GP.SREA <br> D | (U): Start I/O number (first three digits in four-digit hexadecimal representation) of own station or own node | OOH to FEH | 16-bit unsigned binary | ANY16 |
| (s1) |  | Own station start device where control data is stored | Refer to the control data. | Device name | ANY16 |
| (s2) |  | Target station start device where the data to be read is stored | - | String*1 | ANYSTRING_SINGLE*1 |
| (d1) |  | Own station start device for storing the read data | - | Device name | ANY16 |
| (d2) |  | Own station device to be turned on one scan when the instruction completes. (d2)+1 also turns on when the instruction completes with an error. | - | Bit | ANYBIT_ARRAY <br> (Number of elements: 2) |
| (d3) |  | Target station device that is turned on one scan upon completion of instruction (The target station can recognize that data has been read from another station.) | - | String*1 | ANYSTRING_SINGLE*1 |
| EN |  | Execution condition | - | Bit | BOOL |
| ENO |  | Execution result | - | Bit | BOOL |

[^29]Applicable devices

| Operand |  | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J밈， <br>  | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ | J | U |
| （J／U） | $\begin{aligned} & \text { JP.SR } \\ & \text { EAD } \end{aligned}$ | － | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － |
|  | $\begin{aligned} & \text { GP.S } \\ & \text { REA } \\ & \text { D } \end{aligned}$ | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － | $\bigcirc$ |
| （s1） |  | － | － | $\bigcirc{ }^{*}$ | － | － | － | － | $\bigcirc$ | － | － | － | － | － |
| （s2） |  | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － | － |
| （d1） |  | － | － | $0^{*}$ | － | － | － | － | $\bigcirc$ | － | － | － | － | － |
| （d2） |  | O＊1 | － | $0^{* 3}$ | － | － | － | － | － | － | － | － | － | － |
| （d3） |  | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － | － |

＊1 FX and FY cannot be used．
＊2 FD cannot be used．
＊3 T，ST，C，and FD cannot be used．
－The SREAD instruction cannot be executed when the target station is ACPU．
－Specify the own station start device（d1）for storing the data that has been read，by considering the range in which the data that has been read can be stored．
（Example：When areas D150 and after in the own station CPU module are already in use）

－When the target station for which the SREAD instruction is executed is the Basic model QCPU，the read notification device for the target station specified by argument（d3）is ignored．The operations of the SREAD instruction are the same as those of the READ instruction．
－The SREAD instruction can be programmed by omitting argument（d3）．However，the operations are the same as those of the READ instruction．The operations of the SREAD instruction can be selected according to whether（d3）is omitted or not．


| Operand: (s1) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +8 | Arrival monitoring time | [CC-Link IE Controller Network or CC-Link IE Field Network] <br> Specify the monitoring time until completion of processing. If processing is not completed within the monitoring time, the request is resent by the number of resends specified in (s1)+7. <br> ■When " 0 " is specified in bit 8 of ( s 1 ) +0 <br> - 0: 10s <br> - 1 to 32767: 1 to 32767s <br> ■When "0" is specified in bit 8 of ( s 1 )1 <br> - 0: 10s <br> - 1 to 65535 : 1 to $65535 \times 100 \mathrm{~ms}$ | 0 to 65535 | User |
|  |  | [Ethernet] <br> Specify the monitoring time until completion of processing. If processing is not completed within the monitoring time, the request is resent by the number of resends specified in (s1)+7. <br> ■When "0" is specified in bit 8 of ( $s 1$ )+0 <br> Specify the TCP resend timer value or greater for the monitoring time till completion of processing. <br> - 0 to (TCP resend timer value): The TCP resend timer value is assumed as the monitoring time. <br> - (TCP resend timer value +1 ) to 16383: (TCP resend timer value +1 ) to 16383 s <br> ■When " 0 " is specified in bit 8 of ( $s 1$ )1 <br> -0: 10s <br> - 1 to 65535 : 1 to $65535 \times 100 \mathrm{~ms}$ | 0 to 65535 | User |
| +9 | Read data length | Specify the number of words to be read. <br> ■When reading data from RCPU, QCPU, or LCPU <br> - Channels 1 to 8 are used: 1 to 960 (words) <br> - Channels 9 and 10 are used: 1 to 8192 (words) <br> ■When reading data from QnACPU <br> - 1 to 480 (words) | 1 to 8192 | User |
| +10 | Not used | - | - | - |
| +11 | Clock setting flag | The validity status (valid or invalid) of the data in (s1)+12 and later is stored. Note that the data in $(\mathrm{s} 1)+12$ and later is not cleared even when the instruction is completed successfully. <br> - 0: Invalid <br> -1: Valid | - | System |
| +12 | Clock data (Set only in an | Upper 8 bits: Month ( 01 H to 12 H ) <br> Lower 8bits: Year (00H to 99H: Upper two digits of the year) | - | System |
| +13 | abnormal state) | Upper 8 bits: Hour $(00 \mathrm{H}$ to 23 H$)$ Lower 8 bits: Day (01H to 31H) | - | System |
| +14 |  | Upper 8 bits: Second (00H to 59H) Lower 8 bits: Minute ( 00 H to 59 H ) | - | System |
| +15 |  | Upper 8 bits: Year ( 00 H to 99 H : Upper two digits of the year) Lower 8 bits: Day of the week (00H (Sun.) to 06H (Sat.)) | - | System |
| +16 | Error detection network number | ■When " 0 " is specified in bit 15 of ( $\mathbf{s} 1$ )+0 <br> The network number of the station in which an error was detected is stored. (No information is stored if an error is detected in the own station.) <br> - 1 to 239 (Network number) <br> ■When "1" is specified in bit 15 of ( $\mathbf{s} 1$ )+0 (Ethernet only) <br> The IP address (third and fourth octets) of the station where an error was detected is stored. | - | System |


| Operand: (s1) |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description |  |  |  | Setting range | Set by |
| +17 | Error-detected station number | When "0" is specified in bit 15 of (s1)+0 <br> The station number of the station where an error was detected is stored. (No information is stored if an error is detected in the own station.) <br> [Ethernet or CC-Link IE Controller Network] <br> - 1 to 120: Station number <br> [CC-Link IE Field Network] <br> - 125: Master station <br> - 1 to 120: Local station, intelligent device station, submaster station <br> ■When "1" is specified in bit 15 of ( $\mathbf{s} 1$ ) +0 (Ethernet only) <br> The IP address (first and second octets) of the station where an error was detected is stored. |  |  |  | - | System |
| Point $\rho$ |  |  |  |  |  |  |  |
| - The continuous area (a maximum of 8192 words) specified by ( $s 1$ ) +9 is required in the read data storage device (d1). <br> - The number of resends (s1)+7 must be set every time the instruction is executed. |  |  |  |  |  |  |  |

## Processing details

- These instructions read the data from the specified word device in the target station specified by the target network number and target station number of the control data or the target station specified by the IP address. Upon completion of reading the device data, the completion device specified by (d2) turns on. In another station, upon completion of sending the device data specified by (s2), the device specified by (d3) turns on.
- For the target stations that can be specified, refer to the following.
$\longmapsto$ Page 1191 Target networks and target station types
[Own station]

[Target station]

- When "network number" and "station number" are specified (" 0 " is specified in bit 15 of ( s 1 )+0) by the target station address specification method, device data can be read also from stations connected to networks other than the stations connected to the own station network. (If "IP address" is specified (" 1 " is specified in bit 15 of ( s 1 )+0), device data cannot be read from stations connected via a relay station.)
- When executing multiple link dedicated instructions concurrently, be careful not to overlap the channels of the link dedicated instructions. Multiple link dedicated instructions specifying the same channel cannot be used concurrently.
- The execution of the SREAD instruction and whether it has been completed normally or completed with an error can be checked with the send/receive instruction flag, completion device ( d 2 ), or completion status indication device ( d 2 ) +1 .
- Completion device (d2)

The completion device turns on in END processing of the scan performed upon completion of the SREAD instruction and turns off in the next END processing.

- Completion status indication device (d2)+1

The completion device turns on or off depending on the completion status of the SREAD instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on in END processing of the scan performed upon completion of the SREAD instruction and turns off in the next END processing.

- The following figure shows the execution timing of the SREAD instruction.
- When completed normally

- When completed with an error

- Read processing is performed only once on the rising edge when the read command turns on.


## Operation error

| Error code $((\mathrm{s} 1)+1)$ | Description |
| :---: | :---: |
| 4000H to 4FFFH | [] MELSEC iQ-R CPU Module User's Manual (Application) |
| COOOH to CFFFH | L] MELSEC iQ-R Ethernet User's Manual (Application) |
| D000H to DFFFH | [] MELSEC iQ-R CC-Link IE Field Network User's Manual (Application) |
| E000H to EFFFH | [] MELSEC iQ-R CC-Link IE Controller Network User's Manual (Application) |

## Writing data to the programmable controller on another station

## JP.WRITE, GP.WRITE

These instructions write data to a device in the programmable controller of another station (in units of words).


FBD/LD


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| JP.WRITE | - |
| GP.WRITE | - |

## Setting data

Description, range, data type

| Operand |  | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- | :--- |
| (J/U)JP.WRIT <br> E | (J): Own station network number <br> E | (U): Start I/O number (first three digits in four-digit <br> hexadecimal representation) of own station or own <br> node | 00 H to FEH | 16-bit unsigned binary | ANY16 |
| (s1) | Own station start device where control data is <br> stored | Refer to the control data. | Device name | 16-bit unsigned binary | ANY16 |
| (s2) | Own station start device containing write data | - | Device name | ANY16 |  |
| (d1) | Target station start device to which data is to be <br> written | - | String ${ }^{* 1}$ | ANY16 |  |
| (d2) | Own station device to be turned on one scan when <br> the instruction completes. <br> (d2)+1 also turns on when the instruction <br> completes with an error. | - | Bit | ANYSTRING_SINGLE*1 |  |
| EN | Execution condition |  | ANYBIT_ARRAY |  |  |
| (Number of elements: 2) |  |  |  |  |  |

*1 For the specifications of the string data to be specified, refer to the following.
$\longmapsto$ Page 1193 Specifications of character string data specified by link dedicated instructions

Applicable devices

| Operand |  | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3E［l（H）Gロ | Z | LT, LST, LC | LZ |  | K，H | E | \＄ | J | U |
| （J／U） | JP．W <br> RITE | － | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － |
|  | $\begin{aligned} & \text { GP.W } \\ & \text { RITE } \end{aligned}$ | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － | $\bigcirc$ |
| （s1） |  | － | － | $\bigcirc^{*}{ }^{2}$ | － | － | － | － | $\bigcirc$ | － | － | － | － | － |
| （s2） |  | － | － | ${ }^{*}{ }^{2}$ | － | － | － | － | $\bigcirc$ | － | － | － | － | － |
| （d1） |  | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － | － |
| （d2） |  | $\bigcirc{ }^{* 1}$ | － | $0^{* 3}$ | － | － | － | － | － | － | － | － | － | － |

＊1 FX and FY cannot be used．
＊2 FD cannot be used
＊3 T，ST，C，and FD cannot be used．
－The WRITE instruction cannot be executed when the target station is ACPU．
－Specify the target station start device（d1），to which data is to be written，by considering the range in which the data that has been written can be stored．
（Example：When areas D150 and after in the target station CPU module are already in use）

0
Good example
（D50 is specified by（d1）．）



| Operand: (s1) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +0 | Execution/error completion type | b15 $\cdots$ b8 b7 $\cdots$ b0 <br> $(4)$ 0 $(3)$ $(2)$ 0     <br> (1) Execution type (bit 0) <br> - 0: No arrival acknowledgment <br> When the target station is in the own network: Completed when data has been sent from the own station. <br> When the target station is in another network: Completed when data has arrived the relay station of the own network. <br> - 1: Arrival acknowledgment used <br> Sending data is completed when the data is written to the target station. <br> (2) Error completion type (bit 7) <br> Specify whether to set clock data when completed with an error. <br> - 0: Do not set data in (s1)+11 and later at completion with an error. <br> - 1: Set data in (s1)+11 and later at completion with an error. <br> (3) Arrival check time setting (bit 8) <br> - 0: 1s units <br> -1: 100 ms units <br> (4) Target station address specification method (bit 15) <br> - 0: Specify "network No." in (s1)+4, and "station number" in (s1)+5. <br> - 1: Specify the IP address in (s1)+4, 5 (Ethernet only) | $\begin{aligned} & 0000 \mathrm{H} \\ & 0001 \mathrm{H} \\ & 0080 \mathrm{H} \\ & 0081 \mathrm{H} \\ & 0100 \mathrm{H} \\ & 0101 \mathrm{H} \\ & 0180 \mathrm{H} \\ & 0181 \mathrm{H} \\ & 8000 \mathrm{H} \\ & 8001 \mathrm{H} \\ & 8080 \mathrm{H} \\ & 8081 \mathrm{H} \\ & 8100 \mathrm{H} \\ & 8101 \mathrm{H} \\ & 8180 \mathrm{H} \\ & 8181 \mathrm{H} \end{aligned}$ | User |
| +1 | Completion status | The instruction completion status is stored. <br> - 0: Normal <br> - Other than 0: Error (error code) | - | System |
| +2 | Own station channel | Specify the channel to be used by own station. ( $\leftrightarrows$ Page 1192 Range of available channel numbers) | 1 to 18 | User |
| +3 | Target station CPU type | Specify the CPU type of the target station. <br> - 0000H: Addressed to target station CPU (control CPU) <br> - 03D0H: Addressed to control system CPU <br> - 03D1H: Addressed to standby system CPU <br> - 03D2H: Addressed to system A CPU <br> - 03D3H: Addressed to system B CPU <br> - 03EOH: Addressed to multiple CPU No. 1 <br> - 03E1H: Addressed to multiple CPU No. 2 <br> - 03E2H: Addressed to multiple CPU No. 3 <br> - 03E3H: Addressed to multiple CPU No. 4 <br> - 03FFH: Addressed to target station CPU (control CPU) | $\begin{aligned} & 0000 \mathrm{H} \\ & 03 \mathrm{DOH} \text { to 03D3H } \\ & 03 \mathrm{EOH} \text { to 03E3H } \\ & \text { 03FFH } \end{aligned}$ | User |


| Operand: (s1) |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description |  |  |  | Setting range | Set by |
| +4 | Target network number | [CC-Link IE Controller Network or CC-Link IE Field Network] Specify the network number ( 1 to 239) of the target station. |  |  |  | (s1) +4 <br> Network No.: <br> 1 to 239 <br> (s1) +5 <br> Station No.: <br> 1 to 120, 125, 126 <br> Group number: <br> 0081 H to 00A0H <br> All-station specification: <br> 00FFH <br> (s1) $+4,5$ <br> IP address: <br> 00000001H to <br> FFFFFFFEH <br> (1 to 4294967294) | User |
|  |  | [Ethernet] <br> When " 0 " is specified in bit 15 of ( $\mathbf{s} 1$ )+0 <br> Specify the network number ( 1 to 239) of the target station. <br> ■When " 1 " is specified in bit 15 of ( s 1 )+0 (Ethernet only) <br> Specify the IP address (third and fourth octets) of the target station. |  |  |  |  |  |
|  |  |  |  |  |  |  |  |
| +5 | Target station number | [CC-Link IE Controller Network] <br> Specify the station number of the target station. <br> (1) Station number specification <br> - 1 to 120: Station number <br> (2) Group number specification <br> - 0081H to 00A0H: All stations with group numbers 0001 H to 0020 H <br> (Can be set when the execution type specified by ( s 1 ) is " 0 : No arrival acknowledgment") <br> (3) All-station specification <br> - 00FFH: All stations of target network number (broadcast (excluding the own station)) <br> (Can be set when the execution type specified by ( s 1 ) is " 0 : No arrival acknowledgment") |  |  |  |  | User |
|  |  | [CC-Link IE Field Network] <br> Specify the station number of the target station. <br> (1) Station number specification <br> - 125: Master station <br> - 126: Master operating station <br> - 1 to 120: Local station, intelligent device station, submaster station <br> (2) All-station specification <br> - 00FFH: All stations of target network number (broadcast (excluding the own station)) <br> (Can be set when the execution type specified by (s1) is "0: No arrival acknowledgment") |  |  |  |  |  |
|  |  | [Ethernet] <br> -When " 0 " is specified in bit 15 of ( s 1 ) +0 <br> Specify the station number of the target station. <br> (1) Station number specification <br> - 1 to 120: Station number <br> (2) Group number specification <br> - 0081H to 00A0H: All stations with group numbers 0001 H to 0020 H <br> (Can be set when the execution type specified by (s1) is "0: No arrival acknowledgment") <br> (3) All-station specification <br> - 00FFH: All stations of target network number (broadcast (excluding the own station)) <br> (Can be set when the execution type specified by ( s 1 ) is " 0 : No arrival acknowledgment") <br> ■When " 0 " is specified in bit 15 of ( $\mathbf{s} 1$ ) 1 <br> Specify the IP address (first and second octets) of the target station. |  |  |  |  |  |
|  |  |  |  |  |  |  |  |
| +6 | Not used | - |  |  |  |  | - | - |
| +7 | Number of resends | Effective when the execution type specified by (s1) is "1: Arrival acknowledgment used". <br> ■Before instruction execution <br> Specify the number of resends to be performed if the instruction is not completed within the monitoring time specified by ( s 1 ) +8 . <br> - 0 to 15 (times) <br> -At completion of instruction <br> The number of resends performed (result) is stored. <br> - 0 to 15 (times) |  |  |  | 0 to 15 | User/ system |


| Operand: (s1) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +8 | Arrival monitoring time | [CC-Link IE Controller Network or CC-Link IE Field Network] <br> Specify the monitoring time until completion of processing. If processing is not completed within the monitoring time, the request is resent by the number of resends specified in (s1)+7. <br> ■When " 0 " is specified in bit 8 of ( s 1 ) +0 <br> - 0: 10s <br> - 1 to 32767: 1 to 32767s <br> ■When " 0 " is specified in bit 8 of ( s 1 ) 1 <br> - 0: 10s <br> - 1 to 65535 : 1 to $65535 \times 100 \mathrm{~ms}$ | 0 to 65535 | User |
|  |  | [Ethernet] <br> Specify the monitoring time until completion of processing. If processing is not completed within the monitoring time, the request is resent by the number of resends specified in ( s 1 ) +7 . <br> -When " 0 " is specified in bit 8 of ( s 1 ) +0 <br> Specify the TCP resend timer value or greater for the monitoring time till completion of processing. <br> - 0 to (TCP resend timer value): The TCP resend timer value is assumed as the monitoring time. <br> - (TCP resend timer value +1 ) to 16383: (TCP resend timer value +1 ) to 16383 s <br> ■When " 0 " is specified in bit 8 of ( s 1 ) 1 <br> - 0: 10s <br> - 1 to 65535 : 1 to $65535 \times 100 \mathrm{~ms}$ | 0 to 65535 | User |
| +9 | Write data length | Specify the number of words to be written. <br> ■Writing to RCPU, QCPU, or LCPU <br> - Channels 1 to 8 are used: 1 to 960 (words) <br> - Channels 9 and 10 are used: 1 to 8192 (words) <br> -Writing to QnACPU <br> - 1 to 480 (words) | 1 to 8192 | User |
| +10 | Not used | - | - | - |
| +11 | Clock setting flag | The validity status (valid or invalid) of the data in (s1) +12 and later is stored. Note that the data in ( s 1 ) +12 and later is not cleared even when the instruction is completed successfully. <br> - 0: Invalid <br> -1: Valid | - | System |
| +12 | Clock data (Set only in an abnormal state) | Upper 8 bits: Month ( 01 H to 12 H ) <br> Lower 8 bits: Lower 2 digits of year ( 00 H to 99 H ) | - | System |
| +13 |  | Upper 8 bits: Hour ( 00 H to 23 H ) Lower 8 bits: Day ( 01 H to 31 H ) | - | System |
| +14 |  | Upper 8 bits: Second (00H to 59H) Lower 8 bits: Minute ( 00 H to 59 H ) | - | System |
| +15 |  | Upper 8 bits: Year (00H to 99H: Upper two digits of the year) Lower 8 bits: Day of the week (00H (Sun.) to 06H (Sat.)) | - | System |
| +16 | Error detection network number | The network number of the station in which an error was detected is stored. (No information is stored if an error is detected in the own station.) <br> - 1 to 239 (Network number) | 1 to 239 | System |
| +17 | Error-detected station number | The station number of the station where an error was detected is stored. (No information is stored if an error is detected in the own station.) <br> [Ethernet or CC-Link IE Controller Network] <br> - 1 to 120: Station number <br> [CC-Link IE Field Network] <br> - 125: Master station <br> - 1 to 120: Local station, intelligent device station, submaster station | 1 to 120, 125 | System |

- The continuous area (a maximum of 8192 words) for the write data length ((s1)+9) is required in the write data storage device (d1).
- When a number from 1 to 120 is specified for the target station number, the WRITE instruction should be executed with the execution type set to "Arrival acknowledgment used". This is to improve data reliability. When a number from 81 H to AOH or FFH is specified for the target station number, the WRITE instruction should be executed with the execution type set to "No arrival acknowledgment".
- When performing device writing to the same station from multiple stations, do not overlap the write timing. When the execution type is set to "No arrival acknowledgment", successful completion results in the write source station if communication is completed successfully even when the send data contains an error. Also, even when the send data is normal, a timeout results in the write source station if the WRITE instructions are executed for the same station from multiple stations.
- The number of resends (s1)+7 must be set every time the WRITE instruction is executed.


## Processing details

- These instructions write the data in the device/label specified by ( s 2 ) in the own station to the word device in the target station specified by the target network number and target station number of the control data or the target station specified by the IP address. Upon completion of writing device data to another station number, the completion device specified by (d2) turns on.
- For the target stations that can be specified, refer to the following.
$\circledast$ Page 1191 Target networks and target station types
[Own station]

[Target station]

- When "network number" and "station number" are specified (" 0 " is specified in bit 15 of ( s 1 ) +0 ) by the target station address specification method, device data can be written also to the stations connected to networks other than the stations connected to the own station network. (If "IP address" is specified ("1" is specified in bit 15 of ( $\mathbf{s} 1$ )+0), device data cannot be read from stations connected via a relay station.)
- When executing multiple link dedicated instructions concurrently, be careful not to overlap the channels of the link dedicated instructions. Multiple link dedicated instructions specifying the same channel cannot be used concurrently.
- The execution of the WRITE instruction and whether it has been completed normally or completed with an error can be checked with the completion device (d2) or completion status indication device (d2)+1.
- Completion device (d2)

The completion device turns on in END processing of the scan performed upon completion of the WRITE instruction and turns off in the next END processing.

- Completion status indication device (d2)+1

The completion device turns on or off depending on the completion status of the WRITE instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on in END processing of the scan performed upon completion of the WRITE instruction and turns off in the next END processing.

- The following figure shows the execution timing of the WRITE instruction.
- When completed normally

- When completed with an error

- Write processing is performed only once on the rising edge when the write command turns on.


## Operation error

| Error code $((\mathrm{s} 1)+1)$ | Description |
| :---: | :---: |
| 4000H to 4FFFH | [] MELSEC iQ-R CPU Module User's Manual (Application) |
| COOOH to CFFFH | L] MELSEC iQ-R Ethernet User's Manual (Application) |
| D000H to DFFFH | [] MELSEC iQ-R CC-Link IE Field Network User's Manual (Application) |
| E000H to EFFFH | [] MELSEC iQ-R CC-Link IE Controller Network User's Manual (Application) |

$1216^{12}$ NETWORK COMMON INSTRUCTIONS
12.1 Link Dedicated Instructions

## Writing data to the programmable controller on another station (with notification)

## JP.SWRITE, GP.SWRITE

These instructions write data to a device in the programmable controller of another station (in units of words).
After the data writing is completed by the SWRITE instruction, the device of another station is turned on. The other station can recognize that data has been written by the SWRITE instruction.


FBD/LD


## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| JP.SWRITE | - |
| GP.SWRITE | - |

## Setting data

Description, range, data type

| Operand |  | Description | Range | Data type | Data type (label) |
| :---: | :---: | :---: | :---: | :---: | :---: |
| (J/U) | JP.SWRIT <br> E | (J): Own station network number | 1 to 239 | 16-bit unsigned binary | ANY16 |
|  | GP.SWRIT <br> E | (U): Start I/O number (first three digits in fourdigit hexadecimal representation) of own station or own node | OOH to FEH | 16-bit unsigned binary | ANY16 |
| (s1) |  | Own station start device where control data is stored | Refer to the control data. | Device name | ANY16 |
| (s2) |  | Own station start device containing write data | - | Device name | ANY16 |
| (d1) |  | Target station start device to which data is to be written | - | String*1 | ANYSTRING_SINGLE*1 |
| (d2) |  | Own station device to be turned on one scan when the instruction completes. <br> (d2)+1 also turns on when the instruction completes with an error. | - | Bit | ANYBIT_ARRAY <br> (Number of elements: 2) |
| (d3) |  | Target station device that is turned on one scan upon completion of instruction (enabling the target station to recognize that data has been written from another station) | - | String*1 | ANYSTRING_SINGLE*1 |
| EN |  | Execution condition | - | Bit | BOOL |
| ENO |  | Execution result | - | Bit | BOOL |

*1 For the specifications of the string data to be specified, refer to the following.
$\longmapsto$ Page 1193 Specifications of character string data specified by link dedicated instructions

Applicable devices

| Operand |  | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3EDI（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ | J | U |
| （J／U） | JP．S <br> WRIT <br> E | － | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － |
|  | GP．S <br> WRIT E | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － | $\bigcirc$ |
| （s1） |  | － | － | $\mathrm{O}^{*}$ | － | － | － | － | $\bigcirc$ | － | － | － | － | － |
| （s2） |  | － | － | $\mathrm{O}^{*}$ | － | － | － | － | $\bigcirc$ | － | － | － | － | － |
| （d1） |  | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － | － |
| (d2) |  | $\mathrm{O}^{* 1}$ | － | ${ }^{* 3}$ | － | － | － | － | － | － | － | － | － | － |
| （d3） |  | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － | － |

＊1 FX and FY cannot be used．
＊2 FD cannot be used．
＊3 T，ST，C，and FD cannot be used．

## Point ${ }^{\rho}$

－The SWRITE instruction cannot be executed when the target station is ACPU．
－Specify the target station start device（d1），to which data is to be written，by considering the range in which the data that has been written can be stored．
（Example：When areas D150 and after in the target station CPU module are already in use）


Good example
（D50 is specified by（d1）．）

－When the target station for which the SWRITE instruction is executed is the basic model QCPU，the write notification device for the target station specified by argument（d3）is ignored．The operations of the SWRITE instruction are the same as those of the WRITE instruction．
－The SWRITE instruction can be programmed by omitting argument（d3）．However，the operations are the same as those of the WRITE instruction．The operations of the SWRITE instruction can be selected according to whether（ d 3 ）is omitted or not．

| Operand: (s1) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +0 | Execution/error completion type | b15 $\cdots$ b8 b7 $\ldots$ b0 <br> $(4)$ 0 $(3)$ $(2)$ 0     <br> (1) Execution type (bit 0) <br> - 0: No arrival acknowledgment <br> When the target station is in the same network: Completed when data has been sent from the own station. <br> When the target station is in another network: Completed when data has arrived the relay station of the own network. <br> - 1: Arrival acknowledgment used <br> Sending data is completed when the data is written to the target station. <br> (2) Error completion type (bit 7) <br> Specify whether to set clock data when completed with an error. <br> - 0 : Do not set data in ( s 1 ) +11 and later at completion with an error. <br> - 1: Set data in (s1)+11 and later at completion with an error. <br> (3) Arrival check time setting (bit 8) <br> - 0: 1s units <br> -1: 100 ms units <br> (4) Target station address specification method (bit 15) <br> - 0: Specify "network No." in (s1)+4, and "station number" in (s1)+5. <br> - 1: Specify the IP address in ( s 1 ) $+4,5$ (Ethernet only) | $\begin{aligned} & \hline 0000 \mathrm{H} \\ & 0001 \mathrm{H} \\ & 0080 \mathrm{H} \\ & 0081 \mathrm{H} \\ & 0100 \mathrm{H} \\ & 0101 \mathrm{H} \\ & 0180 \mathrm{H} \\ & 0181 \mathrm{H} \\ & 8000 \mathrm{H} \\ & 8001 \mathrm{H} \\ & 8080 \mathrm{H} \\ & 8081 \mathrm{H} \\ & 8100 \mathrm{H} \\ & 8101 \mathrm{H} \\ & 8180 \mathrm{H} \\ & 8181 \mathrm{H} \end{aligned}$ | User |
| +1 | Completion status | The instruction completion status is stored. <br> - 0: Normal <br> - Other than 0: Error (error code) | - | System |
| +2 | Own station channel | Specify the channel to be used by own station. ( $\leftrightarrows$ Page 1192 Range of available channel numbers) | 1 to 18 | User |
| +3 | Target station CPU type | Specify the CPU type of the target station. <br> - 0000H: Addressed to target station CPU (control CPU) <br> - 03DOH: Addressed to control system CPU <br> - 03D1H: Addressed to standby system CPU <br> - 03D2H: Addressed to system A CPU <br> - 03D3H: Addressed to system B CPU <br> - 03EOH: Addressed to multiple CPU No. 1 <br> - 03E1H: Addressed to multiple CPU No. 2 <br> - 03E2H: Addressed to multiple CPU No. 3 <br> - 03E3H: Addressed to multiple CPU No. 4 <br> - 03FFH: Addressed to target station CPU (control CPU) | $\begin{aligned} & \text { 0000H } \\ & \text { 03DOH to 03D3H } \\ & \text { 03EOH to 03E3H } \\ & \text { 03FFH } \end{aligned}$ | User |



| Operand: (s1) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +8 | Arrival monitoring time | [CC-Link IE Controller Network or CC-Link IE Field Network] <br> Specify the monitoring time until completion of processing. If processing is not completed within the monitoring time, the request is resent by the number of resends specified in (s1)+7. <br> ■When " 0 " is specified in bit 8 of ( s 1 ) +0 <br> - 0: 10s <br> - 1 to 32767: 1 to 32767s <br> ■When " 0 " is specified in bit 8 of ( s 1 ) 1 <br> - 0: 10s <br> - 1 to 65535 : 1 to $65535 \times 100 \mathrm{~ms}$ | 0 to 65535 | User |
|  |  | [Ethernet] <br> Specify the monitoring time until completion of processing. If processing is not completed within the monitoring time, the request is resent by the number of resends specified in ( s 1 ) +7 . <br> -When " 0 " is specified in bit 8 of ( s 1 ) +0 <br> Specify the TCP resend timer value or greater for the monitoring time till completion of processing. <br> - 0 to (TCP resend timer value): The TCP resend timer value is assumed as the monitoring time. <br> - (TCP resend timer value +1 ) to 16383: (TCP resend timer value +1 ) to 16383 s <br> ■When " 0 " is specified in bit 8 of ( s 1 ) 1 <br> - 0: 10s <br> - 1 to 65535 : 1 to $65535 \times 100 \mathrm{~ms}$ | 0 to 65535 | User |
| +9 | Write data length | Specify the number of words to be written. <br> ■Writing to RCPU, QCPU, or LCPU <br> - Channels 1 to 8 are used: 1 to 960 (words) <br> - Channels 9 and 10 are used: 1 to 8192 (words) <br> -Writing to QnACPU <br> - 1 to 480 (words) | 1 to 8192 | User |
| +10 | Not used | - | - | - |
| +11 | Clock setting flag | The validity status (valid or invalid) of the data in (s1) +12 and later is stored. Note that the data in ( s 1 ) +12 and later is not cleared even when the instruction is completed successfully. <br> - 0: Invalid <br> -1: Valid | - | System |
| +12 | Clock data (Set only in an abnormal state) | Upper 8 bits: Month ( 01 H to 12 H ) <br> Lower 8 bits: Lower 2 digits of year ( 00 H to 99 H ) | - | System |
| +13 |  | Upper 8 bits: Hour ( 00 H to 23 H ) Lower 8 bits: Day ( 01 H to 31 H ) | - | System |
| +14 |  | Upper 8 bits: Second (00H to 59H) Lower 8 bits: Minute ( 00 H to 59 H ) | - | System |
| +15 |  | Upper 8 bits: Year (00H to 99H: Upper two digits of the year) Lower 8 bits: Day of the week (00H (Sun.) to 06H (Sat.)) | - | System |
| +16 | Error detection network number | The network number of the station in which an error was detected is stored. (No information is stored if an error is detected in the own station.) <br> - 1 to 239 (Network number) | 1 to 239 | System |
| +17 | Error-detected station number | The station number of the station where an error was detected is stored. (No information is stored if an error is detected in the own station.) <br> [Ethernet or CC-Link IE Controller Network] <br> - 1 to 120: Station number <br> [CC-Link IE Field Network] <br> - 125: Master station <br> - 1 to 120: Local station, intelligent device station, submaster station | $\begin{aligned} & 1 \text { to } 120, \\ & 125 \end{aligned}$ | System |

- The continuous area (a maximum of 8192 words) for the write data length ((s1)+9) is required in the write data storage device (d1).
- When a number from 1 to 120 is specified for the target station number, the SWRITE instruction should be executed with the execution type set to "Arrival acknowledgment used". This is to improve data reliability. When a number from 81 H to AOH or FFH is specified for the target station number, the SWRITE instruction should be executed with the execution type set to "No arrival acknowledgment".
- When performing device writing to the same station from multiple stations, do not overlap the write timing. When the execution type is set to "No arrival acknowledgment", successful completion results in the write source station if communication is completed successfully even when the send data contains an error. Also, even when the send data is normal, a timeout results in the write source station if the SWRITE instructions are executed for the same station from multiple stations.
- The number of resends (s1)+7 must be set every time the SWRITE instruction is executed.


## Processing details

- These instructions write the data in the device/label specified by ( s 2 ) in the own station to the word device in the target station specified by the target network number and target station number of the control data or the target station specified by the IP address. Upon completion of writing device data to another station number, the completion device specified by (d2) turns on. In another station, upon completion of writing the device data specified by (s2), the device specified by (d3) turns on.
- For the target stations that can be specified, refer to the following.
$\longmapsto$ Page 1191 Target networks and target station types
[Own station]

[Target station]

- When "network number" and "station number" are specified (" 0 " is specified in bit 15 of ( $\mathbf{s} 1$ )+0) by the target station address specification method, device data can be written also to the stations connected to networks other than the stations connected to the own station network. (If "IP address" is specified ("1" is specified in bit 15 of ( $\mathbf{s} 1$ ) +0 ), device data cannot be read from stations connected via a relay station.)
- When executing multiple link dedicated instructions concurrently, be careful not to overlap the channels of the link dedicated instructions. Multiple link dedicated instructions specifying the same channel cannot be used concurrently.
- The execution of the SWRITE instruction and whether it has been completed normally or completed with an error can be checked with the completion device (d2) or completion status indication device (d2)+1.
- Completion device (d2)

The completion device turns on in END processing of the scan performed upon completion of the SWRITE instruction and turns off in the next END processing.

- Completion status indication device (d2)+1

The completion device turns on or off depending on the completion status of the SWRITE instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on in END processing of the scan performed upon completion of the SWRITE instruction and turns off in the next END processing.

- The following figure shows the execution timing of the SWRITE instruction.
- When completed normally

- When completed with an error


[^30]
## Operation error

| Error code $((\mathrm{s} 1)+1)$ | Description |
| :---: | :---: |
| 4000H to 4FFFH | [] MELSEC iQ-R CPU Module User's Manual (Application) |
| COOOH to CFFFH | L] MELSEC iQ-R Ethernet User's Manual (Application) |
| D000H to DFFFH | [] MELSEC iQ-R CC-Link IE Field Network User's Manual (Application) |
| E000H to EFFFH | [] MELSEC iQ-R CC-Link IE Controller Network User's Manual (Application) |

## Sending data to the programmable controller on another station

## JP.SEND, GP.SEND

These instructions send data to the programmable controller of another station.


FBD/LD


■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| JP.SEND | - |
| GP.SEND | - |

## Setting data

Description, range, data type

| Operand |  | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- | :--- |
| (J/U) | JP.SEND | (J): Own station network number | 1 to 239 | 16 -bit unsigned binary | ANY16 |
|  | GP.SEND | (U): Start I/O number (first three digits in four- <br> digit hexadecimal representation) of own station <br> or own node | 00 H to FEH | 16-bit unsigned binary | ANY16 |
| (s1) | Own station start device where control data is <br> stored | Refer to the control data. | Device name | ANY16 |  |
| (s2) | Own station head device where send data is <br> stored | - | Device name | ANY16 |  |
| (d) | Own station device to be turned on one scan <br> when the instruction completes. <br> If the instruction is completed with an error, (d) +1 <br> is also turned on. | - | Bit | ANYBIT_ARRAY |  |
| (Number of elements: |  |  |  |  |  |
| 2 2) |  |  |  |  |  |

## Point?

The SEND instruction cannot be executed when the target station is ACPU.

Applicable devices

| Operand |  | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3E［l（H）Gロ | Z | LT, LST, LC | LZ |  | K，H | E | \＄ | J | U |
| （ $\mathrm{J} / \mathrm{U}$ ） | $\begin{aligned} & \text { JP.SE } \\ & \text { ND } \end{aligned}$ | － | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － |
|  | GP．S <br> END | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － | $\bigcirc$ |
| （s1） |  | － | － | $0^{*}$ | － | － | － | － | $\bigcirc$ | － | － | － | － | － |
| （s2） |  | － | － | ${ }^{*}{ }^{2}$ | － | － | － | － | $\bigcirc$ | － | － | － | － | － |
| （d） |  | O＊1 | － | $0^{* 3}$ | － | － | － | － | － | － | － | － | － | － |

＊1 FX and FY cannot be used．
＊2 FD cannot be used
＊3 T，ST，C，and FD cannot be used．

## Control data

| Operand：（s1） |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| ＋0 | Execution／error completion type | b15 $\ldots$ b7 $\cdots$ <br> 0 $(2)$ 0 b0 <br> 0 $(1)$   <br> （1）Execution type（bit 0 ） <br> －0：No arrival acknowledgment <br> When the target station is in the same network：Completed when data has been sent from the own station． <br> When the target station is in another network：Completed when data has arrived the relay station of the own network． <br> －1：Arrival acknowledgment used <br> Completed when data has been stored in the specified channel of the target station． <br> （2）Error completion type（bit 7） <br> Specify whether to set clock data when completed with an error． <br> － 0 ：Clock data at error occurrence is not stored in $(\mathrm{s} 1)+11$ and later． <br> －1：Clock data at error occurrence is stored in（ s 1 ）+11 and later． | $\begin{aligned} & \hline 0000 \mathrm{H} \\ & 0001 \mathrm{H} \\ & 0080 \mathrm{H} \\ & 0081 \mathrm{H} \end{aligned}$ | User |
| ＋1 | Completion status | The instruction completion status is stored． <br> －0：Normal <br> －Other than 0：Error（error code） | － | System |
| ＋2 | Own station channel | Specify the channel to be used by own station．（5 Page 1192 Range of available channel numbers） | 1 to 8,11 to 18 | User |
| ＋3 | Target station storage channel | Specify the channel of the target station for storing data． （CC－Link IE Field Network only： 1 or 2） | 1 to 8 | User |


| Operand: (s1) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +4 | Target network number | Specify the network number of the target station. <br> - 1 to 239 (Network number) | 1 to 239 | User |
| +5 | Target station number | Specify the station number of the target station. <br> (1) Station number specification <br> [Ethernet or CC-Link IE Controller Network] <br> - 0001H to 0078H (1 to 120): Station number <br> [CC-Link IE Field Network] <br> - 007DH (125): Master station <br> - 007EH (126): Master operating station <br> - 0001H to 0078H (1 to 120): Local station, intelligent device station, submaster station <br> (2) Group number specification <br> [Ethernet or CC-Link IE Controller Network] <br> 0081 H to 00A0H: All stations with group numbers 01 H to 20 H <br> (Can be set when the execution type specified by (s1) is "0: No arrival acknowledgment") <br> (3) All-station specification <br> 00FFH: All stations of target network number (broadcast (excluding the own station)) (Can be set when the execution type specified by (s1) is "0: No arrival acknowledgment") | 0001 H to 0078 H , 007DH, 007EH, 0081H to 00AOH, 00FFH | User |
| +6 | Not used | - | - | - |
| +7 | Number of resends (retries) | Effective when the execution type specified by (s1) is "1: Arrival acknowledgment used". <br> -Before instruction execution <br> Specify the number of resends to be performed if the instruction is not completed within the monitoring time specified by (s1)+8. <br> - 0 to 15 (times) <br> -At completion of instruction <br> The number of resends performed (result) is stored. <br> - 0 to 15 (times) | 0 to 15 | User/ system |
| +8 | Arrival monitoring time | [CC-Link IE Controller Network or CC-Link IE Field Network] <br> Specify the monitoring time until completion of processing. If processing is not completed within the monitoring time, the request is resent by the number of resends specified in (s1)+7. <br> - 0: 10s <br> - 1 to 32767 : 1 to 32767 s | 0, 1 to 32767 | User |
|  |  | [Ethernet] <br> Specify the TCP resend timer value or greater for the monitoring time till completion of processing. If processing is not completed within the monitoring time, the request is resent by the number of resends specified in ( s 1 ) +7 (the number of resends). <br> - 0 to (TCP resend timer value): The TCP resend timer value is assumed as the monitoring time. <br> - (TCP resend timer value +1 ) to 16383: (TCP resend timer value +1 ) to 16383 s | 0 to 16383 | User |
| +9 | Send data length | Specify the number of send data from (s2) to (s2)+n. (No information is stored if an error is detected in the own station.) <br> ■Sending to RCPU, QCPU, or LCPU <br> - 1 to 960 (words) <br> - Sending to QnACPU <br> - 1 to 480 (words) | 1 to 960, 1 to 480 | User |
| +10 | Not used | - | - | - |
| +11 | Clock setting flag | The validity status (valid or invalid) of the data in ( s 1 ) +12 and later is stored. Note that the data in ( s 1 ) +12 and later is not cleared even when the instruction is completed successfully. <br> - 0: Invalid <br> - 1: Valid | - | System |
| +12 | Clock data (Set only in an abnormal state) | Upper 8 bits: Month ( 01 H to 12 H ) <br> Lower 8 bits: Lower 2 digits of year ( 00 H to 99 H ) | - | System |
| +13 |  | Upper 8 bits: Hour ( 00 H to 23 H ) Lower 8 bits: Day ( 01 H to 31H) | - | System |
| +14 |  | Upper 8 bits: Second ( 00 H to 59 H ) Lower 8 bits: Minute ( 00 H to 59 H ) | - | System |
| +15 |  | Upper 8 bits: Year (00H to 99H: Upper two digits of the year) Lower 8 bits: Day of the week (00H (Sun.) to 06H (Sat.)) | - | System |


| Operand: (s1) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +16 | Error detection network number | The network number of the station in which an error was detected is stored. (No information is stored if an error is detected in the own station.) <br> - 1 to 239 (Network number) | 1 to 239 | System |
| +17 | Error-detected station number | The station number of the station where an error was detected is stored. (No information is stored if an error is detected in the own station.) <br> [Ethernet or CC-Link IE Controller Network] <br> - 1 to 120: Station number <br> [CC-Link IE Field Network] <br> - 125: Master station <br> - 1 to 120 : Local station, intelligent device station, submaster station | 1 to 120, 125 | System |

## Point $\rho$

- The continuous area (a maximum of 960 words) for the send data length ((s1)+9) is required in the send data storage device (d2).
- When a number from 1 to 120 is specified for the target station number, the instruction should be executed with the execution type set to "Arrival acknowledgment used" to improve data reliability. When a group number or all stations are specified, the instruction should be executed with the execution type set to "No arrival acknowledgment".
- When sending data to the same channel in the receiving station, execute the instruction after data has been read by the RECV instruction in the receiving station. When the execution type is set to "No arrival acknowledgment", successful completion results in the sending station if communication is completed successfully even when the send data contains an error. Also, even when the send data is normal, a timeout results in the sending station if the instructions are executed for the same station from multiple stations.
- With the execution type set to "Arrival acknowledgment used", if the sending station sends data to the same channel in the receiving station before the receiving station reads data using the RECV instruction, a buffer full error results in the sending station.

- When multiple network modules are mounted in the target station, specify the network number and station number of the network module that receives a request from the own station.
(Example: In the following figure, specify station No. 1 of network No. 1. (Station No. 5 of network No. 2. cannot be specified.))

- The number of resends (s1)+7 must be set every time the instruction is executed.


## Processing details

- These instructions send the data in the device specified by (s2) and later in the own station to the station connected to the target station specified by the target network number and target station number of the control data. The data that has been sent is stored in the channel specified by (s2) in the target station. To read the sent data in the target station, use the RECV/ RECVS instruction. Upon completion of sending data to the target station number, the completion device specified by (d) turns on.
- For the target stations that can be specified, refer to the following.
$\longmapsto$ Page 1191 Target networks and target station types

- Data can be sent not only to the stations connected to the own station network but also to stations connected to the network number specified by MELSECNET/H, MELSECNET/10, or Ethernet.
- When executing multiple link dedicated instructions concurrently, be careful not to overlap the channels of the link dedicated instructions. Multiple link dedicated instructions specifying the same channel cannot be used concurrently.
- The execution of the SEND instruction and whether it has been completed normally or completed with an error can be checked with the completion device (d) or completion status indication device (d)+1.
- Completion device (d)

The completion device turns on in END processing of the scan performed upon completion of the SEND instruction and turns off in the next END processing.

- Completion status indication device (d)+1

The completion device turns on or off depending on the completion status of the SEND instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on in END processing of the scan performed upon completion of the SEND instruction and turns off in the next END processing.

- The following figure shows the execution timing of the SEND instruction.
- When completed normally

- When completed with an error

- Send processing is performed only once on the rising edge when the send command turns on.


## Operation error

| Error code ((s1)+1) | Description |
| :---: | :---: |
| 4000H to 4FFFH | [] MELSEC iQ-R CPU Module User's Manual (Application) |
| C000H to CFFFH | [] MELSEC iQ-R Ethernet User's Manual (Application) |
| D000H to DFFFH | [] MELSEC iQ-R CC-Link IE Field Network User's Manual (Application) |
| E000H to EFFFH | [] MELSEC iQ-R CC-Link IE Controller Network User's Manual (Application) |

## Receiving data from the programmable controller on another station

## JP．RECV，GP．RECV

These instructions read the data received from the programmable controller of another station（for the main routine program）．


| ■－－－$\square$ |  |
| :---: | :---: |
| EN | ENO |
| JU | d1 |
| s | d2 |

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| JP．RECV | $\uparrow$ |
| GP．RECV | - |

Setting data
Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- | :--- |
| （J／U） | JP．RECV | （J）：Own station network number | 1 to 239 | 16－bit unsigned binary | ANY16 |
|  | GP．RECV | （U）：Start I／O number（first three digits in four－ <br> digit hexadecimal representation）of own station <br> or own node | 00 H to FEH | 16－bit unsigned binary | ANY16 |
| （s） | Own station start device where control data is <br> stored | Refer to the control data． | Device name | ANY16 |  |
| （d1） | Own station head device for storing the receive <br> data <br> （A continuous area for the receive data length is <br> required．） | - | Device name | ANY16 |  |
| （d2） | Own station device to be turned on one scan <br> when the instruction completes． <br> （d2）＋1 also turns on when the instruction <br> completes with an error． | - | Bit | ANYBIT＿ARRAY <br> （Number of elements： <br> $2)$ |  |
| EN | Execution condition | - | Bit | BOOL |  |
| ENO | Execution result | - | Bit | BOOL |  |

Applicable devices

| Operand |  | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDI（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ | J | U |
| （J／U） | $\begin{aligned} & \text { JP.RE } \\ & \text { CV } \end{aligned}$ | － | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － |
|  | $\begin{aligned} & \text { GP.R } \\ & \text { ECV } \end{aligned}$ | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － | $\bigcirc$ |
| （s） |  | － | － | $\mathrm{O}^{*}$ | － | － | － | － | $\bigcirc$ | － | － | － | － | － |
| （d1） |  | － | － | $\mathrm{O}^{*}$ | － | － | － | － | $\bigcirc$ | － | － | － | － | － |
| （d2） |  | $0^{* 1}$ | － | $0^{* 3}$ | － | － | － | － | － | － | － | － | － | － |

＊1 FX and FY cannot be used．
＊2 FD cannot be used
＊3 T，ST，C，and FD cannot be used．

Control data

| Operand: (s) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +0 | Error completion type | (1) Error completion type (bit 7) <br> Specify whether to set clock data when completed with an error. <br> - 0: Clock data at error occurrence is not stored in ( s 1 ) +11 and later. <br> - 1: Clock data at error occurrence is stored in ( s 1 ) +11 and later. | $\begin{aligned} & 0000 \mathrm{H} \\ & 0080 \mathrm{H} \end{aligned}$ | User |
| +1 | Completion status | The instruction completion status is stored. <br> - 0: Normal <br> - Other than 0: Error (error code) | - | System |
| +2 | Own station storage channel | Specify the channel to be used by own station. (凸 Page 1192 Range of available channel numbers) | 1 to 8, 11 to 18 | User |
| +3 | Channel used by send station | The channel number ( 1 to 8) used by the sending station is stored. | 1 to 8 | System |
| +4 | Send station network number | The network number (1 to 239) of the sending station is stored. | 1 to 239 | System |
| +5 | Sending-station No. | The station number of the sending station is stored. <br> [Ethernet or CC-Link IE Controller Network] <br> - 1 to 120: Station number (receiving from station number) [CC-Link IE Field Network] <br> - 125: Master station <br> - 1 to 120 : Local station, intelligent device station, submaster station | 1 to 120, 125 | System |
| +6 | Not used | - | - | - |
| +7 | Not used | - | - | - |
| +8 | Arrival monitoring time | [CC-Link IE Controller Network or CC-Link IE Field Network] <br> Specify the monitoring time until completion of processing. The instruction is completed with an error if it fails to complete within the monitoring time. <br> - 0: 10s <br> - 1 to 32767: 1 to 32767 s | 0, 1 to 32767 | User |
|  |  | [Ethernet] <br> Specify the TCP resend timer value or greater for the monitoring time till completion of processing. The instruction is completed with an error if it fails to complete within the monitoring time. <br> - 0 to (TCP resend timer value): The TCP resend timer value is assumed as the monitoring time. <br> - (TCP resend timer value +1 ) to 16383: (TCP resend timer value +1 ) to 16383 s | 0 to 16383 | User |
| +9 | Receive data length | The number of data received and stored in (d1) to (d1)+n is stored. <br> - 0: No receive data <br> - 1 to 960 : Number of words of received data | 0 to 960 | System |
| +10 | Not used | - | - | - |
| +11 | Clock setting flag | The validity status (valid or invalid) of the data in (s) +12 and later is stored. Note that the data in (s) +12 and later is not cleared even when the instruction is completed successfully. <br> - 0: Invalid <br> - 1: Valid | - | System |
| +12 | Clock data (Set only in an abnormal state) | Upper 8 bits: Month ( 01 H to 12 H ) <br> Lower 8 bits: Lower 2 digits of year ( 00 H to 99 H ) | - | System |
| +13 |  | Upper 8 bits: Hour ( 00 H to 23 H ) <br> Lower 8 bits: Day ( 01 H to 31H) | - | System |
| +14 |  | Upper 8 bits: Second ( 00 H to 59 H ) <br> Lower 8 bits: Minute ( 00 H to 59 H ) | - | System |
| +15 |  | Upper 8 bits: Year ( 00 H to 99 H : Upper two digits of the year) Lower 8 bits: Day of the week (00H (Sun.) to 06H (Sat.)) | - | System |
| +16 | Error detection network number | The network number of the station in which an error was detected is stored. (No information is stored if an error is detected in the own station.) <br> - 1 to 239 (Network number) | 1 to 239 | System |


| Operand: (s) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +17 | Error-detected station number | The station number of the station where an error was detected is stored. (No information is stored if an error is detected in the own station.) <br> [Ethernet or CC-Link IE Controller Network] <br> - 1 to 120: Station number <br> [CC-Link IE Field Network] <br> - 125: Master station <br> - 1 to 120: Local station, intelligent device station, submaster station | 1 to 120, 125 | System |

## Point ${ }^{\rho}$

- The continuous area (a maximum of 960 words) for the receive data length $((s)+9)$ is required in the receive data storage device (d1).
- The RECV instruction is executed each time the bit of the relevant channel in the RECV instruction request area (address 205) in the buffer memory is set to ON (received data exists).
- When multiple network modules with the same network number are mounted in the own station, Specify "U" (start I/O number) to execute the RECV instruction. The RECV instruction cannot be executed by specifying "J" (network number).
(Example: When executing the GP.RECV instruction in station number 3 with regard to the SEND instruction from station number 2, specify "U2".)



## Processing details

- These instructions read the data sent from the station connected to the target station specified by the network number and station number of control data from the module of the own station. Upon completion of reading the received data from the network module, the completion device specified by (d2) turns on.
- For the target stations that can be specified, refer to the following.
$\longmapsto$ Page 1191 Target networks and target station types

- The SEND instruction is executed to send data. The data received from the sending station is stored in the own station channel specified by the sending station and the corresponding bit in the RECV instruction execution request area for each network module is set to ON. When the relevant bit in the RECV instruction execution request area is set to ON, the received data is read from the receive data storage channel. The following table lists the RECV instruction execution request areas for individual networks.

| Network | Name of RECV instruction execution request area | Device number/address of applicable bit |
| :---: | :---: | :---: |
| CC-Link IE Field Network CC-Link IE Controller Network | RECV instruction channel 1 execution request flag | SB0030 |
|  | RECV instruction channel 2 execution request flag | SB0031 |
|  | RECV instruction channel 3 execution request flag | SB0032 |
|  | RECV instruction channel 4 execution request flag | SB0033 |
|  | RECV instruction channel 5 execution request flag | SB0034 |
|  | RECV instruction channel 6 execution request flag | SB0035 |
|  | RECV instruction channel 7 execution request flag | SB0036 |
|  | RECV instruction channel 8 execution request flag | SB0037 |
| Ethernet (Q series compatible) | RECV instruction execution request area | Buffer memory address 205 (CDH) bit 0 |
|  |  | Buffer memory address 205 (CDH) bit 1 |
|  |  | Buffer memory address 205 (CDH) bit 2 |
|  |  | Buffer memory address 205 (CDH) bit 3 |
|  |  | Buffer memory address 205 (CDH) bit 4 |
|  |  | Buffer memory address 205 (CDH) bit 5 |
|  |  | Buffer memory address 205 (CDH) bit 6 |
|  |  | Buffer memory address 205 (CDH) bit 7 |
| $\text { Ethernet }{ }^{* 1}$ | RECV instruction execution request area | Buffer memory address 5301 (14B5H) bit 0 |
|  |  | Buffer memory address 5301 (14B5H) bit 1 |
|  |  | Buffer memory address 5301 (14B5H) bit 2 |
|  |  | Buffer memory address 5301 (14B5H) bit 3 |
|  |  | Buffer memory address 5301 (14B5H) bit 4 |
|  |  | Buffer memory address 5301 (14B5H) bit 6 |
|  |  | Buffer memory address 5301 (14B5H) bit 7 |

*1 When the network type of the Ethernet module is "Ethernet", the buffer memory address of PORT2 is increased by 2000000. [] MELSEC iQ-R Ethernet User's Manual (Application)

- When executing multiple link dedicated instructions concurrently, be careful not to overlap the channels of the link dedicated instructions. Multiple link dedicated instructions specifying the same channel cannot be used concurrently.
- When the RECV instruction is used to read receive data from the same channel, the RECVS instruction (for use in interrupt programs) cannot be used in combination.
- The execution of the RECV instruction and whether it has been completed normally or completed with an error can be checked with the completion device (d2) or completion status indication device (d2)+1.
- Completion device (d2)

The completion device turns on in END processing of the scan performed upon completion of the RECV instruction and turns off in the next END processing.

- Completion status indication device (d2)+1

The completion device turns on or off depending on the completion status of the RECV instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on in END processing of the scan performed upon completion of the RECV instruction and turns off in the next END processing.

- The following figure shows the execution timing of the RECV instruction.
- When completed normally

For the execution timing, refer to the following functions.
$\longmapsto$ Page 1225 JP.SEND, GP.SEND

- When completed with an error

- Read processing is performed only once on the rising edge when the read command turns on.


## Operation error

| Error code ((s)+1) | Description |
| :--- | :--- |
| C000H to CFFFH | L MELSEC iQ-R Ethernet User's Manual (Application) |
| D000H to DFFFH | L $\quad$ MELSEC iQ-R CC-Link IE Field Network User's Manual (Application) |
| E000H to EFFFH | La MELSEC iQ-R CC-Link IE Controller Network User's Manual (Application) |

# Receiving data from the programmable controller on another station (for interrupt programs) 

## G.RECVS, Z.RECVS

These instructions read the data received from the programmable controller of another station (for interrupt programs).


| ■--- $]$ |  |
| :---: | :---: |
| EN | ENO |
| u | d1 |
| s | d2 |

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| G.RECVS | $-\square$ |
| Z.RECVS | $\square$ |

## Setting data

Description, range, data type

| Operand |  | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- | :--- |
| (U) | G.RECVS | Start I/O number (first three digits in four-digit <br> hexadecimal representation) of own station or <br> own node | 00 H to FEH | 16-bit unsigned binary | ANY16 |
|  | Z.RECVS | Start I/O number (first three digits in four-digit <br> hexadecimal representation) of own station or <br> own node | 00 H to FEH | String | ANY16_OR_STRING_ <br> SINGLE |
| (s) | Own station start device where control data is <br> stored | Refer to the control data. | Device name | ANY16 |  |
| (d1) | Own station head device for storing the receive <br> data <br> (A continuous area for the receive data length is <br> required.) | - | Device name | ANY16 |  |
| (d2) | Dummy | - | Bit | ANYBIT_ARRAY |  |
| (Number of elements: |  |  |  |  |  |
| 2) |  | - | Bit | BOOL |  |
| EN | Execution condition | - | Bit | BOOL |  |
| ENO | Execution result |  |  |  |  |

Applicable devices

| Operand |  | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J밈， U3E미（H）G口 | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （U） | $\begin{aligned} & \text { G.RE } \\ & \text { CVS } \end{aligned}$ | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ |
|  | $\begin{aligned} & \text { Z.RE } \\ & \text { CVS } \end{aligned}$ | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ |
| （s） |  | － | － | ${ }^{*}{ }^{2}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） |  | － | － | $\bigcirc^{*}{ }^{2}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d2） |  | O＊1 | － | ${ }^{* 3}$ | － | － | － | － | － | － | － | － | － |

＊1 FX and FY cannot be used．
＊2 FD cannot be used．
＊3 T，ST，C，and FD cannot be used．

## Control data

| Operand：（s） |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| ＋0 | System area | － | － | － |
| ＋1 | Completion status | The instruction completion status is stored． <br> －0：Normal <br> －Other than 0：Error（error code） | － | System |
| ＋2 | Own station storage channel | Specify the channel to be used by own station．（ $\leftrightarrows$ Page 1192 Range of available channel numbers） | 1 to 8,11 to 18 | User |
| ＋3 | Channel used by send station | The channel number（ 1 to 8）used by the sending station is stored． | 1 to 8 | System |
| ＋4 | Send station network number | The network number（1 to 239）of the sending station is stored． | 1 to 239 | System |
| ＋5 | Sending－station No． | The station number of the sending station is stored． <br> ［Ethernet or CC－Link IE Controller Network］ <br> － 1 to 120：Station number（receiving from station number） ［CC－Link IE Field Network］ <br> －125：Master station <br> － 1 to 120：Local station，intelligent device station，submaster station | 1 to 120， 125 | System |
| ＋6 to＋8 | System area | － | － | － |
| ＋9 | Receive data length | The number of data received and stored in（d1）＋0 to（d1）＋$\square$ is stored． <br> －0：No receive data <br> － 1 to 960 ：Number of words of received data | 0 to 960 | System |

## Receive data

| Operand：（d1） |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| ＋0 to＋$\square$ | Receive data | Receive data is stored． | － | System |

## Point？

The continuous area（a maximum of 960 words）for the receive data length $((s)+9)$ is required in the receive data storage device（d1）．

## Processing details

- These instructions read the receive data of the target station sent to (s)+2 (own station storage channel) of control data.
- For the target stations that can be specified, refer to the following.
$\leftrightarrows$ Page 1191 Target networks and target station types
- The SEND instruction is executed to send data. The data received from the sending station is stored in the own station channel specified by the sending station, and the interrupt program with the interrupt number specified by the engineering tool starts. The RECVS instruction is used in this interrupt program to read receive data.

- The following figure shows the execution timing of the RECVS instruction.
- When completed normally

- When completed with an error

Network
module


- The RECVS instruction is used in interrupt programs and processing is completed in a single scan.
- When the RECVS instruction is used to read receive data from the same channel, the RECV instruction (for use in the main program) cannot be used in combination.


## Operation error

| Error code ((s)+1) | Description |
| :--- | :--- |
| C 000 H to CFFFH | $\square$ MELSEC iQ-R Ethernet User's Manual (Application) |
| D000H to DFFFH | $\square \square$ MELSEC iQ-R CC-Link IE Field Network User's Manual (Application) |
| E000H to EFFFH | $\square$ MELSEC iQ-R CC-Link IE Controller Network User's Manual (Application) |

## Reading data from the programmable controller (Q seriescompatible)

## J(P).ZNRD

These instructions read data in units of words from a device in the programmable controller (MELSEC-A/QnA/Q/L series) of another station.

| Ladder |  |  |  |  |  |  | $\begin{aligned} & \text { ST } \\ & \text { ENO:=J_ZNRD(EN,J,s1,s2,n,d1,d2); } \\ & \text { ENO:=JP_ZNRD(EN,J,s1,s2,n,d1,d2); } \end{aligned}$ |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  |  |  |  |  |
|  | (J) | (s1) | (s2) | (d1) | ( n ) | (d2) |  |  |

FBD/LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| J.ZNRD | - |
|  | $\boxed{ }$ |
| JP.ZNRD | - |

## Setting data

Descriptions, ranges, and data types

| Operand | Description | Range | Data type | Data type (label) |
| :---: | :---: | :---: | :---: | :---: |
| (J) | Target station network number | 1 to 239 | 16-bit unsigned binary | ANY16 |
| (s1) | Target station number | 1 to 64 | 16-bit unsigned binary | ANY16 |
| (s2) | Target station start device where the data to be read is stored | - | String** | $\underset{1}{\text { ANYSTRING_SINGLE* }}$ |
| (d1) | Own station start device for storing the read data (A continuous area for the read data length is required.) | - | Device name | ANY16 |
| ( n ) | Read data length | ■When the target station is AnUCPU/QnACPU/QCPU/ LCPU <br> 1 to 230 <br> -When the target station is a MELSEC-A series CPU module other than AnUCPU 1 to 32 | 16-bit unsigned binary | ANY16 |
| (d2) | Own station device to be turned on one scan when the instruction completes. (d2)+1 also turns on when the instruction completes with an error. | - | Bit | ANYBIT_ARRAY <br> (Number of elements: <br> 2) |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

[^31]W Page 1193 Specifications of character string data specified by link dedicated instructions

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> (J) |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ\|ロ | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | U밈ㅁ, J밈, U3EDI(H)GD | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (J) | - | - | - | - | - | - | - | - | - | - | - | $\bigcirc$ |
| (s1) | O*1 | - | $0^{* 2}$ | - | - | - | - | $\bigcirc$ | $\bigcirc$ | - | - | - |
| (s2) | - | - | - | - | - | - | - | - | - | - | $\bigcirc$ | - |
| (d1) | - | - | $0^{*}$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| ( n ) | $\bigcirc^{* 1}$ | - | $0^{*}$ | - | - | - | - | $\bigcirc$ | $\bigcirc$ | - | - | - |
| (d2) | $\bigcirc{ }^{* 1}$ | - | $0^{* 3}$ | - | - | - | - | - | - | - | - | - |

*1 FX and FY cannot be used.
*2 FD cannot be used.
*3 T, ST, C, and FD cannot be used.

## Point $P$

The $J(P)$.ZNRD instruction is executed using the following fixed values in addition to setting data.

- Arrival monitoring time: 10s
- Number of resends: 5


## Processing details

- These instructions read the data from the specified word device in the target station specified by the target network number ( J ) and target station number ( s 1 ). Upon completion of reading the device data, the completion device specified by (d2) turns on.
- For the target stations that can be specified, refer to the following.
$\longmapsto$ Page 1191 Target networks and target station types

[Target station]

- The $J(P)$.ZNRD instruction cannot be executed for RCPU. If executed, error code 4001 H is stored in the completion status and the instruction is completed with an error.
- The $J(P)$.ZNRD instruction is an instruction (Q series compatible instruction) for replacement of the equivalent instruction used in the programs running on the MELSEC-Q series. When creating a new program, use the READ instruction.
- Device data can be read also from stations connected to networks other than the stations connected to the own station network.
- The execution of the $J(P) . Z N R D$ instruction and whether it has been completed normally or completed with an error can be checked with the completion device (d2) or completion status indication device (d2)+1.
- Completion device (d2)

The completion device turns on in END processing of the scan performed upon completion of the $J(P)$.ZNRD instruction and turns off in the next END processing.

- Completion status indication device (d2)+1

The completion device turns on or off depending on the completion status of the $J(P)$.ZNRD instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on in END processing of the scan performed upon completion of the $J(P)$.ZNRD instruction and turns off in the next END processing.

- The following figure shows the execution timing of the $J(P)$.ZNRD instruction.
- When completed normally

- When completed with an error


The completion status is stored in the special register (SW) when the CC-Link IE Controller Network is used or in the buffer memory when Ethernet is used.

- When the $J(P)$.ZNRD instruction is executed, read processing is performed only once on the rising edge when the read command turns on.


## Precautions

The $J(P)$.ZNRD instruction cannot be executed when the CPU module on the target station is one of the following:

- AnUCPU with the version AX (manufactured in July 1995) or earlier
- A2USCPU(-S1) with the version CN (manufactured in July 1995) or earlier
- A2ASCPU(-S1) with the version CN (manufactured in July 1995) or earlier

If executed, the dedicated instruction response timer causes a timeout in the instruction start source and the $J(P)$.ZNRD instruction is completed with an error. If the dedicated instruction response timer causes a timeout, use a CPU module satisfying one of the following versions.

- AnUCPU with the version AY (manufactured in July 1995) or later
- A2USCPU(-S1) with the version CP (manufactured in July 1995) or later
- A2ASCPU(-S1) with the version CP (manufactured in July 1995) or later


## Operation error

| Error code ${ }^{* 4}$ | Description |
| :--- | :--- |
| 4000 H to 4 FFFH | La MELSEC iQ-R CPU Module User's Manual (Application) |
| C000H to CFFFH | La MELSEC iQ-R Ethernet User's Manual (Application) |
| E000H to EFFFH | La MELSEC iQ-R CC-Link IE Controller Network User's Manual (Application) |

*4 The completion status in which an error code is stored is as follows.
CC-Link IE Controller Network: SW003A
Ethernet: Buffer memory address 5323 (14CBH)

## Writing data to the programmable controller (Q seriescompatible)

## J(P).ZNWR

These instructions write data in units of words to a device in the programmable controller (MELSEC-A/QnA/Q/L series) of another station.

| Ladder |  |  |  |  |  |  | ```ST ENO:=J_ZNWR(EN,J,s1,s2,n,d1,d2); ENO:=JP_ZNWR(EN,J,s1,s2,n,d1,d2);``` |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | (J) | (s1) | (d1) | (s2) | ( n ) |  |  |  |


|  |  |
| :---: | :---: |
| EN | ENO |
| J | d2 |
| s1 |  |
| s2 |  |
| n |  |
| d1 |  |

## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| J.ZNWR | - |
|  | - |
| JP.ZNWR | - |

Setting data
■Descriptions，ranges，and data types

| Operand | Description | Range | Data type | Data type（label） |
| :---: | :---: | :---: | :---: | :---: |
| （J） | Target station network number | 1 to 239 | 16－bit unsigned binary | ANY16 |
| （s1） | Target station number | ■Station number specification 1 to 64：Station number station ■Group specification 0081H to 00AOH：All stations with group numbers 1 to 32 ■All－station specification 00FFH：All stations of target network number | 16－bit unsigned binary | ANY16 |
| （d1） | Target station start device to which data is to be written <br> （A continuous area for the write data length is required．） | － | String＊1 | ANYSTRING＿SINGLE＊1 |
| （s2） | Own station start device where write data is stored | － | Device name | ANY16 |
| （ n ） | Write data length | －When the target station is AnUCPU／QnACPU／QCPU／ LCPU <br> 1 to 230 <br> ■When the target station is a MELSEC－A series CPU module other than AnUCPU 1 to 32 | 16－bit unsigned binary | ANY16 |
| （d2） | Own station device to be turned on one scan when the instruction completes． （d2）＋1 also turns on when the instruction completes with an error． | － | Bit | ANYBIT＿ARRAY <br> （Number of elements：2） |
| EN | Execution condition | － | Bit | BOOL |
| ENO | Execution result | － | Bit | BOOL |

＊1 For the specifications of the string data to be specified，refer to the following．
$\mapsto$ Page 1193 Specifications of character string data specified by link dedicated instructions

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （J） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3Eपl（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （J） | － | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ |
| （s1） | ${ }^{* 1}$ | － | $\bigcirc^{* 2}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d1） | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － |
| （s2） | － | － | ${ }^{*}{ }^{2}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $0^{* 1}$ | － | $\mathrm{O}^{*}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d2） | O＊1 | － | $0^{* 3}$ | － | － | － | － | － | － | － | － | － |

＊1 FX and FY cannot be used．
＊2 FD cannot be used
＊3 T，ST，C，and FD cannot be used．

## Point $P$

The $J(P)$ ．ZNWR instruction is executed using the following fixed values in addition to setting data．
－Arrival monitoring time：10s
－Number of resends： 5

## Processing details

- These instructions write the data in the device/label specified by (s2) in the own station to the word device in the target station specified by the target network number and target station number of the control data. Upon completion of writing device data to another station number, the completion device specified by (d2) turns on.
- For the target stations that can be specified, refer to the following.
$\backsim$ Page 1191 Target networks and target station types
[Own station]

[Target station]



## Point ${ }^{\circ}$

- The $J(P)$.ZNWR instruction cannot be executed for RCPU. If executed, error code 4001 H is stored in the completion status and the instruction is completed with an error.
- The $J(P)$.ZNWR instruction is an instruction (Q series compatible instruction) for replacement of the equivalent instruction used in the programs running on the MELSEC-Q series. When creating a new program, use the WRITE instruction.
- Device data can be written also to stations connected to networks other than the stations connected to the own station network.
- The execution of the $J(P)$.ZNWR instruction and whether it has been completed normally or completed with an error can be checked with the completion device (d2) or completion status indication device (d2)+1.

[^32]- The following figure shows the execution timing of the $J(P)$.ZNWR instruction.
- When completed normally

- When completed with an error


The completion status is stored in the special register (SW) when the CC-Link IE Controller Network is used or in the buffer memory when Ethernet is used.

- When the J.ZNWR instruction is used for execution, one complete cycle of write processing is followed by another while the write command is on. When the JP.ZNWR instruction is executed, write processing is performed only once on the rising edge when the write command turns on.


## Precautions

The $J(P)$.ZNWR instruction cannot be executed for the following CPU modules.

- AnUCPU with the version AX (manufactured in July 1995) or earlier
- A2USCPU(-S1) with the version CN (manufactured in July 1995) or earlier
- A2ASCPU(-S1) with the version CN (manufactured in July 1995) or earlier

If executed, the dedicated instruction response timer causes a timeout in the instruction start source and the J(P).ZNWR instruction is completed with an error. If the dedicated instruction response timer causes a timeout, use a CPU module satisfying one of the following versions.

- AnUCPU with the version AY (manufactured in July 1995) or later
- A2USCPU(-S1) with the version CP (manufactured in July 1995) or later
- A2ASCPU(-S1) with the version CP (manufactured in July 1995) or later


## Operation error

| Error code ${ }^{* 4}$ | Description |
| :--- | :--- |
| 4000 H to 4 FFFH | La MELSEC iQ-R CPU Module User's Manual (Application) |
| C 000 H to CFFFH | L C MELSEC iQ-R Ethernet User's Manual (Application) |
| E000H to EFFFH | La MELSEC iQ-R CC-Link IE Controller Network User's Manual (Application) |

*4 The completion status in which an error code is stored is as follows.
CC-Link IE Controller Network: SW003B
Ethernet: Buffer memory address 5325 (14CDH)

## Remote RUN/STOP

J(P).REQ, G(P).REQ
These instructions execute remote RUN or STOP for the programmable controller of another station.


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| J.REQ | - |
| G.REQ | - |
| JP.REQ | - |
| GP.REQ |  |

## Setting data

## ■Description, range, data type

| Operand |  | Description | Range | Data type | Data type (label) |
| :---: | :---: | :---: | :---: | :---: | :---: |
| (J/U) | $J(P)$.REQ | (J): Own station network number | 1 to 239 | 16-bit unsigned binary | ANY16 |
|  | $\begin{aligned} & \mathrm{G}(\mathrm{P}) \cdot \mathrm{RE} \\ & \mathrm{Q} \end{aligned}$ | (U): Start I/O number (first three digits in fourdigit hexadecimal representation) of own station or own node | OOH to FEH | 16-bit unsigned binary | ANY16 |
| (s1) |  | Own station start device where control data is stored | Refer to the control data. | Device name | ANY16 |
| (s2) |  | Own station start device where request data is stored | - | Device name | ANY16 |
| (d1) |  | Own station start device for storing response data | - | Device name | ANY16 |
| (d2) |  | Own station device to be turned on one scan when the instruction completes. (d2)+1 also turns on when the instruction completes with an error. | - | Bit | ANYBIT_ARRAY <br> (Number of elements: <br> 2) |
| EN |  | Execution condition | - | Bit | BOOL |
| ENO |  | Execution result | - | Bit | BOOL |

Applicable devices

| Operand |  | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $\begin{aligned} & X, Y, M, L, S M, \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | J밈 | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | U $\square \backslash \mathrm{IG}$, J $\square \ \square$, U3E $\square$ (H)G $\square$ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ | J | U |
| (J/U) | $\begin{aligned} & \mathrm{J}(\mathrm{P}) . \\ & \mathrm{REQ} \end{aligned}$ | - | - | - | - | - | - | - | - | - | - | - | $\bigcirc$ | - |
|  | $\begin{aligned} & \mathrm{G}(\mathrm{P}) . \\ & \mathrm{REQ} \end{aligned}$ | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | $\bigcirc$ | - | - | - | $\bigcirc$ |
| (s1) |  | - | - | $\bigcirc{ }^{*}$ | - | - | - | - | $\bigcirc$ | - | - | - | - | - |
| (s2) |  | - | - | $\bigcirc{ }^{*}$ | - | - | - | - | $\bigcirc$ | - | - | - | - | - |
| (d1) |  | - | - | $\bigcirc^{* 2}$ | - | - | - | - | $\bigcirc$ | - | - | - | - | - |
| (d2) |  | $\bigcirc{ }^{* 1}$ | - | $\bigcirc{ }^{*}$ | - | - | - | - | - | - | - | - | - | - |

*1 FX and FY cannot be used.
*2 FD cannot be used.
*3 T, ST, C, and FD cannot be used.

## Point ${ }^{\rho}$

The REQ instruction cannot be executed when the target station is ACPU.
Control data

| Operand: (s1) |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description |  |  |  |  | Setting range | Set by |
| +0 | Error completion type |  |  |  |  |  |  | User |
| +1 | Completion status | The instruction completion status is stored. <br> - 0: Normal <br> - Other than 0: Error (error code) |  |  |  |  | - | System |
| +2 | Own station channel | Specify the channel to be used by own station. ( $\leftrightarrows$ Page 1192 Range of available channel numbers) |  |  |  |  | 1 to 8,11 to 18 | User |
| +3 | Target station CPU type | Specify the CPU type of the target station. <br> - 0000H: Addressed to target station CPU (control CPU) <br> - 03DOH: Addressed to control system CPU <br> - 03D1H: Addressed to standby system CPU <br> - 03D2H: Addressed to system A CPU <br> - 03D3H: Addressed to system B CPU <br> - 03EOH: Addressed to multiple CPU No. 1 <br> - 03E1H: Addressed to multiple CPU No. 2 <br> - 03E2H: Addressed to multiple CPU No. 3 <br> - 03E3H: Addressed to multiple CPU No. 4 <br> - 03FFH: Addressed to target station CPU (control CPU) |  |  |  |  | 0000H <br> 03D0H to 03D3H <br> 03EOH to 03E3H <br> 03FFH | User |
| +4 | Target network number | Specify the network number of the target station. <br> - 1 to 239: Network number |  |  |  |  | 1 to 239 | User |
| +5 | Target station number | Specify the station number of the target station. <br> (1) Station number specification <br> [Ethernet or CC-Link IE Controller Network] <br> - 1 to 120: Station number <br> [CC-Link IE Field Network] <br> - 125: Master station <br> - 126: Master operating station <br> - 1 to 120: Local station, intelligent device station, submaster station <br> (2) Group number specification <br> [Ethernet or CC-Link IE Controller Network] <br> 0081 H to 00A0H: All stations with group numbers 0001 H to 0020 H <br> (Can be specified for remote RUN/STOP) <br> (3) All-station specification <br> 00FFH: All stations of target network number (broadcast (excluding the own station)) (Can be specified for remote RUN/STOP) |  |  |  |  | $\begin{aligned} & 1 \text { to } 120, \\ & 125,126, \\ & 0081 \mathrm{H} \text { to } 00 \mathrm{AOH}, \\ & 00 \mathrm{FFH} \end{aligned}$ | User |
| +6 | Not used | - |  |  |  |  | - | - |


| Operand: (s1) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +7 | Number of resends | -Before instruction execution <br> Specify the number of resends to be performed if the instruction is not completed within the monitoring time specified by ( $\mathbf{s} 1$ ) +8 . <br> - 0 to 15 (times) <br> ■At completion of instruction <br> The number of resends performed (result) is stored. <br> - 0 to 15 (times) | 0 to 15 | User/ system |
| +8 | Arrival monitoring time | [CC-Link IE Controller Network or CC-Link IE Field Network] <br> Specify the monitoring time until completion of processing. If processing is not completed within the monitoring time, the request is resent by the number of resends specified in ( s 1 ) +7 . <br> - 0: 10s <br> - 1 to 32767: 1 to 32767s | 0, 1 to 32767 | User |
|  |  | [Ethernet] <br> Specify the monitoring time until completion of processing. If processing is not completed within the monitoring time, the request is resent by the number of resends specified in ( s 1 ) +7 . <br> - 0 to (TCP resend timer value): The TCP resend timer value is assumed as the monitoring time. <br> - (TCP resend timer value +1 ) to 16383: (TCP resend timer value +1 ) to 16383 s | 0 to 16383 | User |
| +9 | Request data length | Specify the number of request data (words). <br> (Number of words of data stored in the request data storage device (s2)) <br> - Remote RUN: 4 <br> - Remote STOP: 3 | 3,4 | User |
| +10 | Response data length | The number of response data (words) is stored. <br> (Number of words of data stored in the response data storage device) <br> - Remote RUN/STOP: 2 | 2 | System |
| +11 | Clock setting flag | The validity status (valid or invalid) of the data in (s1)+12 and later is stored. Note that the data in ( $\mathbf{s} 1$ ) +12 and later is not cleared even when the instruction is completed successfully. <br> - 0: Invalid <br> -1: Valid | - | System |
| +12 | Clock data <br> (Set only in an abnormal state) (No information is stored if an error is detected in the own station.) | Upper 8 bits: Month ( 01 H to 12 H ) <br> Lower 8 bits: Lower 2 digits of year ( 00 H to 99 H ) | - | System |
| +13 |  | Upper 8 bits: Hour ( 00 H to 23 H ) Lower 8 bits: Day ( 01 H to 31H) | - | System |
| +14 |  | Upper 8 bits: Second (00H to 59H) Lower 8 bits: Minute ( 00 H to 59 H ) | - | System |
| +15 |  | Upper 8 bits: Year (00H to 99H: Upper two digits of the year) Lower 8 bits: Day of the week (00H (Sun.) to 06H (Sat.)) | - | System |
| +16 | Error detection network number | The network number of the station in which an error was detected is stored. (No information is stored if an error is detected in the own station.) <br> - 1 to 239 (Network number) | 1 to 239 | System |
| +17 | Error-detected station number | The station number of the station where an error was detected is stored. (No information is stored if an error is detected in the own station.) <br> [Ethernet or CC-Link IE Controller Network] <br> - 1 to 120: Station number <br> [CC-Link IE Field Network] <br> - 125: Master station <br> - 1 to 120 : Local station, intelligent device station, submaster station | 1 to 120, 125 | System |

Request data

| Operand: (s2) |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Remote RUN | Remote STOP | Set by |
| +0 | Request type | - 0010H: When the station number is specified by ( s 1 ) +5 <br> - 0030H: When All Station or the group is specified by ( s 1 ) +5 | $\bigcirc$ | $\bigcirc$ | User |
| +1 | Subrequest type | - 0001H: Remote RUN <br> - 0002H: Remote STOP | $\bigcirc$ | $\bigcirc$ | User |
| +2 | Operation mode | Specify whether to forcibly execute Remote RUN/STOP. <br> -Remote RUN <br> - 0001H: Do not forcibly execute. <br> - 0003H: Forcibly execute (can be specified when executing Remote RUN). <br> -Remote STOP <br> - 0003H (fixed) <br> (Forced execution is the function used, when the station that executed Remote STOP can no longer executed Remote RUN, to allow another station to forcibly execute Remote RUN.) | $\bigcirc$ | $\bigcirc$ | User |
| +3 | Clear mode | Effective only for Remote RUN. Specify whether to clear the CPU device memory. <br> - 0000H: Do not clear (the local device is cleared, though). <br> - 0001H: Clear (excluding the latch range) <br> - 0002H: Clear (including the latch range) | $\bigcirc$ | - | User |

## Response data

| Operand: (d1) |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Remote RUN | Remote STOP | Set by |
| +0 | Request type | - 0090H: When the station number is specified by (s1)+5 | O | $\bigcirc$ | System |
| +1 | Subrequest type | - 0001H: Remote RUN <br> - 0002H: Remote STOP | $\bigcirc$ | $\bigcirc$ | System |

Point 8

- Remote RUN/STOP is valid when the RUN/STOP key switch on the RCPU, QCPU, LCPU, or QnACPU of the target station is set to RUN.
- Remote RUN/STOP is disabled when System Protect is applied to the CPU module of the target station.
- While another station already executes Remote STOP/PAUSE for the target station, the own station cannot execute RUN if "Do not forcibly execute" is set in (s2)+2.
- If the RCPU, QCPU, LCPU, or QnACPU of the target station for which Remote RUN/STOP has been executed is reset, the Remote RUN/STOP information is deleted.
- The clear mode set in (s2)+3 is used to specify the clear processing (initialization) mode of the device memory of RCPU, QCPU, LCPU, or QnACPU when the CPU operation is started by Remote RUN. The RCPU, QCPU, LCPU, or QnACPU performs clearing as specified and runs according to the parameter setting (initial device value).
- The number of resends (s1)+7 must be set every time the instruction is executed.


## Processing details

- These instructions send the request data specified by (S2) in the own station to the target station specified by the target network number and target station number of the control data. Upon completion of the request to the target station, the completion device specified by (d2) turns on.
- For the target stations that can be specified, refer to the following.
$\backsim$ Page 1191 Target networks and target station types
[Own station]

[Target station]

- When executing multiple link dedicated instructions concurrently, be careful not to overlap the channels of the link dedicated instructions. Multiple link dedicated instructions specifying the same channel cannot be used concurrently.
- The execution of the REQ instruction and whether it has been completed normally or completed with an error can be checked with the completion device (d2) or completion status indication device (d2)+1.
- Completion device (d2)

The completion device turns on in END processing of the scan performed upon completion of the REQ instruction and turns off in the next END processing.

- Completion status indication device (d2)+1

The completion device turns on or off depending on the completion status of the REQ instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on in END processing of the scan performed upon completion of the REQ instruction and turns off in the next END processing.

- The following figure shows the execution timing of the REQ instruction.
- When completed normally

- When completed with an error

- When the J.REQ/G.REQ instruction is used for execution, one complete cycle of write processing is followed by another while the write command is on. When the JP.REQ/GP.REQ instruction is executed, write processing is performed only once on the rising edge when the write command turns on.


## Operation error

| Error code $((\mathrm{s} 1)+1)$ | Description |
| :---: | :---: |
| 4000H to 4FFFH | [] MELSEC iQ-R CPU Module User's Manual (Application) |
| COOOH to CFFFH | L] MELSEC iQ-R Ethernet User's Manual (Application) |
| D000H to DFFFH | [] MELSEC iQ-R CC-Link IE Field Network User's Manual (Application) |
| E000H to EFFFH | [] MELSEC iQ-R CC-Link IE Controller Network User's Manual (Application) |

## Reading/writing clock data

## J(P).REQ, G(P).REQ

These instructions read or write clock data from/to the programmable controller of another station.


FBD/LD


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| J.REQ | - |
| G.REQ | - |
| JP.REQ | - |
| GP.REQ |  |

## Setting data

## ■Description, range, data type

| Operand |  | Description | Range | Data type | Data type (label) |
| :---: | :---: | :---: | :---: | :---: | :---: |
| (J/U) | $J(P)$.REQ | (J): Own station network number | 1 to 239 | 16-bit unsigned binary | ANY16 |
|  | $\mathrm{G}(\mathrm{P}) \cdot \mathrm{RE}$ Q | (U): Start I/O number (first three digits in fourdigit hexadecimal representation) of own station or own node | 00 H to FEH | 16-bit unsigned binary | ANY16 |
| (s1) |  | Own station start device where control data is stored | Refer to the control data. | Device name | ANY16 |
| (s2) |  | Own station start device where request data is stored | - | Device name | ANY16 |
| (d1) |  | Own station start device for storing response data | - | Device name | ANY16 |
| (d2) |  | Own station device to be turned on one scan when the instruction completes. (d2)+1 also turns on when the instruction completes with an error. | - | Bit | ANYBIT_ARRAY <br> (Number of elements: <br> 2) |
| EN |  | Execution condition | - | Bit | BOOL |
| ENO |  | Execution result | - | Bit | BOOL |

Applicable devices

| Operand |  | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $\begin{aligned} & \mathbf{X}, \mathbf{Y}, \mathbf{M}, \mathrm{L}, \mathbf{S M}, \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U미G，J밈， U3EDI（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ | J | U |
| （J／U） | $\begin{aligned} & \hline J(P) . \\ & R E Q \end{aligned}$ | － | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － |
|  | $\begin{aligned} & \mathrm{G}(\mathrm{P}) . \\ & \mathrm{REQ} \end{aligned}$ | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － | $\bigcirc$ |
| （s1） |  | － | － | $\bigcirc^{*}{ }^{2}$ | － | － | － | － | $\bigcirc$ | － | － | － | － | － |
| （s2） |  | － | － | ${ }^{*}{ }^{2}$ | － | － | － | － | $\bigcirc$ | － | － | － | － | － |
| （d1） |  | － | － | $\bigcirc^{*}{ }^{2}$ | － | － | － | － | $\bigcirc$ | － | － | － | － | － |
| （d2） |  | O＊1 | － | $0^{* 3}$ | － | － | － | － | － | － | － | － | － | － |

＊1 FX and FY cannot be used．
＊2 FD cannot be used．
＊3 T，ST，C，and FD cannot be used．

## Point $\rho$

The REQ instruction cannot be executed when the target station is ACPU．
Control data

| Operand：（s1） |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description |  |  |  | Setting range | Set by |
| ＋0 | Error completion type | （1）Error completion type（bit 7） <br> Specify whether to set data when completed with an error． <br> － 0 ：Do not set data in $(\mathrm{s} 1)+11$ and later at completion with an error． <br> －1：Set data in（s1）＋11 and later at completion with an error． |  |  |  | $\begin{aligned} & 0011 \mathrm{H} \\ & 0091 \mathrm{H} \end{aligned}$ | User |
| ＋1 | Completion status | The instruction completion status is stored． <br> －0：Normal <br> －Other than 0：Error（error code） |  |  |  | － | System |
| ＋2 | Own station channel | Specify the channel to be used by own station．（ $\mathfrak{F}$ Page 1192 Range of available channel numbers） |  |  |  | 1 to 8,11 to 18 | User |
| ＋3 | Target station CPU type | Specify the CPU type of the target station． <br> －0000H：Addressed to target station CPU（control CPU） <br> －03DOH：Addressed to control system CPU <br> －03D1H：Addressed to standby system CPU <br> －03D2H：Addressed to system A CPU <br> －03D3H：Addressed to system B CPU <br> －03EOH：Addressed to multiple CPU No． 1 <br> －03E1H：Addressed to multiple CPU No． 2 <br> －03E2H：Addressed to multiple CPU No． 3 <br> －03E3H：Addressed to multiple CPU No． 4 <br> －03FFH：Addressed to target station CPU（control CPU） |  |  |  | $\begin{aligned} & 0000 \mathrm{H} \\ & 03 \mathrm{DOH} \text { to 03D3H } \\ & 03 \mathrm{EOH} \text { to 03E3H } \\ & 03 F F H \end{aligned}$ | User |
| ＋4 | Target network number | Specify the network number of the target station． <br> － 1 to 239：Network number |  |  |  | 1 to 239 | User |
| ＋5 | Target station number | Specify the station number of the target station． <br> （1）Station number specification <br> ［Ethernet or CC－Link IE Controller Network］ <br> － 1 to 120：Station number <br> ［CC－Link IE Field Network］ <br> －125：Master station <br> －126：Master operating station <br> － 1 to 120：Local station，intelligent device station，submaster station <br> （2）Group number specification <br> ［Ethernet or CC－Link IE Controller Network］ <br> 0081 H to 00A0H：All stations with group numbers 0001 H to 0020 H <br> （can be specified only for clock data writing） <br> （3）All－station specification <br> 00FFH：All stations of target network number（broadcast（excluding the own station）） （can be specified only for clock data writing） |  |  |  | $\begin{aligned} & 1 \text { to } 120, \\ & 125,126, \\ & 0081 \mathrm{H} \text { to } 00 \mathrm{AOH} \end{aligned}$ 00FFH | User |
| ＋6 | Not used | － |  |  |  | － | － |


| Operand: (s1) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +7 | Number of resends | -Before instruction execution <br> Specify the number of resends to be performed if the instruction is not completed within the monitoring time specified by (s1) +8 . <br> - 0 to 15 (times) <br> ■At completion of instruction <br> The number of resends performed (result) is stored. <br> - 0 to 15 (times) | 0 to 15 | User/ system |
| +8 | Arrival monitoring time | [CC-Link IE Controller Network or CC-Link IE Field Network] <br> Specify the monitoring time until completion of processing. If processing is not completed within the monitoring time, the request is resent by the number of resends specified in $(\mathrm{s} 1)+7$. <br> - 0: 10s <br> - 1 to 32767: 1 to 32767 s | 0, 1 to 32767 | User |
|  |  | [Ethernet] <br> Specify the monitoring time until completion of processing. If processing is not completed within the monitoring time, the request is resent by the number of resends specified in ( s 1 ) +7 . <br> - 0 to (TCP resend timer value): The TCP resend timer value is assumed as the monitoring time. <br> - (TCP resend timer value +1 ) to 16383: (TCP resend timer value +1 ) to 16383 s | 0 to 16383 | User |
| +9 | Request data length | Specify the number of request data (words). <br> (Number of words of data stored in the request data storage device (s2)) <br> - Reading clock data: 2 <br> - Writing clock data: 6 | 2, 6 | User |
| +10 | Response data length | The number of response data (words) is stored. <br> (Number of words of data stored in the response data storage device) <br> - Reading clock data: 6 <br> - Writing clock data: 2 | 2, 6 | System |
| +11 | Clock setting flag | The validity status (valid or invalid) of the data in ( s 1 ) +12 and later is stored. Note that the data in ( s 1 ) +12 and later is not cleared even when the instruction is completed successfully. <br> - 0: Invalid <br> -1: Valid | - | System |
| +12 | Clock data (Set only in an abnormal state) (No information is stored if an error is detected in the own station.) | Upper 8 bits: Month ( 01 H to 12 H ) <br> Lower 8 bits: Lower 2 digits of year ( 00 H to 99 H ) | - | System |
| +13 |  | Upper 8 bits: Hour ( 00 H to 23 H ) Lower 8 bits: Day ( 01 H to 31H) | - | System |
| +14 |  | Upper 8 bits: Second ( 00 H to 59 H ) <br> Lower 8 bits: Minute ( 00 H to 59 H ) | - | System |
| +15 |  | Upper 8 bits: Year (00H to 99H: Upper two digits of the year) Lower 8 bits: Day of the week (00H (Sun.) to 06H (Sat.)) | - | System |
| +16 | Error detection network number | The network number of the station in which an error was detected is stored. (No information is stored if an error is detected in the own station.) <br> - 1 to 239 (Network number) | 1 to 239 | System |
| +17 | Error-detected station number | The station number of the station where an error was detected is stored. (No information is stored if an error is detected in the own station.) <br> [Ethernet or CC-Link IE Controller Network] <br> - 1 to 120: Station number <br> [CC-Link IE Field Network] <br> - 125: Master station <br> - 1 to 120: Local station, intelligent device station, submaster station | 1 to 120, 125 | System |

Request data

| Operand: (s2) |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description |  |  |  | Reading clock data | Writing clock data | Set by |
| +0 | Request type | - 0001H: Reading clock data <br> - 0011H: Writing clock data (with a station number specified in (s1)+5) <br> - 0031H: Writing clock data (with All Station or a group specified in (s1) +5 ) |  |  |  | $\bigcirc$ | $\bigcirc$ | User |
| +1 | Subrequest type | - 0002H: Reading clock data <br> - 0001H: Writing clock data |  |  |  | $\bigcirc$ | $\bigcirc$ | User |
| +2 | Change pattern | ■Change pattern (bits 0 to 6) <br> Specify which item from the upper byte in (s2)+2 to (s2)+5 should be written. <br> - 0: Not changed <br> - 1: Changed <br> ■Changed year (bits 8 to 15) <br> Specify the year (two lower digits) in BCD code. ${ }^{* 1}$ |  |  |  | - | $\bigcirc$ | User |
| +3 | Changed clock data | Upper 8 bits: Day ( 01 H to 31 H ) <br> Lower 8 bits: Month ( 01 H to 12 H ) |  |  |  | - | $\bigcirc$ | User |
| +4 |  | Upper 8 bits: Minute ( 00 H to 59 H ) Lower 8 bits: $\operatorname{Hour}(00 \mathrm{H}$ to 23 H$)$ |  |  |  | - | $\bigcirc$ | User |
| +5 |  | Upper 8 bits: Day of week (00H (Sunday) to 06H (Saturday)) Lower 8 bits: Second (00H to 59H) |  |  |  | - | $\bigcirc$ | User |

*1 Writing clock data using the REQ instruction cannot change the upper two digits of the year. If the year needs to be changed including the upper two digits as well, use the engineering tool to set the clock data.

## -Response data

| Operand: (d1) |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Reading clock data | Writing clock data | Set by |
| +0 | Request type | - 0081H: Reading clock data <br> - 0091H: Writing clock data | $\bigcirc$ | $\bigcirc$ | System |
| +1 | Subrequest type | - 0002H: Reading clock data <br> - 0001H: Writing clock data | $\bigcirc$ | $\bigcirc$ | System |
| +2 | Clock data that has been read | Upper 8 bits: Month ( 01 H to 12 H ) <br> Lower 8 bits: Lower 2 digits of year ( 00 H to 99 H ) | $\bigcirc$ | - | System |
| +3 |  | Upper 8 bits: $\operatorname{Hour}(00 \mathrm{H}$ to 23 H$)$ <br> Lower 8 bits: Day ( 01 H to 31 H ) | $\bigcirc$ | - | System |
| +4 |  | Upper 8 bits: Second ( 00 H to 59 H ) <br> Lower 8 bits: Minute ( 00 H to 59 H ) | $\bigcirc$ | - | System |
| +5 |  | Upper 8 bits: ( 00 H ) <br> Lower 8 bits: Day of the week (00H (Sun.) to 06H (Sat.)) | $\bigcirc$ | - | System |

## Point $\rho$

- Clock data cannot be read or written when System Protect is applied to the CPU module of the target station.
- When clock data is read, a continuous area for six words is required for the response data storage device (d1).


## Processing details

- These instructions send the request data specified by (S2) in the own station to the target station specified by the target network number and target station number of the control data. Upon completion of the request to the target station, the completion device specified by (d2) turns on.
- For the target stations that can be specified, refer to the following.
$\longmapsto$ Page 1191 Target networks and target station types
[Own station]

[Target station]

- When executing multiple link dedicated instructions concurrently, be careful not to overlap the channels of the link dedicated instructions. Multiple link dedicated instructions specifying the same channel cannot be used concurrently.
- The execution of the REQ instruction and whether it has been completed normally or completed with an error can be checked with the completion device (d2) or completion status indication device (d2)+1.
- Completion device (d2)

The completion device turns on in END processing of the scan performed upon completion of the REQ instruction and turns off in the next END processing. - Completion status indication device (d2)+1

The completion device turns on or off depending on the completion status of the REQ instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on in END processing of the scan performed upon completion of the REQ instruction and turns off in the next END processing.

- The following figure shows the execution timing of the REQ instruction.
- When completed normally

- When completed with an error

- When the J.REQ/G.REQ instruction is used for execution, one complete cycle of write processing is followed by another while the write command is on. When the JP.REQ/GP.REQ instruction is executed, write processing is performed only once on the rising edge when the write command turns on.


## Operation error

| Error code $((s 1)+1)$ | Description |
| :---: | :---: |
| 4000H to 4FFFH | [] MELSEC iQ-R CPU Module User's Manual (Application) |
| COOOH to CFFFFH | [] MELSEC iQ-R Ethernet User's Manual (Application) |
| D000H to DFFFH | [] MELSEC iQ-R CC-Link IE Field Network User's Manual (Application) |
| E000H to EFFFH | [] MELSEC iQ-R CC-Link IE Controller Network User's Manual (Application) |

### 12.2 CC-Link Dedicated Instructions

## Reading data from the target station

## J(P).RIRD, G(P).RIRD

These instructions read the specified number of points of data from a device of the target station.



Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| J.RIRD | - |
| G.RIRD | - |
| JP.RIRD | - |
| GP.RIRD |  |

## Setting data

DDescription, range, data type

| Operand |  | Description | Range | Data type | Data type (label) |
| :---: | :---: | :---: | :---: | :---: | :---: |
| (J/U) | $J(P) \cdot R I R$ D | (J): Own station network number | 1 to 239 | 16-bit unsigned binary | ANY16 |
|  | $\begin{aligned} & \text { G(P).RIR } \\ & \mathrm{D} \end{aligned}$ | (U): Start I/O number (first three digits in fourdigit hexadecimal representation) of own station or own node | 00 H to FEH | 16-bit unsigned binary | ANY16 |
| (s) |  | Own station start device where control data is stored | Refer to the control data. | Device name | ANY16 |
| (d1) |  | Own station start device for storing the read data (A continuous area for the read data length is required.) | - | Device name | ANY16 |
| (d2) |  | Own station device to be turned on one scan when the instruction completes. <br> (d2)+1 also turns on when the instruction completes with an error. | - | Bit | ANYBIT_ARRAY <br> (Number of elements: <br> 2) |
| EN |  | Execution condition | - | Bit | BOOL |
| ENO |  | Execution result | - | Bit | BOOL |

Applicable devices

| Operand |  | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $\begin{aligned} & \mathbf{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ | J | U |
| （J／U） | $J(P)$ <br> RIRD | － | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － |
|  | $\begin{aligned} & \mathrm{G}(\mathrm{P}) . \\ & \text { RIRD } \end{aligned}$ | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － | $\bigcirc$ |
| （s） |  | － | － | $0^{* 2}$ | － | － | － | － | $\bigcirc$ | － | － | － | － | － |
| （d1） |  | － | － | $\bigcirc^{* 2}$ | － | － | － | － | $\bigcirc$ | － | － | － | － | － |
| （d2） |  | O＊1 | － | $0^{* 3}$ | － | － | － | － | － | － | － | － | － | － |

＊1 FX and FY cannot be used．
＊2 FD cannot be used．
＊3 T，ST，C，and FD cannot be used．

## ■Control data

| Operand：（s） |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description |  | Setting range | Set by |
| ＋0 | Completion status | The instruction completion status is stored． <br> －0：Normal <br> －Other than 0：Error（error code） |  | － | System |
| ＋1 | Target station number | Specifies the station number of the target station． |  | 0 to 120 | User |
| ＋2 | Access／attribute code | Specify the access code and attribute code of the device to read． |  | Refer to＂Access／ attribute code．＂ | User |
| ＋3 | Device number | Specify the start number of the device to read． |  | Within device range | User |
| ＋4 | Number of read points | Specify the number of data in units of words to read from the device． |  | 1 to 480 | User |

Access/attribute code

| Device category*1 | Name | Date type |  | Unit | Access code*2 | Attribute code*2 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | Bit | Word |  |  |  |
| Input relay | X | $\bigcirc$ | - | Hexadecimal | 01H | 05H |
| Output relay | Y | $\bigcirc$ | - | Hexadecimal | 02H | 05H |
| Internal relay | M | $\bigcirc$ | - | Decimal | 03H | 05H |
| Latch relay | L | $\bigcirc$ | - | Decimal | 83H | 05H |
| Link relay | B | $\bigcirc$ | - | Hexadecimal | 23H | 05H |
| Timer (contact) | T | $\bigcirc$ | - | Decimal | 09H | 05H |
| Timer (coil) | T | $\bigcirc$ | - | Decimal | OAH | 05H |
| Timer (current value) | T | - | $\bigcirc$ | Decimal | OCH | 05H |
| Retentive timer (contact) | ST | $\bigcirc$ | - | Decimal | 89H | 05H |
| Retentive timer (coil) | ST | $\bigcirc$ | - | Decimal | 8AH | 05H |
| Retentive timer (current value) | ST | - | $\bigcirc$ | Decimal | 8 CH | 05H |
| Counter (contact) | C | $\bigcirc$ | - | Decimal | 11H | 05H |
| Counter (coil) | C | $\bigcirc$ | - | Decimal | 12H | 05H |
| Counter (current value) | C | - | $\bigcirc$ | Decimal | 14H | 05H |
| Data register ${ }^{*}$ | D | - | $\bigcirc$ | Decimal | 04H | 05H |
| Link register ${ }^{* 3}$ | W | - | $\bigcirc$ | Hexadecimal | 24H | 05H |
| File register | R | - | $\bigcirc$ | Decimal | 84H | 05H |
| Link special relay | SB | $\bigcirc$ | - | Hexadecimal | 63H | 05H |
| Link special register | SW | - | $\bigcirc$ | Hexadecimal | 64H | 05H |
| Special relay | SM | $\bigcirc$ | - | Decimal | 43H | 05H |
| Special register | SD | - | $\bigcirc$ | Decimal | 44H | 05H |

*1 Any device other than the above cannot be accessed. To access a bit device, specify 0 or a multiple of 16.
*2 When the target station is other than the CC-Link IE Controller Network module and CC-Link IE Field Network master/local module, refer to the manual of the target station.
*3 None of D65536 and the subsequent extended data registers and of W10000 and the subsequent extended link registers are accepted.

## Point ${ }^{\circ}$

The RIRD instruction can set the arrival monitoring time and the number of resends in the following link special registers (SW).

- RIRD/RIWT instruction: Arrival monitoring time (SW0009)
- RIRD/RIWT instruction: Number of resends (SW000B)

When "RIRD/RIWT instruction: Number of resends (SW000B)" is set, the time taken for the RIRD instruction to be completed with an error is determined by the following.
(RIRD/RIWT instruction: Number of resends +1 )×RIRD/RIWT instruction: Arrival monitoring time However, if the target station (relay source station when addressed to another network) has been disconnected at the time of RIRD instruction execution, no retry is executed. If the RIRD instruction is completed with an error, re-execute the RIRD instruction after the target station (relay source station when addressed to another network) returns.

## Processing details

- These instructions read the number of words of data specified by (s)+4 from the start device of the target station specified by ( $s$ ) +2 and ( $s$ ) +3 , and put the data in the word device in the own station specified by (d1) and after. Specify the target station in (s)+1. Upon completion of reading the data from the target station, the completion device specified by (d2) turns on.

- The execution of the RIRD instruction and whether it has been completed normally or completed with an error can be checked with the completion device (d2) or completion status indication device (d2)+1.
- Completion device (d2)

The completion device turns on in END processing of the scan performed upon completion of the RIRD instruction and turns off in the next END processing.

- Completion status indication device (d2)+1

The completion device turns on or off depending on the completion status of the RIRD instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on in END processing of the scan performed upon completion of the RIRD instruction and turns off in the next END processing.

- The following figure shows the execution timing of the RIRD instruction.
- When completed normally

- When completed with an error



## Operation error

| Error code ((s)+0) | Description |
| :--- | :--- |
| 4000 H to 4FFFH | La MELSEC iQ-R CPU Module User's Manual (Application) |
| D000H to DFFFH | La MELSEC iQ-R CC-Link IE Field Network User's Manual (Application) |
| E000H to EFFFH | La MELSEC iQ-R CC-Link IE Controller Network User's Manual (Application) |

## Writing data to the target station

## J(P).RIWT, G(P).RIWT

These instructions write the specified number of points of data to a device of the target station.

-Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| J.RIWT | - |
| G.RIWT | - |
| JP.RIWT | - |
| GP.RIWT |  |

## Setting data

■Description, range, data type

| Operand |  | Description | Range | Data type | Data type (label) |
| :---: | :---: | :---: | :---: | :---: | :---: |
| (J/U) | $\begin{aligned} & \text { J(P).RIW } \\ & \text { T } \end{aligned}$ | (J): Own station network number | 1 to 239 | 16-bit unsigned binary | ANY16 |
|  | $\begin{aligned} & \text { G(P).RIW } \\ & \mathrm{T} \end{aligned}$ | (U): Start I/O number (first three digits in fourdigit hexadecimal representation) of own station or own node | OOH to FEH | 16-bit unsigned binary | ANY16 |
| (s1) |  | Own station start device where control data is stored | Refer to the control data. | Device name | ANY16 |
| (s2) |  | Own station start device for storing written data | - | Device name | ANY16 |
| (d) |  | Own station device to be turned on one scan when the instruction completes. <br> If the instruction is completed with an error, (d) +1 is also turned on. | - | Bit | ANYBIT_ARRAY <br> (Number of elements: <br> 2) |
| EN |  | Execution condition | - | Bit | BOOL |
| ENO |  | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand |  | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，Jㅁㅁ， U3EロI（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ | J | U |
| （J／U） | $J(P) .$ <br> RIWT | － | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － |
|  | $\mathrm{G}(\mathrm{P}) .$ <br> RIWT | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － | $\bigcirc$ |
| （s1） |  | － | － | $0^{*}$ | － | － | － | － | $\bigcirc$ | － | － | － | － | － |
| （s2） |  | － | － | $\bigcirc{ }^{*}$ | － | － | － | － | $\bigcirc$ | － | － | － | － | － |
| （d） |  | O＊1 | － | $0^{* 3}$ | － | － | － | － | － | － | － | － | － | － |

＊1 FX and FY cannot be used．
＊2 FD cannot be used．
＊3 T，ST，C，and FD cannot be used．

## －Control data

| Operand：（s1） |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description |  | Setting range | Set by |
| ＋0 | Completion status | The instruction completion status is stored． <br> －0：Normal <br> －Other than 0：Error（error code） |  | － | System |
| ＋1 | Target station number | Specifies the station number of the target station． |  | 0 to 120 | User |
| ＋2 | Access／atribute code | Specify the access code and attribute code of the device to be written． |  | Refer to＂Access／ attribute code．＂ | User |
| ＋3 | Device number | Specify the start number of the device to be written． |  | Within device range | User |
| ＋4 | Number of write points | Specify the number of data in units of words to read from the device． |  | 1 to 480 | User |

Access/attribute code

| Device category*1 | Name | Date type |  | Unit | Access code*2 | Attribute code*2 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | Bit | Word |  |  |  |
| Input relay | X | $\bigcirc$ | - | Hexadecimal | 01H | 05H |
| Output relay | Y | $\bigcirc$ | - | Hexadecimal | 02H | 05H |
| Internal relay | M | $\bigcirc$ | - | Decimal | 03H | 05H |
| Latch relay | L | $\bigcirc$ | - | Decimal | 83H | 05H |
| Link relay | B | $\bigcirc$ | - | Hexadecimal | 23H | 05H |
| Timer (contact) | T | $\bigcirc$ | - | Decimal | 09H | 05H |
| Timer (coil) | T | $\bigcirc$ | - | Decimal | OAH | 05H |
| Timer (current value) | T | - | $\bigcirc$ | Decimal | OCH | 05H |
| Retentive timer (contact) | ST | $\bigcirc$ | - | Decimal | 89H | 05H |
| Retentive timer (coil) | ST | $\bigcirc$ | - | Decimal | 8AH | 05H |
| Retentive timer (current value) | ST | - | $\bigcirc$ | Decimal | 8 CH | 05H |
| Counter (contact) | C | $\bigcirc$ | - | Decimal | 11H | 05H |
| Counter (coil) | C | $\bigcirc$ | - | Decimal | 12H | 05H |
| Counter (current value) | C | - | $\bigcirc$ | Decimal | 14H | 05H |
| Data register*3 | D | - | $\bigcirc$ | Decimal | 04H | 05H |
| Link register ${ }^{* 3}$ | W | - | $\bigcirc$ | Hexadecimal | 24H | 05H |
| File register | R | - | $\bigcirc$ | Decimal | 84H | 05H |
| Link special relay | SB | $\bigcirc$ | - | Hexadecimal | 63H | 05H |
| Link special register | SW | - | $\bigcirc$ | Hexadecimal | 64H | 05H |
| Special relay | SM | $\bigcirc$ | - | Decimal | 43H | 05H |
| Special register | SD | - | $\bigcirc$ | Decimal | 44H | 05H |

*1 Any device other than the above cannot be accessed. To access a bit device, specify 0 or a multiple of 16.
*2 When the target station is other than the CC-Link IE Controller Network module and CC-Link IE Field Network master/local module, refer to the manual of the target station.
*3 None of D65536 and the subsequent extended data registers and of W10000 and the subsequent extended link registers are accepted.

## Point ${ }^{\circ}$

The RIWT instruction can set the arrival monitoring time and the number of resends in the following link special registers (SW).

- RIRD/RIWT instruction: Arrival monitoring time (SW0009)
- RIRD/RIWT instruction: Number of resends (SW000B)

When "RIRD/RIWT instruction: Number of resends (SW000B)" is set, the time taken for the RIWT instruction to be completed with an error is determined by the following.
(RIRD/RIWT instruction: Number of resends +1 )×RIRD/RIWT instruction: Arrival monitoring time However, if the target station (relay source station when addressed to another network) has been disconnected at the time of RIWT instruction execution, no retry is executed. If the RIWT instruction is completed with an error, re-execute the RIWT instruction after the target station (relay source station when addressed to another network) returns.

## Processing details

- These instructions read the number of words of data specified by ( $s 1$ ) +4 from the start device of the own station specified by ( $s 2$ ), and write the data in the device in the target station specified by ( s 1 ) +2 and ( s 1 ) +3 . Specify the target station in $(\mathrm{s} 1)+1$. Upon completion of writing the data, the completion device specified by (d) turns on.

- The execution of the RIWT instruction and whether it has been completed normally or completed with an error can be checked with the completion device (d) or completion status indication device (d)+1.
- Completion device (d)

The completion device turns on in END processing of the scan performed upon completion of the RIWT instruction and turns off in the next END processing.

- Completion status indication device (d)+1

The completion device turns on or off depending on the completion status of the RIWT instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on in END processing of the scan performed upon completion of the RIWT instruction and turns off in the next END processing.

- The following figure shows the execution timing of the RIWT instruction.
- When completed normally

- When completed with an error



## Operation error

| Error code <br> $((\mathbf{s} 1)+0)$ | Description |
| :--- | :--- |
| 4000 H to 4FFFH | L MELSEC iQ-R CPU Module User's Manual (Application) |
| D000H to DFFFH | L $\square$ MELSEC iQ-R CC-Link IE Field Network User's Manual (Application) |
| E000H to EFFFH | La MELSEC iQ-R CC-Link IE Controller Network User's Manual (Application) |

### 13.1 Open/Close Processing Instructions

## Opening a connection

## GP.CONOPEN

This instruction establishes (opens) a connection with an external device for data communication.

| Ladder |  |  |  |  | STENO:=GP_C |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  |  |
|  |  |  |  |  |  |

FBD/LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| GP.CONOPEN | $\ddots$ |
|  | - |

## Setting data

Description, range, data type

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (U) | Start I/O number (first three digits in four-digit <br> hexadecimal representation) of own station or own node | 00 H to FEH | 16-bit unsigned binary | ANY16 |
| (s1) | Connection No. | 1 to 128 <br> $(1$ to 16: Port 1 fixed buffer <br> communications, 17 to $64:$ Port 1 <br> socket communications, 65 to <br> $128: ~ P o r t ~ 2 ~ s o c k e t ~$ <br> communications) | 16-bit unsigned binary | ANY16 |
| (s2) | Own station start device where control data is stored | Refer to the control data. | Device name | ANY16 |
| (d) | Own station device to be turned on one scan when the <br> instruction completes. <br> If the instruction is completed with an error, (d) +1 is also <br> turned on. | - | Bit | ANYBIT_ARRAY <br> (Number of <br> elements: 2) |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3Eपl（H）G口 | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （U） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ |
| （s1） | O＊1 | － | $0^{* 2}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | － | － | $0^{*}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | $\mathrm{O}^{* 1}$ | － | $0^{* 3}$ | － | － | － | － | － | － | － | － | － |

＊1 FX and FY cannot be used．
＊2 FD cannot be used．
＊3 T，ST，C，and FD cannot be used．

## Control data



*1 When the execution type is set to 0 , the device areas in ( s 2 ) +4 to ( s 2 ) +14 are used by the system and the user must not use the areas.
*2 This item can be set when the connection number in (s1) is one from 1 to 7 and 9 to 15.
*3 This specification is enabled when UDP/IP is specified for the communication method (protocol).
Data cannot be sent through the connection for which 65535 (FFFFH) is specified. Specify a value from 1 to 65534 to send data.
*4 This specification is enabled when TCP/IP is specified for the communication method (protocol).
*5 This specification is enabled when UDP/IP is specified for the communication method (protocol).
*6 Settings are ignored if the open method (bits 10 and 11) specified by ( $s 2$ ) +4 is "10: Unpassive open".

## Processing details

The GP.CONOPEN instruction is used for socket communications or fixed buffer communications.
For the combinations of available settings for connection opening, refer to the following.
[] MELSEC iQ-R Ethernet User's Manual (Application)

## Operation error

| Error code <br> $(($ s2 $)+1)$ | Description |
| :--- | :--- |
| C000H to CFFFH | Lコ MELSEC iQ-R Ethernet User's Manual (Application) |

## Point 9

The following figure shows an IP address setting example.
Example: IP address "10.97.24.200 (0A.61.18.C8)"
[Program example]


## Closing a connection

## GP.CONCLOSE

This instruction disconnects (closes) the connection from the external device during data communication.


FBD/LD


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| GP.CONCLOSE | - |

## Setting data

Description, range, data type

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (U) | Start I/O number (first three digits in four-digit <br> hexadecimal representation) of own station or own node | 00 H to FEH | 16-bit unsigned binary | ANY16 |
| (s1) | Connection No. | 1 to 128 <br> $(1$ to $16:$ Port 1 fixed buffer <br> communications, 17 to $64:$ Port 1 <br> socket communications, 65 to 128: <br> Port 2 socket communications) | 16-bit unsigned binary | ANY16 |
| (s2) | Own station start device where control data is stored | Refer to the control data. | Device name | ANY16 |
| (d) | Own station device to be turned on one scan when the <br> instruction completes. <br> If the instruction is completed with an error, (d) +1 is also <br> turned on. | - | Bit | ANYBIT_ARRAY <br> (Number of <br> elements: 2) |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> (U) |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | J밈 | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | UपIGロ, Jㅁㅁㅁ, U3EDI(H)Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (U) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | $\bigcirc$ | - | - | $\bigcirc$ |
| (s1) | O*1 | - | O*2 | - | - | - | - | $\bigcirc$ | $\bigcirc$ | - | - | - |
| (s2) | - | - | ${ }^{*}{ }^{2}$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (d) | $\bigcirc{ }^{* 1}$ | - | $0^{* 3}$ | - | - | - | - | - | - | - | - | - |

*1 FX and FY cannot be used.
*2 FD cannot be used.
*3 T, ST, C, and FD cannot be used.

## Control data

| Operand: (s2) | Description | Setting range | Set by |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Item | - | - |  |
| +0 | System area | Completion status | The instruction completion status is stored. <br> •0: Completed successfully <br> - Other than 0: Completed with an error (error code) | - |
| +1 |  |  |  |  |

## Processing details

The GP.CONCLOSE instruction is used for socket communications or fixed buffer communications.

## Operation error

| Error code <br> $((\mathbf{s 2})+1)$ | Description |
| :--- | :--- |
| C000H to CFFFH | Lコ MELSEC iQ-R Ethernet User's Manual (Application) |

## Opening a connection

## GP．OPEN，ZP．OPEN

This instruction establishes（opens）a connection with an external device for data communication．


FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| GP．OPEN | - |
| ZP．OPEN | - |

## Setting data

Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :---: | :---: | :---: | :---: | :---: | :---: |
| （U） | GP．OPEN | Start I／O number（first three digits in four－digit hexadecimal representation）of own station or own node | OOH to FEH | 16－bit unsigned binary | ANY16 |
|  | ZP．OPEN | Start I／O number（first three digits in four－digit hexadecimal representation）of own station or own node | 00 H to FEH | String | ANY16＿OR＿STRING ＿SINGLE |
| （s1） |  | Connection No． | 1 to 16 | 16－bit unsigned binary | ANY16 |
| （s2） |  | Own station start device where control data is stored | Refer to the control data． | Device name | ANY16 |
| （d） |  | Own station device to be turned on one scan when the instruction completes． <br> If the instruction is completed with an error，（d）+1 is also turned on． | － | Bit | ANYBIT＿ARRAY <br> （Number of elements： 2） |
| EN |  | Execution condition | － | Bit | BOOL |
| ENO |  | Execution result | － | Bit | BOOL |

## Applicable devices

| Operand |  | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T, ST, C, D, W, SD, SW，FD，R，ZR，RD | U밈，J밈， U3EDI（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （U） | $\begin{aligned} & \text { GP.O } \\ & \text { PEN } \end{aligned}$ | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ |
|  | $\begin{aligned} & \text { ZP.O } \\ & \text { PEN } \end{aligned}$ | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ |
| （s1） |  | O＊1 | － | $0^{* 2}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） |  | － | － | $\bigcirc^{* 2}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） |  | O＊1 | － | ${ }^{*}{ }^{3}$ | － | － | － | － | － | － | － | － | － |

＊1 FX and FY cannot be used．
＊2 FD cannot be used
＊3 T，ST，C，and FD cannot be used．

Control data


## Processing details

- These instructions open the connection specified by (s1) of the module specified by (U).
- The selection of the setting value used for open processing is specified by ( s 2 ) +0 . (Specify whether to use the parameter value set by the engineering tool or the value set in (s2)+2 to (s2)+16 of control data.)
- The execution of the OPEN instruction and whether it has been completed normally or completed with an error can be checked with the completion device (d) or completion status indication device (d)+1.
- Completion device (d)

The completion device turns on in END processing of the scan performed upon completion of the OPEN instruction and turns off in the next END processing.

- Completion status indication device (d) +1

The completion device turns on or off depending on the completion status of the OPEN instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on in END processing of the scan performed upon completion of the OPEN instruction and turns off in the next END processing.

- The following figure shows the operation at completion of the OPEN instruction.

- The OPEN instruction is executed on the rising edge (OFF to ON) of the open command.


## Precautions

For the same connection, do not perform open and close processing using the OPEN and CLOSE instructions concurrently with open and close processing using other means. Concurrent use results in a malfunction.

Operation error

| Error code <br> $((\mathbf{s} 2)+1)$ | Description |
| :--- | :--- |
| C 000 H to CFFFH | D C MELSEC iQ-R Ethernet User's Manual (Application) |

## Restriction ${ }^{N}$

- The communication means of the target connection is fixed buffer communications (with procedure), fixed buffer communications (without procedure), or predefined protocol. Make settings in (b9, b10) (fixed buffer communications with/without procedure) of (s2)+2 in control data.
- When the protocol is set to TCP/IP, the alive check method is fixed to KeepAlive.
- The connection numbers that can be specified range from 1 to 16 . Connection number 17 and after cannot be specified.
- If no parameter data is set in "external device connection configuration" of the engineering tool, the communication data code becomes "binary". If one or more parameter data are set, the instruction follows the value set in "Communication data code".
- If no parameter data is set in "external device connection configuration" of the engineering tool, the opening method becomes "Do not open with the program". If one or more parameter data are set, the instruction follows the value set in "Opening method".


## Closing a connection

## GP．CLOSE，ZP．CLOSE

These instructions disconnect（close）the connection from the external device during data communication．


FBD／LD


## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| GP．CLOSE | - |
| ZP．CLOSE | - |

## Setting data

Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- | :--- |
| （U） | GP．CLOSE | Start I／O number（first three digits in four－digit <br> hexadecimal representation）of own station or own <br> node | 00 H to FEH | 16－bit unsigned binary | ANY16 |
|  | ZP．CLOSE | Start I／O number（first three digits in four－digit <br> hexadecimal representation）of own station or own <br> node | 00 H to FEH | String | ANY16＿OR＿STRING <br> SINGLE |
| （s1） | Connection No． | 1 to 16 | 16－bit unsigned binary | ANY16 |  |
| （s2） | Own station start device where control data is stored | Refer to the control data． | Device name | ANY16 |  |
| （d） | Own station device to be turned on one scan when <br> the instruction completes． <br> If the instruction is completed with an error，（d）＋1 is <br> also turned on． | - | Bit | ANYBIT＿ARRAY <br> （Number of elements： <br> $2)$ |  |
| EN | Execution condition | Execution result | - | Bit | BOOL |
| ENO |  |  | Bit | BOOL |  |

## ■Applicable devices

| Operand |  | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （U） | $\begin{aligned} & \text { GP.C } \\ & \text { LOSE } \end{aligned}$ | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ |
|  | $\begin{aligned} & \text { ZP.CL } \\ & \text { OSE } \end{aligned}$ | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ |
| （s1） |  | $\bigcirc{ }^{* 1}$ | － | $\mathrm{O}^{*}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） |  | － | － | $\bigcirc^{*}{ }^{2}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） |  | O＊1 | － | $0^{* 3}$ | － | － | － | － | － | － | － | － | － |

＊1 FX and FY cannot be used．
＊2 FD cannot be used．
＊3 T，ST，C，and FD cannot be used．

| Operand: (s2) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +0 | System area | - | - | - |
| +1 | Completion status | The instruction completion status is stored. <br> - 0: Completed successfully <br> - Other than 0: Completed with an error (error code) | - | System |

## Processing details

- These instructions close the connection specified by ( s 1 ) of the module specified by (U).
- The execution of the CLOSE instruction and whether it has been completed normally or completed with an error can be checked with the completion device (d) or completion status indication device (d)+1.
- Completion device (d)

The completion device turns on in END processing of the scan performed upon completion of the CLOSE instruction and turns off in the next END processing.

- Completion status indication device (d) +1

The completion device turns on or off depending on the completion status of the CLOSE instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on in END processing of the scan performed upon completion of the CLOSE instruction and turns off in the next END processing.

- The following figure shows the operation at completion of the CLOSE instruction.

- The CLOSE instruction is executed on the rising edge (OFF to ON) of the close command.


## Precautions

- For the same connection, do not perform open and close processing using the OPEN and CLOSE instructions concurrently with open and close processing using other means. Concurrent use results in a malfunction.
- If a connection for which the OPEN instruction is in execution is specified in TCP Unpassive/Fullpassive open mode, an error (C1B2H: OPEN/CLOSE instruction is in execution for the specified connection) occurs.


## Operation error

| Error code <br> $((\mathrm{s} 2)+1)$ | Description |
| :--- | :--- |
| C000H to CFFFH | D $\triangle$ MELSEC iQ-R Ethernet User's Manual (Application) |

## 13．2 Socket Communications Instructions

## Reading receive data

## GP．SOCRCV

This instruction reads receive data from the external device through socket communications．


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| GP．SOCRCV |  |

Setting data
■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （U） | Start I／O number（first three digits in four－digit hexadecimal <br> representation）of own station or own node | 00 H to FEH | 16－bit unsigned binary | ANY16 |
| （s1） | Connection No． | 17 to 128 <br> $(17$ to $64:$ Port 1 socket <br> communications， 65 to $128:$ <br> Port 2 socket communications） | 16－bit unsigned binary | ANY16 |
| （s2） | Own station start device where control data is stored | Refer to the control data． | Device name | ANY16 |
| （d1） | Own station head device for storing the receive data | - | Device name | ANY16 |
| （d2） | Own station device to be turned on one scan when the <br> instruction completes． <br> （d2）＋1 also turns on when the instruction completes with <br> an error． | - | Bit | ANYBIT＿ARRAY <br> （Number of <br> elements：2） |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （U） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ |
| （s1） | O＊1 | － | $0^{* 2}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | － | － | $0^{*}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $0^{*}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d2） | $\bigcirc{ }^{* 1}$ | － | $0^{* 3}$ | － | － | － | － | － | － | － | － | － |

＊1 FX and FY cannot be used．
＊2 FD cannot be used．
＊3 T，ST，C，and FD cannot be used．

## Control data

| Operand: (s2) |  | Description | Setting range | Set by |
| :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Item | - | - |  |
| +0 | System area | Completion status | The instruction completion status is stored. <br> •0: Completed successfully <br> •Other than 0: Completed with an error (error code) | - |
| +1 |  | - | System |  |

## Receive data

| Operand: (d1) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +0 | Receive data length | The length of the data read from the socket communications receive data area is stored. | - | System |
| +1 to + $\square$ | Receive data | The data read from the socket communications receive data area is stored sequentially in ascending order of addresses. | - | System |

## Processing details

The GP.SOCRCV instruction is used for socket communications.

## Operation error

| Error code <br> $((\mathbf{s} 2)+1)$ | Description |
| :--- | :--- |
| C000H to CFFFH | LD MELSEC iQ-R Ethernet User's Manual (Application) |

## Reading receive data（for interrupt programs）

## G．SOCRCVS

This instruction reads receive data from the external device through socket communications（for interrupt programs）．


FBD／LD


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| G．SOCRCVS | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （U） | Start I／O number（first three digits in four－digit hexadecimal <br> representation）of own station or own node | 00 H to FEH | 16－bit unsigned binary | ANY16 |
| （s） | Connection No． | 17 to 128 <br> $(17$ to $64:$ Port 1 socket <br> communications， 65 to $128:$ <br> Port 2 socket communications） | 16－bit unsigned binary | ANY16 |
| （d） | Own station head device for storing the receive data | - | Device name | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDl（H）G口 | z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （U） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ |
| （s） | O＊1 | － | $0^{* 2}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | － | － | $\mathrm{O}^{*}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

＊1 FX and FY cannot be used．
＊2 FD cannot be used．

## Receive data

| Operand：（d） | Sescription | Setting range | Set by |  |
| :--- | :--- | :--- | :--- | :--- |
| Device | Item | Receive data length | The length of the data read from the socket communications receive data area is <br> stored． | - |
| +0 | Receive data | The data read from the socket communications receive data area is stored <br> sequentially in ascending order of addresses． | - | System |
| +1 to＋ |  |  | System |  |

## Processing details

The G．SOCRCVS instruction is used for socket communications．

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| COOOH to CFFFH | L MELSEC iQ-R Ethernet User's Manual (Application) |

## Sending data

## GP．SOCSND

This instruction sends data to the external device through socket communications．

| Ladder |  |  |  |  |  | ST |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| ■－－－ |  |  |  |  |  |  | NO：＝GP＿SOCSND（EN，U，s1，s2，s3，d）； |
|  | （U） | （s1） | （s2） | （s3） | （d） |  |  |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| GP．SOCSND | - |
|  | - |

## Setting data

■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （U） | Start I／O number（first three digits in four－digit hexadecimal <br> representation）of own station or own node | 00 H to FEH | 16－bit unsigned binary | ANY16 |
| （s1） | Connection No． | 17 to 128 <br> $(17$ to $64:$ Port 1 socket <br> communications， 65 to $128: ~ P o r t ~$ <br> 2 socket communications） | 16－bit unsigned binary | ANY16 |
| （s2） | Own station start device where control data is stored | Refer to the control data． | Device name | ANY16 |
| （s3） | Own station head device where send data is stored | - | Device name | ANY16 |
| （d） | Own station device to be turned on one scan when the <br> instruction completes． <br> If the instruction is completed with an error，（d）+1 is also <br> turned on． | - | Bit | ANYBIT＿ARRAY <br> （Number of <br> elements： 2$)$ |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T, ST, C, D, w, SD, SW，FD，R，ZR，RD | U밈，J밈， U3EDl（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （U） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ |
| （s1） | O＊1 | － | $0^{*}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | － | － | ${ }^{*}{ }^{2}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s3） | － | － | ${ }^{*}{ }^{2}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | O＊1 | － | ${ }^{*} 3$ | － | － | － | － | － | － | － | － | － |

＊1 FX and FY cannot be used．
＊2 FD cannot be used．
＊3 T，ST，C，and FD cannot be used．

## Control data

| Operand: (s2) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +0 | System area | - | - | - |
| +1 | Completion status | The instruction completion status is stored. <br> - 0: Completed successfully <br> - Other than 0: Completed with an error (error code) | - | System |

## Send data

| Operand: (s3) |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- |
| Device | Item | Description | Setting range | Set by |
| +0 | Send data length | Specify the send data length. (Number of bytes) | 1 to 10238 |  |
| +1 to $+\square$ | Send data | Specify the send data. | - | User |

## Processing details

The GP.SOCSND instruction is used for socket communications.

## Operation error

| Error code <br> $((\mathrm{s} 2)+1)$ | Description |
| :--- | :--- |
| COOOH to CFFFH | DI MELSEC iQ-R Ethernet User's Manual (Application) |

### 13.3 Fixed Buffer Communications Instructions

## Reading receive data

## GP.BUFRCV, ZP.BUFRCV

These instructions read receive data from the external device through fixed buffer communications


| ■--- $\square$ |  |
| :---: | :---: |
| EN | ENO |
| U | d1 |
| s1 | d2 |
| s2 |  |

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| GP.BUFRCV | }{} |

Setting data
Description, range, data type

| Operand |  | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- | :--- |
| (U) | GP.BUFRC <br> V | Start I/O number (first three digits in four-digit <br> hexadecimal representation) of own station or own <br> node | 00 H to FEH |  |  |
|  | ZP.BUFRC | Start I/O number (first three digits in four-digit <br> hexadecimal representation) of own station or own <br> node | 00 H to FEH | 16-bit unsigned binary | ANY16 |
| (s1) | Connection No. | 1 to 16 | ANY16_OR_STRING <br> SINGLE |  |  |
| (s2) | Own station start device where control data is stored | Refer to the control data. | Device name | ANY16 |  |
| (d1) | Own station head device for storing the receive data | - | Device name | ANY16 |  |
| (d2) | Own station device to be turned on one scan when <br> the instruction completes. <br> (d2)+1 also turns on when the instruction completes <br> with an error. | - | Bit | ANYBIT_ARRAY <br> (Number of elements: <br> 2) |  |
| EN | Execution condition | Execution result | - | Bit | BOOL |
| ENO |  |  | Bit | BOOL |  |

Applicable devices

| Operand |  | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J밈， U3E미（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （U） | GP．B <br> UFR <br> CV | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ |
|  | $\begin{aligned} & \text { ZP.B } \\ & \text { UFR } \\ & \text { CV } \end{aligned}$ | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ |
| （s1） |  | O＊1 | － | ${ }^{*}{ }^{2}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） |  | － | － | ${ }^{*}{ }^{2}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） |  | － | － | ${ }^{*}{ }^{2}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d2） |  | O＊1 | － | $0^{*}$ | － | － | － | － | － | － | － | － | － |

＊1 FX and FY cannot be used．
＊2 FD cannot be used
＊3 T，ST，C，and FD cannot be used．

## Control data

| Operand：（s2） |  |  |  |  |  |  |  | Description | Setting range | Set by |
| :--- | :--- | :--- | :--- | :--- | :--- | :---: | :---: | :---: | :---: | :---: |
| Device | Item | - | - |  |  |  |  |  |  |  |
| +0 | System area | The instruction completion status is stored． <br> $\bullet 0:$ Completed successfully <br> $\cdot$ Other than 0：Completed with an error（error code） |  |  |  |  |  |  |  |  |
| +1 | Completion status | - |  |  |  |  |  |  |  |  |

## ■Receive data

| Operand：（d1） |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description |  | Setting range | Set by |
| ＋0 | Receive data length | The length of the data read from the fixed buffer receive data area is stored． <br> （The data length is represented in words or bytes according to the procedure of fixed buffer communications．） | With procedure（binary）： <br> Number of words | $\begin{aligned} & 1 \text { to } 5113 \\ & (1 \text { to } 1017)^{* 1} \end{aligned}$ | System |
|  |  |  | With procedure（ASCII）： <br> Number of words | $\begin{aligned} & 1 \text { to } 2556 \\ & (1 \text { to } 508)^{* 1} \end{aligned}$ |  |
|  |  |  | Without procedure（binary）： <br> Number of bytes | $\begin{aligned} & 1 \text { to } 10238 \\ & (1 \text { to } 2046)^{* 1} \end{aligned}$ |  |
| ＋1～＋ロ | Receive data | The data read from the fixed buffer receive data ar ascending order of addresses． | s is stored sequentially in | － | System |

＊1 This setting range is applicable when the network type is＂Q－compatible Ethernet＂．

## Processing details

- These instructions read the receive data (fixed buffer communications area) of the connection specified by (s1) of the module specified by (U). These instructions can be used only for the connections for which "fixed buffer communication (with/without procedure)" is set as the communication means.

- The execution of the BUFRCV instruction and whether it has been completed normally or completed with an error can be checked with the completion device (d2) or completion status indication device (d2)+1.
- Completion device (d2)

The completion device turns on in END processing of the scan performed upon completion of the BUFRCV instruction and turns off in the next END processing.

- Completion status indication device (d2)+1

The completion device turns on or off depending on the completion status of the BUFRCV instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on in END processing of the scan performed upon completion of the BUFRCV instruction and turns off in the next END processing.

- The following figure shows the operation at completion of the BUFRCV instruction.

- The BUFRCV instruction is executed on the rising edge (OFF to ON) of the socket/fixed buffer receive status signal (Un\G1900016 to Un\G1900023).


## Restriction ${ }^{\prime \prime}$

When the BUFRCV instruction reads receive data from the same connection, it cannot be used in combination with the BUFRCVS instruction (for use in interrupt programs).

## Operation error

| Error code <br> $((\mathbf{s} 2)+1)$ | Description |
| :--- | :--- |
| C 000 H to CFFFH | LD MELSEC iQ-R Ethernet User's Manual (Application) |

## Reading receive data（for interrupt programs）

G．BUFRCVS，Z．BUFRCVS
These instructions read receive data from the external device through fixed buffer communications（for interrupt programs）．


FBD／LD


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| G．BUFRCVS | - |
| Z．BUFRCVS | $\square$ |

## Setting data

Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- | :--- |
| （U） | GP．BUFRC <br> VS | Start I／O number（first three digits in four－digit <br> hexadecimal representation）of own station or own <br> node | 00 H to FEH |  |  |
|  | ZP．BUFRCV | Start I／O number（first three digits in four－digit <br> hexadecimal representation）of own station or own <br> node | 00 H to FEH | 16－bit unsigned binary | ANY16 |
|  | Connection No． | 1 to 16 | ANY16＿OR＿STRING |  |  |
| （d） | Own station head device for storing the receive data | - | SINGLE | 16－bit unsigned binary | ANY16 |
| EN | Execution condition | - | Device name | ANY16 |  |
| ENO | Execution result | - | Bit | BOOL |  |

Applicable devices

| Operand |  | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3E미（H）Gㅁ | z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （U） | GP．BU FRCVS | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ |
|  | ZP．BUF RCVS | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ |
| （s） |  | O＊1 | － | $\mathrm{O}^{*}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） |  | － | － | $\mathrm{O}^{*}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

＊1 FX and FY cannot be used．
＊2 FD cannot be used

Receive data

| Operand: (d) |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description |  | Setting range | Set by |
| +0 | Receive data length | The length of the data read from the fixed buffer receive data area is stored. <br> (The data length is represented in words or bytes according to the procedure of fixed buffer communications.) | With procedure (binary): <br> Number of words | $\begin{aligned} & 1 \text { to } 5113 \\ & (1 \text { to } 1017)^{* 1} \end{aligned}$ | System |
|  |  |  | With procedure (ASCII): <br> Number of words | $\begin{aligned} & 1 \text { to } 2556 \\ & (1 \text { to } 508)^{* 1} \end{aligned}$ |  |
|  |  |  | Without procedure (binary): <br> Number of bytes | $\begin{aligned} & 1 \text { to } 10238 \\ & (1 \text { to } 2046)^{* 1} \end{aligned}$ |  |
| +1 to + | Receive data | The data read from the fixed buffer receive data ar ascending order of addresses. | s is stored sequentially in | - | System |

*1 This setting range is applicable when the network type is "Q-compatible Ethernet".

## Processing details

- These instructions read the receive data (fixed buffer communications area) of the connection specified by (s) of the module specified by (U). These instructions can be used only for the connections for which "fixed buffer communication (with/without procedure)" is set as the communication means.

- The BUFRCVS instruction is used in interrupt programs and processing is completed in a single scan.



## Restriction

When the BUFRCVS instruction reads receive data from the same connection, it cannot be used in combination with the BUFRCV instruction.

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| C 000 H to CFFFH | $\square \square$ MELSEC iQ-R Ethernet User's Manual (Application) |

## Sending data

GP.BUFSND, ZP.BUFSND
These instructions send data to the external device through fixed buffer communications.


FBD/LD

| ■--- $]$ |  |
| :---: | :---: |
| EN | ENO |
| U | d |
| s1 |  |
| s2 |  |
| s3 |  |

Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| GP.BUFSND | - |
| ZP.BUFSND | - |

## Setting data

■Description, range, data type

| Operand |  | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- | :--- |
| (U) | GP.BUFSN <br> D | Start I/O number (first three digits in four-digit <br> hexadecimal representation) of own station or own <br> node | 00 H to FEH | 16-bit unsigned binary | ANY16 |
|  | ZP.BUFSN <br> D | Start I/O number (first three digits in four-digit <br> hexadecimal representation) of own station or own <br> node | 00 H to FEH | String | ANY16_OR_STRING <br> SINGLE |
| (s1) | Connection No. | 1 to 16 | ANY16 |  |  |
| (s2) | Own station start device where control data is stored | Refer to the control data. | Device name | ANY16 |  |
| (s3) | Own station head device where send data is stored | - | Device name | ANY16 |  |
| (d) | Own station device to be turned on one scan when <br> the instruction completes. <br> If the instruction is completed with an error, (d) +1 is <br> also turned on. | - | Bit | ANYBIT_ARRAY <br> (Number of elements: <br> $2)$ |  |
| EN | Execution condition | - | Bit | BOOL |  |
| ENO | Execution result | - | Bit | BOOL |  |

Applicable devices

| Operand |  | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> (U) |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | J밈 | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | U민ㅁ, J밈, U3EDI(H)Gロ | z | LT, LST, LC | LZ |  | K, H | E | \$ |  |
| (U) | $\begin{aligned} & \text { GP.B } \\ & \text { UFS } \\ & \text { ND } \end{aligned}$ | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | $\bigcirc$ | - | - | $\bigcirc$ |
|  | ZP.B <br> UFS <br> ND | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | $\bigcirc$ | $\bigcirc$ |
| (s1) |  | O*1 | - | $\mathrm{O}^{*}$ | - | - | - | - | $\bigcirc$ | $\bigcirc$ | - | - | - |
| (s2) |  | - | - | $\mathrm{O}^{*}$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (s3) |  | - | - | $\bigcirc^{*}$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (d) |  | $\mathrm{O}^{* 1}$ | - | $\mathrm{O}^{* 3}$ | - | - | - | - | - | - | - | - | - |

*1 FX and FY cannot be used.
*2 FD cannot be used.
*3 T, ST, C, and FD cannot be used.

## Control data

| Operand: (s2) |  |  |  |  |  |  |  | Description | Setting range | Set by |
| :--- | :--- | :--- | :--- | :--- | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Item | - | - |  |  |  |  |  |  |  |
| +0 | System area | The instruction completion status is stored. <br> $\bullet 0:$ Completed successfully <br> $\cdot$ Other than 0: Completed with an error (error code) |  |  |  |  |  |  |  |  |
| +1 | Completion status | - |  |  |  |  |  |  |  |  |

## Send data

| Operand: (s3) |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description |  | Setting range | Set by |
| +0 | Send data length | Specify the send data length. <br> (Specify the data length in words or bytes according to the procedure of fixed buffer communications.) | With procedure (binary): <br> Number of words | $\begin{aligned} & 1 \text { to } 5113 \\ & (1 \text { to } 1017)^{* 1} \end{aligned}$ | User |
|  |  |  | With procedure (ASCII): Number of words | $\begin{aligned} & 1 \text { to } 2556 \\ & (1 \text { to } 508)^{{ }^{1}} \end{aligned}$ |  |
|  |  |  | Without procedure (binary): <br> Number of bytes | $\begin{aligned} & 1 \text { to } 10238 \\ & (1 \text { to } 2046)^{* 1} \end{aligned}$ |  |
| +1 to + $\square$ | Send data | Specify the send data. |  | - | User |

*1 This setting range is applicable when the network type is "Q-compatible Ethernet".

## Processing details

- This instruction sends the data in the device specified by (s3) to the external device of the connection specified by (s1) of the module specified by (U). These instructions can be used only for the connections for which "fixed buffer communication (with/without procedure)" is set as the communication means.

- The execution of the BUFSND instruction and whether it has been completed normally or completed with an error can be checked with the completion device (d) or completion status indication device (d)+1.
- Completion device (d)

The completion device turns on in END processing of the scan performed upon completion of the BUFSND instruction and turns off in the next END processing.

- Completion status indication device (d) +1

The completion device turns on or off depending on the completion status of the BUFSND instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on in END processing of the scan performed upon completion of the BUFSND instruction and turns off in the next END processing.

- The following figure shows the operation at completion of the BUFSND instruction.

- The BUFSND instruction is executed on the rising edge (OFF to ON) of the send command.


## Operation error

| Error code <br> $((\mathbf{s 2})+\mathbf{1})$ | Description |
| :--- | :--- |
| C000H to CFFFH | La MELSEC iQ-R Ethernet User's Manual (Application) |

## 13．4 Reinitializing the Module

## G（P）．UINI，Z（P）．UINI

These instructions reinitialize the module．The UINI instruction can be used only when the network type is＂Q－compatible Ethernet＂．

| Ladder | ST |
| :---: | :---: |
|  | $\begin{aligned} & \text { ENO:=G_UINI(EN,U,s,d); } \\ & \text { ENO:=GP_UINI(EN,U,s,d); } \\ & \text { ENO:=Z_UINI(EN,U,s,d); } \\ & \text { ENO:=ZP_UINI(EN,U,s,d); } \end{aligned}$ |

FBD／LD


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| G．UINI | - |
| Z．UINI | - |
| GP．UINI | - |
| ZP．UINI |  |

## Setting data

Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- | :--- |
| （U） | G（P）．UINI | Start I／O number（first three digits in four－digit <br> hexadecimal representation）of own station or own <br> node | 00 H to FEH | 16－bit unsigned binary | ANY16 |
|  | Z（P）．UINI | Start I／O number（first three digits in four－digit <br> hexadecimal representation）of own station or own <br> node | 00 H to FEH | ANY16＿OR＿STRING <br> ＿SINGLE |  |
| （s） | Own station start device where control data is stored | Refer to the control data． | Device name | ANY16 |  |
| （d） | Own station device to be turned on one scan when <br> the instruction completes． <br> If the instruction is completed with an error，（d）+1 is <br> also turned on． | - | Bit | ANYBIT＿ARRAY <br> （Number of elements： <br> $2)$ |  |
| EN | Execution condition | - | Bit | BOOL |  |
| ENO | Execution result | - | Bit | BOOL |  |

## Applicable devices

| Operand |  | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | J미 | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3E미（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （U） | G（P）． <br> UINI | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ |
|  | $\begin{aligned} & Z(P) . \\ & \text { UINI } \end{aligned}$ | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ |
| （s） |  | － | － | $0^{* 2}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） |  | O＊1 | － | $0^{* 3}$ | － | － | － | － | － | － | － | － | － |

＊1 FX and FY cannot be used．
＊2 FD cannot be used．
＊3 T，ST，C，and FD cannot be used．

| Operand: (s) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +0 | System area | - | - | - |
| +1 | Completion status | The instruction completion status is stored. <br> - 0: Completed successfully <br> - Other than 0: Completed with an error (error code) | - | System |
| +2 | Change target specification | ■To change the address information of the external device held by the Ethernet module <br> Specify 0000H.*1 <br> ■To change the own station IP address, operation setting, transmission speed, or communication mode <br> Specify the parameter of the change target. <br> Note that changing the transmission speed and communication mode cannot be specified together with the change specification of the own station IP address or operation setting. <br> If they are specified together, the change specification of only the own station IP address or operation setting is executed. <br> (1) Own station IP address change specification (bit 0) <br> Specify whether to change the IP address of the own station. (When changing the IP address, specify a new address in (s) +3 , (s)+4.) <br> - 0: Not changed <br> - 1: Changed <br> (2) Operation setting change specification (bit 1) <br> Specify whether to change the Ethernet operation setting. (When changing the setting, specify a new setting in (s)+5.) <br> - 0: Not changed <br> - 1: Changed <br> (3) Transmission speed and communication mode change specification (bits 12 to 15) <br> Specify the transmission speed and communication mode. <br> - 0: Not changed <br> - 1: Auto negotiation*2 <br> - 2: 100Mbps/full duplex <br> - 3: 100Mbps/half-duplex <br> - 4: 10Mbps/full duplex <br> - 5: 10Mbps/half-duplex | 0000 H to 5000 H | User |
| $\begin{aligned} & +3 \\ & +4 \end{aligned}$ | Own station IP address | Specify the IP address of the own station. <br> -(s) +3 <br> - (s) +4 | 00000001H to FFFFFFFEH | User |
| +5 | Ethernet operation setting | Specify the operation setting. <br> (1) Communication data code setting (bit 1 ) <br> - 0: Binary <br> - 1: ASCII <br> (2) Online change enable/disable setting (bit 6) <br> - 0: Disable all at once (SLMP) <br> - 1: Enable all at once (SLMP) <br> (3) Opening method setting (bit 8) <br> - 0 : Open with the program. <br> - 1: Do not open with the program. | Left | User |

*1 The instruction clears and reinitializes the address information of the external device held by the Ethernet module so that data communication can be restarted. (The initialization normal completion signal (X19) turns on.)
*2 When auto negotiation is specified, use 1000BASE-T.

## Processing details

- These instructions reinitializes the Ethernet module specified by (U). If changing the IP address is specified, change the IP address of the Ethernet module of the own station.
- The execution of the UINI instruction and whether it has been completed normally or completed with an error can be checked with the completion device (d) or completion status indication device (d)+1.
- Completion device (d)

The completion device turns on in END processing of the scan performed upon completion of the UINI instruction and turns off in the next END processing

- Completion status indication device (d) +1

The completion device turns on or off depending on the completion status of the UINI instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on in END processing of the scan performed upon completion of the UINI instruction and turns off in the next END processing.

- The following figure shows the operation at completion of the UINI instruction.

- The UINI instruction is executed on the rising edge (OFF to ON) of the reinitialize command.


## Precautions

- When executed, the instruction forcibly closes all connections and initialize the internal information of the module.
- If the IP address is changed, communication is stopped. In engineering tool Ethernet connection mode, the IP address of the Ethernet function module should be corrected to the new IP address by specifying the online connection destination and communication should be restarted.
- If the IP address is changed while "Use IP address" is selected in "Network No. and station number setting method" of the engineering tool, the network number and station number are not changed and the value using the IP address that is set by a parameter remains unchanged.


## Operation error

| Error code ((s)+1) | Description |
| :--- | :--- |
| COOOH to CFFFH | LD MELSEC iQ-R Ethernet User's Manual (Application) |

## 13．5 Executing the Protocols Registered for the Predefined Protocol Support Function

## GP．ECPRTCL

This instruction executes the communication protocol registered using the engineering tool．


FBD／LD

| ■－－－$\square$ |  |
| :---: | :---: |
| EN | ENO |
| U | d |
| s1 |  |
| s2 |  |
| s3 |  |

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| GP．ECPRTCL | $\ddots$ |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （U） | Start I／O number（first three digits in four－digit hexadecimal <br> representation）of own station or own node | 00 H to FEH | 16－bit unsigned binary | ANY16 |
| （s1） | Connection No． | 1 to 16 | 16－bit unsigned binary | ANY16 |
| （s2） | Number of protocols to be executed continuously | 1 to 8 | 16－bit unsigned binary | ANY16 |
| （s3） | Own station start device where control data is stored | Refer to the control data． | Device name | ANY16 |
| （d） | Own station device to be turned on one scan when the <br> instruction completes． <br> If the instruction is completed with an error，（d）+1 is also <br> turned on． | - | Bit | ANYBIT＿ARRAY <br> （Number of elements： <br> $2)$ |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDl（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （U） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ |
| （s1） | O＊1 | － | $\mathrm{O}^{*}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | $\bigcirc{ }^{* 1}$ | － | $0^{*}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s3） | － | － | $\mathrm{O}^{*}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | O＊1 | － | $0^{* 3}$ | － | － | － | － | － | － | － | － | － |

＊1 FX and FY cannot be used．
＊2 FD cannot be used．
＊3 T，ST，C，and FD cannot be used．

| Operand: (s3) |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- |
| Device | Item | Resulting number of <br> executed protocols | The number of protocols executed by the GP.ECPRTCL instruction is stored. <br> Any protocol where an error occurred is also included in the execution number. <br> If the setting of setting data or control data contains an error, "0" is stored. | 0,1 to 8 |


| Operand: (s3) | Description | Setting range | Set by |  |
| :--- | :--- | :--- | :--- | :--- |
| Device | Item | Receive packet number 7 <br> successful in collation <br> match | If receiving is included in the communication type of the protocol that has been <br> executed seventh, the receive packet number successful in collation match is <br> stored. <br> If the communication type is "receive only", 0 is stored. <br> If an error occurs during execution of the seventh protocol, 0 is stored. <br> If the number of protocols executed is less than 7,0 is stored. | 0,1 to 16 |
| +16 | Receive packet number 8 <br> successful in collation <br> match | If receiving is included in the communication type of the protocol that has been <br> executed eighth, the receive packet number successful in collation match is <br> stored. <br> If the communication type is "receive only", 0 is stored. <br> If an error occurs during execution of the eighth protocol, 0 is stored. <br> If the number of protocols executed is less than 8,0 is stored. | 0,1 to 16 | System |
| +17 |  |  |  |  |

## Processing details

- This instruction executes the protocol registered by the communication protocol support tool of the engineering tool for the Ethernet module specified by (U). Using the connection specified by ( s 1 ), the instruction executes the protocol in accordance with the control data stored in the device specified by ( s 3 ) and later.
- The instruction continuously executes as many protocols as specified by (s2) (a maximum of 8 protocols) at one time.
- The number of executed protocols is stored in the device specified by $(\mathrm{s} 3)+0$.
- The execution of the GP.ECPRTCL instruction and whether it has been completed normally or completed with an error can be checked with the completion device (d) or completion status indication device (d)+1.
- Completion device (d)

The completion device turns on in END processing of the scan performed upon completion of the GP.ECPRTCL instruction and turns off in the next END processing.

- Completion status indication device (d) +1

The completion device turns on or off depending on the completion status of the GP.ECPRTCL instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on in END processing of the scan performed upon completion of the GP.ECPRTCL instruction and turns off in the next END processing.

- The following figure shows the operation at completion of the GP.ECPRTCL instruction.


## Precautions

- If an error occurs in the mth protocol while multiple protocols are being executed, the instruction does not execute the " $\mathrm{m}+1$ "th protocol and after and is completed with an error.
- The connections for which the GP.ECPRTCL instruction can be executed are only those for which "Communication protocol" is specified for the communication means.
- If a cancel request is received during execution of the mth protocol while multiple protocols are executed continuously, the Ethernet module stores the protocol number $m$ being executed in the device specified by ( $s 3$ ) +0 , the receive packet number successful in collation match for the already executed protocol in the device specified by 1 to ( $\mathrm{m}-1$ ), and the protocol cancel request error (error code: C 404 H ) in the device specified by (s3)+1.
- When a protocol including no-conversion variables is executed, the total data length of the variables used in one packet may exceed 1920 bytes. In this case, the instruction may obtain CPU device values over several scans. Therefore, do not change the CPU device values specified in non-conversion variables from the start of the dedicated instruction to the end of execution.
- The intelligent function module device (buffer memory) is not affected by the sequence scan of the CPU module and therefore can process protocols faster than assigning programmable controller devices to variables.
- If protocol setting data is written during execution of the GP.ECPRTCL instruction, the protocol in execution is canceled upon completion of writing and the instruction is completed with a protocol setting data write error (error code: C 430 H ).
- If the fixed buffer send/receive mode (such as send) of the specified connection has an invalid combination with the communication type of the protocol to be executed (such as receive only) while the connection number of the Ethernet module is one from 1 to 16 , the GP.ECPRTCL instruction is completed with a connection number setting error (error code: C407H).
- If the receive waiting time is set to "0: Infinite wait", the GP.ECPRTCL instruction is not completed until the data specified in the protocol setting is received.


## Operation error

| Error code <br> $((\mathrm{s} 3)+1)$ | Description |
| :--- | :--- |
| C 000 H to CFFFH | LD MELSEC iQ-R Ethernet User's Manual (Application) |

### 13.6 Clearing Error Information

## GP.ERRCLEAR, ZP.ERRCLEAR

These instructions clear error information.


FBD/LD


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| GP.ERRCLEAR | - |
| ZP.ERRCLEAR | - |

## Setting data

■Description, range, data type

| Operand |  | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- | :--- |
| (U)GP.ERRCLE <br> AR | Start I/O number (first three digits in four-digit <br> hexadecimal representation) of own station or own <br> node | 00 H to FEH | 16-bit unsigned binary | ANY16 |  |
| ZP.ERRCLE <br> AR | Start I/O number (first three digits in four-digit <br> hexadecimal representation) of own station or own <br> node | 00 H to FEH | String | ANY16_OR_STRING |  |
| (s) SINGLE |  |  |  |  |  |

## ■Applicable devices

| Operand |  | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> (U) |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | U밈, J밈, U3EDI(H)GD | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (U) | GP.E <br> RRC <br> LEAR | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | $\bigcirc$ | - | - | $\bigcirc$ |
|  | ZP.E <br> RRC <br> LEAR | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | $\bigcirc$ | $\bigcirc$ |
| (s) |  | - | - | $\mathrm{O}^{* 2}$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (d) |  | O*1 | - | $\mathrm{O}^{*}$ | - | - | - | - | - | - | - | - | - |

*1 FX and FY cannot be used.
*2 FD cannot be used.
*3 T, ST, C, and FD cannot be used.

Control data

| Operand: (s) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +0 | System area | - | - | - |
| +1 | Completion status | The instruction completion status is stored. <br> - 0: Completed successfully <br> - Other than 0: Completed with an error (error code) | - | System |
| +2 | Clear target specification | Specify the error information to clear. | $\begin{aligned} & 0000 \mathrm{H} \text { to } 0080 \mathrm{H}, \\ & 0100 \mathrm{H}, 0101 \mathrm{H}, \\ & 8000 \mathrm{H}, 8101 \mathrm{H}, \\ & \text { FFFFH } \end{aligned}$ | User |
| +3 | Clear function specification | Specify the function to clear. <br> - 0000H: Clear the error code. <br> - FFFFH: Clear the error log. | 0000H, FFFFH | User |
| +4 to +7 | System area | - | - | - |

## Processing details

- These instructions clear following error information with regard to the module specified by (U).

| Target name | $\begin{array}{l}\text { Clear target } \\ \text { specification } \\ \text { (s)+2 }\end{array}$ | $\begin{array}{l}\text { Clear function } \\ \text { specification } \\ \text { (s)+3 }\end{array}$ | $\begin{array}{l}\text { Specify the error } \\ \text { information to clear. }\end{array}$ | $\begin{array}{l}\text { Buffer memory address } \\ \text { compatible Ethernet }\end{array}$ |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
| Initial error (PORT1) | 0000 H | 0000 H | $\begin{array}{l}\text { Network type: } \\ \text { Ethernet }\end{array}$ | PORT1 initial error code | 1 |$]$| 105 |
| :--- |
| Open error |

*1 Only 0001 H to 0010 H can be specified when the network type is Q -compatible Ethernet.
*2 The error log can be cleared only when the network type is Q-compatible Ethernet.
*3 The initial error can be cleared only when the network type is Ethernet.

## Operation error

| Error code ((s)+1) | Description |
| :--- | :--- |
| C 000 H to CFFFH | $\square]$ MELSEC iQ-R Ethernet User's Manual (Application) |

## 13．7 Reading Error Information

## GP．ERRRD，ZP．ERRRD

These instructions read error information．

| Ladder | ST |
| :---: | :---: |
|  | $\begin{aligned} & \text { ENO:=GP_ERRRD(EN,U,s,d); } \\ & \text { ENO:=ZP_ERRRD(EN,U,s,d); } \end{aligned}$ |

FBD／LD


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| GP．ERRRD | $\uparrow$ |
| ZP．ERRRD | - |

## Setting data

■Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- | :--- |
| （U） | GP．ERRRD | Start I／O number（first three digits in four－digit <br> hexadecimal representation）of own station or own <br> node | 00 H to FEH | 16－bit unsigned binary | ANY16 |
|  | ZP．ERRRD | Start I／O number（first three digits in four－digit <br> hexadecimal representation）of own station or own <br> node | 00 H to FEH | ANY16＿OR＿STRING <br> ＿SINGLE |  |
| （s） | Own station start device where control data is stored | Refer to the control data． | Device name | ANY16 |  |
| （d） | Own station device to be turned on one scan when <br> the instruction completes． <br> If the instruction is completed with an error，（d）+1 is <br> also turned on． | - | Bit | ANYBIT＿ARRAY <br> （Number of elements： <br> $2)$ |  |
| EN | Execution condition | - | Bit | BOOL |  |
| ENO | Execution result | - | Bit | BOOL |  |

## Applicable devices

| Operand |  | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，Jपاロ， U3EDl（H）GD | z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （U） | $\begin{aligned} & \text { GP.E } \\ & \text { RRR } \\ & \text { D } \end{aligned}$ | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ |
|  | $\begin{aligned} & \text { ZP.E } \\ & \text { RRR } \\ & \text { D } \end{aligned}$ | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ |
| （s） |  | － | － | $\mathrm{O}^{*}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） |  | $\bigcirc{ }^{* 1}$ | － | $0^{* 3}$ | － | － | － | － | － | － | － | － | － |

＊1 FX and FY cannot be used．
＊2 FD cannot be used．
＊3 T，ST，C，and FD cannot be used．

Control data
$\left.\begin{array}{l|l|l|l|l}\hline \text { Operand: (s) } & \text { Description } & & \\ \hline \text { Device } & \text { Item } & - & \begin{array}{l}\text { Setting range }\end{array} & \text { Set by } \\ \hline+0 & \text { System area } & \begin{array}{l}\text { The instruction completion status is stored. } \\ \cdot \text { O. Completed successfully }\end{array} \\ \hline+1 & \text { Completion status } 0: \text { Completed with an error (error code) }\end{array}\right)$

## Processing details

- These instructions clear following error information with regard to the module specified by (U).

| Target name | Read <br> information <br> specification <br> (s) +2 | Read target <br> information <br> specification <br> (s) +3 | Error information to read |  | Buffer memory address |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :---: |
|  |  |  | Network type: <br> Ethernet | Network type: Q- <br> compatible Ethernet |  |  |
| Initial error | 0000 H | 0000 H | PORT1 initial error code | 1900025 | 105 |  |
| Open error | 0001 H to $0080 \mathrm{H}^{* 1}$ | 0000 H | Open error code of connection <br> The open error code of <br> connection No. 1 to $128(0001 \mathrm{H}$ <br> to 0080 H$)$ is read. | $100,164,228 \cdots$ | $124,134,144, \cdots$ |  |

*1 Only 0001 H to 0010 H can be specified when the network type is Q-compatible Ethernet.

## Operation error

| Error code ((s)+1) | Description |
| :--- | :--- |
| COOOH to CFFFH | La MELSEC iQ-R Ethernet User's Manual (Application) |

## 14 CC-LINK IE CONTROLLER NETWORK INSTRUCTIONS

### 14.1 Remote RUN

## J(P).RRUN, G(P).RRUN, Z(P).RRUN

These instructions execute remote RUN for the programmable controller on another station.


## Setting data

■Description, range, data type

| Operand |  | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- | :--- |
| $(\mathrm{J} / \mathrm{U})$ | $\mathrm{J}(\mathrm{P}) \cdot \mathrm{RRUN}$ | (J): Target station network number | 1 to 239 | 16-bit unsigned binary | ANY16 |
|  | G(P).RRU <br> N | (U): Start I/O number (first three digits in four-digit hexadecimal <br> representation) of own station or own node | 00 H to FEH | 16-bit unsigned binary | ANY16 |
|  | Z(P).RRU <br> N | (J): Target station network number | 1 to 239 | String | ANY16_OR_STRIN <br> G_SINGLE |
|  | Z(P).RRU <br> N | (U): Start I/O number (first three digits in four-digit hexadecimal <br> representation) of own station or own node | 00 H to FEH | String | ANY16_OR_STRIN <br> G_SINGLE |
| (s1) | Own station channel (以 Page 1192 Range of available <br> channel numbers) <br> Specifies the channel to be used by the own station or node. <br> Specify the channel that was specified, as being used by the <br> own station, in the RSTOP instruction. | 1 to 8, 11 to 18 | 16-bit unsigned binary | ANY16 |  |


| Operand | Description | Range | Data type | Data type (label) |
| :---: | :---: | :---: | :---: | :---: |
| (s2) | Specify the station number of the target station/target node. <br> (1) Station number specification <br> 1 to 120 <br> (2) Group specification ${ }^{* 1}$ <br> 81 H to AOH : All stations of group numbers 1 to 32 <br> (3) All-station specification*1 <br> FFH: All stations of target network numbers, except own station | 1 to 120 0081H to 00AOH 00FFH | 16-bit unsigned binary | ANY16 |
| (s3) | Specify that CPU module in the target station that is to be accessed. <br> - 0000H: Addressed to target station CPU (control CPU) <br> - 03DOH: Addressed to control system CPU ${ }^{*}$ <br> - 03D1H: Addressed to standby system CPU*2 <br> - 03D2H: Addressed to system A CPU <br> - 03D3H: Addressed to system B CPU <br> - 03E0H: Addressed to multiple CPU No. 1 <br> - 03E1H: Addressed to multiple CPU No. 2 <br> - 03E2H: Addressed to multiple CPU No. 3 <br> - 03E3H: Addressed to multiple CPU No. 4 <br> - 03FFH: Addressed to target station CPU (control CPU) | 0000H <br> 03D0H to 03D3H <br> 03E0H to 03E3H <br> 03FFH | 16-bit unsigned binary | ANY16 |
| (s4) | Specify the operation and clear modes. <br> (1) Operation mode <br> Specifies whether or not remote RUN be forcibly executed. <br> - 1H: Not forcibly executed <br> - 3H: Forcibly executed <br> The forcible execution function forcibly executes remote RUN from another station when the station that executed remote STOP can no longer execute remote RUN. <br> (2) Clear mode <br> Specifies the CPU unit device status resulting after remote RUN is executed. <br> - OH: Not cleared, except local devices <br> - 1H: Cleared, except the latch range <br> - 2 H : Cleared, including the latch range <br> The clear mode setting specifies the clear, or initialization, processing for the CPU module device at the operation start of CPU module calculation by remote RUN. After completion of the specified clear processing, the CPU module executes RUN according to the initial device values of the engineering tool. | $\begin{aligned} & 0001 \mathrm{H} \\ & 0003 \mathrm{H} \\ & 0011 \mathrm{H} \\ & 0013 \mathrm{H} \\ & 0021 \mathrm{H} \\ & 0023 \mathrm{H} \end{aligned}$ | 16-bit unsigned binary | ANY16 |
| (d) | Own station device to be turned on one scan when the instruction completes. <br> If the instruction is completed with an error, (d) +1 is also turned on. | - | Bit | ANYBIT_ARRAY <br> (Number of elements: 2) |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

*1 If the instruction is executed with group or all stations specification:
For the target station CPU type, specify either 0000 H or 03FFH. If the target station is in CC-Link IE Field Network, group specification is not available. To check that the data has been written successfully in the target station, examine whether the CPU module of the destination has entered the RUN state.
*2 If the instruction is executed with the control system CPU (03D0H) or standby system CPU (03D1H) specified, it may complete with an error if system switching occurs in the target station (error code: $4244 \mathrm{H}, 4248 \mathrm{H}$ ).
Retry to execute the instruction if it completed with an error due to the above error.

- Remote RUN is enabled when the RUN/STOP/RESET switch of the target station CPU module is "RUN."
- If the target station CPU module is system protected, remote RUN will fail.
- If the target station CPU module has already been remotely stopped or paused from another station, RUN will fail if ( $s 4$ ) is "Not forcibly executed $(0001 \mathrm{H})$."

Applicable devices

| Operand |  | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロロロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UПIGロ，JロID， U3EDI（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ | J | U |
| （J／U） | $J(P)$ ． <br> RRU <br> N | － | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － |
|  | $\mathrm{G}(\mathrm{P})$ ． <br> RRU <br> N | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － | $\bigcirc$ |
|  | Z（P）． <br> RRU <br> N | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ |
| （s1） |  | $\mathrm{O}^{* 1}$ | － | $\mathrm{O}^{*}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － | － |
| （s2） |  | $O^{* 1}$ | － | $\mathrm{O}^{*}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － | － |
| （s3） |  | $\mathrm{O}^{* 1}$ | － | $\mathrm{O}^{*}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － | － |
| （s4） |  | $\mathrm{O}^{* 1}$ | － | $\mathrm{O}^{*}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － | － |
| （d） |  | $\mathrm{O}^{* 1}$ | － | $\mathrm{O}^{* 3}$ | － | － | － | － | － | － | － | － | － | － |

FX and FY cannot be used．
FD cannot be used．
T，ST，C，and FD cannot be used．

## Processing details

－Remote RUN is executed for the target station CPU module according to the（ $s 4$ ）specification details．The target station is specified with the target network number（ $J$ ）or the start I／O number（ U ）of the own station or node and the target station number（s2）．After completion of request to the target station，the completion device specified by（d）turns on．

－The specifiable stations are those with the specified network number that are included in one of the following networks．
－MELSECNET／H
－MELSECNET／10
－CC－Link IE Controller Network
－CC－Link IE Field Network
－Ethernet
－Execution and normal／error completion of the RRUN instruction can be checked with the completion device specified by the setting data（d）and the completion status indication device（d）＋1．
－Completion device（d）
Turns on during END processing of the scan that arises upon completion of the RRUN instruction，and turns off during the next END processing．
－Completion status indication device（d）+1
Turns on or off depending on the status resulting from completion of the RRUN．
When completed normally：Unchanged from off．
When completed with an error：Turns on during END processing of the scan that arises upon completion of the RRUN instruction ended，and turns off during the next END processing．

- The following figure shows the RRUN instruction execution timing.
- When completed normally

- When completed with an error

- If the J.RRUN, G.RRUN, or Z.RRUN is used to execute processing, processing of one time is successively followed by the next processing while the start-up contact is on.
- If the JP.RRUN, GP.RRUN, or ZP.RRUN is used to execute processing, processing is executed once at the start-up contact off-to-on rise.


## Operation error

| Error code (SW0030 to <br> SW0037) | Description |
| :--- | :--- |
| 4000 H to 4FFFH | L MELSEC iQ-R CPU Module User's Manual (Application) |
| E000H to EFFFH | La MELSEC iQ-R CC-Link IE Controller Network User's Manual (Application) |

### 14.2 Remote STOP

## J(P).RSTOP, G(P).RSTOP, Z(P).RSTOP

These instructions execute remote STOP for the programmable controller on another station.


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| J.RSTOP | - |
| G.RSTOP |  |
| Z.RSTOP |  |
| JP.RSTOP | - |
| GP.RSTOP |  |
| ZP.RSTOP |  |

## Setting data

Description, range, data type

| Operand |  | Description <br> (J): Target station network number | $\begin{array}{\|l\|} \hline \text { Range } \\ \hline 1 \text { to } 239 \\ \hline \end{array}$ | Data type <br> 16-bit unsigned binary | Data type (label) <br> ANY16 |
| :---: | :---: | :---: | :---: | :---: | :---: |
| (J/U) | $\begin{aligned} & \mathrm{J}(\mathrm{P}) \cdot \mathrm{RSTO} \\ & \mathrm{P} \end{aligned}$ |  |  |  |  |
|  | $\begin{aligned} & \text { G(P).RSTO } \\ & \mathrm{P} \end{aligned}$ | (U): Start I/O number (first three digits in four-digit hexadecimal representation) of own station or own node | 00H to FEH | 16-bit unsigned binary | ANY16 |
|  | Z(P).RSTO P | (J): Target station network number | 1 to 239 | String | ANY16_OR_STRIN G_SINGLE |
|  | $\begin{aligned} & \mathrm{Z}(\mathrm{P}) . \mathrm{RSTO} \\ & \mathrm{P} \end{aligned}$ | (U): Start I/O number (first three digits in four-digit hexadecimal representation) of own station or own node | OOH to FEH | String | ANY16_OR_STRIN G_SINGLE |
| (s1) |  | Own station channel ( $\curvearrowleft$ Page 1192 Range of available channel numbers) | 1 to 8, 11 to 18 | 16-bit unsigned binary | ANY16 |
| (s2) |  | Specify the station number of the target station/target node. <br> (1) Station number specification <br> 1 to 120 <br> (2) Group specification ${ }^{* 1}$ <br> 81 H to AOH : All stations of group numbers 1 to 32 <br> (3) All-station specification ${ }^{* 1}$ <br> FFH: All stations of target network numbers, except own station | 1 to 120 <br> 81 H to AOH FFH | 16-bit unsigned binary | ANY16 |


| Operand | Description | Range | Data type | Data type（label） |
| :---: | :---: | :---: | :---: | :---: |
| （s3） | Specify that CPU module in the target station that is to be accessed． <br> －0000H：Addressed to target station CPU（control CPU） <br> －03DOH：Addressed to control system CPU＊2 <br> －03D1H：Addressed to standby system CPU＊2 <br> －03D2H：Addressed to system A CPU <br> －03D3H：Addressed to system B CPU <br> －03EOH：Addressed to multiple CPU No． 1 <br> －03E1H：Addressed to multiple CPU No． 2 <br> －03E2H：Addressed to multiple CPU No． 3 <br> －03E3H：Addressed to multiple CPU No． 4 <br> －03FFH：Addressed to target station CPU（control CPU） | 0000H <br> 03D0H to 03D3H <br> 03EOH to 03E3H <br> 03FFH | 16－bit unsigned binary | ANY16 |
| （s4） | Specifies the operation mode． | 0001H（fixed） | 16－bit unsigned binary | ANY16 |
| （d） | Own station device to be turned on one scan when the instruction completes． <br> If the instruction is completed with an error，（d）+1 is also turned on． | － | Bit | ANYBIT＿ARRAY <br> （Number of elements：2） |
| EN | Execution condition | － | Bit | BOOL |
| ENO | Execution result | － | Bit | BOOL |

＊1 If the instruction is executed with group or all stations specification：
For the target station CPU type，specify either 0000H or 03FFH．If the target station is in CC－Link IE Field Network，group specification is not available．To check that the data has been written successfully in the target station，examine whether the CPU module of the destination has entered the STOP state．
＊2 If the instruction is executed with the control system CPU（03D0H）or standby system CPU（03D1H）specified，it may complete with an error if system switching occurs in the target station（error code： $4244 \mathrm{H}, 4248 \mathrm{H}$ ）．
Retry to execute the instruction if it completed with an error due to the above error．

## Point ${ }^{\rho}$

－Remote STOP is enabled when the RUN／STOP／RESET switch of the target station CPU module is＂RUN．＂
－If the target station CPU module is system protected，remote STOP will fail．
－Resetting the target station CPU on which remote STOP was executed deletes information about the remote STOP．

## Applicable devices

| Operand |  | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UПIGロ，JロIロ， U3ED（H）GD | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ | J | U |
| （ $\mathrm{J} / \mathrm{U}$ ） | $J(P)$ ． RST OP | － | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － |
|  | $G(P)$ ． RST OP | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － | $\bigcirc$ |
|  | Z（P）． <br> RST <br> OP | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ |
| （s1） |  | $\bigcirc{ }^{* 1}$ | － | $\bigcirc^{*}{ }^{2}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － | － |
| （s2） |  | $0^{* 1}$ | － | $\mathrm{O}^{*}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － | － |
| （s3） |  | $\bigcirc{ }^{* 1}$ | － | $\bigcirc^{*}{ }^{2}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － | － |
| （s4） |  | $0^{* 1}$ | － | $\mathrm{O}^{*}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － | － |
| （d） |  | O＊1 | － | $0^{* 3}$ | － | － | － | － | － | － | － | － | － | － |

＊1 FX and FY cannot be used．
＊2 FD cannot be used．
＊3 T，ST，C，and FD cannot be used．

## Processing details

- Remote STOP is executed for the target station CPU module. The target station is specified with the target network number (J) or the start I/O number (U) of the own station or node and the target station number (s2). After completion of request to the target station, the completion device specified by (d) turns on.

- The specifiable stations are those with the specified network number that are included in one of the following networks.
- MELSECNET/H
- MELSECNET/10
- CC-Link IE Controller Network
- CC-Link IE Field Network
- Ethernet
- Execution and normal/error completion of the RSTOP instruction can be checked with the completion device specified by the setting data (d) and the completion status indication device (d)+1.
- Completion device (d)

Turns on during END processing of the scan that arises upon completion of the RSTOP instruction, and turns off during the next END processing.

- Completion status indication device (d) +1

Turns on or off depending on the status resulting from completion of the RSTOP.
When completed normally: Unchanged from off.
When completed with an error: Turns on during END processing of the scan that arises upon completion of the RSTOP instruction ended, and turns off during the next END processing.

- The following figure shows the RSTOP instruction execution timing.
- When completed normally


Depending on the system configuration and sequence scan time, several scans may be required until the sequence scan STOP completes.

- When completed with an error

- If the J.RSTOP, G.RSTOP, or Z.RSTOP is used to execute processing, processing of one time is successively followed by the next processing while the start-up contact is on.
- If the JP.RSTOP, GP.RSTOP, or ZP.RSTOP is used to execute processing, processing is executed once at the start-up contact off-to-on rise.


## Operation error

| Error code (SW0030 to <br> SW0037) | Description |
| :--- | :--- |
| 4000 H to 4FFFH | L MELSEC iQ-R CPU Module User's Manual (Application) |
| E000H to EFFFH | La MELSEC iQ-R CC-Link IE Controller Network User's Manual (Application) |

### 14.3 Reading Clock Data from the Programmable Controller on Another Station

## J(P).RTMRD, G(P).RTMRD, Z(P).RTMRD

These instruction read clock data from the programmable controller on another station.


FBD/LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| J.RTMRD | - |
| G.RTMRD |  |
| Z.RTMRD |  |
| JP.RTMRD | - |
| GP.RTMRD |  |
| ZP.RTMRD |  |

## Setting data

Description, range, data type

| Operand |  | Description | Range | Data type | Data type (label) |
| :---: | :---: | :---: | :---: | :---: | :---: |
| (J/U) | $J(P) \cdot R T M R$ <br> D | (J): Target station network number | 1 to 239 | 16-bit unsigned binary | ANY16 |
|  | G(P).RTMR <br> D | (U): Start I/O number (first three digits in four-digit hexadecimal representation) of own station or own node | 00 H to FEH | 16-bit unsigned binary | ANY16 |
|  | $\begin{aligned} & \text { Z(P).RTMR } \\ & \text { D } \end{aligned}$ | (J): Target station network number | 1 to 239 | String | ANY16_OR_STRIN G_SINGLE |
|  | $\begin{aligned} & \text { Z(P).RTMR } \\ & \text { D } \end{aligned}$ | (U): Start I/O number (first three digits in four-digit hexadecimal representation) of own station or own node | 00 H to FEH | String | ANY16_OR_STRIN G_SINGLE |
| (s1) |  | Own station channel ( $\leftrightarrows$ Page 1192 Range of available channel numbers) | 1 to 8,11 to 18 | 16-bit unsigned binary | ANY16 |
| (s2) |  | Target station number | 1 to 120 | 16-bit unsigned binary | ANY16 |
| (s3) |  | Specify that CPU module in the target station that is to be accessed. <br> - 0000H: Addressed to target station CPU (control CPU) <br> - 03DOH: Addressed to control system CPU*1 <br> - 03D1H: Addressed to standby system CPU*1 <br> - 03D2H: Addressed to system A CPU <br> - 03D3H: Addressed to system B CPU <br> - 03EOH: Addressed to multiple CPU No. 1 <br> - 03E1H: Addressed to multiple CPU No. 2 <br> - 03E2H: Addressed to multiple CPU No. 3 <br> - 03E3H: Addressed to multiple CPU No. 4 <br> - 03FFH: Addressed to target station CPU (control CPU) | 0000H <br> 03D0H to 03D3H <br> 03EOH to 03E3H <br> 03FFH | 16-bit unsigned binary | ANY16 |


| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （d1） | Own station start device for storing the read clock data． | - | Device name | ANY16＿ARRAY <br> （Number of <br> elements： 4$)$ |
| （d2） | Own station device to be turned on one scan when the <br> instruction completes． <br> （d2）＋1 also turns on when the instruction completes with an <br> error． | - | ANYBIT＿ARRAY <br> （Number of <br> elements：2） |  |
| EN | Execution condition | Bit | BOOL |  |
| ENO | Execution result | - | Bit | BOOL |

＊1 If the instruction is executed with the control system CPU（03D0H）or standby system CPU（03D1H）specified，it may complete with an error if system switching occurs in the target station（error code： $4244 \mathrm{H}, 4248 \mathrm{H}$ ）．
Retry to execute the instruction if it completed with an error due to the above error．

## －Applicable devices

| Operand |  | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロID， U3E $\square 1(\mathrm{H}) \mathrm{G} \square$ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ | J | U |
| （J／U） | $\begin{aligned} & \hline J(P) . \\ & R T M \\ & R D \end{aligned}$ | － | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － |
|  | $\begin{aligned} & \mathrm{G}(\mathrm{P}) . \\ & \mathrm{RTM} \\ & \text { RD } \end{aligned}$ | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － | $\bigcirc$ |
|  | $\begin{aligned} & \mathrm{Z}(\mathrm{P}) . \\ & \mathrm{RTM} \\ & \mathrm{RD} \end{aligned}$ | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ |
| （s1） |  | $\mathrm{O}^{* 1}$ | － | $\mathrm{O}^{*}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － | － |
| （s2） |  | $O^{* 1}$ | － | $\mathrm{O}^{*}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － | － |
| (s3) |  | $\mathrm{O}^{* 1}$ | － | $\mathrm{O}^{*}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － | － |
| （d1） |  | － | － | $\mathrm{O}^{*}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － | － |
| （d2） |  | O＊1 | － | $\mathrm{O}^{* 3}$ | － | － | － | － | － | － | － | － | － | － |

＊1 FX and FY cannot be used．
＊2 FD cannot be used．
＊3 T，ST，C，and FD cannot be used．

## Clock data

| Operand：（d1） |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| ＋0 | Clock data | Upper 8 bits：Year（ 00 H to 99 H ：Upper two digits of the year） Lower 8 bits：Month（ 01 H to 12 H ） <br> The range of the 4 digits of the year is 1980 to 2079. | － | System |
| ＋1 |  | Upper 8 bits：Day（ 01 H to 31 H ） Lower 8 bits：Hour（ 00 H to 23 H ） | － | System |
| ＋2 |  | Upper 8 bits：Minute（ 00 H to 59 H ） Lower 8 bits：Second（ 00 H to 59 H ） | － | System |
| ＋3 |  | Upper 8 bits：Year（19H and 20H：Upper two digits of the year） Lower 8 bits：Day of the week（ 00 H （Sun．）to 06H（Sat．）） The range of the 4 digits of the year is 1980 to 2079. | － | System |

## Processing details

- Clock data is read from the target station CPU module. The target station is specified with the target network number ( J ) or the start I/O number (U) of the own station or node and the target station number (s2). After completion of request to the target station, the completion device specified by (d2) turns on.

- The specifiable stations are those with the specified network number that are included in one of the following networks.
- MELSECNET/H
- MELSECNET/10
- CC-Link IE Controller Network
- CC-Link IE Field Network
- Ethernet
- Execution and normal/error completion of the RTMRD instruction can be checked with the completion device specified by the setting data (d2) and the completion status indication device (d2)+1.
- Completion device (d2)

Turns on during END processing of the scan that arises upon completion of the RTMRD instruction, and turns off during the next END processing.

- Completion status indication device (d2)+1

Turns on or off depending on the status resulting from completion of the RTMRD.
When completed normally: Unchanged from off.
When completed with an error: Turns on during END processing of the scan that arises upon completion of the RTMRD instruction ended, and turns off during the next END processing.

- The following figure shows the RTMRD instruction execution timing.
- When completed normally

- When completed with an error


## CPU module on the own station



- If the J.RTMRD, G.RTMRD, or Z.RTMRD is used to execute processing, processing of one time is successively followed by the next processing while the start-up contact is on.
- If the JP.RTMRD, GP.RTMRD, or ZP.RTMRD is used to execute processing, processing is executed once at the start-up contact off-to-on rise.


## Operation error

| Error code (SW0030 to <br> SW0037) | Description |
| :--- | :--- |
| 4000 H to 4FFFH | L MELSEC iQ-R CPU Module User's Manual (Application) |
| E000H to EFFFH | La MELSEC iQ-R CC-Link IE Controller Network User's Manual (Application) |

### 14.4 Writing Clock Data to the Programmable Controller on Another Station

## J(P).RTMWR, G(P).RTMWR, Z(P).RTMWR

These instruction write clock data to the programmable controller on another station.


FBD/LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| J.RTMWR |  |
| G.RTMWR |  |
| Z.RTMWR |  |
| JP.RTMWR |  |
| GP.RTMWR |  |
| ZP.RTMWR |  |

## Setting data

## Description, range, data type

| Operand |  | Description | Range | Data type | Data type (label) |
| :---: | :---: | :---: | :---: | :---: | :---: |
| (J/U) | J(P).RTMW R | (J): Target station network number | 1 to 239 | 16-bit unsigned binary | ANY16 |
|  | $\begin{aligned} & \text { G(P).RTMW } \\ & \text { R } \end{aligned}$ | (U): Start I/O number (first three digits in four-digit hexadecimal representation) of own station or own node | OOH to FEH | 16-bit unsigned binary | ANY16 |
|  | $\begin{aligned} & \mathrm{Z}(\mathrm{P}) \cdot \mathrm{RTMW} \\ & \mathrm{R} \end{aligned}$ | (J): Target station network number | 1 to 239 | String | ANY16_OR_STRIN G_SINGLE |
|  | $\begin{aligned} & \mathrm{Z}(\mathrm{P}) \cdot \mathrm{RTMW} \\ & \mathrm{R} \end{aligned}$ | (U): Start I/O number (first three digits in four-digit hexadecimal representation) of own station or own node | OOH to FEH | String | ANY16_OR_STRIN G_SINGLE |
| (s1) |  | Own station channel ( channel numbers) | 1 to 8,11 to 18 | 16-bit unsigned binary | ANY16 |
| (s2) |  | Specify the station number of the target station/target node. <br> (1) Station number specification <br> 1 to 120 <br> (2) Group specification ${ }^{* 1}$ <br> 81 H to AOH : All stations of group numbers 1 to 32 <br> (3) All-station specification ${ }^{* 1}$ <br> FFH: All stations of target network numbers, except own station | 1 to 120 <br> 81 H to AOH FFH | 16-bit unsigned binary | ANY16 |


| Operand | Description | Range | Data type | Data type（label） |
| :---: | :---: | :---: | :---: | :---: |
| （s3） | Specify that CPU module in the target station that is to be accessed． <br> －0000H：Addressed to target station CPU（control CPU） <br> －03D0H：Addressed to control system CPU＊2 <br> －03D1H：Addressed to standby system CPU＊2 <br> －03D2H：Addressed to system A CPU <br> －03D3H：Addressed to system B CPU <br> －03EOH：Addressed to multiple CPU No． 1 <br> －03E1H：Addressed to multiple CPU No． 2 <br> －03E2H：Addressed to multiple CPU No． 3 <br> －03E3H：Addressed to multiple CPU No． 4 <br> －03FFH：Addressed to target station CPU（control CPU） | $0000 \mathrm{H}$ <br> 03DOH to 03D3H <br> 03E0H to 03E3H <br> 03FFH | 16－bit unsigned binary | ANY16 |
| （s4） | Own station start device containing the clock data to be written | － | Device name | ANY16＿ARRAY <br> （Number of elements：5） |
| （d） | Own station device to be turned on one scan when the instruction completes． <br> If the instruction is completed with an error，（d）＋1 is also turned on． | － | Bit | ANYBIT＿ARRAY <br> （Number of elements：2） |
| EN | Execution condition | － | Bit | BOOL |
| ENO | Execution result | － | Bit | BOOL |

＊1 If the instruction is executed with group or all stations specification：
For the target station CPU type，specify either 0000 H or $03 F F$ H．If the target station is in CC－Link IE Field Network，group specification is not available．To check that the data has been written successfully in the target station，examine whether the CPU module of the destination has entered the RUN state．
＊2 If the instruction is executed with the control system CPU（03D0H）or standby system CPU（03D1H）specified，it may complete with an error if system switching occurs in the target station（error code： $4244 \mathrm{H}, 4248 \mathrm{H}$ ）．
Retry to execute the instruction if it completed with an error due to the above error．

## Applicable devices

| Operand |  | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロロロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGㅁ，JロID， U3ED ${ }^{(H) G}$（ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ | J | U |
| （J／U） | $\begin{aligned} & \mathrm{J}(\mathrm{P}) . \\ & \mathrm{RTM} \\ & \mathrm{WR} \end{aligned}$ | － | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | － |
|  | $\begin{aligned} & \mathrm{G}(\mathrm{P}) . \\ & \mathrm{RTM} \\ & \mathrm{WR} \end{aligned}$ | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － | $\bigcirc$ |
|  | Z（P）． <br> RTM <br> WR | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ |
| （s1） |  | $\bigcirc{ }^{* 1}$ | － | ${ }^{*}{ }^{2}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － | － |
| （s2） |  | $\bigcirc^{* 1}$ | － | $O^{* 2}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － | － |
| （s3） |  | $\mathrm{O}^{* 1}$ | － | $0^{* 2}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － | － |
| （s4） |  | － | － | $0^{* 2}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － | － |
| （d） |  | $\mathrm{O}^{* 1}$ | － | $0^{* 3}$ | － | － | － | － | － | － | － | － | － | － |

＊1 FX and FY cannot be used．
＊2 FD cannot be used．
＊3 T，ST，C，and FD cannot be used．

Clock data

| Operand: (s4) |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description |  | Setting range | Set by |
| +0 | Change pattern | Specifies the bit pattern that indic <br> - 0: Not changed <br> - 1: Changed | ates the item of the clock data to be changed. | 0, 1 | User |
| +1 | Clock data | Upper 8 bits: Year (00H to 99H: Upper two digits of the year) <br> Lower 8 bits: Month $(01 \mathrm{H}$ to 12 H$)$ <br> The range of the 4 digits of the year is 1980 to 2079. |  | Year: 00 H to 99 H <br> Month: 01H to 12H | User |
| +2 |  | Upper 8 bits: Day (01H to 31H) Lower 8 bits: Hour (00H to 23H) |  | Day: 01H to 31 H Hour: 00 H to 23 H | User |
| +3 |  | Upper 8 bits: Minute ( 00 H to 59 H ) <br> Lower 8 bits: Second (00H to 59H) |  | Minute: 00 H to 59H <br> Second: 00H to 59 H | User |
| +4 |  | Upper 8 bits: Year (19H and 20H: Upper two digits of the year) Lower 8 bits: Day of the week (00H (Sun.) to 06H (Sat.)) The range of the 4 digits of the year is 1980 to 2079. |  | Year: 19 H to 20 H <br> Day of week: 00H <br> to 06H | User |

## Point ${ }^{\rho}$

If the target station CPU module is system protected, writing the clock data will fail.

## Processing details

- Clock data is written from the target station CPU module. The target station is specified with the target network number (J) or the start I/O number (U) of the own station or node and the target station number (s2). After completion of request to the target station, the completion device specified by (d) turns on.

- The specifiable stations are those with the specified network number that are included in one of the following networks.
- MELSECNET/H
- MELSECNET/10
- CC-Link IE Controller Network
- CC-Link IE Field Network
- Ethernet
- Execution and normal/error completion of the RTMWR instruction can be checked with the completion device specified by the setting data (d) and the completion status indication device (d) +1 .
- Completion device (d)

Turns on during END processing of the scan that arises upon completion of the RTMWR instruction, and turns off during the next END processing.

- Completion status indication device (d) +1

Turns on or off depending on the status resulting from completion of the RTMWR.
When completed normally: Unchanged from off.
When completed with an error: Turns on during END processing of the scan that arises upon completion of the RTMWR instruction ended, and turns off during the next END processing.

- The following figure shows the RTMWR instruction execution timing.
- When completed normally

- When completed with an error

- If the J.RTMWR, G.RTMWR, or Z.RTMWR is used to execute processing, processing of one time is successively followed by the next processing while the start-up contact is on.
- If the JP.J.RTMWR, GP.J.RTMWR, or ZP.J.RTMWR is used to execute processing, processing is executed once at the start-up contact off-to-on rise.


## Operation error

| Error code (SW0030 to <br> SW0037) | Description |
| :--- | :--- |
| 4000 H to 4FFFH | L MELSEC iQ-R CPU Module User's Manual (Application) |
| E000H to EFFFH | La MELSEC iQ-R CC-Link IE Controller Network User's Manual (Application) |

## 14．5 Setting the Station Number to Own Station

## G（P）．UINI，Z（P）．UINI

These instructions set the station number for the normal，or own，station whose station number has not yet been set．


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| G．UINI | - |
| Z．UINI | - |
| GP．UINI | - |
| ZP．UINI |  |

Setting data
■Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- | :--- |
| （U） | G（P）．UINI | Start I／O number（first three digits in four－digit <br> hexadecimal representation）of own station or <br> own node | 00 H to FEH | 16－bit unsigned binary | ANY16 |
|  | Z（P）．UINI | Start I／O number（first three digits in four－digit <br> hexadecimal representation）of own station or <br> own node | 00 H to FEH | String | ANY16＿OR＿STRING＿ <br> SINGLE |
| （s） | Own station start device where control data is <br> stored | Refer to the control data． | Device name | ANY16 |  |
| （d） | Own station device to be turned on one scan <br> when the instruction completes． <br> If the instruction is completed with an error，（d）+1 <br> is also turned on． | - | Bit | ANYBIT＿ARRAY <br> （Number of elements： <br> 2） |  |
| EN | Execution condition | - | Bit | BOOL |  |
| ENO | Execution result | - | Bit | BOOL |  |

## Applicable devices

| Operand |  | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UПIGロ，JロIロ， U3ED（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （U） | $\begin{aligned} & \mathrm{G}(\mathrm{P}) . \\ & \text { UINI } \end{aligned}$ | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ |
|  | $\begin{aligned} & \mathrm{Z}(\mathrm{P}) . \\ & \mathrm{UINI} \end{aligned}$ | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ |
| （s） |  | － | － | $0^{*}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） |  | O＊1 | － | ${ }^{*} 3$ | － | － | － | － | － | － | － | － | － |

＊1 FX and FY cannot be used．
＊2 FD cannot be used．
＊3 T，ST，C，and FD cannot be used．

Control data

| Operand: (s) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +0 | Not used | - | - | - |
| +1 | Completion status | The instruction completion status is stored. <br> - 0: Normal <br> - Other than 0: Error (error code) | - | System |
| +2 | Change target specification | 0001H (fixed) | 0001H | User |
| +3 | Station number of own station | Specifies the station number to be set. | 1 to 120 | User |
| +4 to +9 | Not used | - | - | - |

## Point $\rho$

- The UINI instruction can be executed only once.
- If the UINI instruction is issued after the station number has been determined, it will complete with an error.
- If the UINI instruction completes with an error before the station number is determined, take corrective action to correct the error content before retrying to execute the instruction.


## Processing details

- The station number of the normal, or own, station specified by $(U)$ is set. After station number setting, the completion device specified by (d) turns on.

- Execution and normal/error completion of the UINI instruction can be checked with the completion device specified by the setting data (d) and the completion status indication device (d) +1 .
- Completion device (d)

The completion device turns on in END processing of the scan performed upon completion of the UINI instruction and turns off in the next END processing.

- Completion status indication device (d)+1

The completion device turns on or off depending on the completion status of the UINI instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on in END processing of the scan performed upon completion of the UINI instruction and turns off in the next END processing.

- The following figure shows the UINI instruction execution timing.



## Operation error

| Error code <br> $((\mathbf{S})+1)$ | Description |
| :--- | :--- |
| E 000 H to EFFFH | LD MELSEC iQ-R CC-Link IE Controller Network User's Manual (Application) |

## 15 CC-LINK IE FIELD NETWORK INSTRUCTIONS

### 15.1 Reading Data from the Intelligent Device Station/ Remote Device Station

## JP.REMFR, ZP.REMFR

These instructions read data from the buffer memory area of the intelligent or remote device station (in units of words).


## FBD/LD



## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| JP.REMFR | - |
| ZP.REMFR | - |

Setting data
■Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :---: | :---: | :---: | :---: | :---: | :---: |
| （J） | JP．REMFR | Target network number | 1 to 239 | 16－bit unsigned binary | ANY16 |
|  | ZP．REMFR | Target network number | 1 to 239 | String | ANY16＿OR＿STRING ＿SINGLE |
| （s1） |  | Number of own station channel used for remote instruction ${ }^{* 1}$ | 1 to 32 | 16－bit unsigned binary | ANY16 |
| （s2） |  | Target station number | 1 to 120 | 16－bit unsigned binary | ANY16 |
| （s3） |  | Start I／O number of target station | －For head module： 00 H to $\mathrm{FEH}^{* 2}$ <br> －For other modules： <br> 00 H （fixed） | 16－bit unsigned binary | ANY16 |
| （s4） |  | Start address of the buffer memory area of the intelligent or remote device station containing the data to be read | 0000H to FFFFH（0 to 65535） | 16－bit unsigned binary | ANY16 |
| （d1） |  | Own station start device for storing the read data | － | Device name | ANY16 |
| （ n ） |  | Number of read data points（in units of words） | 1 to 240 | 16－bit unsigned binary | ANY16 |
| （d2） |  | Own station device to be turned on one scan when the instruction completes． <br> （d2）＋1 also turns on when the instruction completes with an error． | － | Bit | ANYBIT＿ARRAY <br> （Number of elements： 2） |
| EN |  | Execution condition | － | Bit | BOOL |
| ENO |  | Execution result | － | Bit | BOOL |

＊1 Used only for the REMFR or REMTO instruction．
The same channel number as the channel used for the own station of a link dedicated instruction such as READ is available simultaneously．
＊2 This is the start I／O number（first three digits in four－digit hexadecimal representation）of the intelligent function module．

## ■Applicable devices

| Operand |  | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （J） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J미， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （J） | JP．RE MFR | － | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ |
|  | $\begin{aligned} & \text { ZP.R } \\ & \text { EMF } \\ & \text { R } \end{aligned}$ | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | $\bigcirc$ |
| （s1） |  | O＊1 | － | $0^{*}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） |  | ${ }^{* 1}$ | － | $0^{*}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s3） |  | $\bigcirc^{* 1}$ | － | $\bigcirc^{* 2}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s4） |  | $\mathrm{O}^{* 1}$ | － | $0^{*}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d1） |  | － | － | ${ }^{*}{ }^{2}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） |  | O＊1 | － | $\bigcirc^{* 2}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| (d2) |  | $\bigcirc{ }^{* 1}$ | － | $\mathrm{O}^{* 3}$ | － | － | － | － | － | － | － | － | － |

＊1 FX and FY cannot be used．
＊2 FD cannot be used
＊3 T，ST，C，and FD cannot be used．

## Processing details

- From the start address ( s 4 ) of the buffer memory area of the intelligent or remote device station, the data for the specified number of words ( n ) is read to the own station word device ((d1) and later). To specify the target station, use (J), (s2), and $(\mathrm{s} 3)$ of the setting data. Once all the data has been read, the completion device (d2) turns on.

- Execution and normal/error completion of the REMFR instruction can be checked with the completion device specified by the setting data (d2) and the completion status indication device (d2)+1.
- Completion device (d2)

Turns on during END processing of the scan that arises upon completion of the REMFR instruction, and turns off during the next END processing.

- Completion status indication device (d2)+1

Turns on or off depending on the status resulting from completion of the REMFR instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on during END processing of the scan that arises upon completion of the REMFR instruction ended, and turns off during the next END processing.

- The following figures show how the REMFR instruction operates when its execution completes. - When completed normally

- When completed with an error



## Point?

With the REMFR, the number of resends and the response timer limit setting can be set with the following link special registers (SWs).

- Number of resends for REMFR/REMTO instruction (SW001A)
- REMFR/REMTO instruction response timer limit setting (SW001B)

If the number of resends for REMFR/REMTO instruction (SW001A) has been set, the time up to occurrence of the REMFR instruction error is as follows:
(Number of resends for REMFR/REMTO instruction +1 ) $\times$ REMFR/REMTO instruction response timer limit setting
No retry will, however, be executed if the target station is disconnected. If the REMFR instruction completes with an error, retry to execute this instruction after return of the target station.

## Operation error

| Error code (SW0080 to <br> SW009F) | Description |
| :--- | :--- |
| 4000 H to 4FFFH | $\square \square$ MELSEC iQ-R CPU Module User's Manual (Application) |
| D000H to DFFFH | L MELSEC iQ-R CC-Link IE Field Network User's Manual (Application) |

### 15.2 Writing Data to the Intelligent Device Station/ Remote Device Station

## JP.REMTO, ZP.REMTO

These instructions write data to the buffer memory area of the intelligent or remote device station (in units of words).


FBD/LD


## EExecution condition

| Instruction | Execution condition |
| :--- | :--- |
| JP.REMTO | ZP.REMTO |

Setting data
$\square$ Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :---: | :---: | :---: | :---: | :---: | :---: |
| （J） | JP．REMTO | Target network number | 1 to 239 | 16－bit unsigned binary | ANY16 |
|  | ZP．REMTO | Target network number | 1 to 239 | String | ANY16＿OR＿STRING ＿SINGLE |
| （s1） |  | Number of own station channel used for remote instruction ${ }^{* 1}$ | 1 to 32 | 16－bit unsigned binary | ANY16 |
| （s2） |  | Target station number | 1 to 120 | 16－bit unsigned binary | ANY16 |
| （s3） |  | Start I／O number of target station | For head module： 00 H to $\mathrm{FEH}^{* 2}$ <br> －For other modules： 00H（fixed） | 16－bit unsigned binary | ANY16 |
| （s4） |  | Start address of the intelligent or remote device station buffer memory area to which the data is written． | 0000H to FFFFH（0 to 65535） | 16－bit unsigned binary | ANY16 |
| （s5） |  | Own station start device for storing the written data | － | Device name | ANY16 |
| （ n ） |  | Number of written data points（in units of words） | 1 to 240 | 16－bit unsigned binary | ANY16 |
| （d） |  | Own station device to be turned on one scan when the instruction completes． <br> If the instruction is completed with an error，（d）＋1 is also turned on． | － | Bit | ANYBIT＿ARRAY <br> （Number of elements： <br> 2） |
| EN |  | Execution condition | － | Bit | BOOL |
| ENO |  | Execution result | － | Bit | BOOL |

＊1 Used only for the REMFR or REMTO instruction．
The same channel number as the channel used for the own station of a link dedicated instruction such as READ is available simultaneously．
＊2 This is the start I／O number（first three digits in four－digit hexadecimal representation）of the intelligent function module．

## ■Applicable devices

| Operand |  | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （J） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, } \\ & \text { FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDl（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （J） | JP．REMTO <br> ZP．REMTO | － | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ |
|  |  | － | － | － | － | － | － | － | － | － | － | $\bigcirc$ | $\bigcirc$ |
| （s1） |  | O＊1 | － | $0^{* 2}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） |  | $\bigcirc{ }^{* 1}$ | － | ${ }^{*}{ }^{2}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s3） |  | $\bigcirc{ }^{* 1}$ | － | $\mathrm{O}^{*}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s4） |  | O＊1 | － | $0^{*}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s5） |  | － | － | $0^{*}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） |  | O＊1 | － | $\mathrm{O}^{*}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） |  | $\bigcirc{ }^{* 1}$ | － | $\bigcirc^{* 3}$ | － | － | － | － | － | － | － | － | － |

＊1 FX and FY cannot be used．
＊2 FD cannot be used．
＊3 T，ST，C，and FD cannot be used．

## Processing details

- From the start address (s5) of the own station, the data for the specified number of words ( n ) is written to the buffer memory area of the intelligent or remote device station ((s4) and later). To specify the target station, use (J), (s2), and (s3) of the setting data. Once all the data has been written, the completion device (d) turns on.

- Execution and normal/error completion of the REMTO instruction can be checked with the completion device specified by the setting data (d) and the completion status indication device (d) +1 .
- Completion device (d)

Turns on during END processing of the scan that arises upon completion of the REMTO instruction, and turns off during the next END processing.

- Completion status indication device (d)+1

Turns on or off depending on the status resulting from completion of the REMTO instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on during END processing of the scan that arises upon completion of the REMTO instruction ended, and turns off during the next END processing.

- The following figures show how the REMTO instruction operates when its execution completes. - When completed normally

- When completed with an error


With the REMTO, the number of resends and the response timer limit setting can be set with the following link special registers (SWs).

- Number of resends for REMFR/REMTO instruction (SW001A)
- REMFR/REMTO instruction response timer limit setting (SW001B)

If the number of resends for REMFR/REMTO instruction (SW001A) has been set, the time up to occurrence of the REMTO instruction error is as follows:
(Number of resends for REMFR/REMTO instruction +1 ) $\times$ REMFR/REMTO instruction response timer limit setting
However, if the target station has been disconnected at the time of REMTO instruction execution, no retry is executed. If the REMTO instruction is completed with an error, re-execute the RIWT instruction after the target station returns

## Operation error

| Error code (SW0080 to <br> SW009F) | Description |
| :--- | :--- |
| 4000 H to 4FFFH | L $\quad$ MELSEC iQ-R CPU Module User's Manual (Application) |
| D000H to DFFFH | D $\triangle$ MELSEC iQ-R CC-Link IE Field Network User's Manual (Application) |

### 15.3 Setting Parameters

## G(P).CCPASET

These instructions set the parameters in the CC-Link IE Field Network master and local modules.


## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| G.CCPASET | - |
|  | - |
| GP.CCPASET | - |

Setting data

## ■Description, range, data type

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (U) | Start I/O number (first three digits in four-digit hexadecimal <br> representation) of own station or own node | 00 H to FEH | 16-bit unsigned binary | ANY16 |
| (s1) | Own station start device where control data is stored | Refer to the control data. | Device name | ANY16 |
| (s2) | Own station start device containing the network <br> configuration settings | - | Device name | ANY16 |
| (s3) | Own station start device containing the reserved station <br> specification | - | Device name | ANY16 |
| (s4) | Own station start device containing the error invalid station <br> setting | - | Device name | ANY16 |
| (d) | Own station device to be turned on one scan when the <br> instruction completes. <br> If the instruction is completed with an error, (d) +1 is also <br> turned on. | - | Bit | ANYBIT_ARRAY <br> (Number of elements: <br> $2)$ |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | BOOL |  |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3EDI（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （U） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ |
| （s1） | － | － | ${ }^{*}{ }^{2}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | ${ }^{*}{ }^{2}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s3） | － | － | ${ }^{*}{ }^{2}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s4） | － | － | ${ }^{*}{ }^{2}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | $\bigcirc^{* 1}$ | － | $\bigcirc^{* 3}$ | － | － | － | － | － | － | － | － | － |

＊1 FX and FY cannot be used．
＊2 FD cannot be used．
＊3 T，ST，C，and FD cannot be used．

## Control data

| Operand：（s1） |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description |  |  |  | Setting range＊1 | Set by |
| ＋0 | Completion status | The instruction completion status is stored． <br> －0：Normal <br> －Other than 0：Error（error code） |  |  |  | － | System |
| ＋1 | Setting flag | （1）Presence of network configuration setting data（bit 0） <br> －0：Invalid ${ }^{*}$ <br> －1：Valid <br> （2）Presence of reserved station setting data（bit 1） <br> － 0 ：Invalid ${ }^{*}$ 2 <br> －1：Valid <br> （3）Presence of error invalid station setting data（bit 2） <br> －0：Invalid ${ }^{*}$ <br> －1：Valid <br> （4）Presence of submaster function（bit 3） <br> －0：Invalid ${ }^{*}$ <br> －1：Valid <br> （5）Presence of IP packet transfer function（bits 4，5） <br> －00：Invalid <br> －01：Valid <br> （6）Data link faulty station setting（bit 8） <br> －0：Cleared <br> －1：Held <br> （7）CPU STOP time output setting（bit 9） <br> －0：Held <br> －1：Cleared <br> （8）Link scan mode（bit 10） <br> －0：Sequence scan unsynchronized <br> －1：Sequence scan synchronized <br> （9）Network topology setting（bit 11） <br> －0：Line topology，star topology，or mixed topology of line and star <br> －1：Ring topology <br> （10）CPU error time output setting（bit 12） <br> －0：Cleared <br> －1：Held <br> （11）Master station return time operation setting（bit 13） <br> Enabled only when the station type is master． <br> － 0 ：Returning as the master operating station <br> －1：Returning as the submaster operating station <br> （12）Submaster setting（bit 14） <br> Enabled only when the station type is submaster． <br> － 0 ：Operating with the parameters of the master station <br> －1：Operating with the parameters of the own，or submaster，station |  |  |  | Left | User |
| ＋2 | Total number of slave stations | Sets the total number of slave stations connected． |  |  |  | 1 to $121^{* 3}$ | User |


| Operand: (s1) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range ${ }^{* 1}$ | Set by |
| +3 | Constant link scan time | Sets the constant link scan time. <br> - 0: No operator <br> - 1 to 200: Constant link scan time | 0 to 200 | User |
| $\begin{aligned} & +4 \\ & +5 \end{aligned}$ | IP address | Sets the IPv4 network address (i.e. IP address) when the IP packet transfer function is used. | 00000001H to FFFFFFFFH | User |

*1 If a value outside the setting range is set, the instruction will complete with an error.
*2 If "Invalid" is set, the default parameter will be used.
*3 Set value 121 is available only if the submaster function is used.

## Point ${ }^{\text {P }}$

For the startup in the local station, only Data link faulty station setting (bit 8 of ( s 1 ) +1 ) and CPU STOP time output setting (bit 9 of ( s 1 ) +1 ) will be valid. Any other changed settings will be ignored during execution of the instruction, without causing an error.
-Network configuration setting data (for master station setting only)

| Operand: (s2) |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Item |  | Description |  |  |  |  | Setting range ${ }^{* 1}$ | Set by |
| +0 | For 1st module | Slave station setting information | Sets the station type, the number of occupied stations, and the station number. <br> (1) Station number <br> - 1 to 120: Station number <br> - 0: Master station <br> (2) Number of occupied stations <br> - 1: Fixed <br> (3) Station type <br> - 0: Remote I/O station <br> - 1: Remote device station <br> - 2: Intelligent device station <br> - 3: Local station (master-slave system) <br> - 4: Submaster station <br> - F: Master station |  |  |  |  | Left | User |
| +1 |  | RX/RY offset | Sets the offset value from the head of RX/RY (in units of 16 points).* ${ }^{*}$ |  |  |  |  | 0 to 16368 | User |
| +2 |  | Number of RX/ <br> RY points | Sets the number of RX/RY, LB points. ${ }^{*}$ <br> - If the station type is master or local: 0 to 2048 points <br> - If the station type is intelligent device: 0 to 2048 points (in units of 16 points) <br> - If the station type is remote I/O: 0 to 64 points (in units of 16 points) <br> - If the station type is remote device: 0 to 128 points (in units of 16 points) |  |  |  |  | $\begin{aligned} & 0 \text { to } 2048 \\ & 0 \text { to } 64 \\ & 0 \text { to } 128 \end{aligned}$ | User |
| +3 |  | RWr/RWw offset | Sets the offset value from the start of RWr/RWw (in units of 4 points). ${ }^{*}$ |  |  |  |  | 0 to 8188 | User |
| +4 |  | Number of RWr/ RWw points | Sets the number of $\mathrm{RWr} / \mathrm{RW} w$, LW points. ${ }^{*}{ }^{2}$ <br> - If the station type is master or local: 0 to 256 points (in units of 16 points) <br> - If the station type is intelligent device: 0 to 1024 points (in units of 4 points) <br> - If the station type is remote device: 0 to 64 points (in units of 4 points) |  |  |  |  | $\begin{aligned} & 0 \text { to } 256 \\ & 0 \text { to } 1024 \\ & 0 \text { to } 64 \end{aligned}$ | User |
| +5 to +594 | : |  |  |  |  |  |  |  | User |


| Operand: (s2) |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Item |  | Description | Setting range ${ }^{* 1}$ | Set by |
| +595 | For 120th module | Slave station <br> setting information | Sets the station type. | Same as (s2)+0 | User |
| +596 |  | RX/RY offset | Sets the offset value from the head of RX/RY (in units of 16 points). ${ }^{*}$ | 0 to 16368 | User |
| +597 |  | Number of RX/ RY points | Sets the number of RX/RY points. ${ }^{*}{ }^{2}$ <br> - If the station type is master or local: 0 to 2048 points <br> - If the station type is intelligent device: 0 to 2048 points (in units of 16 points) <br> - If the station type is remote I/O: 0 to 64 points (in units of 16 points) <br> - If the station type is remote device: 0 to 128 points (in units of 16 points) | $\begin{aligned} & 0 \text { to } 2048 \\ & 0 \text { to } 64 \\ & 0 \text { to } 128 \end{aligned}$ | User |
| +598 |  | RWr/RWw offset | Sets the offset value from the start of RWr/RWw (in units of 4 points).*2 | 0 to 8188 | User |
| +599 |  | Number of RWr/ RWw points | Sets the number of RWr/RWw points. ${ }^{*}{ }^{2}$ <br> - If the station type is master or local: 0 to 256 points (in units of 16 points) <br> - If the station type is intelligent device: 0 to 1024 points (in units of 4 points) <br> - If the station type is remote device: 0 to 64 points (in units of 4 points) | 0 to 256 <br> 0 to 1024 <br> 0 to 64 | User |
| $\begin{aligned} & +600 \text { to } \\ & +604 \end{aligned}$ | For 121st module | Same as above |  |  | User |

*1 If a value outside the setting range is set for the appropriate station information setting, the instruction will complete with an error.
*2 The specified number of points is regarded as the number of RX/RY or RWr/RWw points.
Repeat this setting for the total number of slave stations that was set in the control data.
Reserved station setting data (for master station setting only)

*1 Make the setting for the station number that was set with the appropriate setting information setting data.

## Point $\rho$

If the specified total number of slave stations does not match the individual station setting data, the total number of individual stations specified in the total number of slave stations take precedence. Individual station information which is specified exceeding the total number of slave stations is ignored. Note that 1 is added to the total number of slave stations when the submaster function enabled/disabled ((s1)+1 bit 3 ) is enabled (1).

Error invalid station setting data (for master station setting only)

*1 Make the setting for the station number that was set with the appropriate setting information setting data.
If both the error invalid and reserved stations are specified for the same station, the reserved station setting will take priority.

Any station numbers outside the range specified by the number of slave stations or other than those specified in the individual station information settings are ignored even if they are specified as reserved stations or temporary error invalid stations.

## Processing details

- This instruction sets the parameters in the CC-Link IE Field Network master and local modules.
[Own station]

- Execution and normal/error completion of the $G(P)$.CCPASET instruction can be checked with the completion device specified by the setting data (d) and the completion status indication device (d)+1.
- Completion device (d)

Turns on during END processing of the scan that arises upon completion of the G(P).CCPASET instruction, and turns off during the next END processing

- Completion status indication device (d) +1

Turns on or off depending on the status resulting from completion of the G(P).CCPASET instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on during END processing of the scan that arises upon completion of the G(P).CCPASET instruction, and turns off during the next END processing.

- The following figure shows how the $G(P)$.CCPASET instruction operates when completing its execution.
- When completed normally

CPU module on the own station


- When completed with an error



## Operation error

| Error code <br> $((\mathbf{s} 1)+\mathbf{0})$ | Description |
| :--- | :--- |
| D000H to DFFFH | La MELSEC iQ-R CC-Link IE Field Network User's Manual (Application) |

## 15．4 Setting the Station Number to Own Station

## G（P）．UINI，Z（P）．UINI

These instructions set the station number for the local，or own，station whose station number has not yet been set．


## ■xecution condition

| Instruction | Execution condition |
| :--- | :--- |
| G．UINI | - |
| Z．UINI | - |
| GP．UINI | - |
| ZP．UINI |  |

Setting data
■Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- | :--- |
| （U） | G（P）．UINI | $\begin{array}{l}\text { Start I／O number（first three digits in four－digit } \\ \text { hexadecimal representation）of own station or own } \\ \text { node }\end{array}$ | 00 H to FEH | 16－bit unsigned binary | ANY16 |
|  | Z（P）．UINI | $\begin{array}{l}\text { Start I／O number（first three digits in four－digit } \\ \text { hexadecimal representation）of own station or own } \\ \text { node }\end{array}$ | 00 H to FEH | String | ANY16＿OR＿STRING |
| ＿SINGLE |  |  |  |  |  |$]$| ANY16 |
| :--- |
| （s） |
| （d） |

## ■Applicable devices

| Operand |  | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3E미（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （U） | $G(P)$ ． <br> UINI | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ |
|  | $\mathrm{Z}(\mathrm{P})$ <br> UINI | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ |
| （s） |  | － | － | $\bigcirc{ }^{*}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） |  | O＊1 | － | $0^{* 3}$ | － | － | － | － | － | － | － | － | － |

＊1 FX and FY cannot be used．
＊2 FD cannot be used．
＊3 T，ST，C，and FD cannot be used．

Control data

| Operand: (s) | Description | Not used | Setting range | Set by |
| :--- | :--- | :--- | :--- | :--- |
| Device | Item | - | The instruction completion status is stored. <br> $\bullet 0:$ Normal <br> - Other than 0: Error (error code) | System  <br> +0 Completion status <br> +1 Change target <br> specification <br> +2 Station number of own <br> station <br> +3 Specifies the station number to be set. <br> +4 to +9 - |

## Processing details

- Sets the station number for the local station.
[Own station]

- The execution of the UINI instruction and whether it has been completed normally or completed with an error can be checked with the completion device (d) or completion status indication device (d)+1.
- Completion device (d)

The completion device turns on in END processing of the scan performed upon completion of the UINI instruction and turns off in the next END processing.

- Completion status indication device (d) +1

The completion device turns on or off depending on the completion status of the UINI instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on in END processing of the scan performed upon completion of the UINI instruction and turns off in the next END processing.

- The following figure shows the operation at completion of the UINI instruction.
- When completed normally



## Operation error

| Error code ((s)+1) | Description |
| :--- | :--- |
| D000H to DFFFH | La MELSEC iQ-R CC-Link IE Field Network User's Manual (Application) |

## 16 cC－LINK INSTRUCTIONS

## 16．1 Reading Data from the Target Station

## G（P）．RIRD

These instructions read data of the specified number of points from the buffer memory area of the target station or the device of its CPU module．

| Ladder |  |  |  |  | ST <br> ENO：＝G＿RIRD（EN，U，s，d1，d2）； <br> ENO：＝GP＿RIRD（EN，U，s，d1，d2）； |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| ［．＂． （U） （s） （d1） （d2） |  |  |  |  |  |  |
|  |  |  |  |  |  |  |

FBD／LD


■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| G．RIRD | - |
|  | $\boxed{ }$ |
| GP．RIRD | $\uparrow$ |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （U） | Start I／O number（first three digits in four－digit <br> hexadecimal representation）of a module | OOH to FEH | 16－bit unsigned binary | ANY16 |
| （s） | Start device containing the control data | Refer to the control data． | Device name | ANY16 |
| （d1） | Start device for storing the read data | - | Device name | ANY16 |
| （d2） | Device to be turned on one scan upon <br> completion of instruction <br> （d2）＋1 also turns on when the instruction <br> completes with an error． | - | Bit | ANYBIT＿ARRAY <br> （Number of elements：2） |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | J밈 | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （U）${ }^{* 1}$ | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ |
| （s） | － | － | $\mathrm{O}^{* 3}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $\mathrm{O}^{*}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d2） | $\bigcirc{ }^{*}$ | － | $\bigcirc{ }^{*}$ | － | － | － | － | － | － | － | － | － |

＊1 Index modification is not available．
＊2 FX and FY cannot be used．
＊3 FD cannot be used．
＊4 T，ST，C，and FD cannot be used．

## Control data

| Operand: (s) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +0 | Completion status | The instruction completion status is stored. <br> - 0: Completed successfully <br> - Other than 0: Completed with an error (error code) | - | System |
| +1 | Station number | Specifies the station number of the target station. | 0 to 64 | User |
| +2 | Access/attribute code | Specifies the type of the buffer memory or device from which to read data. | Refer to "Access/attribute code." | User |
| +3 | Buffer memory address or device number | Specifies the start address of the buffer memory or the start device from which to read data. | Refer to the manual of the target station. ${ }^{* 1}$ | User |
| +4 | Number of read points | Specifies, in units of words, the number of points to be read. | $\begin{aligned} & 1 \text { to } 480^{* 2} \\ & 1 \text { to } 32^{* 3} \end{aligned}$ | User |

*1 When specifying a random accuracy buffer, specify the address with the start of the random accuracy buffer defined as 0 .
*2 Specify a value within the capacity of the target station buffer memory area or device.
*3 If data is to be read from a CPU module device if the target station CPU module is other than RCPU, QCPU (Q mode), QCPU (A mode), LCPU, QnACPU, or AnUCPU, the setting range is 1 to 32 words.

## Access/attribute code

- When data is read from the buffer memory within the CC-Link module

| Contents of buffer memory |  | Access code | Attribute code |
| :---: | :---: | :---: | :---: |
| Buffer in intelligent device |  | OOH | 04H |
| Buffer in master, local, or standby master station | Random access buffer | 20 H | 04H |
|  | Remote input | 21H | 04H |
|  | Remote output | 22H | 04H |
|  | Remote register | 24H | 04H |
|  | Link special relay | 63H | 04H |
|  | Link special register | 64H | 04H |

- When data is read from a CPU module device

| Device category*1 | Name | Date type |  | Unit | Access code | Attribute code |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | Bit | Word |  |  |  |
| Input relay | X | $\bigcirc$ | - | Hexadecimal | 01H | 05H |
| Output relay | Y | $\bigcirc$ | - | Hexadecimal | 02H | 05H |
| Internal relay | M | $\bigcirc$ | - | Decimal | 03H | 05H |
| Latch relay | L | $\bigcirc$ | - | Decimal | 83H | 05H |
| Link relay | B | $\bigcirc$ | - | Hexadecimal | 23H | 05H |
| Timer (contact) | T | $\bigcirc$ | - | Decimal | 09H | 05H |
| Timer (coil) | T | $\bigcirc$ | - | Decimal | OAH | 05H |
| Timer (current value) | T | - | $\bigcirc$ | Decimal | OCH | 05H |
| Retentive timer (contact) | ST | $\bigcirc$ | - | Decimal | 89H | 05H |
| Retentive timer (coil) | ST | $\bigcirc$ | - | Decimal | 8AH | 05H |
| Retentive timer (current value) | ST | - | $\bigcirc$ | Decimal | 8 CH | 05H |
| Counter (contact) | C | $\bigcirc$ | - | Decimal | 11H | 05H |
| Counter (coil) | C | $\bigcirc$ | - | Decimal | 12H | 05H |
| Counter (current value) | C | - | $\bigcirc$ | Decimal | 14H | 05H |
| Data register ${ }^{*}$ | D | - | $\bigcirc$ | Decimal | 04H | 05H |
| Link register ${ }^{*}{ }^{2}$ | W | - | $\bigcirc$ | Hexadecimal | 24H | 05H |
| File register | R | - | $\bigcirc$ | Decimal | 84H | 05H |
| Link special relay | SB | $\bigcirc$ | - | Hexadecimal | 63H | 05H |
| Link special register | SW | - | $\bigcirc$ | Hexadecimal | 64H | 05H |
| Special relay | SM | $\bigcirc$ | - | Decimal | 43H | 05H |
| Special register | SD | - | $\bigcirc$ | Decimal | 44H | 05H |

*1 Only the above devices can be specified. To read data from a bit device, specify it with 0 or a multiple of 16.
*2 None of D65536 and the subsequent extended data registers and of W10000 and the subsequent extended link registers are accepted.

## Processing details

- This instruction reads data of the specified number of points from the buffer memory area of the target station or the device of its CPU module.
- From the master station, the instruction can be executed for the local, standby master, or intelligent device station. From the local or standby master station, the instruction can be executed for the master, local, or standby master station.
- The following figure shows how the G(P).RIRD instruction operates during execution.
[Own station] [Target station]

$(1)$ Data is read from the buffer memory area specified by (s) +2 and ( $s$ ) +3 that is included in the station specified by (s) +1 or from the device of the CPU module.
(2) The read data is stored in the receive buffer of the master station.
(3) The read data is stored in the device specified by (d1) and later, and the device specified by (2) is turned on.
- Normal/error completion of the G(P).RIRD instruction can be checked with the completion device specified by the setting data (d2) and the completion status indication device (d2)+1.
- Completion device (d2)

Turns on during END processing of the scan that arises upon completion of the G(P).RIRD instruction, and turns off during the next END processing.

- Completion status indication device (d2)+1

Turns on or off depending on the status resulting from completion of the G(P).RIRD instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on during END processing of the scan that arises upon completion of the G(P).RIRD instruction, and turns off during the next END processing.

## Precautions

- The monitoring time and the number of retries can be set with the following link special registers (SWs).
- Monitoring time setting (SW0009)
- Setting of the number of retries for dedicated instruction (SWOOOB)
- The G(P).RIRD instruction can be concurrently executed for two or more local, standby master, or intelligent device stations. Two or more dedicated instructions, including those other than G(P).RIRD, cannot be concurrently executed for a single station. If the next dedicated instruction is issued before completion of the preceding one that has started, the next one will be ignored. Create the program so that the next dedicated instruction will start after the completion device turns on, because processing of a dedicated instruction takes several scans until its completion.


## Operation error

| Error code ((s)+0) | Description |
| :--- | :--- |
| 4000 H to 4FFFH | Lコ MELSEC iQ-R CPU Module User's Manual (Application) |
| B000H to BFFFH | D $\square$ MELSEC iQ-R CC-Link System Master/Local Module User's Manual (Application) |

## 16．2 Writing Data to the Target Station

## G（P）．RIWT

These instructions write data of the specified number of points to the buffer memory area of the target station or the device of its CPU module．


FBD／LD


## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| G．RIWT | - |
|  | - |
| GP．RIWT | - |

Setting data
■Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （U） | Start I／O number（first three digits in four－digit <br> hexadecimal representation）of a module | 00 H to FEH | 16－bit unsigned binary | ANY16 |
| （s1） | Start device containing the control data | Refer to the control data． | Device name | ANY16 |
| （s2） | Start device for storing the data to be written | - | Device name | ANY16 |
| （d） | Device to be turned on one scan upon <br> completion of instruction <br> If the instruction is completed with an error，（d）＋1 <br> is also turned on． | - | Bit | ANYBIT＿ARRAY <br> （Number of elements：2） |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| $(\mathrm{U})^{* 1}$ | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ |
| （s1） | － | － | $0^{* 3}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\mathrm{O}^{*}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | ${ }^{*}{ }^{2}$ | － | $\bigcirc^{* 4}$ | － | － | － | － | － | － | － | － | － |

＊1 Index modification is not available．
＊2 FX and FY cannot be used．
＊3 FD cannot be used．
＊4 T，ST，C，and FD cannot be used．

## Control data

| Operand: (s1) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +0 | Completion status | The instruction completion status is stored. <br> - 0: Completed successfully <br> - Other than 0: Completed with an error (error code) | - | System |
| +1 | Station number | Specifies the station number of the target station. | 0 to 64 | User |
| +2 | Access/atribute code | Specifies the type of the buffer memory or device from which to read data. | Refer to "Access/attribute code." | User |
| +3 | Buffer memory address or device number | Specifies the start address of the buffer memory area or the start device to which to write data. | Refer to the manual of the target station. ${ }^{* 1}$ | User |
| +4 | Number of write points | Specifies, in units of words, the number of points to be written. | $\begin{aligned} & 1 \text { to } 480^{* 2} \\ & 1 \text { to } 32^{* 3} \end{aligned}$ | User |

*1 When specifying a random accuracy buffer, specify the address with the start of the random accuracy buffer defined as 0 .
*2 Specify a value within the capacity of the target station buffer memory area or device.
*3 If data is to be written to a CPU module device if the target station CPU module is other than RCPU, QCPU (Q mode), QCPU (A mode), LCPU, QnACPU, or AnUCPU, the setting range is 1 to 32 words.

## Access/attribute code

- When data is written to the buffer memory within the CC-Link module

| Contents of buffer memory |  | Access code | Attribute code |
| :--- | :--- | :--- | :--- |
| Buffer in intelligent device |  |  | 00 H |
| Buffer in master or local station | Random access buffer | 20 H | 04 H |
|  | Remote input | 21 H | 04 H |
|  | Remote output | 22 H | 04 H |
|  | Remote register | 24 H | 04 H |
|  | Link special relay | 63 H | 04 H |
|  | Link special register | 64 H | 04 H |

- When data is written to the device of the CPU module

| Device category*1 | Name | Date type |  | Unit | Access code | Attribute code |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | Bit | Word |  |  |  |
| Input relay | X | $\bigcirc$ | - | Hexadecimal | 01H | 05H |
| Output relay | Y | $\bigcirc$ | - | Hexadecimal | 02H | 05H |
| Internal relay | M | $\bigcirc$ | - | Decimal | 03H | 05H |
| Latch relay | L | $\bigcirc$ | - | Decimal | 83H | 05H |
| Link relay | B | $\bigcirc$ | - | Hexadecimal | 23H | 05H |
| Timer (contact) | T | $\bigcirc$ | - | Decimal | 09H | 05H |
| Timer (coil) | T | $\bigcirc$ | - | Decimal | OAH | 05H |
| Timer (current value) | T | - | $\bigcirc$ | Decimal | OCH | 05H |
| Retentive timer (contact) | ST | $\bigcirc$ | - | Decimal | 89H | 05H |
| Retentive timer (coil) | ST | $\bigcirc$ | - | Decimal | 8AH | 05H |
| Retentive timer (current value) | ST | - | $\bigcirc$ | Decimal | 8CH | 05H |
| Counter (contact) | C | $\bigcirc$ | - | Decimal | 11H | 05H |
| Counter (coil) | C | $\bigcirc$ | - | Decimal | 12 H | 05H |
| Counter (current value) | C | - | $\bigcirc$ | Decimal | 14H | 05H |
| Data register ${ }^{*}$ | D | - | $\bigcirc$ | Decimal | 04H | 05H |
| Link register ${ }^{*}{ }^{2}$ | W | - | $\bigcirc$ | Hexadecimal | 24H | 05H |
| File register | R | - | $\bigcirc$ | Decimal | 84H | 05H |
| Link special relay | SB | $\bigcirc$ | - | Hexadecimal | 63H | 05H |
| Link special register | SW | - | $\bigcirc$ | Hexadecimal | 64H | 05H |
| Special relay | SM | $\bigcirc$ | - | Decimal | 43H | 05H |
| Special register | SD | - | $\bigcirc$ | Decimal | 44H | 05H |

*1 Only the above devices can be specified. To write data to a bit device, specify it with 0 or a multiple of 16.
*2 None of D65536 and the subsequent extended data registers and of W10000 and the subsequent extended link registers are accepted.

## Processing details

- This instruction writes data of the specified number of points from the buffer memory area of the target station or the device of its CPU module.
- From the master station, the instruction can be executed for the local, standby master, or intelligent device station. From the local or standby master station, the instruction can be executed for the master, local, or standby master station.
- The following figure shows how the $G(P)$.RIWT instruction operates during execution.
[Own station] [Target station]

$(1$ The data specified by ( s 2 ) is stored in the send buffer of the master station.
(2) Data is stored in the buffer memory area specified by ( $s 1$ ) +2 and ( $s 1$ ) +3 that is included in the station specified by ( $s 1$ ) +1 or to the device of the CPU module.
(3) The response indicating the write completion returns from the target station to the master station.
(4) The device specified by (d) is turned on.
- Normal/error completion of the G(P).RIWT instruction can be checked with the completion device specified by the setting data (d) and the completion status indication device (d)+1.
- Completion device (d)

Turns on during END processing of the scan that arises upon completion of the $G(P)$.RIWT instruction, and turns off during the next END processing.

- Completion status indication device (d) +1

Turns on or off depending on the status resulting from completion of the G(P).RIW instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on during END processing of the scan that arises upon completion of the $G(P)$.RIWT instruction, and turns off during the next END processing.

## Precautions

- The monitoring time and the number of retries can be set with the following link special registers (SWs).
- Monitoring time setting (SW0009)
- Setting of the number of retries for dedicated instruction (SWOOOB)
- The G(P).RIWT can be concurrently executed for two or more local or intelligent device stations. Two or more dedicated instructions, including those other than $G(P)$.RIRD, cannot be concurrently executed for a single station. If the next dedicated instruction is issued before completion of the preceding one that has started, the next one will be ignored. Create the program so that the next dedicated instruction will start after the completion device turns on, because processing of a dedicated instruction takes several scans until its completion.


## Operation error

| Error code <br> $((\mathbf{s} 1)+\mathbf{0})$ | Description |
| :--- | :--- |
| 4000 H to 4FFFH | L] MELSEC iQ-R CPU Module User's Manual (Application) |
| B000H to BFFFH | $\square]$ MELSEC iQ-R CC-Link System Master/Local Module User's Manual (Application) |

### 16.3 Reading Data from the Buffer Memory of the Specified Intelligent Device Station

## G(P).RIRCV

These instructions automatically perform handshake with the specified intelligent device station and reads data from its buffer memory. It is available for communication with a module with a handshake signal (e.g. AJ65BT-R2N).



## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| G.RIRCV | - |
|  | - |
| GP.RIRCV | - |

## Setting data

■Description, range, data type

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (U) | Start I/O number (first three digits in four-digit <br> hexadecimal representation) of a module | O0H to FEH | 16-bit unsigned binary | ANY16 |
| (s1) | Start device containing the control data | Refer to the control data. | Device name | ANY16 |
| (d1) | Start device for storing the read data | - | Device name | ANY16 |
| (s2) | Start device for storing the interlock signal | - | Device name | ANY16 |
| (d2) | Device to be turned on one scan upon <br> completion of instruction <br> (d2)+1 also turns on when the instruction <br> completes with an error. | - | Bit | ANYBIT_ARRAY <br> (Number of elements: 2) |
| EN | Execution condition | - | Bit | Bit |
| ENO | Execution result | - | BOOL |  |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3EDI（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （U）＊1 | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ |
| （s1） | － | － | $0^{* 3}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $0^{* 3}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $0^{* 3}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d2） | $\bigcirc^{* 2}$ | － | $\bigcirc{ }^{*} 4$ | － | － | － | － | － | － | － | － | － |

＊1 Index modification is not available．
＊2 FX and FY cannot be used．
＊3 FD cannot be used
＊4 T，ST，C，and FD cannot be used．

## Control data

| Operand：（s1） |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| ＋0 | Completion status | The instruction completion status is stored． <br> －0：Completed successfully <br> －Other than 0：Completed with an error（error code） | － | System |
| ＋1 | Station number | Specifies the station number of the target station． | 1 to 64 | User |
| ＋2 | Access／attribute code | Specifies 0004H． | 0004H | User |
| ＋3 | Buffer memory address | Specifies the start address of the buffer memory area from which to read data． | Refer to the manual of the target station． | User |
| ＋4 | Number of read points | Specifies，in units of words，the number of points to be read． | 1 to 480＊1 | User |

＊1 Specify a value within the capacity of the target station buffer memory area．

## ■Interlock signal


＊1 For the interlock signal，refer to the following．
［］Manual of the intelligent device station from which to read data

## Processing details

- This instruction automatically performs handshake with the specified intelligent device station and reads data from its buffer memory.
- The instruction can be executed, from the master station, for an intelligent device station with a handshake signal (e.g. AJ65BT-R2N).
- The following figure shows how the $G(P)$.RIRCV instruction operates during execution.

(1) A request is issued to read data from the buffer memory address specified by ( $s 1$ ) +3 that is included in the station specified by ( $s 1$ ) +1 .
(2) The remote input ( $R X$ ) specified by ( $s 2$ ) +1 that is in the station specified by ( $s 1$ ) +1 is monitored.
(3) Turning on the remote input ( $R X$ ) causes the master station to read the data from the buffer memory of the target station. The read data is stored in the receive buffer of the master station.
(4) The master station turns on the remote output (RY) specified by (s2)+0. It turns off the remote output (RY) specified by (s2)+0 by turning on and off the remote output ( $R X$ ) specified by ( $s 2$ ) +1 .
5 The data read from the target station is stored in the device specified by (d1) and later, and the device specified by (d2) is turned on.
- Normal/error completion of the G(P).RIRCV instruction can be checked with the completion device specified by the setting data (d2) and the completion status indication device (d2)+1.
- Completion device (d2)

Turns on during END processing of the scan that arises upon completion of the G(P).RIRCV instruction, and turns off during the next END processing.

- Completion status indication device (d2)+1

Turns on or off depending on the status resulting from completion of the G(P).RIRCV instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on during END processing of the scan that arises upon completion of the G(P).RIRCV instruction, and turns off during the next END processing.

## Precautions

- The monitoring time and the number of retries can be set with the following link special registers (SWs).
- Monitoring time setting (SW0009)
- Setting of the number of retries for dedicated instruction (SWOOOB)
- The G(P).RIRCV instruction can be concurrently executed for two or more intelligent device stations. Two or more dedicated instructions, including those other than G(P).RIRD, cannot be concurrently executed for a single station. If the next dedicated instruction is issued before completion of the preceding one that has started, the next one will be ignored. Create the program so that the next dedicated instruction will start after the completion device turns on, because processing of a dedicated instruction takes several scans until its completion.


## Operation error

| Error code <br> $((\mathbf{s} 1)+\mathbf{0})$ | Description |
| :--- | :--- |
| 4000 H to 4FFFH | D MELSEC iQ-R CPU Module User's Manual (Application) |
| B000H to BFFFH | L $\square$ MELSEC iQ-R CC-Link System Master/Local Module User's Manual (Application) |

### 16.4 Writing Data to the Buffer Memory of the Specified Intelligent Device Station

## G(P).RISEND

These instructions automatically perform handshake with the specified intelligent device station and writes data to its buffer memory. It is available for communication with a module with a handshake signal (e.g. AJ65BT-R2N).


FBD/LD

-Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| G.RISEND | - |
|  | - |
| GP.RISEND | - |

## Setting data

Description, range, data type

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (U) | Start I/O number (first three digits in four-digit <br> hexadecimal representation) of a module | 00 H to FEH | 16 -bit unsigned binary | ANY16 |
| (s1) | Start device containing the control data | Refer to the control data. | Device name | ANY16 |
| (s2) | Start device for storing the data to be written | - | Device name | ANY16 |
| (s3) | Start device for storing the interlock signal | - | Device name | ANY16 |
| (d) | Device to be turned on one scan upon <br> completion of instruction <br> If the instruction is completed with an error, (d)+1 <br> is also turned on. | - | Bit | ANYBIT_ARRAY <br> (Number of elements: 2) |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | J미민 | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UवIGロ，JロIロ， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （U）＊${ }^{*}$ | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ |
| （s1） | － | － | $0^{* 3}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $0^{* 3}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s3） | － | － | $0^{* 3}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | $0^{* 2}$ | － | $0^{* 4}$ | － | － | － | － | － | － | － | － | － |

＊1 Index modification is not available．
＊2 FX and FY cannot be used．
＊3 FD cannot be used．
＊4 T，ST，C，and FD cannot be used．

## Control data

| Operand：（s1） |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| ＋0 | Completion status | The instruction completion status is stored． <br> －0：Completed successfully <br> －Other than 0：Completed with an error（error code） | － | System |
| ＋1 | Station number | Specifies the station number of the target station． | 1 to 64 | User |
| ＋2 | Access／attribute code | Specifies 0004H． | 0004H | User |
| ＋3 | Buffer memory address | Specifies the start address of the buffer memory area to which to write data． | Refer to the manual of the target station． | User |
| ＋4 | Number of write points | Specifies，in units of words，the number of points to be written． | 1 to 480＊1 | User |

＊1 Specify a value within the capacity of the target station buffer memory area．

## ■Interlock signal

| Operand：（s3） |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| ＋0 | b15 $\ldots$ b8 b7 $\ldots$ <br> 0 b0    <br> 0  RY   | RY：Specifies the number of the write request signal（RY）to be used for interlock．${ }^{* 1}$ | 00H to 7FH | User |
| ＋1 |  | RY：Specifies the number of the write completion signal（RY）to be used for interlock．${ }^{* 1}$ | 00H to 7FH | User |
|  |  | RWr：Specifies the number of the device in which to store the error code （ RWr ）．If the error code storage device does not exist，specify FFH． | $\begin{aligned} & 00 \mathrm{H} \text { to } 0 \mathrm{FH}, \\ & \mathrm{FFH}^{* 2} \end{aligned}$ | User |
| ＋2 | b15 <br> Completion mode | Specifies how the completion signal behaves． <br> －0：Using Device 1 <br> During completion，the RX signal specified by（s2）＋1 turns on． <br> －1：Using Device 2 <br> For normal completion，the RX signal specified by（s2）＋1 turns on．For error completion，both RX and $\mathrm{RX}+1$ turn on simultaneously． | 0， 1 | User |

＊1 For the interlock signal，refer to the following．
［］Manual of the intelligent device station from which to read data
＊2 The error code stored in the error code storage device is the same as the completion status of control data（（s1）＋0）．

## Processing details

- This instruction automatically performs handshake with the specified intelligent device station and writes data to its buffer memory.
- The instruction can be executed, from the master station, for an intelligent device station with a handshake signal (e.g. AJ65BT-R2N).
- The following figure shows how the $G(P)$.RISEND instruction operates during execution.

(1) A request is issued to write data to the buffer memory address specified by ( $s 1$ ) +3 that is included in the station specified by ( $s 1$ ) +1 .

2 The data from the device specified by ( s 2 ) and later is stored in the send buffer of the master station.
(3) The master station writes the data of the send buffer in the buffer memory of the target station.
(4) The master station turns on the remote output (RY) specified by (s3)+0.

5 Upon completion of processing against the remote output (RY), the target station turns on the remote input (RX) specified by (s3)+1. It turns off the remote input (RX) specified by (s3)+1 by turning on and off the remote output (RY) specified by (s3)+1.
(6) The device specified by (d) is turned on.

- Normal/error completion of the G(P).RISEND instruction can be checked with the completion device specified by the setting data (d) and the completion status indication device (d)+1.
- Completion device (d)

Turns on during END processing of the scan that arises upon completion of the $G(P)$.RISEND instruction, and turns off during the next END processing.

- Completion status indication device (d)+1

Turns on or off depending on the status resulting from completion of RISEND.
When completed normally: Unchanged from off.
When completed with an error: Turns on during END processing of the scan that arises upon completion of the G(P).RISEND instruction, and turns off during the next END processing.

## Precautions

- The monitoring time and the number of retries can be set with the following link special registers (SWs).
- Monitoring time setting (SW0009)
- Setting of the number of retries for dedicated instruction (SWOOOB)
- The G(P).RISEND instruction can be concurrently executed for two or more intelligent device stations. Two or more dedicated instructions, including those other than G(P).RIRD, cannot be concurrently executed for a single station. If the next dedicated instruction is issued before completion of the preceding one that has started, the next one will be ignored. Create the program so that the next dedicated instruction will start after the completion device turns on, because processing of a dedicated instruction takes several scans until its completion.


## Operation error

| Error code <br> $((\mathbf{s} 1)+\mathbf{0})$ | Description |
| :--- | :--- |
| 4000 H to 4FFFH | L] MELSEC iQ-R CPU Module User's Manual (Application) |
| B000H to BFFFH | La MELSEC iQ-R CC-Link System Master/Local Module User's Manual (Application) |

### 16.5 Reading Data from the Automatic Update Buffer

## G(P).RIFR

These instructions read data from an automatic update or random access buffer. It is available for communication with a module with an automatic update buffer (e.g. AJ65BT-R2N).


FBD/LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| G.RIFR | - |
|  | $\boxed{T}$ |
| GP.RIFR | - |

## Setting data

Description, range, data type

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (U) | Start I/O number (first three digits in four-digit <br> hexadecimal representation) of a module | 00 H to FEH | 16 -bit unsigned binary | ANY16 |
| (s1) | If data is read from an automatic update buffer: Station <br> number of intelligent device station | 1 to 64 | 16 -bit unsigned binary | ANY16 |
|  | If data is read from a random access buffer: Specifies <br> the random access buffer. | 00 FFH | 16 -bit unsigned binary | ANY16 |
| (s2) | Offset value from the start of the automatic update or <br> random access buffer that has been assigned to the <br> target station | 0 to parameter set <br> value*1 | 16 -bit unsigned binary | ANY16 |
| (d) | Start device for storing the read data | - | Device name | ANY16 |
| (n) | Number of read points | 1 to 4096 | 16-bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | BOOL |  |

[^33]Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3E미（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （U）${ }^{* 1}$ | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ |
| （s1） | $0^{*}$ | － | $0^{* 3}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | $0^{*}$ | － | $0^{* 3}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | － | － | $0^{* 3}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （ n ） | $\mathrm{O}^{*}$ | － | $0^{* 3}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |

＊1 Index modification is not available．
＊2 FX and FY cannot be used．
＊3 FD cannot be used．

## Processing details

－Data is read from the automatic update or random access buffer of the master station．
－The instruction can be executed，from the master station，for an intelligent device station that performs communication with an automatic update buffer（e．g．AJ65BT－R2N）．
－The following figure shows how the $G(P)$ ．RIFR instruction operates during execution．


1 Data is read from the automatic update or random access buffer specified by（s1）and（s2）that is in the master station．
（2）The read data is stored in the device specified by（d）and later．

## Precautions

－Assignment of the automatic update buffer can be achieved by CC－Link configuration setting of the module parameters．

## Operation error

| Error code（SDO） | Description |
| :--- | :--- |
| 4000 H to 4FFFH | CJ MELSEC iQ－R CPU Module User＇s Manual（Application） |

### 16.6 Writing Data to the Automatic Update Buffer

## G(P).RITO

These instructions write data to an automatic update or random access buffer. It is available for communication with a module with an automatic update buffer (e.g. AJ65BT-R2N).


FBD/LD


■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| G.RITO | - |
|  | $\boxed{ }$ |
| GP.RITO | - |

## Setting data

Description, range, data type

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (U) | Start I/O number (first three digits in four-digit <br> hexadecimal representation) of a module | 00 H to FEH | 16 -bit unsigned binary | ANY16 |
| (d1) | If data is written to an automatic update buffer: Station <br> number of target station | 1 to 64 | 16 -bit unsigned binary | ANY16 |
|  | If data is written to a random access buffer: Specifies <br> the random access buffer. | 00 FFH | 16 -bit unsigned binary | ANY16 |
| (d2) | Offset value from the start of the automatic update or <br> random access buffer that has been assigned to the <br> target station | 0 to parameter set <br> value | 16 -bit unsigned binary | ANY16 |
| (s) | Start device for storing the data to be written | - | Device name | ANY16 |
| (n) | Number of write points | 1 to 4096 | 16 -bit unsigned binary | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

*1 Value that was set in the CC-Link configuration window of the engineering tool.

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> (U) |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | J밈 | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | UपIGロ, J밈, U3E미(H)G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (U)** | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | $\bigcirc$ | - | - | $\bigcirc$ |
| (d1) | $\mathrm{O}^{*}$ | - | $0^{* 3}$ | - | - | - | - | $\bigcirc$ | $\bigcirc$ | - | - | - |
| (d2) | $\mathrm{O}^{*}$ | - | $0^{* 3}$ | - | - | - | - | $\bigcirc$ | $\bigcirc$ | - | - | - |
| (s) | - | - | $0^{* 3}$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| ( n ) | $\mathrm{O}^{*}$ | - | $0^{* 3}$ | - | - | - | - | $\bigcirc$ | $\bigcirc$ | - | - | - |

*1 Index modification is not available.
*2 FX and FY cannot be used.
*3 FD cannot be used.

## Processing details

- Data is written to the automatic update or random access buffer of the master station.
- The instruction can be executed, from the master station, for an intelligent device station that performs communication with an automatic update buffer (e.g. AJ65BT-R2N).
- The following figure shows how the $G(P)$.RITO instruction operates during execution.

$(1)$ Data is read from the device specified by (s) and later of the master station.
(2) The read data is written to the automatic update or random access buffer specified by (d1) and (d2).


## Precautions

- Assignment of the automatic update buffer can be achieved by CC-Link configuration setting of the module parameters.


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 4000 H to 4FFFH | CJ MELSEC iQ-R CPU Module User's Manual (Application) |

### 16.7 Setting Network Parameters

## G(P).RLPASET

These instructions set the network parameters in the master station and starts up the data link.


■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| G.RLPASET | - |
|  | $\boxed{ }$ |
| GP.RLPASET | - |

## Setting data

Description, range, data type

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (U) | Start I/O number (first three digits in four-digit <br> hexadecimal representation) of a module | 00 H to FEH | 16-bit unsigned binary | ANY16 |
| (s1) | Start device containing the control data | Refer to the control data. | Device name | ANY16 |
| $(\mathrm{s} 2)^{* 1}$ | Start device containing the slave station setting data | - | Device name | ANY16 |
| $(\mathrm{s})^{* 1}$ | Start device containing the reserved station <br> specification data | - | Device name | ANY16 |
| $(\mathrm{s} 4)^{* 1}$ | Start device containing the error invalid station <br> specification data | - | Device name | ANY16 |
| $(\mathrm{s} 5)^{* 1}$ | Start device containing the automatic update buffer <br> assignment data | - | Device name | ANY16 |
| (d) | Device to be turned on one scan upon completion of <br> instruction <br> If the instruction is completed with an error, (d) +1 is <br> also turned on. | - | Bit | (Number of elements: 2) |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | BOOL |  |

[^34]Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> (U) |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | J밈 | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | U민ㅁ, J밈, U3EDI(H)Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (U)* ${ }^{*}$ | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | $\bigcirc$ | - | - | $\bigcirc$ |
| (s1) | - | - | $0^{* 3}$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (s2) | - | - | $0^{* 3}$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (s3) | - | - | $0^{* 3}$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (s4) | - | - | $0^{* 3}$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (s5) | - | - | $0^{* 3}$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (d) | $\bigcirc^{* 2}$ | - | ${ }^{*} 4$ | - | - | - | - | - | - | - | - | - |

Index modification is not available.
FX and FY cannot be used.
FD cannot be used.
T, ST, C, and FD cannot be used.

## Control data


*1 The default value will be used for any setting data specified as invalid. For the default values, refer to the following.
W Page 1373 Slave station setting data
$\longmapsto$ Page 1373 Reserved station specification data
F Page 1374 Error invalid station specification data
F Page 1374 Automatic update buffer assignment data

Slave station setting data

| Operand: (s2) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +0 to +63 | Station information settings* ${ }^{*}$ | Specifies the station number, the number of occupied stations, and the station type for each slave station. <br> The default value range is 0101 H to 0140 H , meaning that the station number is 1 to 64 , the number of occupied stations is 1 station occupied, and the station type is remote I/O station for Ver. 1. | - | User |
|  |  | (1) Station number setting 1 to 64 | 01H to 40H | User |
|  |  | (2) Number of occupied stations setting <br> - 1H: 1 station occupied <br> - $2 \mathrm{H}: 2$ stations occupied <br> - $3 \mathrm{H}: 3$ stations occupied <br> - $4 \mathrm{H}: 4$ stations occupied | 1 H to 4H | User |
|  |  | (3) Station type setting ${ }^{*}{ }^{2}$ <br> - OH: Remote I/O station for Ver. 1 <br> - 1H: Remote device station for Ver. 1 <br> - 2H: Intelligent device station for Ver. 1 <br> - 5 H : Single remote device station for Ver. 2 <br> -6H: Single intelligent device station for Ver. 2 <br> - 8H: Double remote device station for Ver. 2 <br> -9H: Double intelligent device station for Ver. 2 <br> - BH: Quadruple remote device station for Ver. 2 <br> - BH: Quadruple intelligent device station for Ver. 2 <br> - EH: Octuple remote device station for Ver. 2 <br> - FH: Quadruple intelligent device station for Ver. 2 | OH to FH | User |

*1 Repeat this setting for the total number of connected modules/stations.
*2 If a local station is specified, specify the intelligent device station.

## Reserved station specification data

| Operand: (s3) |  |  |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description |  |  |  |  |  |  |  |  |  | Setting range | Set by |
| +0 to +3 | Reserved station specification | Specifies <br> - 0: Not <br> - 1: Spe <br> (s3) +0 <br> (s3) +1 <br> (s3)+2 <br> (s3)+3 <br> The defa | a res <br> speci <br> cified <br> b15 <br> 16 <br> 32 <br> 48 <br> 64 | ed <br> b14 <br> 15 <br> 31 <br> 47 <br> 63 | b13 <br> 14 <br> 30 <br> 46 <br> 62 <br> Not s | sta <br> b12 <br> 13 <br> 29 <br> 45 <br> 61 <br> cified | by-s | ion <br> b3 <br> 4 <br> 20 <br> 36 <br> 52 <br> tion | $\begin{gathered} \text { b2 } \\ \hline 3 \\ \hline 19 \\ \hline 35 \\ \hline 51 \end{gathered}$ | b1 <br> 2 <br> 18 <br> 34 <br> 50 | b0 <br> 1 <br> 17 <br> 33 <br> 49 | - | User |

[^35]Error invalid station specification data

| Operand: (s4) |  |  |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description |  |  |  |  |  |  |  |  |  | Setting range | Set by |
| +0 to +3 | Error invalid station specification | Specifies <br> - 0: Not sp <br> - 1: Spec <br> (s4) +0 <br> (s4)+1 <br> (s4)+2 <br> (s4)+3 <br> The defau |  | b14 <br> 15 <br> 31 <br> 47 <br> 63 | b13 <br> 14 <br> 30 <br> 46 <br> 62 <br> ot sp | in a <br>  <br> b12 <br> 13 <br> 29 <br> 45 <br> 61 | on- $\square$ <br> all | b3 <br> 4 <br> 20 <br> 36 <br> 52 <br> ions | b2 <br> 3 <br> 19 <br> 35 <br> 51 | ${ }^{* 1 * 2}$ <br> b1 <br> 2 <br> 18 <br> 34 <br> 50 | b0 <br> 1 <br> 17 <br> 33 <br> 49 | - | User |

*1 For two or more stations occupied, specify only the slave station start number.
*2 If both the reserved and error invalid stations are specified for the same station, the reserved station specification will take priority.

## Automatic update buffer assignment data

| Operand: (s5) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +0 to +77 | Automatic update buffer assignment specification | Specifies the assigned buffer memory size (words) that is used for the transient transmission with the automatic update buffer that is performed to the local or intelligent device station. ${ }^{* 1}$ <br> - 0: Not specified <br> - 1: Specified <br> The default value is 0080 H . | OH (no setting) 0080 H to $1000 \mathrm{H}^{* 2}$ | User |

*1 Beginning at the smallest station number, set the size for the slave stations for which the local or intelligent device station has been set with the slave station setting data ((s2)+0 to (s2)+63).
*2 The automatic update buffer size must be 1000 H (4096) words or less in total. For the automatic update buffer size, specify the required size for each intelligent device station.

## Processing details

- This instruction sets the network parameters in the master station and starts up the data link.
- This instruction can be executed only for the master station.
- The following figure shows how the $G(P)$.RLPASET instruction operates during execution.

(1) The network parameters specified by ( s 1 ) to ( s 5 ) are written to the master station.
(2) The data link is started up.
(3) The device specified by (d) is turned on.
- Normal/error completion of the G(P).RLPASET instruction can be checked with the completion device specified by the setting data (d) and the completion status indication device (d)+1.
- Completion device (d)

Turns on during END processing of the scan that arises upon completion of the G(P).RLPASET instruction, and turns off during the next END processing.

- Completion status indication device (d)+1

Turns on or off depending on the status resulting from completion of the $G(P)$.RLPASET instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on during END processing of the scan that arises upon completion of the G(P).RLPASET instruction, and turns off during the next END processing.

- After completion of the $G(P)$.RLPASET instruction, turn on SB0003, a refresh instruction that is used for parameter change with the dedicated instruction, to start the cyclic data refresh.
- If no stations are faulty

- If all stations are faulty



## Precautions

- Two or more G(P).RLPASET instructions cannot be executed concurrently.
- The G(P).RLPASET instruction is not available in a system containing a standby master station.
- Do not set network parameters using the engineering tool for modules for which network parameters are set using the $G(P)$.RLPASET instruction. If the $G(P)$.RLPASET instruction is executed for a module for which network parameters are set using the engineering tool, the instruction will be terminated with an error and the network parameter settings are not reflected.
- Stop the data link using "Data Link Stop" (SB0002) before executing the G(P).RLPASET instruction.


## Operation error

| Error code <br> $((\mathbf{s 1})+\mathbf{0})$ | Description |
| :--- | :--- |
| B000H to BFFFH | LD MELSEC iQ-R CC-Link System Master/Local Module User's Manual (Application) |

## 16．8 Performing a Message Transmission to a Remote Device Station

## G（P）．RDMSG

These instructions performs a message transmission to a remote device station．

|  |  |  |  |  |  | ```ST ENO:=G_RDMSG(EN,U,s1,s2,d1,d2); ENO:=GP_RDMSG(EN,U,s1,s2,d1,d2);``` |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| ■－－－ | Ladder |  |  |  |  |  |
|  | （U） | （s1） | （s2） | （d1） | （d2） |  |

FBD／LD

| ᄃ－二」 |  |
| :---: | :---: |
| en | Eno |
| u | d1 |
| s1 | ${ }^{\text {d2 }}$ |
| ${ }^{2} 2$ |  |

## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| G．RDMSG | - |
|  | $\boxed{ }$ |
| GP．RDMSG | - |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （U） | Start I／O number of module <br> （Upper 2 digits of the 3－digit representation of I／O <br> number） | 0000 H to 00FEH | 16－bit unsigned binary | ANY16 |
| （s1） | Start device containing the control data | Refer to the control data． | Device name | ANY16＿ARRAY <br> （Number of elements：5） |
| （s2） | Start device for storing the message data to be <br> sent | - | Device name | ANY16＿ARRAY <br> （Number of elements： 1 to 255） |
| （d1） | Start device for storing the message data <br> received | - | Device name | ANY16＿ARRAY <br> （Number of elements： 1 to 255） |
| （d2） | Device to be turned on one scan upon <br> （dompletion of instruction <br> （d2）＋1 also turns on when the instruction <br> completes with an error． | - | Bit | ANYBIT＿ARRAY <br> （Number of elements：2） |

Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | J밈 | T，ST，C，D，W， SD，SW，FD，R， ZR，RD | UロIGロ，JロIロ， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （U）＊${ }^{*}$ | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ |
| （s1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d2） | $\bigcirc$ | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

＊1 Index modification is not available．

| Operand: (s1) | Description |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Item | Completion status | Used to store the completion status. <br> 0: Normal completion <br> $\cdot$ Other than 0: Error completion (error code) | Setting range | Set by |
| +0 | Station number | Specifies the station number of the target station. | - | System |  |
| +1 | Send data size | Specify the send message data size in bytes. | 1 to 64 | User |  |
| +2 | Receivable data size | Specify the maximum size of the device that stores the receive message data in <br> bytes. | 0 to 255 |  |  |
| +3 | Receive data size | The receive message data size is stored in bytes. | User |  |  |
| +4 |  |  | 0 to 255 |  |  |

## Processing details

- These instructions performs a message transmission to a remote device station.
- These instructions can be executed to a remote device station that supports the message transmission function.
- The following figure shows how the $G(P)$.RDMSG instruction operates during execution.


[^36]- Normal/error completion of the G(P).RDMSG instruction can be checked with the completion device (d2) specified by the setting data and the completion status indication device (d2)+1.
- Completion device (d2)

Turns on during END processing of the scan that arises upon completion of the $G(P)$.RDMSG instruction, and turns off during the next END processing

- Completion status indication device (d2)+1

Turns on or off depending on the status resulting from completion of the $G(P)$.RDMSG instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on during END processing of the scan that arises upon completion of the G(P).RDMSG instruction, and turns off during the next END processing.

- When completed with an error, an error code is stored in the completion status (s1)+0.


Send data size

$\square$ Receivable data size and receive data size
Set the receivable data size ((s1)+3) so that it satisfies the following.
Receivable data size ((s1)+3) $\geq$ Receive data size ((s1)+4)


## When the receivable data size is not equal to the receive data size

The operation will be as follows.

- Receivable data size ((s1)+3) > Receive data size ((s1)+4)

The instruction completes successfully, and the receive data are stored in the device specified by (d1) and later. Data are not stored in the area exceeding the receive data size.


- Receivable data size ((s1)+3) < Receive data size ((s1)+4)

The instruction completes with an error, and the data are not stored in the device specified by (d1) and later.

## When the number of bytes in message data is odd

When the number of bytes in the send data is odd, only the lower byte is send as for the last data in the device memory.


When the number of bytes in the receive data is odd, the last receive data is stored in the lower byte of the last data storage area in the device memory. In the upper byte, 0 is stored.


## Precautions

- The $G(P)$.RDMSG instruction can be simultaneously executed to two or more remote device stations (up to four stations). Note, however, that only one dedicated instruction can be executed to a single remote device station. If a dedicated instruction is executed before the processing of another instruction has not been completed, the instruction executed later will complete with an error.
- Create the program so that the next dedicated instruction will start after the completion device turns on, because processing of a dedicated instruction takes several scans until its completion.
- The G(P).RDMSG instruction uses a part of remote register that performs cyclic transmission between the master station and the target station. For programming, refer to the manual for the remote device station targetted.


## Operation error

| Error code <br> $(\mathbf{s 1})+\mathbf{0})$ | Description |
| :--- | :--- |
| B000H to BFFFH | Lコ MELSEC iQ-R CC-Link System Master/Local Module User's Manual (Application) |

## 17 SERIAL COMMUNICATION INSTRUCTIONS

## 17．1 Sending Data Using the On－Demand Function

## G（P）．ONDEMAND

These instructions send data of the specified amount from the specified device by using the MC protocol on－demand function．

| Ladder |  |  |  |  | STENO：＝G＿ONDEMAND（EN，U，s1，s2，d）；ENO：＝GP＿ONDEMAND（EN，U，s1，s2 |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  |  |
|  |  |  |  |  |  |

FBD／LD


## －Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| G．ONDEMAND | - |
|  | $\boxed{ }$ |
| GP．ONDEMAND |  |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （U） | Start I／O number（first three digits in four－digit hexadecimal <br> representation）of a module | 00 H to FEH | 16－bit unsigned binary | ANY16 |
| （s1） | Start device containing the control data | Refer to the control data． | Device name | ANY16 |
| （s2） | Start device for storing the send data | - | Device name | ANY16 |
| （d） | Device to be turned on one scan upon completion of <br> instruction <br> If the instruction is completed with an error，（d）＋1 is also <br> turned on． | - | Bit | ANYBIT＿ARRAY <br> （Number of elements： <br> 2） |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ\ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDl（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （U）＊1 | － | － | $\mathrm{O}^{* 3}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ |
| （s1） | － | － | $\mathrm{O}^{*} 4$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\mathrm{O}^{*}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | $\bigcirc^{* 2}$ | － | $\bigcirc{ }^{* 5}$ | － | － | － | － | － | － | － | － | － |

＊1 Index modification is not available．
＊2 FX and FY cannot be used．
＊3 RD cannot be used．
＊4 FD cannot be used．
＊5 T，ST，C，and FD cannot be used．

| Operand: (s1) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +0 | Send channel | Specifies the send channel. <br> - 1: Channel 1 (CH1 side) <br> - 2: Channel 2 (CH2 side) | 1,2 | User |
| +1 | Result of sending | Used to store the result of sending with the $G(P)$.ONDEMAND instruction. <br> - 0: Normal <br> - Other than 0: Error (error code) | - | System |
| +2 | Amount of send data | Specifies the amount of the data to be sent. ${ }^{* 1}$ | 1 or more | User |

*1 The amount of send data must be set in units as specified in the engineering tool. For word specification, set the number of words. For byte specification, set the number of bytes.

## Processing details

- By using the on-demand function of MC protocol of the module specified by (U), the data stored in the device specified by (s2) and later is sent according to the control data in the device specified by (s1) and later.

- Normal/error completion of the G(P).ONDEMAND instruction can be checked with the completion device (d) and the completion status indication device (d)+1.
- Completion device (d)

Turns on during END processing of the scan that arises upon completion of the $G(P)$.ONDEMAND instruction, and turns off during the next END processing.

- Completion status indication device (d) +1

Turns on or off depending on the status resulting from completion of the $G(P)$.ONDEMAND instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on during END processing of the scan that arises upon completion of the $G(P)$.ONDEMAND instruction, and turns off during the next END processing.

- The following figure shows how the $G(P)$.ONDEMAND instruction operates when completing its execution.



## Precautions

- The following table summarizes the processes that take place if another instruction is issued while the $G(P)$.ONDEMAND instruction is active or the $G(P)$.ONDEMAND instruction is issued while another instruction is active with the same channel.

| Instruction to execute <br> concurrently ${ }^{2}$ | Possibility of concurrent <br> execution | Handling for concurrent execution |
| :--- | :--- | :--- |
| G(P).ONDEMAND | $\times$ | The instruction is ignored and does not start processing until the active instruction <br> completes. <br> Concurrent execution is, however, possible if they use different channels. |
| ZP.CSET | $\times$ | A dedicated instruction concurrent execution error (7FFOH) occurs in the second <br> instruction. <br> Concurrent execution is, however, possible if they use different channels. |
| G(P).PUTE | - | - |
| G(P).GETE | $\times$ | A dedicated instruction concurrent execution error (7FFOH) occurs in the second <br> instruction. |
| G(P).SPBUSY |  |  |
| ZP.UINI |  |  |

*2 The following instructions are not available with the same channel as for the $G(P)$.ONDEMAND instruction because they use a communication protocol different from the one used by this instruction. In addition, if these are used with the same channel as for the $G(P)$.ONDEMAND instruction, a communication protocol setting error (7FF2H) will occur. (Except for the Z.BUFRCVS instruction). - G(P).CPRTCL, G(P).OUTPUT, G.INPUT, G(P).BIDOUT, Z.BUFRCVS, and G(P).PRR instructions

- The local device and the file register for each program are not available for setting data.


## Operation error

| Error code <br> $((\mathrm{s} 1)+1)$ | Description |
| :--- | :--- |
| 7000 H to 7FFFH | LD MELSEC iQ-R Serial Communication Module User's Manual (Application) |

### 17.2 Executing the Protocols Registered for the Predefined Protocol Support Function

## G(P).CPRTCL

These instructions execute the protocols or special protocols that have been written to the flash ROM by using the communication protocol support function of the engineering tool.


FBD/LD

-Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| G.CPRTCL | - |
| GP.CPRTCL | - |

## Setting data

Description, range, data type

| Operand | Description | Range | Data type | Data type (label) |
| :---: | :---: | :---: | :---: | :---: |
| (U) | Start I/O number (first three digits in four-digit hexadecimal representation) of a module | OOH to FEH | 16-bit unsigned binary | ANY16 |
| ( n 1 ) | Channel to communicate with the external device <br> - 1: Channel 1 (CH1 side) <br> - 2: Channel 2 (CH2 side) | 1,2 | 16-bit unsigned binary | ANY16 |
| (n2) | Number of protocols to be executed consecutively (1 to 8) | 1 to 8 | 16-bit unsigned binary | ANY16 |
| (s) | Start device containing the control data | Refer to the control data. | Device name | ANY16 |
| (d) | Device to be turned on one scan upon completion of instruction <br> If the instruction is completed with an error, (d) +1 is also turned on. | - | Bit | ANYBIT_ARRAY <br> (Number of elements: <br> 2) |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGㅁ，J밈， U3EDI（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| $(\mathrm{U})^{* 1}$ | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ |
| （ n 1 ） | － | － | $0^{* 3}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （n2） | － | － | $0^{* 3}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s） | － | － | $0^{* 3}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | $0^{* 2}$ | － | $0^{* 3}$ | － | － | － | － | － | － | － | － | － |

＊1 Index modification is not available．
2 FX and FY cannot be used．
＊3 FD cannot be used．

## Control data

| Operand：（s） |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| ＋0 | Execution result | Used to store the execution result of the $G(P)$ ．CPRTCL instruction．If two or more protocols are executed，the execution result of the last protocol will be stored．${ }^{* 1}$ <br> －0：Normal <br> －Other than 0：Error（error code） | － | System |
| ＋1 | Resulting number of executed protocols | Used to store the number of actually executed protocols． <br> Also for the protocols that caused an error，the execution result of the last protocol will be stored． <br> If an error is included in the setting data or the control data setting details，＂ 0 ＂will be stored． | 1 to 8 | System |
| ＋2 | Execution protocol number specification | Sets the number of the first protocol or special protocol to be executed．${ }^{*}$ 2 | $\begin{aligned} & 1 \text { to } 128,201 \text { to } \\ & 207 \end{aligned}$ | User |
| ！ |  | ！ |  |  |
| ＋9 |  | Sets the number of the eighth protocol or special protocol to be executed．${ }^{*}$ |  |  |
| ＋10 | Matched receive packet number | Used to store the matched receive packet number if the communication type of the first protocol executed is＂Send only＂or＂Send and receive＂． <br> If the first protocol caused an error during its execution，＂0＂will be stored． <br> If a special protocol is executed，＂ 0 ＂will be stored．${ }^{\text {2 }}$ | 0， 1 to 16 | System |
| ！ |  |  |  |  |
| ＋17 |  | Used to store the matched receive packet number if the communication type of the eighth protocol executed is＂Send only＂or＂Send and receive＂． <br> If the eighth protocol caused an error during its execution，＂ 0 ＂will be stored． If a special protocol is executed，＂ 0 ＂will be stored．${ }^{*}$ 2 |  |  |

＊1 If the nth protocol caused an error during its execution while two or more protocols are being executed，none of the subsequent protocols will be executed．
＊2 For details on the special protocols，refer to the following．
$\hbar$ Page 1387 Special protocol

## Processing details

- The protocol setting data written to the flash ROM is executed using the module specified by (U). All the protocols to be executed follow the contents of the control data that has been stored in the device specified by ( $s$ ) and later. The channel specified by ( n 1 ) is used.
- One attempt of the instruction consecutively executes the specified number of protocols (by (n2); maximum 8).
- Normal/error completion of the G(P).CPRTCL instruction can be checked with the completion device (d) and the completion status indication device (d)+1.
- Completion device (d)

Turns on during END processing of the scan that arises upon completion of the $G(P)$.CPRTCL instruction, and turns off during the next END processing.

- Completion status indication device (d)+1

Turns on or off depending on the status resulting from completion of the $G(P) . C P R T C L$ instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on during END processing of the scan that arises upon completion of the G(P).CPRTCL instruction, and turns off during the next END processing.

- The following figure shows how the $G(P) . C P R T C L$ instruction operates when completing its execution.



## Point ${ }^{\circ}$

The program execution status can be checked with the buffer memory (address: $4041 \mathrm{H} / 4051 \mathrm{H}$ ).

## Special protocol

The following functions are available by executing special protocols with the $G(P) . C P R T C L$ instruction.

| Functions | Description |
| :--- | :--- |
| Clearing receive data | Enables the receive data to be cleared from the OS area. |
| Send/receive data monitoring start/stop | Specifies the start or stop of send/receive data monitoring. <br> When special protocols 202 or 203 is executed, the module sets the monitor start instruction "0001H" or the <br> monitor stop instruction " 0000 H " for the send/receive data monitoring specification (address: 2018H, 2118H). |
| RS/DTR signal status specification | Specifies the on/off state of the RS(RTS) and ER(DTR) signals. <br> When one of special protocols 204 to 207 is executed, the module turns on/off the bit corresponding to the RS/ <br> DTR signal status specification (address: $92 \mathrm{H} / 132 \mathrm{H})$. |
|  | For details on the RS/DTR signal status specification, refer to the following. <br> LD MELSEC iQ-R Serial Communication Module User's Manual (Application) |

In control data (s)+2 to (s) +9 of the $G(P)$.CPRTCL instruction, specify the number of the special protocol of the function to be executed.

| Functions | Special protocol number | Type | Keyword*1 |
| :--- | :--- | :--- | :--- |
| Clearing receive data | 201 | Clearing receive data | Receive Data Clear |
| Send/receive data <br> monitoring start/stop | 202 | Start send/receive data monitoring | Send/Recv Monitor Start |
|  | 203 | Stop send/receive data monitoring | Send/Recv Monitor Stop |
| RS/DTR signal status <br> specification | 204 | Turn on the ER (DTR) signal | DTR ON |
|  | 205 | Turn off the ER (DTR) signal | DTR OFF |
|  | 206 | Turn on the RS (RTS) signal | RS ON |
|  | 207 | Turn off the RS (RTS) signal | RS OFF |

*1 When a special protocol is executed, the keyword is defined as the character string that is stored in the protocol name of the protocol execution history.

## Precautions

- The following table summarizes the processes that take place if another instruction is issued while the $G(P) . C P R T C L$ instruction is active or the $G(P) . C P R T C L$ instruction is issued while another instruction is active with the same channel.

| Instruction to execute <br> concurrently ${ }^{* 2}$ | Possibility of concurrent <br> execution | Handling for concurrent execution |
| :--- | :--- | :--- |
| G(P).CPRTCL | $\times$ | The instruction is ignored and does not start processing until the active instruction <br> completes. <br> Concurrent execution is, however, possible if they use different channels. |
| G(P).PUTE |  | - |
| G(P).GETE | $\times$ | A dedicated instruction concurrent execution error (7FFOH) occurs in the second <br> instruction. |
| G(P).SPBUSY |  |  |
| ZP.UINI |  |  |

*2 The following instructions are not available with the same channel as for the $G(P)$.CPRTCL instruction because they use a communication protocol different from the one used by this instruction. In addition, if these are used with the same channel as for the $G(P) . C P R T C L$ instruction, a communication protocol setting error (7FF2H) will occur (Except for the Z.BUFRCVS instruction).

- G(P).ONDEMAND, G(P).OUTPUT, G.INPUT, G(P).BIDOUT, G(P).BIDIN, Z.BUFRCVS, G(P).PRR, and ZP.CSET instructions
- The local device and the file register for each program are not available for setting data.


## Operation error

| Error code ((s)+0) | Description |
| :--- | :--- |
| 7000 H to 7 FFFH | La MELSEC iQ-R Serial Communication Module User's Manual (Application) |

## 17．3 Sending Data Using the Nonprocedural Protocol

## G（P）．OUTPUT

These instructions send data in a user－defined message format by using the nonprocedural protocol．


FBD／LD


## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| G．OUTPUT | - |
|  | $\boxed{T}$ |
| GP．OUTPUT | - |

## Setting data

## DDescription，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （U） | Start I／O number（first three digits in four－digit hexadecimal <br> representation）of a module | 00 H to FEH | 16－bit unsigned binary | ANY16 |
| （s1） | Start device containing the control data | Refer to the control data． | Device name | ANY16 |
| （s2） | Start device for storing the send data | - | Device name | ANY16 |
| （d） | Device to be turned on one scan upon completion of <br> instruction <br> If the instruction is completed with an error，（d）＋1 is also <br> turned on． | - | Bit | ANYBIT＿ARRAY <br> （Number of elements： <br> $2)$ |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDl（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （U）＊${ }^{*}$ | － | － | $\mathrm{O}^{* 3}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ |
| （s1） | － | － | ${ }^{*} 4$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\mathrm{O}^{*}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | $0^{* 2}$ | － | $\bigcirc^{* 5}$ | － | － | － | － | － | － | － | － | － |

＊1 Index modification is not available．
＊2 FX and FY cannot be used．
＊3 RD cannot be used．
＊4 FD cannot be used．
＊5 T，ST，C，and FD cannot be used．

| Operand: (s1) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +0 | Send channel | Specifies the send channel. <br> - 1: Channel 1 (CH1 side) <br> - 2: Channel 2 (CH2 side) | 1,2 | User |
| +1 | Result of sending | Used to store the result of sending with the G(P).OUTPUT instruction. <br> - 0: Normal <br> - Other than 0: Error (error code) | - | System |
| +2 | Amount of send data | Specifies the amount of the data to be sent. ${ }^{* 1}$ | 1 or more | User |

*1 The amount of the data to be sent must be set in units as specified in the engineering tool. For byte specification, set the number of bytes. For word specification, set the number of words.

## Processing details

- By using the nonprocedural protocol of the module specified by (U), the data stored in the device specified by (s2) and later is sent according to the control data in the device specified by (s1) and later.

- Normal/error completion of the $G(P)$.OUTPUT instruction can be checked with the completion device (d) and the completion status indication device (d)+1.
- Completion device (d)

Turns on during END processing of the scan that arises upon completion of the $G(P)$.OUTPUT instruction, and turns off during the next END processing.

- Completion status indication device (d) +1

Turns on or off depending on the status resulting from completion of the $\mathrm{G}(\mathrm{P})$.OUTPUT instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on during END processing of the scan that arises upon completion of the G(P).OUTPUT instruction, and turns off during the next END processing.

- The following figure shows how the $G(P)$.OUTPUT instruction operates when completing its execution.



## Precautions

- The following table summarizes the processes that take place if another instruction is issued while the $G(P)$.OUTPUT instruction is active or the $G(P)$.OUTPUT instruction is issued while another instruction is active with the same channel.

| Instruction to execute <br> concurrently ${ }^{*}$ | Possibility of concurrent <br> execution | Handling for concurrent execution |
| :--- | :--- | :--- |
| G(P).OUTPUT | $\times$ | The instruction is ignored and does not start processing until the active instruction <br> completes. <br> Concurrent execution is, however, possible if they use different channels. |
| G.INPUT | $\times$ | - |
| G(P).PRR |  | A dedicated instruction concurrent execution error (7FFOH) occurs in the second <br> instruction. <br> Concurrent execution is, however, possible if they use different channels. |
| ZP.CSET |  | - |
| G(P).PUTE |  | $\times$ |
| G(P).GETE |  | A dedicated instruction concurrent execution error (7FFOH) occurs in the second <br> instruction. |
| Z.BUFRCVS |  |  |

*2 The following instructions are not available with the same channel as for the $G(P)$.OUTPUT instruction because they use a communication protocol different from the one used by this instruction. In addition, if these are used with the same channel as for the $G(P)$.OUTPUT instruction, a communication protocol setting error (7FF2H) will occur.

- G(P).ONDEMAND, G(P).CPRTCL, G(P).BIDOUT, and G(P).BIDIN instructions
- The local device and the file register for each program are not available for setting data.


## Operation error

| Error code <br> $((\mathbf{s 1 )}+\mathbf{1})$ | Description |
| :--- | :--- |
| 7000 H to 7FFFH | D C MELSEC iQ-R Serial Communication Module User's Manual (Application) |

## 17．4 Receiving Data Using the Nonprocedural Protocol

## G．INPUT

This instruction receives data in a user－defined message format by using the nonprocedural protocol．


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| G．INPUT | $-\square$ |

## Setting data

## Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （U） | Start I／O number（first three digits in four－digit hexadecimal <br> representation）of a module | 00 H to FEH | 16－bit unsigned binary | ANY16 |
| （s） | Start device containing the control data | Refer to the control data． | Device name | ANY16 |
| （d1） | Start device for storing the receive data | - | Device name | ANY16 |
| （d2） | Device to be turned on one scan upon completion of <br> instruction <br> （d2）＋1 also turns on when the instruction completes with <br> an error． | - | Bit | ANYBIT＿ARRAY <br> （Number of elements： <br> 2） |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）GD | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （U）＊${ }^{*}$ | － | － | $\bigcirc^{* 3}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ |
| （s） | － | － | $\bigcirc^{* 4}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $\bigcirc{ }^{*} 4$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d2） | $\mathrm{O}^{*}$ | － | $\bigcirc{ }^{* 5}$ | － | － | － | － | － | － | － | － | － |

＊1 Index modification is not available．
＊2 FX and FY cannot be used．
＊3 RD cannot be used．
＊4 FD cannot be used．
＊5 T，ST，C，and FD cannot be used．

Control data

| Operand: (s) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +0 | Receive channel | Specifies the receive channel. <br> - 1: Channel 1 (CH1 side) <br> - 2: Channel 2 (CH2 side) | 1,2 | User |
| +1 | Result of receiving | Used to store the result of receiving with the G.INPUT instruction. <br> - 0: Normal <br> - Other than 0: Error (error code) | - | System |
| +2 | Amount of receive data | Specifies the amount of actually received data. | - | System |
| +3 | Allowable amount of receive data | Sets the allowable number of words of receive data that can be stored in (d1).* ${ }^{\text {¹ }}$ | 1 or more | User |

*1 The amount of the data to be received must be set in units as specified in the engineering tool. For byte specification, set the number of bytes. For word specification, set the number of words.

## Processing details

- The data received through the nonprocedural protocol of the module specified by $(U)$ is stored in the device specified by (d1) and later, according to the control data in the device specified by (s) and later.
- If the amount of actually received data is larger than the allowable amount of receive data specified by (s)+3, the data for the allowable amount of receive data is only stored with the remaining receive data discarded. In this case, the allowable amount of receive data is stored in the amount of receive data (s)+2. (the instruction completes successfully).
- Normal/error completion of the G.INPUT instruction can be checked with the completion device (d2) and the completion status indication device (d2)+1.
- Completion device (d2)

Turns on during END processing of the scan that arises upon completion of the G.INPUT instruction, and turns off during the next END processing.

- Completion status indication device (d2)+1

Turns on or off depending on the status resulting from completion of the G.INPUT instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on during END processing of the scan that arises upon completion of the G.INPUT instruction ended, and turns off during the next END processing

- The following figures show how the G.INPUT instruction operates when its execution completes.



## Precautions

- Any command of G.INPUT cannot be pulse converted.
- G.INPUT must be executed while the I/O signal read request signal is on.
- The following table summarizes the processes that take place if another instruction is issued while the G.INPUT instruction is active or the G.INPUT instruction is issued while another instruction is active with the same channel.

| Instruction to execute <br> concurrently ${ }^{* 2}$ | Possibility of concurrent <br> execution | Handling for concurrent execution |
| :--- | :--- | :--- |
| G(P).OUTPUT | O | - |
| G(P).PRR | $\times$ | The instruction is ignored and does not start processing until the active instruction <br> completes. <br> Concurrent execution is, however, possible if they use different channels. |
| G.INPUT | See the right | - If the G.INPUT and ZP.CSET instructions are issued in this order <br> A dedicated instruction concurrent execution error (7FFOH) occurs in the ZP.CSET <br> instruction. Concurrent execution is, however, possible if they use different channels. <br> - If the ZP.CSET and G.INPUT instructions are issued in this order <br> Concurrent execution is possible. |
| ZP.CSET | O | - <br> G(P).PUTE |
| G(P).GETE | $\times$ | The G.INPUT and Z.BUFRCVS instructions cannot be used simultaneously. <br> Concurrent execution is, however, possible if they use different channels. |
| Z.BUFRCVS | - | A dedicated instruction concurrent execution error (7FFOH) occurs in the second <br> instruction. |
| G(P).SPBUSY | $\times$ |  |
| ZP.UINI |  |  |

*2 The following instructions are not available with the same channel as for the G.INPUT instruction because they use a communication protocol different from the one used by this instruction. In addition, if these are used with the same channel as for the G.INPUT instruction, a communication protocol setting error (7FF2H) will occur.

- G(P).ONDEMAND, G(P).CPRTCL, G(P).BIDOUT, and G(P).BIDIN instructions
- The local device and the file register for each program are not available for setting data.


## Operation error

7000H to 7FFFH [D] MELSEC iQ-R Serial Communication Module User's Manual (Application)

## 17．5 Sending Data Using the Bidirectional Protocol

## G（P）．BIDOUT

These instructions send data using the bidirectional protocol．


FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| G．BIDOUT | - |
|  | $\boxed{T}$ |
| GP．BIDOUT | - |

## Setting data

## DDescription，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （U） | Start I／O number（first three digits in four－digit hexadecimal <br> representation）of a module | 00 H to FEH | 16－bit unsigned binary | ANY16 |
| （s1） | Start device containing the control data | Refer to the control data． | Device name | ANY16 |
| （s2） | Start device for storing the send data | - | Device name | ANY16 |
| （d） | Device to be turned on one scan upon completion of <br> instruction <br> If the instruction is completed with an error，（d）＋1 is also <br> turned on． | - | Bit | ANYBIT＿ARRAY <br> （Number of elements： <br> 2） |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）GD | Z | $\begin{array}{\|l} \text { LT, LST, } \\ \text { LC } \end{array}$ | LZ |  | K，H | E | \＄ |  |
| $(\mathrm{U})^{* 1}$ | － | － | ${ }^{*}{ }^{3}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ |
| （s1） | － | － | ${ }^{*} 4$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | ${ }^{*} 4$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | $0^{* 2}$ | － | ${ }^{* 5}$ | － | － | － | － | － | － | － | － | － |

＊1 Index modification is not available．
＊2 FX and FY cannot be used．
＊3 RD cannot be used．
＊4 FD cannot be used．
＊5 T，ST，C，and FD cannot be used．

| Operand: (s1) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +0 | Send channel | Specifies the send channel. <br> - 1: Channel 1 (CH1 side) <br> - 2: Channel 2 (CH2 side) | 1,2 | User |
| +1 | Result of sending | Used to store the result of sending with the G(P).BIDOUT instruction. <br> - 0: Normal <br> - Other than 0: Error (error code) | - | System |
| +2 | Amount of send data | Specifies the amount of the data to be sent. ${ }^{* 1}$ | 1 or more | User |

*1 The amount of the data to be sent must be set in units as specified in the engineering tool. For byte specification, set the number of bytes. For word specification, set the number of words.

## Processing details

- By using the bidirectional protocol of the module specified by (U), the data stored in the device specified by (s2) and later is sent according to the control data in the device specified by (s1) and later.
- Normal/error completion of the $G(P)$.BIDOUT instruction can be checked with the completion device (d) and the completion status indication device (d)+1.
- Completion device (d)

Turns on during END processing of the scan that arises upon completion of the $G(P)$.BIDOUT instruction, and turns off during the next END processing.

- Completion status indication device (d) +1

Turns on or off depending on the status resulting from completion of the $G(P)$.BIDOUT instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on during END processing of the scan that arises upon completion of the G(P).BIDOUT instruction, and turns off during the next END processing.

- The following figure shows how the $G(P)$. BIDOUT instruction operates when completing its execution.



## Precautions

- The following table summarizes the processes that take place if another instruction is issued while the $G(P)$.BIDOUT instruction is active or the $G(P)$.BIDOUT instruction is issued while another instruction is active with the same channel.

| Instruction to execute <br> concurrently ${ }^{*}$ | Possibility of concurrent <br> execution | Handling for concurrent execution |
| :--- | :--- | :--- |
| G(P).BIDOUT | $\times$ | The instruction is ignored and does not start processing until the active instruction <br> completes. <br> Concurrent execution is, however, possible if they use different channels. |
| G(P).BIDIN | $\times$ | - |
| ZP.CSET |  | The instruction is ignored and does not start processing until the active instruction <br> completes. <br> Concurrent execution is, however, possible if they use different channels. |
| G(P).PUTE |  | - |
| G(P).GETE |  | $\times$ |
| Z.BUFRCVS |  | A dedicated instruction concurrent execution error (7FFOH) occurs in the second <br> instruction. |
| G(P).SPBUSY |  |  |

*2 The following instructions are not available with the same channel as for the $G(P)$.BIDOUT instruction because they use a communication protocol different from the one used by this instruction. In addition, if these are used with the same channel as for the $\mathrm{G}(\mathrm{P})$.BIDOUT instruction, a communication protocol setting error (7FF2H) will occur.
$\cdot G(P) . O N D E M A N D, G(P) . C P R T C L, G(P)$.BIDOUT, and $G(P)$.BIDIN instructions

## Operation error

| Error code <br> $(($ s1 $)+1)$ | Description |
| :--- | :--- |
| 7000 H to 7FFFH | LD MELSEC iQ-R Serial Communication Module User's Manual (Application) |

## 17．6 Receiving Data Using the Bidirectional Protocol

## G（P）．BIDIN

These instructions receive data using the bidirectional protocol．


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| G．BIDIN | - |
|  | $\boxed{Z}$ |
| GP．BIDIN | - |

## Setting data

## Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （U） | Start I／O number（first three digits in four－digit hexadecimal <br> representation）of a module | 00 H to FEH | 16－bit unsigned binary | ANY16 |
| （s） | Start device containing the control data | Refer to the control data． | Device name | ANY16 |
| （d1） | Start device for storing the receive data | - | Device name | ANY16 |
| （d2） | Device to be turned on one scan upon completion of <br> instruction <br> （d2）＋1 also turns on when the instruction completes with <br> an error． | - | Bit | ANYBIT＿ARRAY <br> （Number of elements： <br> $2)$ |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3EDI（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （U）＊${ }^{*}$ | － | － | $\mathrm{O}^{*}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ |
| （s） | － | － | ${ }^{*} 4$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | ${ }^{*} 4$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d2） | $\mathrm{O}^{*}$ | － | O＊ | － | － | － | － | － | － | － | － | － |

＊1 Index modification is not available．
＊2 FX and FY cannot be used．
＊3 RD cannot be used．
＊4 FD cannot be used．
＊5 T，ST，C，and FD cannot be used．

Control data

| Operand: (s) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +0 | Receive channel | Specifies the receive channel. <br> - 1: Channel 1 (CH1 side) <br> - 2: Channel 2 (CH2 side) | 1,2 | User |
| +1 | Result of receiving | Used to store the result of sending with the G(P).BIDIN instruction. <br> - 0: Normal <br> - Other than 0: Error (error code) | - | System |
| +2 | Amount of receive data | Stores the number of data received. | - | System |
| +3 | Allowable amount of receive data | Sets the allowable number of words of receive data that can be stored in (d1).* ${ }^{\text {* }}$ | 1 or more | User |

*1 The amount of the data to be received must be set in units as specified in the engineering tool. For byte specification, set the number of bytes. For word specification, set the number of words.

## Processing details

- The data received using the bidirectional protocol of the module specified by $(U)$ is stored in the device specified by (d1) and later, according to the control data in the device specified by (s) and later.
- Normal/error completion of the $G(P)$.BIDIN instruction can be checked with the completion device (d2) and the completion status indication device (d)+1.
- Completion device (d2)

Turns on during END processing of the scan that arises upon completion of the $G(P)$. BIDIN instruction, and turns off during the next END processing

- Completion status indication device (d2)+1

Unchanged from off.

- The following figure shows how the $G(P)$. BIDIN instruction operates when completing its execution.



## Precautions

- The following table summarizes the processes that take place if another instruction is issued while the $G(P) . B I D I N$ instruction is active or the $G(P)$.BIDIN instruction is issued while another instruction is active with the same channel.

| Instruction to execute <br> concurrently ${ }^{* 2}$ | Possibility of concurrent <br> execution | Handling for concurrent execution |
| :--- | :--- | :--- |
| G(P).BIDOUT | $\times$ | - |
| G(P).BIDIN | $\bigcirc$ | The instruction is ignored and does not start processing until the active instruction <br> completes. <br> Concurrent execution is, however, possible if they use different channels. |
| ZP.CSET | $\times$ | A dedicated instruction concurrent execution error (7FFOH) occurs in the second <br> instruction. <br> Concurrent execution is, however, possible if they use different channels. |
| G(P).PUTE | - | Concurrent execution of the G(P).BIDIN and Z.BUFRCVS instructions is not possible. <br> Concurrent execution is, however, possible if they use different channels. |
| G(P).GETE | $\times$ | - |
| Z.BUFRCVS | $\times$ | A dedicated instruction concurrent execution error (7FFOH) occurs in the second <br> instruction. |
| G(P).SPBUSY | $\times$ |  |
| ZP.UINI |  |  |

*2 The following instructions are not available with the same channel as for the G(P).BIDIN instruction because they use a communication protocol different from the one used by this instruction. In addition, if these are used with the same channel as for the $G(P)$.BIDIN instruction, a communication protocol setting error (7FF2H) will occur.

- G(P).ONDEMAND, G(P).CPRTCL, G(P).OUTPUT, G.INPUT, and G(P).PRR instructions


## Operation error

| Error code ((s)+1) | Description |
| :--- | :--- |
| 7000 H to 7FFFH | D MELSEC iQ-R Serial Communication Module User's Manual (Application) |

### 17.7 Reading the Data Send/Receive Status

## G(P).SPBUSY

These instructions read the send/receive status of data using the instruction.


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| G.SPBUSY | - |
| GP.SPBUSY | - |

## Setting data

■Description, range, data type

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (U) | Start I/O number (first three digits in four-digit hexadecimal <br> representation) of a module | 00 H to FEH | 16-bit unsigned binary | ANY16 |
| (d) | Start device for storing the read communication status | - | Device name | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> (U) |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロום | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | UपIGㅁ, J밈, U3EDI(H)G口 | z | LT, LST, LC | LZ |  | K, H | E | \$ |  |
| (U)* ${ }^{*}$ | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | $\bigcirc$ | - | - | $\bigcirc$ |
| (d) | - | - | $O^{*}$ | - | - | - | - | $\bigcirc$ | - | - | - | - |

*1 Index modification is not available.
*2 FD cannot be used.

## Processing details

- The execution status of the instruction to the module specified by the start I/O number is read and stored in the device specified by (d) and later.
- In the associated bit of the device specified by ( d ), " 1 " is stored when each instruction starts its processing, and " 0 " is stored when the instruction completes its processing. Completion of processing of each instruction is at the time when the completion flag of the instruction changes from on to off.
) Execution status of the $G(P)$.ONDEMAND, $G(P)$.OUTPUT, $G(P) . P R R$, or $G(P)$.BIDOUT instruction for channel 1
(2) Execution status of the G.INPUT or G(P).BIDIN instruction for channel 1
(3) Execution status of the $G(P)$.ONDEMAND, $G(P)$.OUTPUT, $G(P) . P R R$, or $G(P)$.BIDOUT instruction for channel 2
(4) Execution status of the G.INPUT or G(P).BIDIN instruction for channel 2
(5) Execution status of the $G(P)$.GETE or $G(P)$.PUTE instruction
(6) Execution status of the $G(P)$.CPRTCL instruction for channel 1
(7) Execution status of the G(P).CPRTCL instruction for channel 2
- If the instruction has been set to be executed while being on, the $G(P)$.SPBUSY instruction operates every scan while the read instruction is on; if the instruction has been set to be executed at the rise, it operates only one scan when the read instruction rises from off to on.
- While the $G(P)$.SPBUSY instruction is active, another instruction can be executed. In addition, while another instruction is active, the $G(P)$.SPBUSY instruction can be executed.


## Operation error

| Error code (SD0) | Description |
| :--- | :--- |
| 4000 H to 4FFFH | $\square]$ MELSEC iQ-R CPU Module User's Manual (Application) |

## 17．8 Receiving Data Using an Interrupt Program

## Z．BUFRCVS

This instruction uses the interrupt program to receive communication data by using the nonprocedural or bidirectional protocol．


FBD／LD


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| Z．BUFRCVS | $\square$ |

## Setting data

## Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （U） | Start I／O number（first three digits in four－digit hexadecimal <br> representation）of a module | 00 H to FEH | ANY16＿OR＿STRING |  |
| （s） | Specifies the receive channel． <br> $-1:$ Channel 1 $(\mathrm{CH} 1$ side） <br> $\cdot 2: ~ C h a n n e l ~ 2 ~(C H 2 ~ s i d e) ~$ | 1,2 | 16－bit unsigned binary | ANY16 |
| （d） | Start device for storing the receive data <br> （The receive data is read from the receive area in the buffer <br> memory．） | - | Device name | ANY16 |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3E미（H）G口 | z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| $(\mathrm{U})^{*}{ }^{\text {d }}$ | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ |
| （s） | $\mathrm{O}^{*}$ | － | $0^{* 3}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （d） | － | － | $0^{* 3}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

＊1 Index modification is not available．
＊2 FX and FY cannot be used．
＊3 FD cannot be used．

Receive data

| Operand: (d) |  |  |  |  |  |  | Description | Setting range | Set by |
| :--- | :--- | :--- | :--- | :--- | :--- | :---: | :---: | :---: | :---: |
| Device | Item | Receive data length | Used to store the amount of data that was read from the data storage area for <br> amount of receive data. ${ }^{*}$ | - | System |  |  |  |  |
| +0 | Receive data | Used to store the data, in ascending order of the address, that was read from the <br> receive data storage area. | - | System |  |  |  |  |  |
| +1 to n |  |  |  |  |  |  |  |  |  |

*1 The amount of the data to be received must be set in units as specified in the engineering tool. For byte specification, set the number of bytes. For word specification, set the number of words.

## Processing details

- The data received from the external device is stored in the specified device.
- The receive data can be read without considering the address of the receive area of the buffer memory.
- When the Z.BUFRCVS instruction is executed, receive completion takes place; the receive read request signal (Xn3/XnA) or receive error detection signal $(X n 4 / X n B)$ turns off automatically. If the receive has been read using the Z.BUFRCVS instruction, the receive read completion signal (Yn1/Yn8) does not need to be turned on.
- The Z.BUFRCVS instruction is used in the interrupt program, and processing completes within one scan.
- The following figures show how the Z.BUFRCVS instruction operates when its execution completes.



## Precautions

- When data is to be received with the interrupt program, use the Z.BUFRCVS instruction.
- The following table summarizes the processes that take place if another instruction is issued while the Z.BUFRCVS instruction is active or the Z.BUFRCVS instruction is issued while another instruction is active with the same channel.

| Instruction to execute <br> concurrently 2 | Possibility of concurrent <br> execution | Handling for concurrent execution |
| :--- | :--- | :--- |
| G(P).OUTPUT | O | - |
| G(P).PRR | $\times$ | The G.INPUT and Z.BUFRCVS instructions cannot be used simultaneously. <br> Concurrent execution is, however, possible if they use different channels. |
| G.INPUT |  | - |
| G(P).BIDOUT |  | Concurrent execution of the G(P).BIDIN and Z.BUFRCVS instructions is not possible. <br> Concurrent execution is, however, possible if they use different channels. |
| G(P).BIDIN |  | - |
| ZP.CSET |  |  |
| $\mathrm{G}(P) . P U T E$ |  |  |
| G(P).GETE |  |  |
| Z.BUFRCVS |  |  |
| G(P).SPBUSY |  |  |
| ZP.UINI |  |  |

*2 The following instructions are not available with the same channel as for the Z.BUFRCVS instruction because they use a communication protocol different from the one used by this instruction.

- G(P).ONDEMAND and G(P).CPRTCL instructions
- For the receive data storage device for the Z.BUFRCVS instruction, secure in advance the area larger than the amount of data that will be received from the external device. If it has not been secured, the data located at the rear of the storage device will be overwritten.
- The local device and the file register for each program are not available for setting data.
- The read error codes issued during occurrence of a receive error can be read from the data receive result storage area (address: $258 \mathrm{H} / 268 \mathrm{H}$ ) of the buffer memory.


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 4000 H to 4FFFH | LD MELSEC iQ-R CPU Module User's Manual (Application) |

## 17．9 Sending Data by Using User Frames

## G（P）．PRR

These instructions send data with user frames according to the specification in the user frame specification area for sending， through communication with the nonprocedural protocol．

| Ladder | ST |
| :---: | :---: |
|  | $\begin{aligned} & \mathrm{ENO}:=\mathrm{G}=\mathrm{PRR}(\mathrm{EN}, \mathrm{U}, \mathrm{~s}, \mathrm{~d}) ; \\ & \text { ENO:=GP_PRR(EN,U,s,d); } \end{aligned}$ |

FBD／LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| G．PRR | - |
|  | - |
| GP．PRR | - |

## Setting data

## Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （U） | Start I／O number（first three digits in four－digit hexadecimal <br> representation）of a module | 00 H to FEH | 16－bit unsigned binary | ANY16 |
| （s） | Start device containing the control data | Refer to the control data． | Device name | ANY16 |
| （d） | Device to be turned on one scan upon completion of <br> instruction <br> If the instruction is completed with an error，（d）+1 is also <br> turned on． | - | Bit | ANYBIT＿ARRAY <br> （Number of elements： <br> $2)$ |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3EDI（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （U）＊${ }^{*}$ | － | － | $\mathrm{O}^{* 3}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ |
| （s） | － | － | ${ }^{*} 4$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | $\bigcirc^{* 2}$ | － | ${ }^{*} 4$ | － | － | － | － | － | － | － | － | － |

＊1 Index modification is not available．
＊2 FX and FY cannot be used．
＊3 RD cannot be used．
＊4 FD cannot be used．

Control data

| Operand: (s) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +0 | Send channel | Specifies the send channel. <br> - 1: Channel 1 (CH1 side) <br> - 2: Channel 2 (CH2 side) | 1,2 | User |
| +1 | Result of sending | Used to store the result of sending with the $G(P) . P R R$ instruction. <br> - 0: Normal <br> - Other than 0: Error (error code) | - | System |
| +2 | CR/LF append specification | Specifies whether to append CR or LF to send data. | 0,1 | User |
| +3 | Send pointer | Specifies the frame number that indicates the start position of send data within the user frame specification area for sending. | 1 to 100 | User |
| +4 | Amount of output | Specifies the number of user frames to be sent. | 1 to 100 | User |

## Processing details

- By using the nonprocedural protocol of the module specified by (U), the data in the user frames is sent according to the control data stored in the device specified by (s) and later and the user frame specification area for sending of the module.
- Normal/error completion of the G(P).PRR instruction can be checked with the completion device (d) and the completion status indication device (d)+1.
- Completion device (d)

Turns on during END processing of the scan that arises upon completion of the $G(P)$.PRR instruction, and turns off during the next END processing.

- Completion status indication device (d)+1

Turns on or off depending on the status resulting from completion of the G(P).PRR instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on during END processing of the scan that arises upon completion of the $G(P)$.PRR instruction, and turns off during the next END processing.

- The following figure shows how the $G(P) . P R R$ instruction operates when completing its execution.



## Precautions

- The following table summarizes the processes that take place if another instruction is issued while the $G(P)$.PRR instruction is active or the $G(P) . P R R$ instruction is issued while another instruction is active with the same channel.

| Instruction to execute <br> concurrently ${ }^{*}$ | Possibility of concurrent <br> execution | Handling for concurrent execution |
| :--- | :--- | :--- |
| G(P).PRR | $\times$ | The instruction is ignored and does not start processing until the active instruction <br> completes. <br> Concurrent execution is, however, possible if they use different channels. |
| G.INPUT | $\times$ | - |
| G(P).OUTPUT |  | A dedicated instruction concurrent execution error (7FFOH) occurs in the second <br> instruction. <br> Concurrent execution is, however, possible if they use different channels. |
| ZP.CSET |  | - |
| G(P).PUTE | $\times$ | A dedicated instruction concurrent execution error (7FFOH) occurs in the second <br> instruction. |
| G(P).GETE |  |  |
| Z.BUFRCVS |  |  |
| G(P).SPBUSY |  |  |

*1 The following instructions are not available with the same channel as for the $G(P)$.PRR instruction because they use a communication protocol different from the one used by this instruction.

- G(P).ONDEMAND, G(P).CPRTCL, G(P).BIDOUT, and G(P).BIDIN instructions
- The local device and the file register for each program are not available for setting data.


## Operation error

| Error code ((s)+1) | Description |
| :--- | :--- |
| 7000 to 7FFFH | La MELSEC iQ-R Serial Communication Module User's Manual (Application) |

## 17．10 Clearing Receive Data

## ZP．CSET

This instruction clears the receive data area without stopping send processing by using the nonprocedural protocol．


FBD／LD

| ■－－－$]$ |  |
| :---: | :---: |
| EN | ENO |
| U | d1 |
| s1 | d2 |
| s2 |  |

## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| ZP．CSET | $\uparrow$ |

Setting data

## Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （U） | Start I／O number（first three digits in four－digit hexadecimal <br> representation）of a module | 00 H to FEH | String | ANY16＿OR＿STRING <br> ＿SINGLE |
| （s1） | Channel number of the channel for which the receive data <br> clear request is issued <br> $-1:$ Channel（CH1 side） <br> $\cdot 2: ~ C h a n n e l ~(C H 2 ~ s i d e) ~$ | 1,2 | 16－bit unsigned binary | ANY16 |
| （s2） | Start device containing the control data | Refer to the control data． | Device name | ANY16 |
| （d1） | Dummy | - | Device name | ANY16 |
| （d2） | Device to be turned on one scan upon completion of <br> instruction <br> （d2）＋1 also turns on when the instruction completes with <br> an error． | - | Bit | ANYBIT＿ARRAY <br> （Number of elements： <br> 2） |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロロロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3EDI（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { I } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| $(\mathrm{U})^{* 1}$ | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ |
| （s1） | $0^{* 2}$ | － | $0^{* 3}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | － | － | $0^{* 3}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $0^{* 3}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d2） | $\mathrm{O}^{*}$ | － | $\mathrm{O}^{* 3}$ | － | － | － | － | － | － | － | － | － |

＊1 Index modification is not available．
＊2 FX and FY cannot be used．
＊3 FD cannot be used．

| Operand: (s2) | Description |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
| Device | Item | Specifies 0. | Setting range | Set by |  |
| +0 | Execution type | Used to store the completion status. <br> •0: Normal <br> - Other than 0: Error (error code) | 0 | User |  |
| +1 | Completion status | Specifies the contents of request. <br> 4: Receive data clear request | - | System |  |
| +2 | Request type | Use prohibited (not available also for other applications such as programming) | - | 4 | User |
| +3 to +111 | For system |  |  |  |  |

## Processing details

- This function only clears receive data from the OS area, but does not clear any data from the user receive area in the buffer memory.
- If the ZP.CSET instruction is issued when the receive read request (Xn3/XnA) or receive error detection (Xn4/XnB) is on, it has to wait until the signal turns off.
- Normal/error completion of the ZP.CSET instruction can be checked with the completion device (d2) and the completion status indication device (d2)+1.
- Completion device (d2)

Turns on during END processing of the scan that arises upon completion of the ZP.CSET instruction, and turns off during the next END processing

- Completion status indication device (d2)+1

Turns on or off depending on the status resulting from completion of the ZP.CSET instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on during END processing of the scan that arises upon completion of the ZP.CSET instruction ended, and turns off during the next END processing.

- The following figures show how the ZP.CSET instruction operates when its execution completes.



## Precautions

- The following table summarizes the processes that take place if another instruction is issued while the ZP.CSET instruction is active or the ZP.CSET instruction is issued while another instruction is active with the same channel.

| Instruction to execute concurrently* ${ }^{*}$ | Possibility of concurrent execution | Handling for concurrent execution |
| :---: | :---: | :---: |
| G(P).ONDEMAND | $\times$ | A dedicated instruction concurrent execution error (7FFOH) occurs in the second instruction. <br> Concurrent execution is, however, possible if they use different channels. |
| G(P).OUTPUT |  |  |
| G(P).PRR |  |  |
| G(P).BIDOUT |  |  |
| G(P).BIDIN |  |  |
| G.INPUT | See the right | - If the G.INPUT and ZP.CSET instructions are issued in this order <br> A dedicated instruction concurrent execution error (7FFOH) occurs in the ZP.CSET instruction. <br> Concurrent execution is, however, possible if they use different channels. <br> - If the ZP.CSET and G.INPUT instructions are issued in this order Concurrent execution is possible. |
| ZP.CSET | $\times$ | The instruction is ignored and does not start processing until the active instruction completes. <br> Concurrent execution is, however, possible if they use different channels. |
| G(P).PUTE | $\bigcirc$ | - |
| G(P).GETE |  |  |
| Z.BUFRCVS |  |  |
| G(P).SPBUSY |  |  |
| ZP.UINI | $\times$ | A dedicated instruction concurrent execution error (7FFOH) occurs in the second instruction. |

*1 The $G(P)$.CPRTCL instruction is not available with the same channel as for the $Z(P)$.CSET instruction because it uses a communication protocol different from the one used by the ZP.CSET instruction. If the above instruction is used with the same channel as for the $\mathrm{G}(\mathrm{P})$.CPRTCL instruction, a communication protocol setting error (7FF2H) will occur.

- The local device and the file register for each program are not available for setting data.


## Operation error

| Error code <br> $((\mathbf{s 2})+1)$ | Description |
| :--- | :--- |
| 7000 H to 7FFFH | LD MELSEC iQ-R Serial Communication Module User's Manual (Application) |

## 17．11 Registering／Canceling the Programmable Controller CPU Monitoring

## ZP．CSET

This instruction performs programmable controller CPU monitoring registration to enable use of the programmable controller CPU monitoring function，or cancels it．

| Ladder |  |  |  |  |  | $\begin{aligned} & \text { ST } \\ & \text { ENO:=ZP_CSET(EN,U,s1,s2,d1,d2); } \end{aligned}$ |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  |  |  |  |
|  | （U） | （s1） | （s2） | （d1） | （d |  |  |

FBD／LD


## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| ZP．CSET | $\ddots$ |

## Setting data

Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （U） | Start I／O number（first three digits in four－digit hexadecimal <br> representation）of a module | 00 H to FEH | String | ANY16＿OR＿STRING <br> ＿SINGLE |
| （s1） | Channel number of the channel for which to send the <br> monitoring result <br> $-1:$ Channel（CH1 side） <br> •2：Channel（CH2 side） | 1,2 | 16－bit unsigned binary | ANY16 |
| （s2） | Start device containing the control data | Refer to the control data． | Device name | ANY16 |
| （d1） | Dummy | - | Device name | ANY16 |
| （d2） | Device to be turned on one scan upon completion of <br> instruction <br> （d2）＋1 also turns on when the instruction completes with <br> an error． | - | Bit | ANYBIT＿ARRAY <br> （Number of elements： <br> 2） |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGㅁ，Jㅁㅁ， U3EDl（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| $(\mathrm{U})^{* 1}$ | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ |
| （s1） | $\mathrm{O}^{*}$ | － | $0^{* 3}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | － | － | $0^{* 3}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $\mathrm{O}^{* 3}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d2） | $\mathrm{O}^{*}$ | － | $0^{* 3}$ | － | － | － | － | － | － | － | － | － |

＊1 Index modification is not available．
＊2 FX and FY cannot be used．
＊3 FD cannot be used．

## Control data

- When programmable controller CPU monitoring is registered

| Operand |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Item |  |  | Description | Setting range | Set by |
| +0 | Execution type |  |  | Specifies 0 . | 0 | User |
| +1 | Completion status |  |  | Used to store the completion status. <br> - 0: Normal <br> - Other than 0: Error (error code) | - | System |
| +2 | Request type |  |  | Specifies the contents of request. <br> - 2: Performs programmable controller CPU monitoring registration. | 2 | User |
| +3 | Unit of cycle time |  |  | Specifies the unit of the cycle time. <br> -0: 100ms <br> -1: Second <br> - 2: Minute | 0 to 2 | User |
| +4 | Cycle time |  |  | Specifies the cycle time. | 1H to FFFFFH | User |
| +5 | Programmable controller CPU monitoring function |  |  | Specifies the monitoring function. <br> - 1: Sending with a fixed cycle <br> - 2: Sending upon condition matching | 1,2 | User |
| +6 | Sending means for programmable controller CPU monitoring |  |  | Specifies the sending means. <br> - 0 : Sending data (device data, CPU error information) | 0 | User |
| +7 | Cyclic broadcast send | User fram pointer | output start | Specifies the start pointer of the table where the user frame numbers for sending with a fixed cycle have been set. <br> - 0 : Not specified (sending upon condition matching) <br> - 1 to 100: Amount of data sent | 0, 1 to 100 | User |
| +8 |  | Number sent | user frames | Specifies the number of user frames that are sent, or output, with a fixed cycle. <br> - 0 : Not specified (sending upon condition matching) <br> - 1 to 100: Amount of data sent | 0, 1 to 100 | User |
| +9 | For system |  |  | Use prohibited | - | - |
| +10 | Number of reserved word blocks |  |  | Specifies the number of blocks of the word device to be monitored. | 0 to 10 | User |
| +11 | Number of registered bit blocks |  |  | Specifies the number of blocks of the bit device to be monitored. | 0 to 10 | User |
| +12 | Programmable controller CPU error monitoring (programmable controller CPU status monitoring) |  |  | Specifies whether to monitor programmable controller CPU errors. <br> - 0: Not monitored <br> - 1: Monitored | 0, 1 | User |
| +13 | Programm able controller CPU monitoring setting | Device co |  | Specifies the code of the device to be monitored. <br> - 0: Not monitored <br> - Other than 0: Monitored (code of the device to be monitored) | 0 or more | User |
| +14, +15 | 1st piece (1st block) | Start devic | monitored | Specifies the start device monitored of this block. | 0 or more | User |
| +16 |  | Number points | registered | Specifies the number of registered, or read, points of this block. For a bit device, specify the number of points in units of words. <br> - 0: Device not monitored <br> - 1 or more: Number of registered points | 0 or more | User |
| +17 |  | Sending upon condition | Monitoring condition | Specifies the monitoring condition for this block. <br> - 0 : Not specified (when sending with a fixed cycle) <br> - 1 or more: Monitoring conditions | Refer to the following. <br> [] MELSEC iQ- | User |
| +18 |  | matching | Monitoring condition value | Specifies the monitoring condition value for this block. Specify 0 for sending with a fixed cycle. <br> - 0 or more: Monitoring conditions | R Serial Communication Module User's Manual (Application) | User |
| +19 |  |  | User frame output start pointer | Specifies the start pointer of the table where the user frame numbers for sending upon condition matching have been set. <br> - 0 : Not specified (when sending with a fixed cycle) <br> - 1 to 100: Start pointer | 0, 1 to 100 | User |
| +20 |  |  | Number of user frames sent | Specifies the number of user frames that are sent, or output, upon condition matching. <br> - 0 : Not specified (when sending with a fixed cycle) <br> - 1 to 100: Amount of data sent | 0, 1 to 100 | User |
| +21 | For system |  |  | Use prohibited | - | - |


| Operand: (s2) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Item |  |  | Description | Setting range | Set by |
| $\begin{aligned} & \text { +22 to } \\ & +102 \end{aligned}$ | 2nd to 10th piece of programmable controller CPU monitoring setting (2nd to 10th block) |  |  | Same row as 1st piece of programmable controller CPU monitoring setting | - | User |
| +103 | CPU status monitoring setting (error monitoring 11th piece) (11th block) | Sending upon condition matching | Fixed value | Specifies the fixed value if the CPU status is monitored. | 1 | User |
| +104 |  |  |  |  | 0 | User |
| +105 |  |  |  |  | 0 | User |
| +106 |  |  |  |  | 1 | User |
| +107 |  |  |  |  | 5 | User |
| +108 |  |  |  |  | 1 | User |
| +109 |  |  | User frame output start pointer | Specifies the start pointer of the table where the user frame numbers for sending upon condition matching have been set. <br> - 0 : Not specified (when sending with a fixed cycle) <br> - 1 to 100: Start pointer | 0, 1 to 100 | User |
| +110 |  |  | Number of user frames sent | Specifies the number of user frames that are sent, or output, upon condition matching. <br> - 0 : Not specified (when sending with a fixed cycle) <br> - 1 to 100: Amount of data sent | 0, 1 to 100 | User |
| +111 | For system |  |  | Use prohibited | - | - |

- When programmable controller CPU monitoring is canceled

| Operand: (s2) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +0 | Execution type | Specifies "0." | 0 | User |
| +1 | Completion status | Used to store the completion status. <br> - 0: Normal <br> - Other than 0: Error (error code) | - | System |
| +2 | Request type | Specifies the contents of request. <br> - 3: Cancels programmable controller CPU monitoring. | 3 | User |
| +3 to +111 | For system | Use prohibited (not available also for other applications such as programming) | - | System |

## Processing details

- To perform programmable controller CPU monitoring registration, register the data that is used for the module to execute the CPU module function. Once the data used to execute the programmable controller CPU monitoring function has completed successfully, the module starts monitoring the programmable controller CPU and sending the send result to the external device.
- To cancel programmable controller CPU monitoring, stop the programmable controller CPU monitoring processing that is being executed by the module. Once programmable controller CPU monitoring has been canceled successfully, the module stops operation of the programmable controller CPU monitoring function.
- If device memory is monitored, a maximum of 10 blocks can be specified for the word and bit devices. To register the device memory to be monitored, specify the word device blocks corresponding to the number of registered word blocks before specifying the bit device blocks corresponding to the registered bit blocks.
- To send the CPU monitoring result to the external device, register the user frames and the user frame numbers using the engineering tool.
- To perform programmable controller CPU monitoring registration again, cancel programmable controller CPU monitoring before registration.
- Normal/error completion of the ZP.CSET instruction can be checked with the completion device (d2) and the completion status indication device (d2)+1.
- Completion device (d2)

Turns on during END processing of the scan that arises upon completion of the ZP.CSET instruction, and turns off during the next END processing.

- Completion status indication device (d2)+1

Turns on or off depending on the status resulting from completion of the ZP.CSET instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on during END processing of the scan that arises upon completion of the ZP.CSET instruction ended, and turns off during the next END processing.

- If the programmable controller CPU monitoring registration is to be performed, the following data may be specified in the control data only if the communication protocol setting for the target interface is nonprocedural protocol.
- User frame output start pointer for sending with a fixed cycle/sending upon condition matching
- Number of user frames sent for sending with a fixed cycle/sending upon condition matching

When the communication setting for the target interface is MC protocol, specifying the user frame output start pointer or the number of user frames sent is not required. (They will be ignored if specified).

- The following figures show how the ZP.CSET instruction operates when its execution completes.



## Precautions

- The following table summarizes the processes that take place if another instruction is issued while the ZP.CSET instruction is active or the ZP.CSET instruction is issued while another instruction is active with the same channel.

| Instruction to execute concurrently* ${ }^{*}$ | Possibility of concurrent execution | Handling for concurrent execution |
| :---: | :---: | :---: |
| G(P).ONDEMAND | $\times$ | A dedicated instruction concurrent execution error (7FFOH) occurs in the second instruction. <br> Concurrent execution is, however, possible if they use different channels. |
| G(P).OUTPUT |  |  |
| G(P).PRR |  |  |
| G(P).BIDOUT |  |  |
| G(P).BIDIN |  |  |
| G.INPUT | See the right | - If the G.INPUT and ZP.CSET instructions are issued in this order <br> A dedicated instruction concurrent execution error (7FFOH) occurs in the ZP.CSET instruction. <br> Concurrent execution is, however, possible if they use different channels. <br> - If the ZP.CSET and G.INPUT instructions are issued in this order Concurrent execution is possible. |
| ZP.CSET | $\times$ | The instruction is ignored and does not start processing until the active instruction completes. <br> Concurrent execution is, however, possible if they use different channels. |
| G(P).PUTE | $\bigcirc$ | - |
| G(P).GETE |  |  |
| Z.BUFRCVS |  |  |
| G(P).SPBUSY |  |  |
| ZP.UINI | $\times$ | A dedicated instruction concurrent execution error (7FFOH) occurs in the second instruction. |

*1 The following instructions are not available with the same channel as for the ZP.CSET instruction because they use a communication protocol different from the one used by this instruction. In addition, if these are used with the same channel as for the ZP.CSET instruction, a communication protocol setting error (7FF2H) will occur.

- $G(P)$.CPRTCL, $G(P)$.BIDOUT, and $G(P)$.BIDIN instructions
- The local device and the file register for each program are not available for setting data.


## Operation error

| Error code <br> $((\mathbf{s} 2)+1)$ | Description |
| :--- | :--- |
| 7000 H to 7FFFH | D C MELSEC iQ-R Serial Communication Module User's Manual (Application) |

The programmable controller CPU monitoring settings (control data (S2)+13 to (S2)+102) are checked, not when the CSET instruction is executed, when the specified cycle time has elapsed.
If the CSET instruction is completed successfully and the registered monitoring data is not sent from the serial communication module within the specified cycle time, check the programmable controller CPU monitoring function execution results (buffer memory address: $2205 \mathrm{H} / 2305 \mathrm{H}$ ) to check for errors and troubleshoot.

## 17．12 Initial Setting

## ZP．CSET

This instruction sets the unit（word or byte）of the amount of send／receive data and the data communication area．

| Ladder |  |  |  |  |  | ST | T |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  |  |  | NO：＝ZP＿CSET（EN，U，s1，s2，d1，d2）； |
|  | （U） | （s1） | （s2） | （d1） | (d2) |  |  |

FBD／LD

| ■－－－$]$ |  |
| :---: | :---: |
| EN | ENO |
| U | d1 |
| s1 | d2 |
| s2 |  |

## ■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| ZP．CSET | $\uparrow$ |

Setting data

## Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （U） | Start I／O number（first three digits in four－digit hexadecimal <br> representation）of a module | 00 H to FEH | String | ANY16＿OR＿STRING <br> ＿SINGLE |
| （s1） | Channel number of the channel for which the set value is <br> changed． <br> $-1:$ Channel（CH1 side） <br> $\cdot 2: ~ C h a n n e l ~(C H 2 ~ s i d e) ~$ | 1,2 | 16－bit unsigned binary | ANY16 |
| （s2） | Start device containing the control data | Refer to the control data． | Device name | ANY16 |
| （d1） | Dummy | - | Device name | ANY16 |
| （d2） | Device to be turned on one scan upon completion of <br> instruction <br> （d2）＋1 also turns on when the instruction completes with <br> an error． | - | Bit | ANYBIT＿ARRAY <br> （Number of elements： <br> 2） |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロロロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3EDI（H）Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { I } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| $(\mathrm{U})^{* 1}$ | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ |
| （s1） | $0^{* 2}$ | － | $0^{* 3}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | － |
| （s2） | － | － | $0^{* 3}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d1） | － | － | $0^{* 3}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d2） | $\mathrm{O}^{*}$ | － | $\mathrm{O}^{* 3}$ | － | － | － | － | － | － | － | － | － |

＊1 Index modification is not available．
＊2 FX and FY cannot be used．
＊3 FD cannot be used．

Control data

| Operand: (s2) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +0 | Execution type | Specifies 0. | 0 | User |
| +1 | Completion status | Used to store the completion status. <br> - 0: Normal <br> - Other than 0: Error (error code) | - | System |
| +2 | Request type | Specifies the contents of request. <br> - 1: Changes the unit of data (word or byte) and the buffer memory assignment. | 1 | User |
| +3 | Word/byte specification | Specifies the unit of the amount of send/receive data. <br> - 0: Current set value <br> - 1: In units of words <br> - 2: In units of bytes | 0 to 2 | User |
| +4 | Buffer memory start address for on-demand function | Specifies the start address of the buffer memory area to be used for the on-demand function. <br> - 0 : The current set value is used. <br> - 400 H to $1 \mathrm{AFFH}, 2600 \mathrm{H}$ to 3 FFFH: Start address | $0 \mathrm{H}, 400 \mathrm{H}$ to 1AFFH, 2600H to 3 FFFH | User |
| +5 | Size of buffer memory for on-demand function | Specifies the size (words) of the buffer memory area to be used for the on-demand function. <br> - 0 : The current set value is used. <br> - 1 H to 1 A 00 H : Size | $0 \mathrm{H}, 1 \mathrm{H}$ to 1A00H | User |
| +6 | Sending area start address | Specifies the start address of the sending area to be used for the nonprocedural or bidirectional protocol. <br> - 0 : The current set value is used. <br> - 400 H to $1 \mathrm{AFFH}, 2600 \mathrm{H}$ to 3 FFFH: Start address | $0 \mathrm{H}, 400 \mathrm{H}$ to 1AFFH, 2600H to 3FFFH | User |
| +7 | Size of sending area | Specifies the size (words) of the sending area to be used for the nonprocedural or bidirectional protocol. The one word start area of the sending area is used for specifying the amount of send data. <br> - 0 : The current set value is used. <br> - 1H to 1A00H: Size | $0 \mathrm{H}, 1 \mathrm{H}$ to 1 A 00 H | User |
| +8 | Receiving area start address | Specifies the start address of the receiving area to be used for the nonprocedural or bidirectional protocol. <br> - 0 : The current set value is used. <br> - 400 H to $1 \mathrm{AFFH}, 2600 \mathrm{H}$ to 3 FFFH: Start address | $0 \mathrm{H}, 400 \mathrm{H}$ to 1AFFH, 2600H to 3 FFFH | User |
| +9 | Size of receiving area | Specifies the size (words) of the receiving area to be used for the nonprocedural or bidirectional protocol. <br> The one word start area of the receiving area is used for specifying the amount of send data. <br> - OH : The current set value is used. <br> - 1 H to 1 A 00 H : Size | $0 \mathrm{H}, 1 \mathrm{H}$ to 1A00H | User |
| $\begin{aligned} & \text { +10 to } \\ & +111 \end{aligned}$ | For system | Use prohibited (not available also for other applications such as programming) | - | System |

## Processing details

- The following current set values are changed, which are used for data communication with each communication protocol.
- Unit of the amount of the data to be sent or received (word or byte)
- Sending area of the buffer memory to be used for the MC protocol on-demand function
- Sending or receiving area of the buffer memory to be used for the nonprocedural protocol
- Sending or receiving area of the buffer memory to be used for the bidirectional protocol
- To change any of the above set values from the CPU module, execute the ZP.CSET instruction. In addition, execute it one scan before the start of data communication or earlier. After the start of data communication, the ZP.CSET instruction cannot be executed, therefore, the set value cannot be changed. The system does not allow concurrent execution of two or more ZP.CSET instructions set to perform the initial setting.
- Normal/error completion of the ZP.CSET instruction can be checked with the completion device (d2) and the completion status indication device (d2)+1.
- Completion device (d2)

Turns on during END processing of the scan that arises upon completion of the ZP.CSET instruction, and turns off during the next END processing.

- Completion status indication device (d2)+1

Turns on or off depending on the status resulting from completion of the ZP.CSET instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on during END processing of the scan that arises upon completion of the ZP.CSET instruction ended, and turns off during the next END processing.

- The following figures show how the ZP.CSET instruction operates when its execution completes.



## Precautions

- The following table summarizes the processes that take place if another instruction is issued while the ZP.CSET instruction is active or the ZP.CSET instruction is issued while another instruction is active with the same channel.

| Instruction to execute concurrently* ${ }^{*}$ | Possibility of concurrent execution | Handling for concurrent execution |
| :---: | :---: | :---: |
| G(P).ONDEMAND | $\times$ | A dedicated instruction concurrent execution error (7FFOH) occurs in the second instruction. <br> Concurrent execution is, however, possible if they use different channels. |
| G(P).OUTPUT |  |  |
| G(P).PRR |  |  |
| G(P).BIDOUT |  |  |
| G(P).BIDIN |  |  |
| G.INPUT | See the right | - If the G.INPUT and ZP.CSET instructions are issued in this order <br> A dedicated instruction concurrent execution error (7FFOH) occurs in the ZP.CSET instruction. <br> Concurrent execution is, however, possible if they use different channels. <br> - If the ZP.CSET and G.INPUT instructions are issued in this order Concurrent execution is possible. |
| ZP.CSET | $\times$ | The instruction is ignored and does not start processing until the active instruction completes. <br> Concurrent execution is, however, possible if they use different channels. |
| G(P).PUTE | $\bigcirc$ | - |
| G(P).GETE |  |  |
| Z.BUFRCVS |  |  |
| G(P).SPBUSY |  |  |
| ZP.UINI | $\times$ | A dedicated instruction concurrent execution error (7FFOH) occurs in the second instruction. |

*1 The $G(P)$.CPRTCL instruction is not available with the same channel as for the $Z(P)$.CSET instruction because it uses a communication protocol different from the one used by the ZP.CSET instruction. If the above instruction is used with the same channel as for the $\mathrm{G}(\mathrm{P})$.CPRTCL instruction, a communication protocol setting error (7FF2H) will occur.

- The local device and the file register for each program are not available for setting data.


## Operation error

| Error code <br> $((\mathbf{s 2})+1)$ | Description |
| :--- | :--- |
| 7000 H to 7FFFH | LD MELSEC iQ-R Serial Communication Module User's Manual (Application) |

## 17．13 Registering User Frames

## G（P）．PUTE

These instructions register user frames．


FBD／LD

| ■－－－$\square$ |  |
| :---: | :---: |
| EN | ENO |
| U | d |
| s1 |  |
| s2 |  |

## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| G．PUTE | - |
|  | $\boxed{T}$ |
| GP．PUTE | - |

## Setting data

## DDescription，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （U） | Start I／O number（first three digits in four－digit hexadecimal <br> representation）of a module | 00 H to FEH | 16－bit unsigned binary | ANY16 |
| （s1） | Start device containing the control data | Refer to the control data． | Device name | ANY16 |
| （s2） | Start device for storing the registered data | - | Device name | ANY16 |
| （d） | Device to be turned on one scan upon completion of <br> instruction <br> If the instruction is completed with an error，（d）＋1 is also <br> turned on． | - | Bit | ANYBIT＿ARRAY <br> （Number of elements： <br> $2)$ |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \mathrm{X}, \mathrm{Y}, \mathrm{M}, \mathrm{~L}, \mathrm{SM}, \\ & \mathrm{~F}, \mathrm{~B}, \mathrm{SB}, \mathrm{FX}, \mathrm{FY} \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JपID， U3EDI（H）G口 | Z | LT, LST, LC | LZ |  | K，H | E | \＄ |  |
| （U）＊${ }^{*}$ | － | － | $\mathrm{O}^{* 3}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ |
| （s1） | － | － | $\bigcirc{ }^{*}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc{ }^{*} 4$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | $0^{* 2}$ | － | $\bigcirc{ }^{* 5}$ | － | － | － | － | － | － | － | － | － |

＊1 Index modification is not available．
＊2 FX and FY cannot be used．
＊3 RD cannot be used．
＊4 FD cannot be used．
＊5 T，ST，C，and FD cannot be used．

| Operand: (s1) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +0 | Registration/delete specification | Specifies whether to register or delete the user frame specified by (s1)+2. <br> - 1: Registered <br> - 3: Deleted | 1,3 | User |
| +1 | Registration/delete result | Used to store the result of registration or deletion with the G(P).PUTE instruction. <br> - 0: Normal <br> - Other than 0: Error (error code) | - | System |
| +2 | Frame number | Specifies the user frame number. | 1000 to 1199 | User |
| +3 | Number of registered bytes | - 1 to 80 : Number of bytes of the user frame to be registered To delete the user frame, specify a value from 1 to 80 as the dummy. | 1 to 80 | User |

## Processing details

- User frames are registered or deleted for the module specified by (U).
- If a user frame is registered
- To register a user frame, specify 1 for the device of ( $s 1$ 1) +0 . The data in the device specified by ( $s 2$ ) and later will be registered according to the control data
- The registered data must be stored as follows in the device specified by ( s 2 ) and later. Each of the devices to store the registered data requires the data for the following number of points from the device specified by ( s 2 ): (amount of registered data)/2.

- If a user frame is deleted
- To delete a user frame, specify " 3 " for the device of $(\mathrm{s} 1)+0$. The user frame with the frame number specified by ( s 1 ) +2 will be deleted.
- Both the number of registered bytes specified by ( s 1$)+3$ and the registered data storage device are required for the $G(P)$.PUTE instruction format although they are not used by the $G(P)$.PUTE instruction. Specify a value from 1 to 80 for ( s 1 ) +3 . Specify a dummy device for (s2)
- Normal/error completion of the G(P).PUTE instruction can be checked with the completion device (d) and the completion status indication device (d)+1.
- Completion device (d)

Turns on during END processing of the scan that arises upon completion of the $G(P)$.PUTE instruction, and turns off during the next END processing

- Completion status indication device (d)+1

Turns on or off depending on the status resulting from completion of the G(P).PUTE instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on during END processing of the scan that arises upon completion of the $\mathrm{G}(\mathrm{P})$.PUTE instruction, and turns off during the next END processing.

- The following figure shows how the G(P).PUTE instruction operates when completing its execution.



## Precautions

- The following table summarizes the processes that take place if another instruction is issued while the $G(P)$.PUTE instruction is active or the $G(P)$.PUTE instruction is issued while another instruction is active with the same channel.

| Instruction to execute concurrently | Possibility of concurrent execution | Handling for concurrent execution |
| :---: | :---: | :---: |
| G(P).ONDEMAND | $\bigcirc$ | - |
| G(P).OUTPUT |  |  |
| G(P).PRR |  |  |
| G(P).BIDOUT |  |  |
| G(P).BIDIN |  |  |
| G.INPUT |  |  |
| ZP.CSET |  |  |
| G(P).PUTE | $\times$ | A dedicated instruction concurrent execution error (7FFOH) occurs in the second instruction. |
| G(P).GETE | $\times$ | The instruction is ignored and does not start processing until the active instruction completes. |
| Z.BUFRCVS | $\bigcirc$ | - |
| G(P).SPBUSY |  |  |
| ZP.UINI | $\times$ | A dedicated instruction concurrent execution error (7FFOH) occurs in the second instruction. |
| G(P).CPRTCL | $\bigcirc$ | - |

Operation error

| Error code <br> $((\mathbf{s} 1)+1)$ | Description |
| :--- | :--- |
| 7000 H to 7FFFH | LD MELSEC iQ-R Serial Communication Module User's Manual (Application) |

## 17．14 Reading User Frames

## G（P）．GETE

These instructions read user frames．


## ■xecution condition

| Instruction | Execution condition |
| :--- | :--- |
| G．GETE | - |
|  | $\boxed{T}$ |
| GP．GETE | - |

## Setting data

## DDescription，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （U） | Start I／O number（first three digits in four－digit hexadecimal <br> representation）of a module | 00 H to FEH | 16 －bit unsigned binary | ANY16 |
| （s1） | Start device containing the control data | Refer to the control data． | Device name | ANY16 |
| （s2） | Start device for storing the registered data that has been <br> read． | - | Device name | ANY16 |
| （d） | Device to be turned on one scan upon completion of <br> instruction <br> If the instruction is completed with an error，（d）+1 is also <br> turned on． | - | Bit | ANYBIT＿ARRAY <br> （Number of elements： <br> $2)$ |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | J미민 | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UロIGロ，JロIロ， U3E미（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| $(\mathrm{U})^{* 1}$ | － | － | $\mathrm{O}^{* 3}$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ |
| （s1） | － | － | $\bigcirc{ }^{*} 4$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （s2） | － | － | $\bigcirc{ }^{*}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | $\bigcirc^{* 2}$ | － | $\bigcirc^{* 5}$ | － | － | － | － | － | － | － | － | － |

＊1 Index modification is not available．
＊2 FX and FY cannot be used．
＊3 RD cannot be used．
＊4 FD cannot be used．
＊5 T，ST，C，and FD cannot be used．

| Operand: (s1) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +0 | Dummy | - | 0 | - |
| +1 | Result of reading | Used to store the result of sending with the $G(P)$.GETE instruction. <br> - 0: Normal <br> - Other than 0: Error (error code) | - | System |
| +2 | Specified frame number | Specifies the user-registered number. | 1000 to 1199 | User |
| +3 | Allowable number of bytes | Specifies the number of bytes storable in (s2), of the registered data of the read user frame. | 1 to 80 | User |
|  | Number of registered bytes | Used to store the number of bytes of the registered data of the read user frame. <br> - 1 to 80 : Number of bytes of registered data | - | System |

## Processing details

- The data of the user frame is read from the module specified by (U).

- Normal/error completion of the G(P).GETE instruction can be checked with the completion device (d) and the completion status indication device (d)+1.
- Completion device (d)

Turns on during END processing of the scan that arises upon completion of the $G(P)$.GETE instruction, and turns off during the next END processing.

- Completion status indication device (d)+1

Turns on or off depending on the status resulting from completion of the $G(P)$.GETE instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on during END processing of the scan that arises upon completion of the $G(P)$.GETE instruction, and turns off during the next END processing.

- The following figure shows how the $G(P) . G E T E$ instruction operates when completing its execution.



## Precautions

- The following table summarizes the processes that take place if another instruction is issued while the $G(P)$.GETE instruction is active or the $G(P)$.GETE instruction is issued while another instruction is active with the same channel.

| Instruction to execute concurrently | Possibility of concurrent execution | Handling for concurrent execution |
| :---: | :---: | :---: |
| G(P).ONDEMAND | $\bigcirc$ | - |
| G(P).OUTPUT |  |  |
| G(P).PRR |  |  |
| G(P).BIDOUT |  |  |
| G(P).BIDIN |  |  |
| G.INPUT |  |  |
| ZP.CSET |  |  |
| G(P).PUTE | $\times$ | A dedicated instruction concurrent execution error (7FFOH) occurs in the second instruction. |
| G(P).GETE | $\times$ | The instruction is ignored and does not start processing until the active instruction completes. |
| Z.BUFRCVS | $\bigcirc$ | - |
| G(P).SPBUSY |  |  |
| ZP.UINI | $\times$ | A dedicated instruction concurrent execution error (7FFOH) occurs in the second instruction. |
| G(P).CPRTCL | $\bigcirc$ | - |

## Operation error

| Error code <br> $((\mathbf{s} 1)+1)$ | Description |
| :--- | :--- |
| 7000 H to 7FFFH | LD MELSEC iQ-R Serial Communication Module User's Manual (Application) |

## 17．15 Switching the Mode

## ZP．UINI

This instruction changes the communication protocol，transmission setting，or station number．


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| ZP．UINI | - |
|  | - |

## Setting data

## Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （U） | Start I／O number（first three digits in four－digit hexadecimal <br> representation）of a module | 00 H to FEH | String | ANY16＿OR＿STRING <br> ＿SINGLE |
| （s） | Start device containing the control data | Refer to the control data． | Device name | ANY16 |
| （d） | Device to be turned on one scan upon completion of <br> instruction <br> If the instruction is completed with an error，（d）+1 is also <br> turned on． | - | Bit | ANYBIT＿ARRAY <br> （Number of elements： <br> $2)$ |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，J밈， U3E미（H）Gㅁ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （U）${ }^{* 1}$ | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ |
| （s） | － | － | $0^{* 3}$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | $\mathrm{O}^{*}$ | － | $\mathrm{O}^{*}$ | － | － | － | － | － | － | － | － | － |

＊1 Index modification is not available．
＊2 FX and FY cannot be used．
＊3 FD cannot be used．
＊4 T，ST，C，and FD cannot be used．

Control data

| Operand: (s) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +0 | For system | Always specifies 0 . | 0 | User |
| +1 | Execution result | Used to store the result of sending with the ZP.UINI instruction. <br> - 0: Normal <br> - Other than 0: Error (error code) | - | System |
| +2 | Execution type | Specify the execution type. <br> - 0 : Change the settings according to the setting details indicated by (s) +3 and later. <br> - 1: Restores the setting details of the user parameters that have been set in the engineering tool. | 0,1 | User |
| +3 | CH 1 transmission specifications setting | Sets the CH 1 side transmission setting. <br> Check the current value of the buffer memory area (595 (253H)) to make the settings identical. | $\begin{aligned} & 0 \text { to } 4095 \\ & (0000 \mathrm{H} \text { to } \\ & \text { OFFFH) } \end{aligned}$ | User |
| +4 | CH 1 communication protocol setting | Sets the CH 1 side communication protocol. <br> Check the current value of the buffer memory area $(594(252 \mathrm{H})$ ) to make the settings identical. | 0 to 9 | User |
| +5 | CH 2 transmission specifications setting | Sets the CH 2 side transmission setting. <br> Check the current value of the buffer memory area $(611(263 \mathrm{H})$ ) to make the settings identical. | $\begin{aligned} & 0 \text { to } 4095 \\ & \text { (0000H to } \\ & \text { OFFFH) } \end{aligned}$ | User |
| +6 | CH 2 communication protocol setting | Sets the CH 2 side communication protocol. <br> Check the current value of the buffer memory area $(610(262 \mathrm{H})$ ) to make the settings identical. | 0 to 9 | User |
| +7 | Station number setting | Sets the station number. | 0 to 31 | User |
| +8 to +12 | For system | Always specifies 0 . | 0 | User |

## Processing details

- This instruction changes the communication protocol, transmission setting, or station number of each channel of the module specified by (U).
- Normal/error completion of the ZP.UINI instruction can be checked with the completion device (d) and the completion status indication device (d)+1
- Completion device (d)

Turns on during END processing of the scan that arises upon completion of the ZP.UINI instruction, and turns off during the next END processing

- Completion status indication device (d) +1

Turns on or off depending on the status resulting from completion of the ZP.UINI instruction
When completed normally: Unchanged from off.
When completed with an error: Turns on during END processing of the scan that arises upon completion of the ZP.UINI instruction ended, and turns off during the next END processing.

- The following figures show how the ZP.UINI instruction operates when its execution completes.



## Precautions

- Stop the whole data communication with the external device before issuing the ZP.UINI instruction. If the ZP.UINI instruction
- If the ZP.UINI instruction is issued during receiving

Receiving is interrupted, and the whole receive data stored so far is discarded.

- If the ZP.UINI instruction is issued during sending

When the ZP.UINI instruction is accepted, sending is interrupted.

- If connection is provided through a modem, cut off the line to the external device before issuing the ZP.UINI instruction. If the ZP.UINI instruction is issued, the line will be cut off.
- If the communication protocol or transmission setting has been changed using the ZP.UINI instruction, the communication protocol and transmission settings of the external device side must be matched with the new settings. In addition, if the station number setting, or own station number, has been changed, the station number in the request message of the external device must be modified to the new station number.
- The following table summarizes the processes that take place if another instruction is issued while the ZP.UINI instruction is active or the ZP.UINI instruction is issued while another instruction is active with the same channel.

| Instruction to execute concurrently | Possibility of concurrent execution | Handling for concurrent execution |
| :---: | :---: | :---: |
| G(P).ONDEMAND | $\times$ | A dedicated instruction concurrent execution error (7FFOH) occurs in the second instruction. |
| G(P).OUTPUT |  |  |
| G(P).PRR |  |  |
| G(P).BIDOUT |  |  |
| G(P).BIDIN |  |  |
| G.INPUT |  |  |
| ZP.CSET |  |  |
| G(P).PUTE |  |  |
| G(P).GETE |  |  |
| G(P).CPRTCL |  |  |
| Z.BUFRCVS | $\bigcirc$ | - |
| G(P).SPBUSY |  |  |
| ZP.UINI | $\times$ | The instruction is ignored and does not start processing until the active instruction completes. |

- Before the ZP.UINI instruction is executed, the setting change in the transmission settings must be kept set to "Permitted" by using the parameter setting of the module. In addition, if the parameter is not yet set, the setting change in the transmission settings will be assumed to be "Permitted" during operation.
- Simultaneous use of mode change with the ZP.UINI instruction and that with the mode change request signal (Yn2/Yn9) must not be attempted. If it is attempted, an error will occur at an error code of 7FF5H.
- To use the current value of the communication protocol, transmission specifications, or station number as it is, acquire the value from each of the status storage areas in buffer memory before setting it in the control data.

| Address (decimal (hexadecimal)) |  | Application |
| :--- | :--- | :--- |
| CH1 | CH2 |  |
| $591(24 \mathrm{FH})$ | Station number (instruction setting) |  |
| $594(252 \mathrm{H})$ | $610(262 \mathrm{H})$ | Communication protocol status |
| $595(253 \mathrm{H})$ | $611(263 \mathrm{H})$ | Transmission status |

Operation error

| Error code ((s)+1) | Description |
| :--- | :--- |
| 7000 H to 7FFFH | C] MELSEC iQ-R Serial Communication Module User's Manual (Application) |

## 18 A／D CONVERSION INSTRUCTIONS

## 18．1 Switching the Mode

## G（P）．OFFGAN

These instructions switch the analog module mode．

| Ladder | ST |
| :---: | :---: |
| ■－$-\square$ （U） （s） | ENO：＝G＿OFFGAN（EN，U，s）； <br> ENO：＝GP＿OFFGAN（EN，U，s）； |

FBD／LD


■Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| G．OFFGAN | - |
|  | $\boxed{ }$ |
| GP．OFFGAN |  |

Setting data
Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| （U） | Start I／O number（first three digits in four－digit hexadecimal <br> representation）of a module | 00 H to FEH | 16－bit unsigned binary | ANY16 |
| （s） | Switching the mode <br> •0：Shift to normal mode（normal output mode） |  |  |  |
| •1：Shift to offset／gain setting mode <br> Setting a value outside the above range results in＂Shift to <br> offset／gain setting mode＂． | 0,1 | 16－bit unsigned binary | ANY16 |  |
| EN | Execution condition | - | Bit |  |
| ENO | Execution result | - | Bit | BOOL |

＊1 The mode name differs depending on the module used：normal output mode for the R60DA4，R60DAV8，and R60DAI8，and normal mode for other analog modules．

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indir <br> ect <br> spec <br> ifica <br> tion | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, } \\ & \text { FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD |  U3EDl（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （U） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |

## Processing details

These instructions switch the mode of an analog module.

- Normal mode (Normal output mode) $\rightarrow$ Offset/gain setting mode (The offset/gain setting mode status flag (XA) turns on.)
- Offset/gain setting mode $\rightarrow$ Normal mode (Normal output mode) (The offset/gain setting mode status flag (XA) turns off.)


## Point $\rho$

- When the mode shifts from normal or normal output to offset/gain setting, Module Ready (X0) changes on to off.
- When the mode shifts from offset/gain setting to normal or normal output, Module Ready (X0) changes off to on. If a program exists that performs initial setting with Module Ready (X0) on, note that initial setting processing will be executed.
- When the mode shifts from offset/gain setting to normal on the A/D converter module and temperature input module, it automatically resumes the operation with the previous operating conditions.
- When the D/A converter module shifts from the offset/gain setting mode to the normal mode, all channels enter in the D/A conversion disabled state. To resume D/A conversion, set " 0 " (D/A conversion enabled) in "CHロD/A conversion enabled/disabled setting" of the appropriate channel before turning on and off Operating condition setting request (Y9).


## Program example

The following figure shows an example of a program which uses the G(P).OFFGAN instruction with the R60AD4.

- Program content


## Description

When "Mode Shift Request" (M10) is turned on, the A/D converter module mounted at the position of I/O numbers $\mathrm{X} / \mathrm{Y} 10$ to $\mathrm{X} / \mathrm{Y} 1 \mathrm{~F}$ shifts to the offset/gain setting mode.
When "Mode Shift Request" (M10) is turned off, the module returns to the normal mode.

- Label setting

- Program example

(16) Shifts to offset/gain setting mode.
(77) Describes the offset/gain setting processing.
(111) Shifts to normal mode.
(172) Describes the normal mode processing.


## Precautions

In the following cases, the $G(P)$.OFFGAN instruction will be disabled.

- The module has been set as a target of synchronization
- The wave output mode has been selected on the D/A converter module.


## Operation error

There is no operation error.

### 18.2 Reading the User Range Setting Values

## G(P).OGLOAD

These instructions read the offset/gain setting values of the user range settings of an analog module into the CPU module.
Ladder

ST
ENO:=G_OGLOAD(EN,U,s,d); ENO:=GP_OGLOAD(EN,U,s,d);

FBD/LD


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| G.OGLOAD | - |
|  | $\boxed{ }$ |
| GP.OGLOAD | - |

Setting data
Description, range, data type

| Operand | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- |
| (U) | Start I/O number (first three digits in four-digit hexadecimal <br> representation) of a module | 00 H to FEH | 16-bit unsigned binary | ANY16 |
| (s) | Start device where the control data is stored | Predefined devices | Device name | ANY16 |
| (d) | Device to be turned on one scan when instruction <br> processing completes. <br> If the instruction is completed with an error, (d)+1 is also <br> turned on. | Predefined devices | Bit | ANYBIT_ARRAY <br> (Number of elements: <br> $2)$ |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

## -Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> (U) |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | U $\square$ IG $\square$, J $\square$ ID, U3ED(H)GD | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K, H | E | \$ |  |
| (U) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | $\bigcirc$ | - | - | $\bigcirc$ |
| (s) | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (d) | $\bigcirc$ | - | - | - | - | - | - | - | - | - | - | - |

## Control data

- For R60AD4

Set only the save data type setting (s)+2. A data write to an area to be set by the system does not result in a normal read of the offset/gain setting values.

| Operand: (s) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +0 | System area | - | - | - |
| +1 | Completion status | The instruction completion status is stored. <br> - 0: Completed successfully <br> - Other than 0: Completed with an error (error code) | - | System |
| +2 | Save data type setting | Specifies which one of the voltage and current is read as the offset/gain setting value. <br> - 0: Voltage specification <br> - 1: Current specification | 0000 H to 000FH | User |
| +3 | System area | - | - | - |
| +4 | CH1 Factory default setting offset value (L) | - | - | System |
| +5 | CH1 Factory default setting offset value (H) | - | - | System |
| +6 | CH1 Factory default setting gain value (L) | - | - | System |
| +7 | CH1 Factory default setting gain value (H) | - | - | System |
| +8 | CH2 Factory default setting offset value (L) | - | - | System |
| +9 | CH 2 Factory default setting offset value (H) | - | - | System |
| +10 | CH2 Factory default setting gain value (L) | - | - | System |
| +11 | CH 2 Factory default setting gain value (H) | - | - | System |
| +12 | CH3 Factory default setting offset value (L) | - | - | System |
| +13 | CH3 Factory default setting offset value (H) | - | - | System |
| +14 | CH3 Factory default setting gain value (L) | - | - | System |
| +15 | CH3 Factory default setting gain value (H) | - | - | System |
| +16 | CH4 Factory default setting offset value (L) | - | - | System |
| +17 | CH4 Factory default setting offset value (H) | - | - | System |
| +18 | CH4 Factory default setting gain value (L) | - | - | System |
| +19 | CH4 Factory default setting gain value (H) | - | - | System |
| +20 | CH 1 User range setting offset value (L) | - | - | System |
| +21 | CH 1 User range setting offset value (H) | - | - | System |
| +22 | CH 1 User range setting gain value (L) | - | - | System |
| +23 | CH 1 User range setting gain value (H) | - | - | System |
| +24 | CH2 User range setting offset value (L) | - | - | System |
| +25 | CH 2 User range setting offset value (H) | - | - | System |
| +26 | CH 2 User range setting gain value (L) | - | - | System |
| +27 | CH 2 User range setting gain value (H) | - | - | System |
| +28 | CH3 User range setting offset value (L) | - | - | System |
| +29 | CH 3 User range setting offset value (H) | - | - | System |
| +30 | CH3 User range setting gain value (L) | - | - | System |
| +31 | CH3 User range setting gain value (H) | - | - | System |
| +32 | CH 4 User range setting offset value (L) | - | - | System |
| +33 | CH 4 User range setting offset value (H) | - | - | System |
| +34 | CH4 User range setting gain value (L) | - | - | System |
| +35 | CH 4 User range setting gain value (H) | - | - | System |

## - For R60DA4

Set only the save data type setting (s)+2. A data write to an area to be set by the system does not result in a normal read of the offset/gain setting values.

| Operand: (s) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +0 | System area | - | - | - |
| +1 | Completion status | The instruction completion status is stored. <br> - 0: Completed successfully <br> - Other than 0: Completed with an error (error code) | - | System |
| +2 | Save data type setting | Specify the user range setting for reading offset/gain setting values. <br> - 0 : User range setting (voltage specification) <br> - 1: User range setting (current specification) | 0000H to 000FH | User |
| +3 | System area | - | - | - |
| +4 | CH1 Factory default setting offset value | - | - | System |
| +5 | CH1 Factory default setting gain value | - | - | System |
| +6 | CH2 Factory default setting offset value | - | - | System |
| +7 | CH 2 Factory default setting gain value | - | - | System |
| +8 | CH3 Factory default setting offset value | - | - | System |
| +9 | CH3 Factory default setting gain value | - | - | System |
| +10 | CH4 Factory default setting offset value | - | - | System |
| +11 | CH4 Factory default setting gain value | - | - | System |
| +12 | CH1 User range setting offset value | - | - | System |
| +13 | CH1 User range setting gain value | - | - | System |
| +14 | CH2 User range setting offset value | - | - | System |
| +15 | CH2 User range setting gain value | - | - | System |
| +16 | CH3 User range setting offset value | - | - | System |
| +17 | CH3 User range setting gain value | - | - | System |
| +18 | CH4 User range setting offset value | - | - | System |
| +19 | CH4 User range setting gain value | - | - | System |

- For R60ADV8, R60ADI8, R60AD8-G

| Operand: (s) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +0 | System area | - | - | - |
| +1 | Completion status | The instruction completion status is stored. <br> - 0: Completed successfully <br> - Other than 0: Completed with an error (error code) | - | System |
| +2 | Save data type setting*1 | Specifies which one of the voltage and current is read as the offset/gain setting value. <br> - 0: Voltage specification <br> - 1: Current specification | 0000H to 00FFH | User |
| +3 | System area | - | - | - |
| +4 | CH1 Factory default setting offset value (L) | - | - | System |
| +5 | CH1 Factory default setting offset value (H) | - | - | System |
| +6 | CH1 Factory default setting gain value (L) | - | - | System |
| +7 | CH1 Factory default setting gain value (H) | - | - | System |
| +8 | CH2 Factory default setting offset value (L) | - | - | System |
| +9 | CH2 Factory default setting offset value (H) | - | - | System |
| +10 | CH2 Factory default setting gain value (L) | - | - | System |
| +11 | CH2 Factory default setting gain value (H) | - | - | System |
| +12 | CH3 Factory default setting offset value (L) | - | - | System |
| +13 | CH3 Factory default setting offset value (H) | - | - | System |
| +14 | CH3 Factory default setting gain value (L) | - | - | System |
| +15 | CH3 Factory default setting gain value (H) | - | - | System |
| +16 | CH4 Factory default setting offset value (L) | - | - | System |
| +17 | CH4 Factory default setting offset value (H) | - | - | System |
| +18 | CH4 Factory default setting gain value (L) | - | - | System |
| +19 | CH4 Factory default setting gain value (H) | - | - | System |
| +20 | CH5 Factory default setting offset value (L) | - | - | System |
| +21 | CH5 Factory default setting offset value (H) | - | - | System |
| +22 | CH5 Factory default setting gain value (L) | - | - | System |
| +23 | CH5 Factory default setting gain value (H) | - | - | System |
| +24 | CH6 Factory default setting offset value (L) | - | - | System |
| +25 | CH6 Factory default setting offset value (H) | - | - | System |
| +26 | CH6 Factory default setting gain value (L) |  | - | System |
| +27 | CH6 Factory default setting gain value (H) | - | - | System |
| +28 | CH7 Factory default setting offset value (L) | - | - | System |
| +29 | CH7 Factory default setting offset value (H) | - | - | System |
| +30 | CH7 Factory default setting gain value (L) | - | - | System |
| +31 | CH7 Factory default setting gain value (H) | - | - | System |
| +32 | CH8 Factory default setting offset value (L) | - | - | System |
| +33 | CH8 Factory default setting offset value (H) | - | - | System |
| +34 | CH8 Factory default setting gain value (L) | - | - | System |
| +35 | CH8 Factory default setting gain value (H) | - | - | System |
| +36 | CH1 User range setting offset value (L) | - | - | System |
| +37 | CH 1 User range setting offset value (H) | - | - | System |
| +38 | CH1 User range setting gain value (L) | - | - | System |
| +39 | CH 1 User range setting gain value (H) | - | - | System |
| +40 | CH2 User range setting offset value (L) | - | - | System |
| +41 | CH 2 User range setting offset value (H) | - | - | System |
| +42 | CH 2 User range setting gain value (L) | - | - | System |
| +43 | CH 2 User range setting gain value (H) | - | - | System |
| +44 | CH3 User range setting offset value (L) | - | - | System |

18 A/D CONVERSION INSTRUCTIONS 18.2 Reading the User Range Setting Values

| Operand: (s) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +45 | CH3 User range setting offset value (H) | - | - | System |
| +46 | CH 3 User range setting gain value (L) | - | - | System |
| +47 | CH3 User range setting gain value (H) | - | - | System |
| +48 | CH4 User range setting offset value (L) | - | - | System |
| +49 | CH 4 User range setting offset value (H) | - | - | System |
| +50 | CH 4 User range setting gain value (L) | - | - | System |
| +51 | CH4 User range setting gain value (H) | - | - | System |
| +52 | CH5 User range setting offset value (L) | - | - | System |
| +53 | CH5 User range setting offset value (H) | - | - | System |
| +54 | CH5 User range setting gain value (L) | - | - | System |
| +55 | CH5 User range setting gain value (H) | - | - | System |
| +56 | CH6 User range setting offset value (L) | - | - | System |
| +57 | CH6 User range setting offset value (H) | - | - | System |
| +58 | CH6 User range setting gain value (L) | - | - | System |
| +59 | CH6 User range setting gain value (H) | - | - | System |
| +60 | CH7 User range setting offset value (L) | - | - | System |
| +61 | CH7 User range setting offset value (H) | - | - | System |
| +62 | CH7 User range setting gain value (L) | - | - | System |
| +63 | CH 7 User range setting gain value (H) | - | - | System |
| +64 | CH8 User range setting offset value (L) | - | - | System |
| +65 | CH8 User range setting offset value (H) | - | - | System |
| +66 | CH8 User range setting gain value (L) | - | - | System |
| +67 | CH8 User range setting gain value (H) | - | - | System |

*1 For the R60ADV8 and the R60ADI8, this area corresponds to System area and so is not available.

- For R60DAV8, R60DAI8, R60DA8-G

| Operand: (s) |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description |  |  |  |  |  |  |  | Setting range | Set by |
| +0 | System area | - |  |  |  |  |  |  |  | - | - |
| +1 | Completion status | The instruction completion status is stored. <br> - 0: Completed successfully <br> - Other than 0: Completed with an error (error code) |  |  |  |  |  |  |  | - | System |
| +2 | Save data type setting*2 | Specify the user range setting for reading offset/gain setting values. <br> - OH : User range setting 1 (current specification) <br> - 1 H : User range setting 2 (voltage specification) <br> - 2 H : User range setting 3 (voltage specification) <br>  |  |  |  |  |  |  |  | $0000 \mathrm{H} \text { to }$ <br> AAAAH | User |
| +3 | System area | - |  |  |  |  |  |  |  | - | - |
| +4 | CH1 Factory default setting offset value | - |  |  |  |  |  |  |  | - | System |
| +5 | CH1 Factory default setting gain value | - |  |  |  |  |  |  |  | - | System |
| +6 | CH2 Factory default setting offset value | - |  |  |  |  |  |  |  | - | System |
| +7 | CH 2 Factory default setting gain value | - |  |  |  |  |  |  |  | - | System |
| +8 | CH3 Factory default setting offset value | - |  |  |  |  |  |  |  | - | System |
| $+9$ | CH3 Factory default setting gain value | - |  |  |  |  |  |  |  | - | System |
| +10 | CH4 Factory default setting offset value | $-$ |  |  |  |  |  |  |  | - | System |
| +11 | CH4 Factory default setting gain value | - |  |  |  |  |  |  |  | - | System |
| +12 | CH5 Factory default setting offset value | - |  |  |  |  |  |  |  | - | System |
| +13 | CH5 Factory default setting gain value | - |  |  |  |  |  |  |  | - | System |
| +14 | CH6 Factory default setting offset value | - |  |  |  |  |  |  |  | - | System |
| +15 | CH6 Factory default setting gain value | - |  |  |  |  |  |  |  | - | System |
| +16 | CH7 Factory default setting offset value | - |  |  |  |  |  |  |  | - | System |
| +17 | CH7 Factory default setting gain value | $-$ |  |  |  |  |  |  |  | - | System |
| +18 | CH8 Factory default setting offset value | - |  |  |  |  |  |  |  | - | System |
| +19 | CH8 Factory default setting gain value | - |  |  |  |  |  |  |  | - | System |
| +20 | CH1 User range setting offset value | $-$ |  |  |  |  |  |  |  | - | System |
| +21 | CH1 User range setting gain value | $-$ |  |  |  |  |  |  |  | - | System |
| +22 | CH2 User range setting offset value | - |  |  |  |  |  |  |  | - | System |
| +23 | CH 2 User range setting gain value | - |  |  |  |  |  |  |  | - | System |
| +24 | CH3 User range setting offset value | - |  |  |  |  |  |  |  | - | System |
| +25 | CH 3 User range setting gain value | - |  |  |  |  |  |  |  | - | System |
| +26 | CH4 User range setting offset value | - |  |  |  |  |  |  |  | - | System |
| +27 | CH4 User range setting gain value | $-$ |  |  |  |  |  |  |  | - | System |
| +28 | CH5 User range setting offset value | - |  |  |  |  |  |  |  | - | System |
| +29 | CH5 User range setting gain value | - |  |  |  |  |  |  |  | - | System |
| +30 | CH6 User range setting offset value | - |  |  |  |  |  |  |  | - | System |
| +31 | CH6 User range setting gain value | - |  |  |  |  |  |  |  | - | System |
| +32 | CH7 User range setting offset value | - |  |  |  |  |  |  |  | - | System |
| +33 | CH7 User range setting gain value | - |  |  |  |  |  |  |  | - | System |
| +34 | CH8 User range setting offset value | - |  |  |  |  |  |  |  | - | System |
| +35 | CH8 User range setting gain value | - |  |  |  |  |  |  |  | - | System |
| +36 | System area | $-$ |  |  |  |  |  |  |  | - | - |

*2 For the R60DAV8 and the R60DAI8, this area corresponds to System area and so is not available.

- For R60TD8-G

| Operand: (s) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +0 | System area | - | - | - |
| +1 | Completion status | The instruction completion status is stored. <br> - 0: Completed successfully <br> - Other than 0: Completed with an error (error code) | - | System |
| +2 | System area | - | - | - |
| +3 | System area | - | - | - |
| +4 | CH1 Factory default setting offset value (L) | - | - | System |
| +5 | CH1 Factory default setting offset value (H) | - | - | System |
| +6 | CH1 Factory default setting gain value (L) | - | - | System |
| +7 | CH1 Factory default setting gain value (H) | - | - | System |
| +8 | CH 1 User range setting offset value (L) | - | - | System |
| +9 | CH 1 User range setting offset value (H) | - | - | System |
| +10 | CH 1 User range setting gain value (L) | - | - | System |
| +11 | CH 1 User range setting gain value (H) | - | - | System |
| +12 | CH1 User range setting thermoelectromotive force offset value (L) | - | - | System |
| +13 | CH1 User range setting thermoelectromotive force offset value (H) | - | - | System |
| +14 | CH1 User range setting thermoelectromotive force gain value (L) | - | - | System |
| +15 | CH1 User range setting thermoelectromotive force gain value $(\mathrm{H})$ | - | - | System |
| +16 | CH2 Factory default setting offset value (L) | - | - | System |
| +17 | CH 2 Factory default setting offset value (H) | - | - | System |
| +18 | CH2 Factory default setting gain value (L) | - | - | System |
| +19 | CH 2 Factory default setting gain value (H) | - | - | System |
| +20 | CH 2 User range setting offset value (L) | - | - | System |
| +21 | CH 2 User range setting offset value (H) | - | - | System |
| +22 | CH 2 User range setting gain value (L) | - | - | System |
| +23 | CH 2 User range setting gain value (H) | - | - | System |
| +24 | CH2 User range setting thermoelectromotive force offset value (L) | - | - | System |
| +25 | CH2 User range setting thermoelectromotive force offset value (H) | - | - | System |
| +26 | CH2 User range setting thermoelectromotive force gain value (L) | - | - | System |
| +27 | CH2 User range setting thermoelectromotive force gain value (H) | - | - | System |
| +28 | CH3 Factory default setting offset value (L) | - | - | System |
| +29 | CH3 Factory default setting offset value (H) | - | - | System |
| +30 | CH3 Factory default setting gain value (L) | - | - | System |
| +31 | CH3 Factory default setting gain value (H) | - | - | System |
| +32 | CH3 User range setting offset value (L) | - | - | System |
| +33 | CH3 User range setting offset value (H) | - | - | System |
| +34 | CH3 User range setting gain value (L) | - | - | System |
| +35 | CH3 User range setting gain value (H) | - | - | System |
| +36 | CH3 User range setting thermoelectromotive force offset value (L) | - | - | System |
| +37 | CH3 User range setting thermoelectromotive force offset value (H) | - | - | System |
| +38 | CH3 User range setting thermoelectromotive force gain value (L) | - | - | System |
| +39 | CH3 User range setting thermoelectromotive force gain value $(\mathrm{H})$ | - | - | System |
| +40 | CH4 Factory default setting offset value (L) | - | - | System |


| Operand: (s) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +41 | CH4 Factory default setting offset value (H) | - | - | System |
| +42 | CH4 Factory default setting gain value (L) | - | - | System |
| +43 | CH4 Factory default setting gain value (H) | - | - | System |
| +44 | CH 4 User range setting offset value (L) | - | - | System |
| +45 | CH4 User range setting offset value (H) | - | - | System |
| +46 | CH4 User range setting gain value (L) | - | - | System |
| +47 | CH 4 User range setting gain value (H) | - | - | System |
| +48 | CH4 User range setting thermoelectromotive force offset value (L) | - | - | System |
| +49 | CH4 User range setting thermoelectromotive force offset value (H) | - | - | System |
| +50 | CH4 User range setting thermoelectromotive force gain value (L) | - | - | System |
| +51 | CH4 User range setting thermoelectromotive force gain value $(\mathrm{H})$ | - | - | System |
| +52 | CH5 Factory default setting offset value (L) | - | - | System |
| +53 | CH5 Factory default setting offset value (H) | - | - | System |
| +54 | CH5 Factory default setting gain value (L) | - | - | System |
| +55 | CH5 Factory default setting gain value (H) | - | - | System |
| +56 | CH5 User range setting offset value (L) | - | - | System |
| +57 | CH5 User range setting offset value (H) | - | - | System |
| +58 | CH5 User range setting gain value (L) | - | - | System |
| +59 | CH 5 User range setting gain value (H) | - | - | System |
| +60 | CH5 User range setting thermoelectromotive force offset value (L) | - | - | System |
| +61 | CH5 User range setting thermoelectromotive force offset value (H) | - | - | System |
| +62 | CH5 User range setting thermoelectromotive force gain value (L) | - | - | System |
| +63 | CH5 User range setting thermoelectromotive force gain value (H) | - | - | System |
| +64 | CH6 Factory default setting offset value (L) | - | - | System |
| +65 | CH6 Factory default setting offset value (H) | - | - | System |
| +66 | CH6 Factory default setting gain value (L) | - | - | System |
| +67 | CH6 Factory default setting gain value (H) | - | - | System |
| +68 | CH6 User range setting offset value (L) | - | - | System |
| +69 | CH6 User range setting offset value (H) | - | - | System |
| +70 | CH6 User range setting gain value (L) | - | - | System |
| +71 | CH6 User range setting gain value (H) | - | - | System |
| +72 | CH6 User range setting thermoelectromotive force offset value (L) | - | - | System |
| +73 | CH6 User range setting thermoelectromotive force offset value (H) | - | - | System |
| +74 | CH6 User range setting thermoelectromotive force gain value (L) | - | - | System |
| +75 | CH6 User range setting thermoelectromotive force gain value (H) | - | - | System |
| +76 | CH7 Factory default setting offset value (L) | - | - | System |
| +77 | CH7 Factory default setting offset value (H) | - | - | System |
| +78 | CH7 Factory default setting gain value (L) | - | - | System |
| +79 | CH7 Factory default setting gain value (H) | - | - | System |
| +80 | CH7 User range setting offset value (L) | - | - | System |
| +81 | CH7 User range setting offset value (H) | - | - | System |
| +82 | CH7 User range setting gain value (L) | - | - | System |
| +83 | CH 7 User range setting gain value (H) | - | - | System |

18 A/D CONVERSION INSTRUCTIONS 18.2 Reading the User Range Setting Values

| Operand: (s) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +84 | CH7 User range setting thermoelectromotive force offset value (L) | - | - | System |
| +85 | CH7 User range setting thermoelectromotive force offset value (H) | - | - | System |
| +86 | CH7 User range setting thermoelectromotive force gain value (L) | - | - | System |
| +87 | CH7 User range setting thermoelectromotive force gain value (H) | - | - | System |
| +88 | CH8 Factory default setting offset value (L) | - | - | System |
| +89 | CH8 Factory default setting offset value (H) | - | - | System |
| +90 | CH8 Factory default setting gain value (L) | - | - | System |
| +91 | CH8 Factory default setting gain value (H) | - | - | System |
| +92 | CH8 User range setting offset value (L) | - | - | System |
| +93 | CH8 User range setting offset value (H) | - | - | System |
| +94 | CH8 User range setting gain value (L) | - | - | System |
| +95 | CH 8 User range setting gain value (H) | - | - | System |
| +96 | CH8 User range setting thermoelectromotive force offset value (L) | - | - | System |
| +97 | CH8 User range setting thermoelectromotive force offset value (H) | - | - | System |
| +98 | CH8 User range setting thermoelectromotive force gain value (L) | - | - | System |
| +99 | CH8 User range setting thermoelectromotive force gain value (H) | - | - | System |

- For R60RD8-G

| Operand: (s) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +0 | System area | - | - | - |
| +1 | Completion status | The instruction completion status is stored. <br> - 0: Completed successfully <br> - Other than 0: Completed with an error (error code) | - | System |
| +2 | System area | - | - | - |
| +3 | System area | - | - | - |
| +4 | CH1 Factory default setting offset value (L) | - | - | System |
| +5 | CH 1 Factory default setting offset value (H) | - | - | System |
| +6 | CH1 Factory default setting gain value (L) | - | - | System |
| +7 | CH1 Factory default setting gain value (H) | - | - | System |
| +8 | CH 1 User range setting offset value (L) | - | - | System |
| +9 | CH1 User range setting offset value (H) | - | - | System |
| +10 | CH 1 User range setting gain value (L) | - | - | System |
| +11 | CH 1 User range setting gain value (H) | - | - | System |
| +12 | CH1 User range setting offset resistance value (L) | - | - | System |
| +13 | CH1 User range setting offset resistance value (H) | - | - | System |
| +14 | CH1 User range setting gain resistance value (L) | - | - | System |
| +15 | CH 1 User range setting gain resistance value (H) | - | - | System |
| +16 | CH2 Factory default setting offset value (L) | - | - | System |
| +17 | CH 2 Factory default setting offset value (H) | - | - | System |
| +18 | CH2 Factory default setting gain value (L) | - | - | System |
| +19 | CH 2 Factory default setting gain value (H) | - | - | System |
| +20 | CH2 User range setting offset value (L) | - | - | System |
| +21 | CH 2 User range setting offset value (H) | - | - | System |
| +22 | CH 2 User range setting gain value (L) | - | - | System |
| +23 | CH 2 User range setting gain value (H) | - | - | System |
| +24 | CH2 User range setting offset resistance value (L) | - | - | System |
| +25 | CH 2 User range setting offset resistance value (H) | - | - | System |
| +26 | CH 2 User range setting gain resistance value (L) | - | - | System |
| +27 | CH 2 User range setting gain resistance value (H) | - | - | System |
| +28 | CH3 Factory default setting offset value (L) | - | - | System |
| +29 | CH3 Factory default setting offset value (H) | - | - | System |
| +30 | CH3 Factory default setting gain value (L) | - | - | System |
| +31 | CH3 Factory default setting gain value (H) | - | - | System |
| +32 | CH3 User range setting offset value (L) | - | - | System |
| +33 | CH3 User range setting offset value (H) | - | - | System |
| +34 | CH3 User range setting gain value (L) | - | - | System |
| +35 | CH 3 User range setting gain value (H) | - | - | System |
| +36 | CH3 User range setting offset resistance value (L) | - | - | System |
| +37 | CH3 User range setting offset resistance value (H) | - | - | System |
| +38 | CH3 User range setting gain resistance value (L) | - | - | System |
| +39 | CH3 User range setting gain resistance value (H) | - | - | System |
| +40 | CH4 Factory default setting offset value (L) | - | - | System |

18 A/D CONVERSION INSTRUCTIONS

| Operand: (s) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +41 | CH4 Factory default setting offset value (H) | - | - | System |
| +42 | CH4 Factory default setting gain value (L) | - | - | System |
| +43 | CH4 Factory default setting gain value (H) | - | - | System |
| +44 | CH4 User range setting offset value (L) | - | - | System |
| +45 | CH 4 User range setting offset value (H) | - | - | System |
| +46 | CH 4 User range setting gain value (L) | - | - | System |
| +47 | CH 4 User range setting gain value (H) | - | - | System |
| +48 | CH4 User range setting offset resistance value (L) | - | - | System |
| +49 | CH4 User range setting offset resistance value (H) | - | - | System |
| +50 | CH4 User range setting gain resistance value (L) | - | - | System |
| +51 | CH4 User range setting gain resistance value (H) | - | - | System |
| +52 | CH5 Factory default setting offset value (L) | - | - | System |
| +53 | CH5 Factory default setting offset value (H) | - | - | System |
| +54 | CH5 Factory default setting gain value (L) | - | - | System |
| +55 | CH5 Factory default setting gain value (H) | - | - | System |
| +56 | CH5 User range setting offset value (L) | - | - | System |
| +57 | CH5 User range setting offset value (H) | - | - | System |
| +58 | CH5 User range setting gain value (L) | - | - | System |
| +59 | CH5 User range setting gain value (H) | - | - | System |
| +60 | CH5 User range setting offset resistance value (L) | - | - | System |
| +61 | CH5 User range setting offset resistance value (H) | - | - | System |
| +62 | CH5 User range setting gain resistance value (L) | - | - | System |
| +63 | CH5 User range setting gain resistance value (H) | - | - | System |
| +64 | CH6 Factory default setting offset value (L) | - | - | System |
| +65 | CH6 Factory default setting offset value (H) | - | - | System |
| +66 | CH6 Factory default setting gain value (L) | - | - | System |
| +67 | CH6 Factory default setting gain value (H) | - | - | System |
| +68 | CH6 User range setting offset value (L) | - | - | System |
| +69 | CH6 User range setting offset value (H) | - | - | System |
| +70 | CH6 User range setting gain value (L) | - | - | System |
| +71 | CH6 User range setting gain value (H) | - | - | System |
| +72 | CH6 User range setting offset resistance value (L) | - | - | System |
| +73 | CH6 User range setting offset resistance value (H) | - | - | System |
| +74 | CH6 User range setting gain resistance value (L) | - | - | System |
| +75 | CH6 User range setting gain resistance value (H) | - | - | System |
| +76 | CH7 Factory default setting offset value (L) | - | - | System |
| +77 | CH7 Factory default setting offset value (H) | - | - | System |
| +78 | CH7 Factory default setting gain value (L) | - | - | System |
| +79 | CH7 Factory default setting gain value (H) | - | - | System |
| +80 | CH7 User range setting offset value (L) | - | - | System |
| +81 | CH7 User range setting offset value (H) | - | - | System |
| +82 | CH 7 User range setting gain value (L) | - | - | System |
| +83 | CH 7 User range setting gain value (H) | - | - | System |


| Operand: (s) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +84 | CH7 User range setting offset resistance value (L) | - | - | System |
| +85 | CH7 User range setting offset resistance value (H) | - | - | System |
| +86 | CH7 User range setting gain resistance value (L) | - | - | System |
| +87 | CH7 User range setting gain resistance value (H) | - | - | System |
| +88 | CH8 Factory default setting offset value (L) | - | - | System |
| +89 | CH8 Factory default setting offset value (H) | - | - | System |
| +90 | CH8 Factory default setting gain value (L) | - | - | System |
| +91 | CH8 Factory default setting gain value (H) | - | - | System |
| +92 | CH8 User range setting offset value (L) | - | - | System |
| +93 | CH8 User range setting offset value (H) | - | - | System |
| +94 | CH8 User range setting gain value (L) | - | - | System |
| +95 | CH 8 User range setting gain value (H) | - | - | System |
| +96 | CH8 User range setting offset resistance value (L) | - | - | System |
| +97 | CH8 User range setting offset resistance value (H) | - | - | System |
| +98 | CH8 User range setting gain resistance value (L) | - | - | System |
| +99 | CH8 User range setting gain resistance value (H) | - | - | System |

- For R60AD16-G

| Operand: (s) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +0 | System area | - | - | - |
| +1 | Completion status | The instruction completion status is stored. <br> - 0: Completed successfully <br> - Other than 0: Completed with an error (error code) | - | System |
| +2 | Save data type setting | Specifies which one of the voltage and current is read as the offset/gain setting value. <br> - 0: Voltage specification <br> - 1: Current specification | 0000H to FFFFH | User |
| +3 | System area | - | - | - |
| +4 | CH1 Factory default setting offset value (L) | - | - | System |
| +5 | CH1 Factory default setting offset value (H) | - | - | System |
| +6 | CH1 Factory default setting gain value (L) | - | - | System |
| +7 | CH1 Factory default setting gain value (H) | - | - | System |
| +8 | CH2 Factory default setting offset value (L) | - | - | System |
| +9 | CH 2 Factory default setting offset value (H) | - | - | System |
| +10 | CH2 Factory default setting gain value (L) | - | - | System |
| +11 | CH 2 Factory default setting gain value (H) | - | - | System |
| +12 | CH3 Factory default setting offset value (L) | - | - | System |
| +13 | CH3 Factory default setting offset value (H) | - | - | System |
| +14 | CH3 Factory default setting gain value (L) | - | - | System |
| +15 | CH3 Factory default setting gain value (H) | - | - | System |
| +16 | CH4 Factory default setting offset value (L) | - | - | System |
| +17 | CH4 Factory default setting offset value (H) | - | - | System |
| +18 | CH4 Factory default setting gain value (L) | - | - | System |
| +19 | CH4 Factory default setting gain value (H) | - | - | System |
| +20 | CH5 Factory default setting offset value (L) | - | - | System |
| +21 | CH5 Factory default setting offset value (H) | - | - | System |
| +22 | CH5 Factory default setting gain value (L) | - | - | System |
| +23 | CH5 Factory default setting gain value (H) | - | - | System |
| +24 | CH6 Factory default setting offset value (L) | - | - | System |
| +25 | CH6 Factory default setting offset value (H) | - | - | System |
| +26 | CH6 Factory default setting gain value (L) | - | - | System |
| +27 | CH6 Factory default setting gain value (H) | - | - | System |
| +28 | CH7 Factory default setting offset value (L) | - | - | System |
| +29 | CH7 Factory default setting offset value (H) | - | - | System |
| +30 | CH7 Factory default setting gain value (L) | - | - | System |
| +31 | CH7 Factory default setting gain value (H) | - | - | System |
| +32 | CH8 Factory default setting offset value (L) | - | - | System |
| +33 | CH8 Factory default setting offset value (H) | - | - | System |
| +34 | CH8 Factory default setting gain value (L) | - | - | System |
| +35 | CH8 Factory default setting gain value (H) | - | - | System |
| +36 | CH9 Factory default setting offset value (L) | - | - | System |
| +37 | CH9 Factory default setting offset value (H) | - | - | System |
| +38 | CH9 Factory default setting gain value (L) | - | - | System |
| +39 | CH9 Factory default setting gain value (H) | - | - | System |
| +40 | CH10 Factory default setting offset value (L) | - | - | System |
| +41 | CH10 Factory default setting offset value (H) | - | - | System |
| +42 | CH 10 Factory default setting gain value (L) | - | - | System |
| +43 | CH10 Factory default setting gain value (H) | - | - | System |
| +44 | CH11 Factory default setting offset value (L) | - | - | System |


| Operand: (s) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +45 | CH11 Factory default setting offset value (H) | - | - | System |
| +46 | CH11 Factory default setting gain value (L) | - | - | System |
| +47 | CH11 Factory default setting gain value (H) | - | - | System |
| +48 | CH12 Factory default setting offset value (L) | - | - | System |
| +49 | CH12 Factory default setting offset value (H) | - | - | System |
| +50 | CH 12 Factory default setting gain value (L) | - | - | System |
| +51 | CH 12 Factory default setting gain value (H) | - | - | System |
| +52 | CH13 Factory default setting offset value (L) | - | - | System |
| +53 | CH13 Factory default setting offset value (H) | - | - | System |
| +54 | CH13 Factory default setting gain value (L) | - | - | System |
| +55 | CH 13 Factory default setting gain value (H) | - | - | System |
| +56 | CH14 Factory default setting offset value (L) | - | - | System |
| +57 | CH14 Factory default setting offset value (H) | - | - | System |
| +58 | CH14 Factory default setting gain value (L) | - | - | System |
| +59 | CH14 Factory default setting gain value (H) | - | - | System |
| +60 | CH15 Factory default setting offset value (L) | - | - | System |
| +61 | CH15 Factory default setting offset value (H) | - | - | System |
| +62 | CH15 Factory default setting gain value (L) | - | - | System |
| +63 | CH 15 Factory default setting gain value (H) | - | - | System |
| +64 | CH16 Factory default setting offset value (L) | - | - | System |
| +65 | CH16 Factory default setting offset value (H) | - | - | System |
| +66 | CH16 Factory default setting gain value (L) | - | - | System |
| +67 | CH16 Factory default setting gain value (H) | - | - | System |
| +68 | CH 1 User range setting offset value (L) | - | - | System |
| +69 | CH 1 User range setting offset value (H) | - | - | System |
| +70 | CH 1 User range setting gain value (L) | - | - | System |
| +71 | CH 1 User range setting gain value (H) | - | - | System |
| +72 | CH 2 User range setting offset value (L) | - | - | System |
| +73 | CH 2 User range setting offset value (H) | - | - | System |
| +74 | CH 2 User range setting gain value (L) | - | - | System |
| +75 | CH2 User range setting gain value (H) | - | - | System |
| +76 | CH3 User range setting offset value (L) | - | - | System |
| +77 | CH3 User range setting offset value (H) | - | - | System |
| +78 | CH3 User range setting gain value (L) | - | - | System |
| +79 | CH3 User range setting gain value (H) | - | - | System |
| +80 | CH4 User range setting offset value (L) | - | - | System |
| +81 | CH 4 User range setting offset value (H) | - | - | System |
| +82 | CH 4 User range setting gain value (L) | - | - | System |
| +83 | CH 4 User range setting gain value (H) | - | - | System |
| +84 | CH5 User range setting offset value (L) | - | - | System |
| +85 | CH5 User range setting offset value (H) | - | - | System |
| +86 | CH5 User range setting gain value (L) | - | - | System |
| +87 | CH5 User range setting gain value (H) | - | - | System |
| +88 | CH6 User range setting offset value (L) | - | - | System |
| +89 | CH6 User range setting offset value (H) | - | - | System |
| +90 | CH6 User range setting gain value (L) | - | - | System |
| +91 | CH6 User range setting gain value (H) | - | - | System |
| +92 | CH7 User range setting offset value (L) | - | - | System |
| +93 | CH 7 User range setting offset value (H) | - | - | System |
| +94 | CH7 User range setting gain value (L) | - | - | System |
| +95 | CH7 User range setting gain value (H) | - | - | System |
| +96 | CH8 User range setting offset value (L) | - | - | System |

18 A/D CONVERSION INSTRUCTIONS 18.2 Reading the User Range Setting Values

| Operand: (s) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +97 | CH8 User range setting offset value (H) | - | - | System |
| +98 | CH8 User range setting gain value (L) | - | - | System |
| +99 | CH8 User range setting gain value (H) | - | - | System |
| +100 | CH9 User range setting offset value (L) | - | - | System |
| +101 | CH9 User range setting offset value (H) | - | - | System |
| +102 | CH9 User range setting gain value (L) | - | - | System |
| +103 | CH9 User range setting gain value (H) | - | - | System |
| +104 | CH10 User range setting offset value (L) | - | - | System |
| +105 | CH 10 User range setting offset value (H) | - | - | System |
| +106 | CH 10 User range setting gain value (L) | - | - | System |
| +107 | CH 10 User range setting gain value (H) | - | - | System |
| +108 | CH11 User range setting offset value (L) | - | - | System |
| +109 | CH 11 User range setting offset value (H) | - | - | System |
| +110 | CH11 User range setting gain value (L) | - | - | System |
| +111 | CH 11 User range setting gain value (H) | - | - | System |
| +112 | CH12 User range setting offset value (L) | - | - | System |
| +113 | CH 12 User range setting offset value (H) | - | - | System |
| +114 | CH 12 User range setting gain value (L) | - | - | System |
| +115 | CH 12 User range setting gain value (H) | - | - | System |
| +116 | CH 13 User range setting offset value (L) | - | - | System |
| +117 | CH 13 User range setting offset value (H) | - | - | System |
| +118 | CH 13 User range setting gain value (L) | - | - | System |
| +119 | CH 13 User range setting gain value (H) | - | - | System |
| +120 | CH14 User range setting offset value (L) | - | - | System |
| +121 | CH14 User range setting offset value (H) | - | - | System |
| +122 | CH 14 User range setting gain value (L) | - | - | System |
| +123 | CH 14 User range setting gain value (H) | - | - | System |
| +124 | CH 15 User range setting offset value (L) | - | - | System |
| +125 | CH 15 User range setting offset value (H) | - | - | System |
| +126 | CH 15 User range setting gain value (L) | - | - | System |
| +127 | CH15 User range setting gain value (H) | - | - | System |
| +128 | CH16 User range setting offset value (L) | - | - | System |
| +129 | CH 16 User range setting offset value (H) | - | - | System |
| +130 | CH 16 User range setting gain value (L) | - | - | System |
| +131 | CH 16 User range setting gain value (H) | - | - | System |

- For R60DA16-G

| Operand: (s) |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description |  |  |  |  |  |  |  | Setting range | Set by |
| +0 | System area | - |  |  |  |  |  |  |  | - | - |
| +1 | Completion status | The instruction completion status is stored. <br> - 0: Completed successfully <br> - Other than 0: Completed with an error (error code) |  |  |  |  |  |  |  | - | System |
| +2 | Save data type setting 1 | Specify the user range setting for reading offset/gain setting values. This device allows a specification from CH 1 to CH 8 . <br> - OH : User range setting 1 (current specification) <br> - 1 H : User range setting 2 (voltage specification) <br> - 2 H : User range setting 3 (voltage specification) <br> b15 b14 b13 b12 b11 b10 b9 b8 b7 b6 b5 b4 b3 b2 b1 b0 |  |  |  |  |  |  |  | $0000 \mathrm{H} \text { to }$ <br> AAAAH | User |
| +3 | Save data type setting 2 | Specify the user range setting for reading offset/gain setting values. This device allows a specification from CH 9 to CH 16 . <br> - OH : User range setting 1 (current specification) <br> - 1 H : User range setting 2 (voltage specification) <br> - 2 H : User range setting 3 (voltage specification) <br> b15 b14 b13 b12 b11 b10 b9 b8 b7 b6 b5 b4 b3 b2 b1 b0 |  |  |  |  |  |  |  | $0000 \mathrm{H} \text { to }$ <br> AAAAH | User |
| +4 | CH1 Factory default setting offset value | - |  |  |  |  |  |  |  | - | System |
| +5 | CH1 Factory default setting gain value | - |  |  |  |  |  |  |  | - | System |
| +6 | CH2 Factory default setting offset value | - |  |  |  |  |  |  |  | - | System |
| +7 | CH2 Factory default setting gain value | - |  |  |  |  |  |  |  | - | System |
| +8 | CH3 Factory default setting offset value | - |  |  |  |  |  |  |  | - | System |
| +9 | CH3 Factory default setting gain value | - |  |  |  |  |  |  |  | - | System |
| +10 | CH4 Factory default setting offset value | - |  |  |  |  |  |  |  | - | System |
| +11 | CH4 Factory default setting gain value | - |  |  |  |  |  |  |  | - | System |
| +12 | CH5 Factory default setting offset value | - |  |  |  |  |  |  |  | - | System |
| +13 | CH5 Factory default setting gain value | - |  |  |  |  |  |  |  | - | System |
| +14 | CH6 Factory default setting offset value | - |  |  |  |  |  |  |  | - | System |
| +15 | CH6 Factory default setting gain value | - |  |  |  |  |  |  |  | - | System |
| +16 | CH7 Factory default setting offset value | $-$ |  |  |  |  |  |  |  | - | System |
| +17 | CH7 Factory default setting gain value | - |  |  |  |  |  |  |  | - | System |
| +18 | CH8 Factory default setting offset value | - |  |  |  |  |  |  |  | - | System |
| +19 | CH8 Factory default setting gain value | - |  |  |  |  |  |  |  | - | System |
| +20 | CH9 Factory default setting offset value | - |  |  |  |  |  |  |  | - | System |
| +21 | CH9 Factory default setting gain value | - |  |  |  |  |  |  |  | - | System |
| +22 | CH10 Factory default setting offset value | - |  |  |  |  |  |  |  | - | System |
| +23 | CH10 Factory default setting gain value | - |  |  |  |  |  |  |  | - | System |
| +24 | CH11 Factory default setting offset value | - |  |  |  |  |  |  |  | - | System |
| +25 | CH11 Factory default setting gain value | - |  |  |  |  |  |  |  | - | System |
| +26 | CH12 Factory default setting offset value | - |  |  |  |  |  |  |  | - | System |
| +27 | CH12 Factory default setting gain value | - |  |  |  |  |  |  |  | - | System |
| +28 | CH13 Factory default setting offset value | - |  |  |  |  |  |  |  | - | System |
| +29 | CH13 Factory default setting gain value | - |  |  |  |  |  |  |  | - | System |
| +30 | CH14 Factory default setting offset value | - |  |  |  |  |  |  |  | - | System |
| +31 | CH14 Factory default setting gain value | - |  |  |  |  |  |  |  | - | System |
| +32 | CH15 Factory default setting offset value | - |  |  |  |  |  |  |  | - | System |
| +33 | CH15 Factory default setting gain value | - |  |  |  |  |  |  |  | - | System |
| +34 | CH16 Factory default setting offset value | - |  |  |  |  |  |  |  | - | System |
| +35 | CH16 Factory default setting gain value | - |  |  |  |  |  |  |  | - | System |
| +36 | CH1 User range setting offset value | - |  |  |  |  |  |  |  | - | System |
| +37 | CH1 User range setting gain value | - |  |  |  |  |  |  |  | - | System |
| +38 | CH2 User range setting offset value | - |  |  |  |  |  |  |  | - | System |

18 A/D CONVERSION INSTRUCTIONS
18.2 Reading the User Range Setting Values

| Operand: (s) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +39 | CH2 User range setting gain value | - | - | System |
| +40 | CH3 User range setting offset value | - | - | System |
| +41 | CH3 User range setting gain value | - | - | System |
| +42 | CH4 User range setting offset value | - | - | System |
| +43 | CH4 User range setting gain value | - | - | System |
| +44 | CH5 User range setting offset value | - | - | System |
| +45 | CH5 User range setting gain value | - | - | System |
| +46 | CH6 User range setting offset value | - | - | System |
| +47 | CH6 User range setting gain value | - | - | System |
| +48 | CH7 User range setting offset value | - | - | System |
| +49 | CH7 User range setting gain value | - | - | System |
| +50 | CH8 User range setting offset value | - | - | System |
| +51 | CH8 User range setting gain value | - | - | System |
| +52 | CH9 User range setting offset value | - | - | System |
| +53 | CH9 User range setting gain value | - | - | System |
| +54 | CH10 User range setting offset value | - | - | System |
| +55 | CH 10 User range setting gain value | - | - | System |
| +56 | CH11 User range setting offset value | - | - | System |
| +57 | CH11 User range setting gain value | - | - | System |
| +58 | CH12 User range setting offset value | - | - | System |
| +59 | CH 12 User range setting gain value | - | - | System |
| +60 | CH13 User range setting offset value | - | - | System |
| +61 | CH13 User range setting gain value | - | - | System |
| +62 | CH14 User range setting offset value | - | - | System |
| +63 | CH14 User range setting gain value | - | - | System |
| +64 | CH15 User range setting offset value | - | - | System |
| +65 | CH15 User range setting gain value | - | - | System |
| +66 | CH16 User range setting offset value | - | - | System |
| +67 | CH16 User range setting gain value | - | - | System |
| +68 | System area | - | - | - |

## Processing details

- These instructions read the offset/gain setting values of the user range settings of an analog module into the CPU module.
- Execution and normal/error completion of the G(P).OGLOAD instruction can be checked with the completion device specified by the setting data (d) and the completion status indication device (d)+1.
- Completion device (d)

Turns on during END processing of the scan that arises upon completion of the $G(P)$.OGLOAD instruction, and turns off during the next END processing.

- Completion status indication device (d)+1

Turns on or off depending on the status resulting from completion of the $G(P)$.OGLOAD instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on during END processing of the scan that arises upon completion of the G(P).OGLOAD instruction, and turns off during the next END processing.

- The following figure shows how the $G(P)$.OGLOAD instruction operates when completing its execution.



## IProgram example

The following figure shows an example of a program which uses the $G(P)$.OGLOAD instruction with the R60AD4.

- Program content


## Description

When "Dedicated Instruction Execution Request" (M11) is turned on, the offset/gain settings of the analog module mounted at the position of I/O numbers X/ Y10 to X/Y1F are read out to the "head device (D100) for storing the offset gain setting" and later.

- Label setting

| Classification | Label name |  |  | Description |  | Device |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Module label | - |  |  | - |  | - |
| Label to be defined | Define the global label as shown below. |  |  |  |  |  |
|  | Label Name | Data Type |  | Class | Assign (Device/Label) |  |
|  | G_bOgStartReq | Bit |  | VAR_GLOBAL - | M11 |  |
|  | G_bReadStart | Bit |  | VAR_GLOBAL - | M12 |  |
|  | G_bWiteStart | Bit |  | VAR_GLOBAL - | M13 |  |
|  | G_bnOgLoadCmp ${ }^{\text {a }}$ | Bit(0.1) |  | VAR_GLOBAL - | M20 |  |
|  | G_bnOgStorCmp Flg | Bit(0.1) |  | VAR_GLOBAL - | M30 |  |
|  | G_wCHData TopDev | Word [Signed] |  | VAR_GLOBAL - | D100 |  |
|  | G_uSaveDataType | Word [Unsigned]/Bit String [16-bit] |  | VAR_GLOBAL - | D102 |  |
|  | G_bReadAbnormalComplete | Bit |  | VAR_GLOBAL - | F0 |  |
|  | G_bWiteAbnormalComplete | Bit |  | VAR_GLOBAL - | F1 |  |

- Program example

(14) Set control data.

Save data types need not be set for the following modules.

- R60ADV8
- R60ADI8
- R60DAV8
- R60DAI8
- R60TD8-G
- R60RD8-G
(43) Read the offset/gain settings.


## Precautions

In the following cases, the $G(P)$.OGLOAD instruction will be disabled.

- The module has been set as a target of synchronization.
- The wave output mode has been selected on the D/A converter module.


## Operation error

| Error code $((\mathbf{s})+\mathbf{1})$ | Description |
| :--- | :--- |
| 1863 H | The value set in the save data type setting $1(\mathrm{~S})+2$ or the save data type setting $2(\mathrm{~s})+3$ is out of the valid range. |

## 18．3 Restoring the User Range Setting Values

## G（P）．OGSTOR

These instructions restore the offset／gain settings in the user range setting stored in a CPU module into an analog module．


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| G．OGSTOR | - |
|  | $\boxed{ }$ |
| GP．OGSTOR | - |

Setting data
Description，range，data type

| Operand | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- |
| $(\mathrm{U})$ | Start I／O number（first three digits in four－digit hexadecimal <br> representation）of a module | 0000 H to 00 FEH | 16－bit unsigned binary | ANY16 |
| $(\mathrm{s})^{* 1}$ | Start device where the control data is stored | Predefined devices | Device name | ANY16 |
| （d） | Device to be turned on one scan when instruction <br> processing completes． <br> If the instruction is completed with an error，（d）+1 is also <br> turned on． | Predefined devices | Bit | ANYBIT＿ARRAY <br> （Number of elements： <br> $2)$ |
| EN | Execution condition | - | Bit | BOOL |
| ENO | Execution result | - | Bit | BOOL |

＊1 Specify the device that is specified in（s）when the $G(P)$ ．OGLOAD instruction is to be executed．The data read with the G（P）．OGLOAD instruction must be not be changed．Otherwise，normal operation cannot be guaranteed．

## ■Applicable devices

| Operand | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3E $\square(\mathrm{H}) \mathrm{G} \square$ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （U） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ |
| （s） | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） | $\bigcirc$ | － | － | － | － | － | － | － | － | － | － | － |

## Control data

- For R60AD4

| Operand: (s) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +0 | System area | - | - | - |
| +1 | Completion status | The instruction completion status is stored. <br> - 0: Completed successfully <br> - Other than 0: Completed with an error (error code) | - | System |
| +2 | Save data type setting | Used to store the set value that was set for Save data type setting (s) +2 in the $G(P)$.OGLOAD instruction. <br> - 0: Voltage specification <br> - 1: Current specification | 0000H to 000FH | System |
| +3 | System area | - | - | - |
| +4 | CH1 Factory default setting offset value (L) | - | - | System |
| +5 | CH1 Factory default setting offset value (H) | - | - | System |
| +6 | CH1 Factory default setting gain value (L) | - | - | System |
| +7 | CH1 Factory default setting gain value (H) | - | - | System |
| +8 | CH 2 Factory default setting offset value (L) | - | - | System |
| +9 | CH2 Factory default setting offset value (H) | - | - | System |
| +10 | CH 2 Factory default setting gain value (L) | - | - | System |
| +11 | CH2 Factory default setting gain value (H) | - | - | System |
| +12 | CH3 Factory default setting offset value (L) | - | - | System |
| +13 | CH3 Factory default setting offset value (H) | - | - | System |
| +14 | CH3 Factory default setting gain value (L) | - | - | System |
| +15 | CH3 Factory default setting gain value (H) | - | - | System |
| +16 | CH4 Factory default setting offset value (L) | - | - | System |
| +17 | CH4 Factory default setting offset value (H) | - | - | System |
| +18 | CH4 Factory default setting gain value (L) | - | - | System |
| +19 | CH 4 Factory default setting gain value (H) | - | - | System |
| +20 | CH1 User range setting offset value (L) | - | - | System |
| +21 | CH1 User range setting offset value (H) | - | - | System |
| +22 | CH 1 User range setting gain value (L) | - | - | System |
| +23 | CH 1 User range setting gain value (H) | - | - | System |
| +24 | CH 2 User range setting offset value (L) | - | - | System |
| +25 | CH2 User range setting offset value (H) | - | - | System |
| +26 | CH 2 User range setting gain value (L) | - | - | System |
| +27 | CH 2 User range setting gain value (H) | - | - | System |
| +28 | CH3 User range setting offset value (L) | - | - | System |
| +29 | CH3 User range setting offset value (H) | - | - | System |
| +30 | CH 3 User range setting gain value (L) | - | - | System |
| +31 | CH3 User range setting gain value (H) | - | - | System |
| +32 | CH4 User range setting offset value (L) | - | - | System |
| +33 | CH4 User range setting offset value (H) | - | - | System |
| +34 | CH4 User range setting gain value (L) | - | - | System |
| +35 | CH 4 User range setting gain value (H) | - | - | System |

- For R60DA4

| Operand: (s) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +0 | System area | - | - | - |
| +1 | Completion status | The instruction completion status is stored. <br> - 0: Completed successfully <br> - Other than 0: Completed with an error (error code) | - | System |
| +2 | Save data type setting | Used to store the set value that was set for Save data type setting (s) +2 in the G(P).OGLOAD instruction. <br> - 0 : User range setting (voltage specification) <br> - 1: User range setting (current specification) | 0000H to 000FH | System |
| +3 | System area | - | - | - |
| +4 | CH1 Factory default setting offset value | - | - | System |
| +5 | CH 1 Factory default setting gain value | - | - | System |
| +6 | CH2 Factory default setting offset value | - | - | System |
| +7 | CH 2 Factory default setting gain value | - | - | System |
| +8 | CH3 Factory default setting offset value | - | - | System |
| +9 | CH3 Factory default setting gain value | - | - | System |
| +10 | CH4 Factory default setting offset value | - | - | System |
| +11 | CH 4 Factory default setting gain value | - | - | System |
| +12 | CH1 User range setting offset value | - | - | System |
| +13 | CH1 User range setting gain value | - | - | System |
| +14 | CH2 User range setting offset value | - | - | System |
| +15 | CH 2 User range setting gain value | - | - | System |
| +16 | CH3 User range setting offset value | - | - | System |
| +17 | CH3 User range setting gain value | - | - | System |
| +18 | CH4 User range setting offset value | - | - | System |
| +19 | CH4 User range setting gain value | - | - | System |

- For R60ADV8, R60ADI8, R60AD8-G

| Operand: (s) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +0 | System area | - | - | - |
| +1 | Completion status | The instruction completion status is stored. <br> - 0: Completed successfully <br> - Other than 0: Completed with an error (error code) | - | System |
| +2 | Save data type setting*1 | Used to store the set value that was set for Save data type setting (s) +2 in the G(P).OGLOAD instruction. <br> - 0: Voltage specification <br> - 1: Current specification | 0000H to 00FFH | System |
| +3 | System area | - | - | - |
| +4 | CH1 Factory default setting offset value (L) | - | - | System |
| +5 | CH1 Factory default setting offset value (H) | - | - | System |
| +6 | CH1 Factory default setting gain value (L) | - | - | System |
| +7 | CH 1 Factory default setting gain value (H) | - | - | System |
| +8 | CH2 Factory default setting offset value (L) | - | - | System |
| +9 | CH 2 Factory default setting offset value (H) | - | - | System |
| +10 | CH2 Factory default setting gain value (L) | - | - | System |
| +11 | CH2 Factory default setting gain value (H) | - | - | System |
| +12 | CH3 Factory default setting offset value (L) | - | - | System |
| +13 | CH3 Factory default setting offset value (H) | - | - | System |
| +14 | CH3 Factory default setting gain value (L) | - | - | System |
| +15 | CH3 Factory default setting gain value (H) | - | - | System |
| +16 | CH4 Factory default setting offset value (L) | - | - | System |
| +17 | CH4 Factory default setting offset value (H) | - | - | System |
| +18 | CH4 Factory default setting gain value (L) | - | - | System |
| +19 | CH4 Factory default setting gain value (H) | - | - | System |
| +20 | CH5 Factory default setting offset value (L) | - | - | System |
| +21 | CH5 Factory default setting offset value (H) | - | - | System |
| +22 | CH5 Factory default setting gain value (L) | - | - | System |
| +23 | CH5 Factory default setting gain value (H) | - | - | System |
| +24 | CH6 Factory default setting offset value (L) | - | - | System |
| +25 | CH6 Factory default setting offset value (H) | - | - | System |
| +26 | CH6 Factory default setting gain value (L) | - | - | System |
| +27 | CH6 Factory default setting gain value (H) | - | - | System |
| +28 | CH7 Factory default setting offset value (L) | - | - | System |
| +29 | CH7 Factory default setting offset value (H) | - | - | System |
| +30 | CH7 Factory default setting gain value (L) | - | - | System |
| +31 | CH7 Factory default setting gain value (H) | - | - | System |
| +32 | CH8 Factory default setting offset value (L) | - | - | System |
| +33 | CH8 Factory default setting offset value (H) | - | - | System |
| +34 | CH8 Factory default setting gain value (L) | - | - | System |
| +35 | CH8 Factory default setting gain value (H) | - | - | System |
| +36 | CH 1 User range setting offset value (L) | - | - | System |
| +37 | CH1 User range setting offset value (H) | - | - | System |
| +38 | CH1 User range setting gain value (L) | - | - | System |
| +39 | CH 1 User range setting gain value (H) | - | - | System |
| +40 | CH2 User range setting offset value (L) | - | - | System |
| +41 | CH 2 User range setting offset value (H) | - | - | System |
| +42 | CH2 User range setting gain value (L) | - | - | System |
| +43 | CH 2 User range setting gain value (H) | - | - | System |
| +44 | CH3 User range setting offset value (L) | - | - | System |


| Operand: (s) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +45 | CH3 User range setting offset value (H) | - | - | System |
| +46 | CH 3 User range setting gain value (L) | - | - | System |
| +47 | CH3 User range setting gain value (H) | - | - | System |
| +48 | CH4 User range setting offset value (L) | - | - | System |
| +49 | CH4 User range setting offset value (H) | - | - | System |
| +50 | CH4 User range setting gain value (L) | - | - | System |
| +51 | CH 4 User range setting gain value (H) | - | - | System |
| +52 | CH5 User range setting offset value (L) | - | - | System |
| +53 | CH5 User range setting offset value (H) | - | - | System |
| +54 | CH5 User range setting gain value (L) | - | - | System |
| +55 | CH5 User range setting gain value (H) | - | - | System |
| +56 | CH6 User range setting offset value (L) | - | - | System |
| +57 | CH6 User range setting offset value (H) | - | - | System |
| +58 | CH6 User range setting gain value (L) | - | - | System |
| +59 | CH6 User range setting gain value (H) | - | - | System |
| +60 | CH7 User range setting offset value (L) | - | - | System |
| +61 | CH7 User range setting offset value (H) | - | - | System |
| +62 | CH 7 User range setting gain value (L) | - | - | System |
| +63 | CH 7 User range setting gain value (H) | - | - | System |
| +64 | CH8 User range setting offset value (L) | - | - | System |
| +65 | CH8 User range setting offset value (H) | - | - | System |
| +66 | CH8 User range setting gain value (L) | - | - | System |
| +67 | CH8 User range setting gain value (H) | - | - | System |

*1 For the R60ADV8 and the R60ADI8, this area corresponds to System area and so is not available.

- For R60DAV8, R60DAI8, R60DA8-G

| Operand: (s) |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description |  |  |  |  |  |  |  | Setting range | Set by |
| +0 | System area | - |  |  |  |  |  |  |  | - | - |
| +1 | Completion status | The instruction completion status is stored. <br> - 0: Completed successfully <br> - Other than 0: Completed with an error (error code) |  |  |  |  |  |  |  | - | System |
| +2 | Save data type setting*2 | Used to store the set value that was set for Save data type setting ( s ) +2 in the $\mathrm{G}(\mathrm{P})$.OGLOAD instruction. <br> - OH : User range setting 1 (current specification) <br> - 1 H : User range setting 2 (voltage specification) <br> - 2 H : User range setting 3 (voltage specification) <br>  |  |  |  |  |  |  |  | $0000 \mathrm{H} \text { to }$ <br> AAAAH | System |
| +3 | System area | - |  |  |  |  |  |  |  | - | - |
| +4 | CH1 Factory default setting offset value | - |  |  |  |  |  |  |  | - | System |
| +5 | CH1 Factory default setting gain value | - |  |  |  |  |  |  |  | - | System |
| +6 | CH2 Factory default setting offset value | - |  |  |  |  |  |  |  | - | System |
| +7 | CH 2 Factory default setting gain value | - |  |  |  |  |  |  |  | - | System |
| +8 | CH3 Factory default setting offset value | - |  |  |  |  |  |  |  | - | System |
| +9 | CH3 Factory default setting gain value | - |  |  |  |  |  |  |  | - | System |
| +10 | CH4 Factory default setting offset value | - |  |  |  |  |  |  |  | - | System |
| +11 | CH4 Factory default setting gain value | - |  |  |  |  |  |  |  | - | System |
| +12 | CH5 Factory default setting offset value | $-$ |  |  |  |  |  |  |  | - | System |
| +13 | CH5 Factory default setting gain value | - |  |  |  |  |  |  |  | - | System |
| +14 | CH6 Factory default setting offset value | - |  |  |  |  |  |  |  | - | System |
| +15 | CH6 Factory default setting gain value | - |  |  |  |  |  |  |  | - | System |
| +16 | CH7 Factory default setting offset value | - |  |  |  |  |  |  |  | - | System |
| +17 | CH 7 Factory default setting gain value | - |  |  |  |  |  |  |  | - | System |
| +18 | CH8 Factory default setting offset value | - |  |  |  |  |  |  |  | - | System |
| +19 | CH8 Factory default setting gain value |  |  |  |  |  |  |  |  | - | System |
| +20 | CH1 User range setting offset value | - |  |  |  |  |  |  |  | - | System |
| +21 | CH1 User range setting gain value | $-$ |  |  |  |  |  |  |  | - | System |
| +22 | CH2 User range setting offset value | $-$ |  |  |  |  |  |  |  | - | System |
| +23 | CH 2 User range setting gain value | - |  |  |  |  |  |  |  | - | System |
| +24 | CH3 User range setting offset value | - |  |  |  |  |  |  |  | - | System |
| +25 | CH3 User range setting gain value | $-$ |  |  |  |  |  |  |  | - | System |
| +26 | CH4 User range setting offset value | - |  |  |  |  |  |  |  | - | System |
| +27 | CH4 User range setting gain value | - |  |  |  |  |  |  |  | - | System |
| +28 | CH5 User range setting offset value | - |  |  |  |  |  |  |  | - | System |
| +29 | CH5 User range setting gain value | $-$ |  |  |  |  |  |  |  | - | System |
| +30 | CH6 User range setting offset value | - |  |  |  |  |  |  |  | - | System |
| +31 | CH6 User range setting gain value | - |  |  |  |  |  |  |  | - | System |
| +32 | CH7 User range setting offset value | - |  |  |  |  |  |  |  | - | System |
| +33 | CH7 User range setting gain value | - |  |  |  |  |  |  |  | - | System |
| +34 | CH8 User range setting offset value | - |  |  |  |  |  |  |  | - | System |
| +35 | CH 8 User range setting gain value | - |  |  |  |  |  |  |  | - | System |
| +36 | System area | - |  |  |  |  |  |  |  | - | - |

*2 For the R60DAV8 and the R60DAI8, this area corresponds to System area and so is not available.

- For R60TD8-G

| Operand: (s) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +0 | System area | - | - | - |
| +1 | Completion status | The instruction completion status is stored. <br> - 0: Completed successfully <br> - Other than 0: Completed with an error (error code) | - | System |
| +2 | System area | - | - | - |
| +3 | System area | - | - | - |
| +4 | CH1 Factory default setting offset value (L) | - | - | System |
| +5 | CH1 Factory default setting offset value (H) | - | - | System |
| +6 | CH1 Factory default setting gain value (L) | - | - | System |
| +7 | CH1 Factory default setting gain value (H) | - | - | System |
| +8 | CH1 User range setting offset value (L) | - | - | System |
| +9 | CH 1 User range setting offset value (H) | - | - | System |
| +10 | CH1 User range setting gain value (L) | - | - | System |
| +11 | CH1 User range setting gain value (H) | - | - | System |
| +12 | CH1 User range setting thermoelectromotive force offset value (L) | - | - | System |
| +13 | CH1 User range setting thermoelectromotive force offset value (H) | - | - | System |
| +14 | CH1 User range setting thermoelectromotive force gain value (L) | - | - | System |
| +15 | CH1 User range setting thermoelectromotive force gain value (H) | - | - | System |
| +16 | CH2 Factory default setting offset value (L) | - | - | System |
| +17 | CH2 Factory default setting offset value (H) | - | - | System |
| +18 | CH2 Factory default setting gain value (L) | - | - | System |
| +19 | CH 2 Factory default setting gain value (H) | - | - | System |
| +20 | CH2 User range setting offset value (L) | - | - | System |
| +21 | CH2 User range setting offset value (H) | - | - | System |
| +22 | CH 2 User range setting gain value (L) | - | - | System |
| +23 | CH 2 User range setting gain value (H) | - | - | System |
| +24 | CH2 User range setting thermoelectromotive force offset value (L) | - | - | System |
| +25 | CH2 User range setting thermoelectromotive force offset value (H) | - | - | System |
| +26 | CH2 User range setting thermoelectromotive force gain value (L) | - | - | System |
| +27 | CH2 User range setting thermoelectromotive force gain value (H) | - | - | System |
| +28 | CH3 Factory default setting offset value (L) | - | - | System |
| +29 | CH3 Factory default setting offset value (H) | - | - | System |
| +30 | CH3 Factory default setting gain value (L) | - | - | System |
| +31 | CH3 Factory default setting gain value (H) | - | - | System |
| +32 | CH3 User range setting offset value (L) | - | - | System |
| +33 | CH3 User range setting offset value (H) | - | - | System |
| +34 | CH3 User range setting gain value (L) | - | - | System |
| +35 | CH 3 User range setting gain value (H) | - | - | System |
| +36 | CH3 User range setting thermoelectromotive force offset value (L) | - | - | System |
| +37 | CH3 User range setting thermoelectromotive force offset value (H) | - | - | System |
| +38 | CH3 User range setting thermoelectromotive force gain value (L) | - | - | System |
| +39 | CH3 User range setting thermoelectromotive force gain value (H) | - | - | System |
| +40 | CH4 Factory default setting offset value (L) | - | - | System |


| Operand: (s) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +41 | CH4 Factory default setting offset value (H) | - | - | System |
| +42 | CH4 Factory default setting gain value (L) | - | - | System |
| +43 | CH 4 Factory default setting gain value (H) | - | - | System |
| +44 | CH4 User range setting offset value (L) | - | - | System |
| +45 | CH 4 User range setting offset value (H) | - | - | System |
| +46 | CH4 User range setting gain value (L) | - | - | System |
| +47 | CH 4 User range setting gain value (H) | - | - | System |
| +48 | CH4 User range setting thermoelectromotive force offset value (L) | - | - | System |
| +49 | CH4 User range setting thermoelectromotive force offset value (H) | - | - | System |
| +50 | CH4 User range setting thermoelectromotive force gain value (L) | - | - | System |
| +51 | CH4 User range setting thermoelectromotive force gain value (H) | - | - | System |
| +52 | CH5 Factory default setting offset value (L) | - | - | System |
| +53 | CH5 Factory default setting offset value (H) | - | - | System |
| +54 | CH5 Factory default setting gain value (L) | - | - | System |
| +55 | CH5 Factory default setting gain value (H) | - | - | System |
| +56 | CH5 User range setting offset value (L) | - | - | System |
| +57 | CH5 User range setting offset value (H) | - | - | System |
| +58 | CH5 User range setting gain value (L) | - | - | System |
| +59 | CH5 User range setting gain value (H) | - | - | System |
| +60 | CH5 User range setting thermoelectromotive force offset value (L) | - | - | System |
| +61 | CH5 User range setting thermoelectromotive force offset value (H) | - | - | System |
| +62 | CH5 User range setting thermoelectromotive force gain value (L) | - | - | System |
| +63 | CH5 User range setting thermoelectromotive force gain value (H) | - | - | System |
| +64 | CH6 Factory default setting offset value (L) | - | - | System |
| +65 | CH6 Factory default setting offset value (H) | - | - | System |
| +66 | CH6 Factory default setting gain value (L) | - | - | System |
| +67 | CH6 Factory default setting gain value (H) | - | - | System |
| +68 | CH6 User range setting offset value (L) | - | - | System |
| +69 | CH6 User range setting offset value (H) | - | - | System |
| +70 | CH6 User range setting gain value (L) | - | - | System |
| +71 | CH6 User range setting gain value (H) | - | - | System |
| +72 | CH6 User range setting thermoelectromotive force offset value (L) | - | - | System |
| +73 | CH6 User range setting thermoelectromotive force offset value (H) | - | - | System |
| +74 | CH6 User range setting thermoelectromotive force gain value (L) | - | - | System |
| +75 | CH6 User range setting thermoelectromotive force gain value (H) | - | - | System |
| +76 | CH7 Factory default setting offset value (L) | - | - | System |
| +77 | CH7 Factory default setting offset value (H) | - | - | System |
| +78 | CH7 Factory default setting gain value (L) | - | - | System |
| +79 | CH7 Factory default setting gain value (H) | - | - | System |
| +80 | CH 7 User range setting offset value (L) | - | - | System |
| +81 | CH 7 User range setting offset value (H) | - | - | System |
| +82 | CH 7 User range setting gain value (L) | - | - | System |
| +83 | CH 7 User range setting gain value (H) | - | - | System |


| Operand: (s) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +84 | CH7 User range setting thermoelectromotive force offset value (L) | - | - | System |
| +85 | CH7 User range setting thermoelectromotive force offset value (H) | - | - | System |
| +86 | CH7 User range setting thermoelectromotive force gain value (L) | - | - | System |
| +87 | CH7 User range setting thermoelectromotive force gain value (H) | - | - | System |
| +88 | CH8 Factory default setting offset value (L) | - | - | System |
| +89 | CH8 Factory default setting offset value (H) | - | - | System |
| +90 | CH8 Factory default setting gain value (L) | - | - | System |
| +91 | CH8 Factory default setting gain value (H) | - | - | System |
| +92 | CH8 User range setting offset value (L) | - | - | System |
| +93 | CH8 User range setting offset value (H) | - | - | System |
| +94 | CH8 User range setting gain value (L) | - | - | System |
| +95 | CH 8 User range setting gain value (H) | - | - | System |
| +96 | CH8 User range setting thermoelectromotive force offset value (L) | - | - | System |
| +97 | CH8 User range setting thermoelectromotive force offset value (H) | - | - | System |
| +98 | CH8 User range setting thermoelectromotive force gain value (L) | - | - | System |
| +99 | CH8 User range setting thermoelectromotive force gain value (H) | - | - | System |

- For R60RD8-G

| Operand: (s) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +0 | System area | - | - | - |
| +1 | Completion status | The instruction completion status is stored. <br> - 0 : Completed successfully <br> - Other than 0: Completed with an error (error code) | - | System |
| +2 | System area | - | - | - |
| +3 | System area | - | - | - |
| +4 | CH1 Factory default setting offset value (L) | - | - | System |
| +5 | CH1 Factory default setting offset value (H) | - | - | System |
| +6 | CH 1 Factory default setting gain value (L) | - | - | System |
| +7 | CH 1 Factory default setting gain value (H) | - | - | System |
| +8 | CH 1 User range setting offset value (L) | - | - | System |
| +9 | CH 1 User range setting offset value (H) | - | - | System |
| +10 | CH1 User range setting gain value (L) | - | - | System |
| +11 | CH 1 User range setting gain value (H) | - | - | System |
| +12 | CH1 User range setting offset resistance value (L) | - | - | System |
| +13 | CH1 User range setting offset resistance value (H) | - | - | System |
| +14 | CH1 User range setting gain resistance value (L) | - | - | System |
| +15 | CH 1 User range setting gain resistance value (H) | - | - | System |
| +16 | CH2 Factory default setting offset value (L) | - | - | System |
| +17 | CH 2 Factory default setting offset value (H) | - | - | System |
| +18 | CH2 Factory default setting gain value (L) | - | - | System |
| +19 | CH 2 Factory default setting gain value (H) | - | - | System |
| +20 | CH 2 User range setting offset value (L) | - | - | System |
| +21 | CH 2 User range setting offset value (H) | - | - | System |
| +22 | CH 2 User range setting gain value (L) | - | - | System |
| +23 | CH 2 User range setting gain value (H) | - | - | System |
| +24 | CH2 User range setting offset resistance value (L) | - | - | System |
| +25 | CH2 User range setting offset resistance value (H) | - | - | System |
| +26 | CH2 User range setting gain resistance value (L) | - | - | System |
| +27 | CH2 User range setting gain resistance value (H) | - | - | System |
| +28 | CH3 Factory default setting offset value (L) | - | - | System |
| +29 | CH3 Factory default setting offset value (H) | - | - | System |
| +30 | CH3 Factory default setting gain value (L) | - | - | System |
| +31 | CH3 Factory default setting gain value (H) | - | - | System |
| +32 | CH3 User range setting offset value (L) | - | - | System |
| +33 | CH3 User range setting offset value (H) | - | - | System |
| +34 | CH3 User range setting gain value (L) | - | - | System |
| +35 | CH 3 User range setting gain value (H) | - | - | System |
| +36 | CH3 User range setting offset resistance value (L) | - | - | System |
| +37 | CH3 User range setting offset resistance value (H) | - | - | System |
| +38 | CH3 User range setting gain resistance value (L) | - | - | System |
| +39 | CH3 User range setting gain resistance value (H) | - | - | System |
| +40 | CH4 Factory default setting offset value (L) | - | - | System |


| Operand: (s) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +41 | CH4 Factory default setting offset value (H) | - | - | System |
| +42 | CH4 Factory default setting gain value (L) | - | - | System |
| +43 | CH4 Factory default setting gain value (H) | - | - | System |
| +44 | CH4 User range setting offset value (L) | - | - | System |
| +45 | CH 4 User range setting offset value (H) | - | - | System |
| +46 | CH4 User range setting gain value (L) | - | - | System |
| +47 | CH4 User range setting gain value (H) | - | - | System |
| +48 | CH4 User range setting offset resistance value (L) | - | - | System |
| +49 | CH4 User range setting offset resistance value (H) | - | - | System |
| +50 | CH 4 User range setting gain resistance value (L) | - | - | System |
| +51 | CH4 User range setting gain resistance value (H) | - | - | System |
| +52 | CH5 Factory default setting offset value (L) | - | - | System |
| +53 | CH5 Factory default setting offset value (H) | - | - | System |
| +54 | CH5 Factory default setting gain value (L) | - | - | System |
| +55 | CH5 Factory default setting gain value (H) | - | - | System |
| +56 | CH5 User range setting offset value (L) | - | - | System |
| +57 | CH5 User range setting offset value (H) | - | - | System |
| +58 | CH5 User range setting gain value (L) | - | - | System |
| +59 | CH5 User range setting gain value (H) | - | - | System |
| +60 | CH5 User range setting offset resistance value (L) | - | - | System |
| +61 | CH5 User range setting offset resistance value (H) | - | - | System |
| +62 | CH5 User range setting gain resistance value (L) | - | - | System |
| +63 | CH5 User range setting gain resistance value (H) | - | - | System |
| +64 | CH6 Factory default setting offset value (L) | - | - | System |
| +65 | CH6 Factory default setting offset value (H) | - | - | System |
| +66 | CH6 Factory default setting gain value (L) | - | - | System |
| +67 | CH6 Factory default setting gain value (H) | - | - | System |
| +68 | CH6 User range setting offset value (L) | - | - | System |
| +69 | CH6 User range setting offset value (H) | - | - | System |
| +70 | CH6 User range setting gain value (L) | - | - | System |
| +71 | CH6 User range setting gain value (H) | - | - | System |
| +72 | CH6 User range setting offset resistance value (L) | - | - | System |
| +73 | CH6 User range setting offset resistance value (H) | - | - | System |
| +74 | CH6 User range setting gain resistance value (L) | - | - | System |
| +75 | CH6 User range setting gain resistance value (H) | - | - | System |
| +76 | CH 7 Factory default setting offset value (L) | - | - | System |
| +77 | CH 7 Factory default setting offset value (H) | - | - | System |
| +78 | CH7 Factory default setting gain value (L) | - | - | System |
| +79 | CH7 Factory default setting gain value (H) | - | - | System |
| +80 | CH7 User range setting offset value (L) | - | - | System |
| +81 | CH7 User range setting offset value (H) | - | - | System |
| +82 | CH 7 User range setting gain value (L) | - | - | System |
| +83 | CH 7 User range setting gain value (H) | - | - | System |

18 A/D CONVERSION INSTRUCTIONS 18.3 Restoring the User Range Setting Values

| Operand: (s) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +84 | CH7 User range setting offset resistance value (L) | - | - | System |
| +85 | CH7 User range setting offset resistance value (H) | - | - | System |
| +86 | CH7 User range setting gain resistance value (L) | - | - | System |
| +87 | CH7 User range setting gain resistance value (H) | - | - | System |
| +88 | CH8 Factory default setting offset value (L) | - | - | System |
| +89 | CH8 Factory default setting offset value (H) | - | - | System |
| +90 | CH8 Factory default setting gain value (L) | - | - | System |
| +91 | CH8 Factory default setting gain value (H) | - | - | System |
| +92 | CH8 User range setting offset value (L) | - | - | System |
| +93 | CH8 User range setting offset value (H) | - | - | System |
| +94 | CH8 User range setting gain value (L) | - | - | System |
| +95 | CH 8 User range setting gain value (H) | - | - | System |
| +96 | CH8 User range setting offset resistance value (L) | - | - | System |
| +97 | CH8 User range setting offset resistance value (H) | - | - | System |
| +98 | CH8 User range setting gain resistance value (L) | - | - | System |
| +99 | CH8 User range setting gain resistance value (H) | - | - | System |

- For R60AD16-G


18 A/D CONVERSION INSTRUCTIONS 18.3 Restoring the User Range Setting Values

| Operand: (s) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +45 | CH11 Factory default setting offset value (H) | - | - | System |
| +46 | CH11 Factory default setting gain value (L) | - | - | System |
| +47 | CH11 Factory default setting gain value (H) | - | - | System |
| +48 | CH12 Factory default setting offset value (L) | - | - | System |
| +49 | CH12 Factory default setting offset value (H) | - | - | System |
| +50 | CH 12 Factory default setting gain value (L) | - | - | System |
| +51 | CH 12 Factory default setting gain value (H) | - | - | System |
| +52 | CH13 Factory default setting offset value (L) | - | - | System |
| +53 | CH13 Factory default setting offset value (H) | - | - | System |
| +54 | CH13 Factory default setting gain value (L) | - | - | System |
| +55 | CH 13 Factory default setting gain value (H) | - | - | System |
| +56 | CH14 Factory default setting offset value (L) | - | - | System |
| +57 | CH14 Factory default setting offset value (H) | - | - | System |
| +58 | CH14 Factory default setting gain value (L) | - | - | System |
| +59 | CH14 Factory default setting gain value (H) | - | - | System |
| +60 | CH15 Factory default setting offset value (L) | - | - | System |
| +61 | CH15 Factory default setting offset value (H) | - | - | System |
| +62 | CH15 Factory default setting gain value (L) | - | - | System |
| +63 | CH 15 Factory default setting gain value (H) | - | - | System |
| +64 | CH16 Factory default setting offset value (L) | - | - | System |
| +65 | CH16 Factory default setting offset value (H) | - | - | System |
| +66 | CH16 Factory default setting gain value (L) | - | - | System |
| +67 | CH16 Factory default setting gain value (H) | - | - | System |
| +68 | CH 1 User range setting offset value (L) | - | - | System |
| +69 | CH 1 User range setting offset value (H) | - | - | System |
| +70 | CH 1 User range setting gain value (L) | - | - | System |
| +71 | CH 1 User range setting gain value (H) | - | - | System |
| +72 | CH 2 User range setting offset value (L) | - | - | System |
| +73 | CH 2 User range setting offset value (H) | - | - | System |
| +74 | CH 2 User range setting gain value (L) | - | - | System |
| +75 | CH2 User range setting gain value (H) | - | - | System |
| +76 | CH3 User range setting offset value (L) | - | - | System |
| +77 | CH3 User range setting offset value (H) | - | - | System |
| +78 | CH3 User range setting gain value (L) | - | - | System |
| +79 | CH3 User range setting gain value (H) | - | - | System |
| +80 | CH4 User range setting offset value (L) | - | - | System |
| +81 | CH 4 User range setting offset value (H) | - | - | System |
| +82 | CH 4 User range setting gain value (L) | - | - | System |
| +83 | CH 4 User range setting gain value (H) | - | - | System |
| +84 | CH5 User range setting offset value (L) | - | - | System |
| +85 | CH5 User range setting offset value (H) | - | - | System |
| +86 | CH5 User range setting gain value (L) | - | - | System |
| +87 | CH5 User range setting gain value (H) | - | - | System |
| +88 | CH6 User range setting offset value (L) | - | - | System |
| +89 | CH6 User range setting offset value (H) | - | - | System |
| +90 | CH6 User range setting gain value (L) | - | - | System |
| +91 | CH6 User range setting gain value (H) | - | - | System |
| +92 | CH7 User range setting offset value (L) | - | - | System |
| +93 | CH 7 User range setting offset value (H) | - | - | System |
| +94 | CH7 User range setting gain value (L) | - | - | System |
| +95 | CH7 User range setting gain value (H) | - | - | System |
| +96 | CH8 User range setting offset value (L) | - | - | System |


| Operand: (s) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +97 | CH8 User range setting offset value (H) | - | - | System |
| +98 | CH8 User range setting gain value (L) | - | - | System |
| +99 | CH8 User range setting gain value (H) | - | - | System |
| +100 | CH9 User range setting offset value (L) | - | - | System |
| +101 | CH9 User range setting offset value (H) | - | - | System |
| +102 | CH9 User range setting gain value (L) | - | - | System |
| +103 | CH9 User range setting gain value (H) | - | - | System |
| +104 | CH10 User range setting offset value (L) | - | - | System |
| +105 | CH 10 User range setting offset value (H) | - | - | System |
| +106 | CH 10 User range setting gain value (L) | - | - | System |
| +107 | CH 10 User range setting gain value (H) | - | - | System |
| +108 | CH11 User range setting offset value (L) | - | - | System |
| +109 | CH 11 User range setting offset value (H) | - | - | System |
| +110 | CH11 User range setting gain value (L) | - | - | System |
| +111 | CH 11 User range setting gain value (H) | - | - | System |
| +112 | CH12 User range setting offset value (L) | - | - | System |
| +113 | CH 12 User range setting offset value (H) | - | - | System |
| +114 | CH 12 User range setting gain value (L) | - | - | System |
| +115 | CH 12 User range setting gain value (H) | - | - | System |
| +116 | CH 13 User range setting offset value (L) | - | - | System |
| +117 | CH 13 User range setting offset value (H) | - | - | System |
| +118 | CH 13 User range setting gain value (L) | - | - | System |
| +119 | CH 13 User range setting gain value (H) | - | - | System |
| +120 | CH14 User range setting offset value (L) | - | - | System |
| +121 | CH14 User range setting offset value (H) | - | - | System |
| +122 | CH 14 User range setting gain value (L) | - | - | System |
| +123 | CH 14 User range setting gain value (H) | - | - | System |
| +124 | CH 15 User range setting offset value (L) | - | - | System |
| +125 | CH 15 User range setting offset value (H) | - | - | System |
| +126 | CH 15 User range setting gain value (L) | - | - | System |
| +127 | CH15 User range setting gain value (H) | - | - | System |
| +128 | CH16 User range setting offset value (L) | - | - | System |
| +129 | CH 16 User range setting offset value (H) | - | - | System |
| +130 | CH 16 User range setting gain value (L) | - | - | System |
| +131 | CH 16 User range setting gain value (H) | - | - | System |

- For R60DA16-G

| Operand: (s) |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description |  |  |  |  |  |  |  | Setting range | Set by |
| +0 | System area | - |  |  |  |  |  |  |  | - | - |
| +1 | Completion status | The instruction completion status is stored. <br> - 0: Completed successfully <br> - Other than 0: Completed with an error (error code) |  |  |  |  |  |  |  | - | System |
| +2 | Save data type setting 1 | Used to store the set value that was set for Save data type setting $1(\mathrm{~s})+2$ in the $G(P)$.OGLOAD instruction. <br> - OH: User range setting 1 (current specification) <br> - 1 H : User range setting 2 (voltage specification) <br> - 2 H : User range setting 3 (voltage specification) <br> b15 b14 b13 b12 b11 b10 b9 b8 b7 b6 b5 b4 b3 b2 b1 b0 |  |  |  |  |  |  |  | $0000 \mathrm{H} \text { to }$ <br> AAAAH | System |
| +3 | Save data type setting 2 | Used to store the set value that was set for Save data type setting $2(\mathrm{~s})+3$ in the $G(P)$.OGLOAD instruction. <br> - OH: User range setting 1 (current specification) <br> - 1 H : User range setting 2 (voltage specification) <br> - 2 H : User range setting 3 (voltage specification) <br> b15 b14 b13 b12 b11 b10 b9 b8 b7 b6 b5 b4 b3 b2 b1 b0 |  |  |  |  |  |  |  | $0000 \mathrm{H} \text { to }$ <br> AAAAH | System |
| +4 | CH1 Factory default setting offset value | - |  |  |  |  |  |  |  | - | System |
| +5 | CH1 Factory default setting gain value | - |  |  |  |  |  |  |  | - | System |
| +6 | CH2 Factory default setting offset value | - |  |  |  |  |  |  |  | - | System |
| +7 | CH2 Factory default setting gain value | - |  |  |  |  |  |  |  | - | System |
| +8 | CH3 Factory default setting offset value | - |  |  |  |  |  |  |  | - | System |
| +9 | CH3 Factory default setting gain value | - |  |  |  |  |  |  |  | - | System |
| +10 | CH4 Factory default setting offset value | - |  |  |  |  |  |  |  | - | System |
| +11 | CH4 Factory default setting gain value | - |  |  |  |  |  |  |  | - | System |
| +12 | CH5 Factory default setting offset value | $-$ |  |  |  |  |  |  |  | - | System |
| +13 | CH5 Factory default setting gain value | $-$ |  |  |  |  |  |  |  | - | System |
| +14 | CH6 Factory default setting offset value | - |  |  |  |  |  |  |  | - | System |
| +15 | CH6 Factory default setting gain value | - |  |  |  |  |  |  |  | - | System |
| +16 | CH7 Factory default setting offset value | $-$ |  |  |  |  |  |  |  | - | System |
| +17 | CH7 Factory default setting gain value | $-$ |  |  |  |  |  |  |  | - | System |
| +18 | CH8 Factory default setting offset value | - |  |  |  |  |  |  |  | - | System |
| +19 | CH8 Factory default setting gain value | - |  |  |  |  |  |  |  | - | System |
| +20 | CH9 Factory default setting offset value | $-$ |  |  |  |  |  |  |  | - | System |
| +21 | CH9 Factory default setting gain value | - |  |  |  |  |  |  |  | - | System |
| +22 | CH10 Factory default setting offset value | - |  |  |  |  |  |  |  | - | System |
| +23 | CH10 Factory default setting gain value | - |  |  |  |  |  |  |  | - | System |
| +24 | CH11 Factory default setting offset value | - |  |  |  |  |  |  |  | - | System |
| +25 | CH11 Factory default setting gain value | - |  |  |  |  |  |  |  | - | System |
| +26 | CH12 Factory default setting offset value | - |  |  |  |  |  |  |  | - | System |
| +27 | CH12 Factory default setting gain value | - |  |  |  |  |  |  |  | - | System |
| +28 | CH13 Factory default setting offset value | - |  |  |  |  |  |  |  | - | System |
| +29 | CH13 Factory default setting gain value | - |  |  |  |  |  |  |  | - | System |
| +30 | CH14 Factory default setting offset value | - |  |  |  |  |  |  |  | - | System |
| +31 | CH14 Factory default setting gain value | - |  |  |  |  |  |  |  | - | System |
| +32 | CH15 Factory default setting offset value | - |  |  |  |  |  |  |  | - | System |
| +33 | CH15 Factory default setting gain value | - |  |  |  |  |  |  |  | - | System |
| +34 | CH16 Factory default setting offset value | - |  |  |  |  |  |  |  | - | System |
| +35 | CH16 Factory default setting gain value | - |  |  |  |  |  |  |  | - | System |
| +36 | CH1 User range setting offset value | - |  |  |  |  |  |  |  | - | System |
| $+37$ | CH1 User range setting gain value | - |  |  |  |  |  |  |  | - | System |
| +38 | CH 2 User range setting offset value | - |  |  |  |  |  |  |  | - | System |


| Operand: $(\mathbf{s})$ | Description | Setting range | Set by |  |
| :--- | :--- | :--- | :--- | :--- |
| Device | Item | - | - |  |
| +39 | CH2 User range setting gain value | - | - | System |
| +40 | CH3 User range setting offset value | - | - | System |
| +41 | CH3 User range setting gain value | - | - | System |
| +42 | CH4 User range setting offset value | - | - | System |
| +43 | CH4 User range setting gain value | - | - | System |
| +44 | CH5 User range setting offset value | - | - | System |
| +45 | CH5 User range setting gain value | - | - | System |
| +46 | CH6 User range setting offset value | - | - | System |
| +47 | CH6 User range setting gain value | - | - | System |
| +48 | CH7 User range setting offset value | - | - | System |
| +49 | CH7 User range setting gain value | - | - | System |
| +50 | CH8 User range setting offset value | - | - | System |
| +51 | CH8 User range setting gain value | - | - | System |
| +52 | CH9 User range setting offset value | - | - | System |
| +53 | CH9 User range setting gain value | - | - | System |
| +54 | CH10 User range setting offset value | - | - | System |
| +55 | CH10 User range setting gain value | - | - | System |
| +56 | CH11 User range setting offset value | - | - | System |
| +57 | CH11 User range setting gain value | - | - | System |
| +58 | CH12 User range setting offset value | - | - | System |
| +59 | CH12 User range setting gain value | - | - | System |
| +60 | CH13 User range setting offset value | - | - | System |
| +61 | CH13 User range setting gain value | - | - | System |
| +62 | CH14 User range setting offset value | - | - | System |
| +63 | CH14 User range setting gain value | - | - | System |
| +64 | CH15 User range setting offset value | - | - | System |
| +65 | CH15 User range setting gain value | - | - | System |
| +66 | CH16 User range setting offset value | - | - | System |
| +67 | CH16 User range setting gain value | - | - |  |
| +68 | System area | - | - | - |

## Processing details

- These instructions restore the offset/gain settings in the user range setting stored in a CPU module into an analog module.
- Execution and normal/error completion of the G(P).OGSTOR instruction can be checked with the completion device specified by the setting data (d) and the completion status indication device (d)+1.
- Completion device (d)

Turns on during END processing of the scan that arises upon completion of the $G(P)$.OGSTOR instruction, and turns off during the next END processing.

- Completion status indication device (d)+1

Turns on or off depending on the status resulting from completion of the $G(P)$.OGSTOR instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on during END processing of the scan that arises upon completion of the G(P).OGSTOR instruction, and turns off during the next END processing.

- The following figure shows how the $G(P)$.OGSTOR instruction operates when completing its execution.

- After the offset/gain setting value has been restored, the base rate decreases to about three times or more of the unrestored accuracy.


## Point ${ }^{\circ}$

When the $G(P)$.OGSTOR instruction is executed, the D/A converter module stops the D/A conversion. To resume the D/A conversion, turn on and off the operating condition setting request (Y9).

## Program example

The following figure shows an example of a program which uses the $G(P)$.OGSTOR instruction with the R60AD4.

- Program content


## Description

When "Dedicated Instruction Execution Request" (M11) is turned off, the offset/gain settings are restored in the analog module mounted at the position of I/O numbers $\mathrm{X} / \mathrm{Y} 10$ to $\mathrm{X} / \mathrm{Y} 1 \mathrm{~F}$.

- Label setting

| Classification | Label name |  |  | Description |  | Device |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Module label | - |  |  | - |  | - |
| Label to be defined | Define the global label as shown below. |  |  |  |  |  |
|  | Label Name | Data Type |  | Class | Assign (Device/Label) |  |
|  | G_bOgStarkeq | Bit |  | VAR_GLOBAL $-1 M$ | M11 |  |
|  | G_bReadStart | Bit |  | VAR_GLOBAL - M | M12 |  |
|  | G_bWiteStart | Bit |  | VAR_GLOBAL - M | M13 |  |
|  | G_bnOgLoadCmp Fl ( | Bit(0.1) |  | VAR_GLOBAL - M | M20 |  |
|  | G_bnOgStorCmp Fl ( | Bit(0.1) |  | VAR_GLOBAL - M | M30 |  |
|  | G_wCHDataTopDev | Word [Signed] |  | VAR_GLOBAL $\rightarrow$ D | D100 |  |
|  | G_uSaveDataType | Word [Unsigned//Bit String [16-bit] |  | VAR_GLOBAL - D | D102 |  |
|  | G_bReadAbnomalComplete | Bit |  | VAR_GLOBAL - F | F0 |  |
|  | G_bWiteAbnomalComplete | Bit |  | VAR_GLOBAL - F | F1 |  |

- Program example

(14) Set control data.
(31) Restore the offset/gain settings.


## Precautions

In the following cases, execution of the $G(P)$.OGSTOR instruction results in an error.

- The module has been set as a target of synchronization.
- The wave output mode has been selected on the D/A converter module.


## Operation error

| Error code ((s)+1) | Description |
| :---: | :---: |
| 1860H | In the following cases, the $G(P)$.OGSTOR instruction will be being executed. <br> - During offset/gain setting module <br> - Has been set as a target of module-to-module synchronization. <br> - During wave output mode (D/A converter module) |
| 1861H | The G(P).OGSTOR instruction is being executed consecutively. |
| 1862H | The $G(P)$.OGSTOR instruction is being executed for a model different from that for which the $G(P)$.OGLOAD instruction has been executed. |
|  | The G(P).OGSTOR instruction is being executed before the G(P).OGLOAD instruction is executed. |
| 1863H | The value set in the save data type setting $1(\mathrm{~S})+2$ or the save data type setting $2(\mathrm{~s})+3$ is out of the valid range. |

## 19 postitionng instructions

### 19.1 Restoring the Absolute Position

## G.ABRSTD, Z.ABRSTD

These instructions restore the absolute position of specified axis.


ENO:=G_ABRST1(EN,U,s,d); ENO:=G_ABRST2(EN,U,s,d); ENO:=G_ABRST3(EN,U,s,d); ENO:=G_ABRST4(EN,U,s,d); ENO:=Z_ABRST1(EN,U,s,d); ENO:=Z_ABRST2(EN,U,s,d); ENO:=Z_ABRST3(EN,U,s,d); ENO:=Z_ABRST4(EN,U,s,d);

## FBD/LD



Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| G.ABRST1 |  |
| G.ABRST2 |  |
| G.ABRST3 |  |
| G.ABRST4 |  |
| Z.ABRST1 |  |
| Z.ABRST2 |  |
| Z.ABRST3 |  |
| Z.ABRST4 |  |

## Setting data

Description, range, data type

| Operand | Description | Range | Data type | Data type (label) |  |
| :--- | :--- | :--- | :--- | :--- | :--- |
| (U) | G.ABRST <br> 口 | Start I/O number (first three digits in four-digit <br> hexadecimal representation) of a module | 00 H to FEH |  |  |
|  | Z.ABRST <br> $\square$ | Start I/O number (first three digits in four-digit <br> hexadecimal representation) of a module | 00 H to FEH | 16-bit unsigned binary | ANY16 |
| (s) | Start device where the control data is stored | Refer to the control data. | Device name | ANY16_OR_STRIN <br> G_SINGLE |  |
| (d) | Device to be turned on one scan upon completion of <br> instruction <br> If the instruction is completed with an error, (d) +1 is also <br> turned on. | - | Bit | ANYBIT_ARRAY <br> (Number of <br> elements: 2) |  |
| EN | Execution condition | - | Bit | BOOL |  |
| ENO | Execution result | - | Bit | BOOL |  |

## Applicable devices

| Operand |  | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | Jロ｜ロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JपIロ， U3EDI（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （U） | G．ABRST | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ |
|  | Z．ABRST | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ |
| （s） |  | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） |  | $\bigcirc$ | － | $\bigcirc{ }^{* 1}$ | － | － | － | － | － | － | － | － | － |

＊1 T，ST，and C cannot be used．

## Control data

| Operand：（s） |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| ＋0 | System area | － | － | － |
| ＋1 | Completion status | The instruction completion status is stored． <br> －0：Completed successfully <br> －Other than 0：Completed with an error（error code） | － | System |
| ＋2 | Signal received from the servo amplifier | Write the signal status，below，imported from the servo amplifier to the input module． <br> －b0：ABS data bit 0 <br> －b1：ABS data bit 1 <br> －b2：Send data ready flag | 0， 1 | User |
| ＋3 | Signal to be sent to the servo amplifier | Performs the operation with the dedicated instruction by using the＂Signal received from the servo amplifier＂in（s）＋2．The ON／ OFF state of the following data，output to the servo amplifier，is stored． <br> －b0：Servo on <br> －b1：ABS transfer mode <br> －b2：ABS request flag | － | System |
| ＋4 | Status | Status of communication with the servo amplifier <br> － 0 ：Communication complete（set by the user at start of communication） <br> －Other than 0 ：Now communicating（stored by the system） | 0 | User／ system |
| +5 to +7 | System area | － | － | － |

## Processing details

- The positioning data is read from the servo amplifier corresponding to the absolute position along the following target axis, and the value with the unit converted is stored in the "Current read value" and "Machine feed value" areas in the positioning module. For the absolute position detection system, restore the absolute position once when it is powered on or the CPU module is reset.

| Instruction symbol | Target axis |
| :--- | :--- |
| G.ABRST1, Z.ABRST1 | Axis 1 |
| G.ABRST2, Z.ABRST2 | Axis 2 |
| G.ABRST3, Z.ABRST3 | Axis 3 |
| G.ABRST4, Z.ABRST4 | Axis 4 |

- The following figure shows how the ABRSTD instruction operates.

- For communication with the servo amplifier corresponding to the absolute position (data read/write), the I/O module is used. To use the ABRSTD instruction, prepare I/O modules having the following number of points to communicate with servo amplifier per axis.
- Input: 3 points
- Output: 3 points
- The execution of the ABRSTD instruction and whether it has been completed normally or with an error can be checked with the completion device (d) or completion status indication device (d) +1 .
- Completion device (d)

This device turns on during the END processing of the scan where the ABRSTD instruction completed, and turns off during the next END processing.

- Completion status indication device (d) +1

This device turns on or off depending on the completion status of the ABRSTD instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on during the END processing of the scan where the ABRSTD instruction completed, and turns off during the next END processing (the same on/off operation as the completion device is performed).

- The following figure shows the operation at completion of the ABRSTロ instruction.

- Completion of absolute position restoration can be checked with the (s) +4 status.
- The ABRSTD instruction performs absolute position restoration by following procedure.

(1) Servo on, ABS transfer mode, and ABS request flag are output to the output module by using the program.
(2) ABS data bit $0 /$ bit 1 and the send data ready flag are set using the program.


## Precautions

- If the absolute position detection system has been constructed, absolute position restoration must be performed once after the power is turned on or reset. The servo does not turn on until the absolute position restoration completes with the positioning module.
- Absolute position restoration must be performed while the programmable controller ready signal [Y0] is off.
- The absolute position can be restored (the ABRSTD instruction can be executed) while a servo amplifier is operable. Note that when the absolute position is restored, the Servo on signal may turn off (servo off) during the period of the scan time plus approximately 60 ms and the module may operate. To restore the absolute position during the servo off state, install an electromagnetic brake separately so that signals are output to the electromagnetic brake while the ABRSTD instruction is being executed.
- The following instructions cannot be executed simultaneously to a single axis. For different axes, any of the following can be executed concurrently with a G.ABRST instruction.
- Positioning start instruction (PSTRTロ)
- Absolute position restoration instruction (ABRSTロ)
- Teaching instruction (TEACHD)


## Operation error

| Error code ((s)+1) | Description |
| :--- | :--- |
| 1860 H | A value other than 0 was set in "Status" of (s)+4 (at start of communication with the servo amplifier). |
| 1861 H | "Status" of (s)+4 was changed during absolute position restoration (i.e. during communication with the servo amplifier). |
| 1865 H | An instruction was specified for an undefined axis (e.g. the G.ABRST3 instruction was specified when RD75P2 is used). |

## 19．2 Starting the Positioning

## GP．PSTRTD，ZP．PSTRTD

These instructions start to position the specified axis．

| Ladder | ST |
| :--- | :--- | :--- |

FBD／LD


## Execution condition

| Instruction | Execution condition |
| :--- | :---: |
| GP．PSTRT1 | - |
| GP．PSTRT2 |  |
| GP．PSTRT3 |  |
| GP．PSTRT4 |  |
| ZP．PSTRT1 |  |
| ZP．PSTRT2 |  |
| ZP．PSTRT3 |  |
| ZP．PSTRT4 |  |

## Setting data

## Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :---: | :---: | :---: | :---: | :---: | :---: |
| （U） | GP．PSTRT | Start I／O number（first three digits in four－digit hexadecimal representation）of a module | 00H to FEH | 16－bit unsigned binary | ANY16 |
|  | ZP．PSTRT | Start I／O number（first three digits in four－digit hexadecimal representation）of a module | 00H to FEH | String | ANY16＿OR＿STRIN G＿SINGLE |
| （s） |  | Start device where the control data is stored | Refer to the control data． | Device name | ANY16 |
| （d） |  | Device to be turned on one scan upon completion of instruction <br> If the instruction is completed with an error，（d）+1 is also turned on． | － | Bit | ANYBIT＿ARRAY <br> （Number of elements：2） |
| EN |  | Execution condition | － | Bit | BOOL |
| ENO |  | Execution result | － | Bit | BOOL |

## Applicable devices

| Operand |  | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | U밈，J밈， U3E［l（H）G口 | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （U） | GP．PSTRT | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ |
|  | ZP．PSTRT | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ |
| （s） |  | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） |  | $\bigcirc$ | － | $\bigcirc{ }^{* 1}$ | － | － | － | － | － | － | － | － | － |

[^37]Control data

| Operand: (s) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +0 | System area | - | - | - |
| +1 | Completion status | The instruction completion status is stored. <br> - 0: Completed successfully <br> - Other than 0: Completed with an error (error code) | - | System |
| +2 | Start number | Specifies the number of the following data that is started with the PSTRTD instruction. <br> - Positioning data number: 1 to 600 <br> - Block start: 7000 to 7004 <br> - Machine OPR: 9001 <br> - Fast OPR: 9002 <br> - Present value change: 9003 <br> - Multiple axes concurrent start: 9004 | 1 to 600 7000 to 7004 9001 to 9004 | User |

## Processing details

- The positioning is started for the specified axes below.

| Instruction symbol | Target axis |
| :--- | :--- |
| GP.PSTRT1, ZP.PSTRT1 | Axis 1 |
| GP.PSTRT2, ZP.PSTRT2 | Axis 2 |
| GP.PSTRT3, ZP.PSTRT3 | Axis 3 |
| GP.PSTRT4, ZP.PSTRT4 | Axis 4 |

- Block start, origin return start, present value change start, and multiple axes concurrent start are available by specifying one of 7000 to 7004 or 9001 to 9004 with "Start number" in (s)+2.
- The execution of the RSTRTロ instruction and whether it has been completed normally or with an error can be checked with the completion device (d) or completion status indication device (d)+1.
- Completion device (d)

This device turns on during the END processing of the scan where the PSTRTD instruction completed, and turns off during the next END processing.

- Completion status indication device (d) +1

This device turns on or off depending on the completion status of the PSTRTD instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on during the END processing of the scan where the PSTRTD instruction completed, and turns off during the next END processing (the same on/off operation as the completion device is performed).

- The following figure shows the operation at completion of the PSTRTD instruction.



## Precautions

- If the positioning is started by using the PSTRTD instruction, the Position start signal [Y10, Y11, Y12, Y13] will not turn on. In this case, although the start completion signals [X10, X11, X12, X13] turn on, the ON time is short; the program may fail to detect the ON state. For this reason, start completion cannot be checked using the start completion signals [X10, X11, $\mathrm{X} 12, \mathrm{X} 13$ ]. Check the positioning control status with the start command of the PSTRTD instruction or the BUSY signal [XC, XD, XE, XF].
- If the positioning is started by using the PSTRTD instruction, and then the stop command is input before the positioning completes, the completion device (d) turns on for one scan and the execution of the PSTRTD instruction completes.
- The following instructions cannot be executed simultaneously to a single axis. For different axes, any of the following can be executed concurrently with a G.ABRST instruction.
- Positioning start instruction (PSTRTD)
- Absolute position restoration instruction (ABRSTD)
- Teaching instruction (TEACHD)
- The PSTRTD instruction is executed when the RD75 READY signal [X0] is on. While the RD75 READY signal [X0] is off, the instruction cannot be executed even though it is requested. (No processing is performed.) Before executing the instruction, turn on both the PLC READY signal [Y0] and the RD75 READY signal [X0].
- If multiple axes concurrent start is executed using the PSTRTD instruction, the completion device (d) turns on when the positioning completes for the axis where the PSTRTD instruction has been executed (e.g. Axis 1 for GP.PSTRT1).
- If the PSTRTD instruction is used, the starting time will delay 0 to 0.88 ms relative to the Positioning start signal [Y10, Y11, Y12, Y13].


## Operation error

| Error code ((s)+1) | Description |
| :--- | :--- |
| 1862 H | A value other than 1 to 600,7000 to 7004, or 9001 to 9004 was set in "Starting number" of (s)+2. |
| 1865 H | An instruction was specified for an undefined axis (e.g. the GP.PSTRT3 instruction was specified when RD75P2 is used). |

## 19．3 Teaching

## GP．TEACH口，ZP．TEACHロ

These instructions perform teaching for the specified axis．


## Execution condition

| Instruction | Execution condition |
| :--- | :---: |
| GP．TEACH1 | - |
| GP．TEACH2 |  |
| GP．TEACH3 |  |
| GP．TEACH4 |  |
| ZP．TEACH1 |  |
| ZP．TEACH2 |  |
| ZP．TEACH3 |  |
| ZP．TEACH4 |  |

## Setting data

## Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :---: | :---: | :---: | :---: | :---: | :---: |
| （U） | GP．TEACH | Start I／O number（first three digits in four－digit hexadecimal representation）of a module | OOH to FEH | 16－bit unsigned binary | ANY16 |
|  | ZP.TEACH | Start I／O number（first three digits in four－digit hexadecimal representation）of a module | OOH to FEH | String | ANY16＿OR＿STRIN G＿SINGLE |
| （s） |  | Start device where the control data is stored | Refer to the control data． | Device name | ANY16 |
| （d） |  | Device to be turned on one scan upon completion of instruction <br> If the instruction is completed with an error，（d）+1 is also turned on． | － | Bit | ANYBIT＿ARRAY <br> （Number of elements：2） |
| EN |  | Execution condition | － | Bit | BOOL |
| ENO |  | Execution result | － | Bit | BOOL |

## Applicable devices

| Operand |  | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGㅁ，J밈， U3EDl（H）G口 | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （U） | GP.TEACH | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ |
|  | ZP.TEACH | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ |
| （s） |  | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） |  | $\bigcirc$ | － | $\bigcirc{ }^{* 1}$ | － | － | － | － | － | － | － | － | － |

[^38]Control data

| Operand: (s) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| +0 | System area | - | - | - |
| +1 | Completion status | The instruction completion status is stored. <br> - 0: Completed successfully <br> - Other than 0: Completed with an error (error code) | - | System |
| +2 | Teaching data selection | Selects the address to which to write the current feed value (positioning or arc address). <br> - 0 : Writes the current feed value to the positioning address. <br> - 1: Writes the current feed value to the arc address. | 0, 1 | User |
| +3 | Positioning data number | Sets the positioning data number at which to perform the teaching. | 1 to 600 | User |

## Processing details

- The data of "[Md.20] Current feed value" of one of the following target axes is set in the positioning or arc address. The positioning data other than the positioning or arc address must be set using the engineering tool or program.

| Instruction symbol | Target axis |
| :--- | :--- |
| GP.TEACH1, ZP.TEACH1 | Axis 1 |
| GP.TEACH2, ZP.TEACH2 | Axis 2 |
| GP.TEACH3, ZP.TEACH3 | Axis 3 |
| GP.TEACH4, ZP.TEACH4 | Axis 4 |

- Positioning data No. 1 to 600 can be taught.
- To move to an address (position) to be set as the positioning or arc address of the positioning data, JOG, inching, or manual pulse operation is used.
- The execution of the TEACH $\square$ instruction and whether it has been completed normally or with an error can be checked with the completion device (d) or completion status indication device (d) +1 .
- Completion device (d)

This device turns on during the END processing of the scan where the TEACHD instruction completed, and turns off during the next END processing.

- Completion status indication device (d)+1

This device turns on or off depending on the completion status of the TEACHD instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on during the END processing of the scan where the TEACHD instruction completed, and turns off during the next END processing (the same on/off operation as the completion device is performed).

- The following figure shows the operation at completion of the TEACHD instruction.



## Precautions

- For a single axis, any of the following instructions cannot be executed concurrently with a G.ABRST instruction. although different axes, any of the following can be executed concurrently with a GP.TEACH instruction.
- Positioning start instruction (PSTRTロ)
- Absolute position restoration instruction (ABRSTD)
- Teaching instruction (TEACHロ)
- The TEACHD instruction is executed when the BUSY signal [XC, XD, XE, or XF] is off. While the BUSY signal [XC, XD, $X E, X F]$ is on, the instruction cannot be executed. (No processing is performed.) Before executing the instruction, check that the BUSY signal [XC, XD, XE, XF] of the corresponding axis is off.


## Operation error

| Error code $(\mathbf{( s )} \mathbf{+ 1})$ | Description |
| :--- | :--- |
| 1863 H | A value other than 0 or 1 was set in "Teaching data selection" of (s)+2. |
| 1864 H | A value other than 1 to 600 was set in "Positioning data number" of (s)+3 |
| 1865 H | An instruction was specified for an undefined axis (e.g. the GP.TEACH3 instruction was specified when RD75P2 is used). |

### 19.4 Backing up Module Data (Writing Data to the Flash ROM)

## GP.PFWRT, ZP.PFWRT

This instruction writes to the flash ROM, the positioning data and block start data for the positioning module.


Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| GP.PFWRT | $\ddots$ |
| ZP.PFWRT | - |

## Setting data

## ■Description, range, data type

| Operand |  | Description | Range | Data type | Data type (label) |
| :--- | :--- | :--- | :--- | :--- | :--- |
| (U) | GP.PFWR <br> T | Start I/O number (first three digits in four-digit <br> hexadecimal representation) of a module | 00 H to FEH | 16-bit unsigned binary | ANY16 |
| ZP.PFWR <br> T | Start I/O number (first three digits in four-digit <br> hexadecimal representation) of a module | 00 H to FEH | String | ANY16_OR_STRING <br> _SINGLE |  |
| (s) | Start device where the control data is stored | Refer to the control data. | Device name | ANY16 |  |
| (d) | Device to be turned on one scan upon completion of <br> instruction <br> If the instruction is completed with an error, (d) +1 is <br> also turned on. | - | Bit | ANYBIT_ARRAY <br> (Number of elements: <br> $2)$ |  |
| EN | Execution condition | Execution result | - | Bit | BOOL |
| ENO |  | - | Bit | BOOL |  |

## Applicable devices

| Operand |  | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> (U) |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $\begin{aligned} & \mathbf{X}, \mathbf{Y}, \mathbf{M}, \mathbf{L}, \mathbf{S M}, \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | J미 | T, ST, C, D, W, SD, SW, FD, R, ZR, RD | U미G, J밈, U3EDI(H)Gロ | z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | Lz |  | K, H | E | \$ |  |
| (U) | GP.PFWR <br> T | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | $\bigcirc$ | - | - | $\bigcirc$ |
|  | ZP.PFWRT | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | $\bigcirc$ | $\bigcirc$ |
| (s) |  | - | - | $\bigcirc$ | - | - | - | - | $\bigcirc$ | - | - | - | - |
| (d) |  | $\bigcirc$ | - | O* ${ }^{*}$ | - | - | - | - | - | - | - | - | - |

[^39]| Operand: (s) |  |  |  |  |  |  |  | Description | Setting range | Set by |
| :--- | :--- | :--- | :--- | :--- | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Item | - | - |  |  |  |  |  |  |  |
| +0 | System area | The instruction completion status is stored. <br> $\bullet 0:$ Completed successfully <br> $\bullet$ Other than 0: Completed with an error (error code) | - |  |  |  |  |  |  |  |
| +1 | Completion status | System |  |  |  |  |  |  |  |  |

## Processing details

- The execution of the PFWRT instruction and whether it has been completed normally or with an error can be checked with the completion device (d) or completion status indication device (d)+1.
- Completion device (d)

This device turns on during the END processing of the scan where the PFWRT instruction completed, and turns off during the next END processing.

- Completion status indication device (d)+1

This device turns on or off depending on the completion status of the PFWRT instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on during the END processing of the scan where the PFWRT instruction completed, and turns off during the next END processing (the same on/off operation as the completion device is performed).

- The following figure shows the operation at completion of the PFWRT instruction.



## Precautions

- While the positioning data and block start data are being written to the flash ROM by using the PFWRT instruction, do not power off the system or reset the CPU module. Otherwise, the positioning will be unable to start normally because the positioning or block start data is written normally to the flash ROM. If the normal positioning start can no longer be enabled because the power was turned off or the CPU module was reset during writing to the flash ROM, restart the system as follows.

| Method | Description |
| :--- | :--- |
| Restart using the engineering tool | Write the positioning and block start data to the positioning module from the "Write to programmable controller" <br> of the engineering tool. |
| Restart using the program | Initialize the positioning module by using the PINIT instruction. Then, write the positioning data and block start <br> data to the positioning module by using the PFWRT instruction. |

- Data write to the flash ROM can repeat a maximum of one hundred thousand times. Any attempt to write data to the flash memory beyond this count results in failure.
- After the power is turned on or the CPU module is reset once, data write to the flash ROM can repeat a maximum of 25 times if the program is used. Any attempt to write data to the flash ROM memory beyond 25 times results in a flash ROM write count error at error code 1080 H , resulting in failure in data write. If one try of flash ROM write results in a flash ROM write error, check and modify the program that writes the data to the flash ROM. If a flash ROM write error occurred, reset the error of the positioning module using "[Cd.5] Axis error reset," or turn on the power or reset the CPU module again.
- The PFWRT instruction is executed when the RD75 READY signal [X0] is off. While the RD75 READY signal [X0] is on, the instruction cannot be executed. Before executing the instruction, turn off both the PLC READY signal [Y0] and the RD75 READY signal [X0].


## Operation error

| Error code $((\mathbf{s})+\mathbf{1})$ | Description |
| :--- | :--- |
| 1080 H | Flash ROM write count error |

## 19．5 Initializing the Module

## GP．PINIT，ZP．PINIT

This instruction initializes the positioning module setting data．


## Execution condition

| Instruction | Execution condition |
| :--- | :--- |
| GP．PINIT | - |
| ZP．PINIT | - |

## Setting data

## Description，range，data type

| Operand |  | Description | Range | Data type | Data type（label） |
| :--- | :--- | :--- | :--- | :--- | :--- |
| （U） | GP．PINIT | Start I／O number（first three digits in four－digit <br> hexadecimal representation）of a module | 00 H to FEH | 16－bit unsigned binary | ANY16 |
|  | ZP．PINIT | Start I／O number（first three digits in four－digit <br> hexadecimal representation）of a module | 00 H to FEH | String | ANY16＿OR＿STRING <br> SINGLE |
| （s） | Start device where the control data is stored | Refer to the control data． | Device name | ANY16 |  |
| （d） | Device to be turned on one scan upon completion of <br> instruction <br> If the instruction is completed with an error，（d）+1 is <br> also turned on． | - | Bit | ANYBIT＿ARRAY <br> （Number of elements： <br> $2)$ |  |
| EN | Execution condition | Execution result | - | Bit | BOOL |
| ENO |  |  | Bit | BOOL |  |

Applicable devices

| Operand |  | Bit |  | Word |  |  | Double word |  | Indirect specification | Constant |  |  | Others <br> （U） |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $\begin{aligned} & \text { X, Y, M, L, SM, } \\ & \text { F, B, SB, FX, FY } \end{aligned}$ | JロIロ | T，ST，C，D，W，SD， SW，FD，R，ZR，RD | UपIGロ，JロIロ， U3ED（H）Gロ | Z | $\begin{aligned} & \text { LT, LST, } \\ & \text { LC } \end{aligned}$ | LZ |  | K，H | E | \＄ |  |
| （U） | GP．PINIT | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | $\bigcirc$ | － | － | $\bigcirc$ |
|  | ZP．PINIT | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | $\bigcirc$ | $\bigcirc$ |
| （s） |  | － | － | $\bigcirc$ | － | － | － | － | $\bigcirc$ | － | － | － | － |
| （d） |  | $\bigcirc$ | － | $0^{* 1}$ | － | － | － | － | － | － | － | － | － |

＊1 T，ST，and C cannot be used．

## Control data

| Operand：（s） |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Device | Item | Description | Setting range | Set by |
| ＋0 | System area | － | － | － |
| ＋1 | Completion status | The instruction completion status is stored． <br> －0：Completed successfully <br> －Other than 0：Completed with an error（error code） | － | System |

## Processing details

- The setting data in the buffer memory and flash ROM of the positioning module is restored to the factory default setting values, or initial values.
- The setting data initialized include the parameters, positioning data (No. 1 to 600), and block start data (No. 7000 to 7004).
- The execution of the PINIT instruction and whether it has been completed normally or with an error can be checked with the completion device (d) or completion status indication device (d)+1.
- Completion device (d)

This device turns on during the END processing of the scan where the PINIT instruction completed, and turns off during the next END processing.

- Completion status indication device (d)+1

This device turns on or off depending on the completion status of the PINIT instruction.
When completed normally: Unchanged from off.
When completed with an error: Turns on during the END processing of the scan where the PINIT instruction completed, and turns off during the next END processing (the same on/off operation as the completion device is performed).

- The following figure shows the operation at completion of the PINIT instruction.



## Precautions

- The PINIT instruction is executed when the RD75 READY signal [X0] is off. While the RD75 READY signal [X0] is on, the instruction cannot be executed. Before executing the instruction, turn off both the PLC READY signal [Y0] and the RD75 READY signal [X0].
- Data write to the flash ROM can repeat a maximum of one hundred thousand times. Any attempt to write data to the flash memory beyond this count results in failure.
- After the power is turned on or the CPU module is reset once, data write to the flash ROM can repeat a maximum of 25 times if the program is used. Any attempt to write data to the flash ROM memory beyond 25 times results in a flash ROM write count error at error code 1080 H , resulting in failure in data write. If one try of flash ROM write results in a flash ROM write error, check and modify the program that writes the data to the flash ROM. If a flash ROM write error occurred, reset the error of the positioning module using "[Cd.5] Axis error reset," or turn on the power or reset the CPU module again.


## Operation error

| Error code ((s)+1) | Description |
| :--- | :--- |
| 1080 H | Flash ROM write count error |
| 1931 H | Flash ROM write error |

## PART 5 STANDARD FUNCTIONS

Part 5 consists of the following chapters.

20 TYPE CONVERSION FUNCTIONS

21 SINGLE VARIABLE FUNCTIONS

22 ARITHMETIC OPERATION FUNCTIONS
23 BIT SHIFT FUNCTIONS

24 BOOLEAN FUNCTIONS

25 SELECTION FUNCTIONS

26 COMPARISON FUNCTIONS
27 STRING FUNCTIONS

28 TIME DATA TYPE FUNCTIONS

## 20 TYPE CONVERSION FUNCTIONS

### 20.1 Converting BOOL to WORD

## BOOL_TO_WORD(_E)

These functions convert a value from BOOL data type to WORD data type.


## Setting data

## ■Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | BOOL |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | WORD |

## Processing details

## Operation processing

- These functions convert the value input to (s) from BOOL data type to WORD data type, and output the converted value from (d).
- When the input value is FALSE, $O H$ (WORD data type) is output.
- When the input value is TRUE, 1 H (WORD data type) is output.

- Input a BOOL data type value to (s).


## ©Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error.

### 20.2 Converting BOOL to DWORD

## BOOL_TO_DWORD(_E)

These functions convert a value from BOOL data type to DWORD data type.

| Ladder, FBD/LD |
| :--- |
| [Without EN/ENO] [With EN/ENO]  Structured text <br> [Without EN/ENO]    <br> d:=BOOL_TO_DWORD(s);    <br> [With EN/ENO]    <br> d:=BOOL_TO_DWORD_E(EN,ENO,s);    |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | BOOL |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | DWORD |

## Processing details

■Operation processing

- These functions convert the value input to (s) from BOOL data type to DWORD data type, and output the converted value from (d).
- When the input value is FALSE, OH (DWORD data type) is output.
- When the input value is TRUE, 1 H (DWORD data type) is output.

- Input a BOOL data type value to (s).


## ©Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE*1 $^{* 1}$ | Undefined value |

[^40]
## Operation error

There is no operation error.

### 20.3 Converting BOOL to INT

## BOOL_TO_INT(_E)

These functions convert a value from BOOL data type to INT data type.

| Ladder, FBD/LD |  | Structured text |
| :---: | :---: | :---: |
| [Without EN/ENO] | [With EN/ENO] | [Without EN/ENO] d:=BOOL_TO_INT(s); <br> [With EN/ENO] d:=BOOL_TO_INT_E(EN,ENO,s); |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | BOOL |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal or operation stop) | Output variable | BOOL |
| d | Output | Output variable | INT |

## Processing details

## ■Operation processing

- These functions convert the value input to (s) from BOOL data type to INT data type, and output the converted value from (d).
- When the input value is FALSE, 0 (INT data type) is output.
- When the input value is TRUE, 1 (INT data type) is output.

- Input a BOOL data type value to (s).


## ©Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE*1 | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error.

### 20.4 Converting BOOL to DINT

## BOOL_TO_DINT(_E)

These instructions convert a value from BOOL data type to DINT data type.


## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | BOOL |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal or operation stop) | Output variable | BOOL |
| d | Output | Output variable | DINT |

## Processing details

■Operation processing

- These functions convert the value input to (s) from BOOL data type to DINT data type, and output the converted value from (d).
- When the input value is FALSE, 0 (DINT data type) is output.
- When the input value is TRUE, 1 (DINT data type) is output.

- Input a BOOL data type value to (s).


## ©Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE*1 $^{* 1}$ | Undefined value |

[^41]
## Operation error

There is no operation error.

### 20.5 Converting BOOL to TIME

## BOOL_TO_TIME(_E)

These functions convert a value from BOOL data type to TIME data type.

| Ladder, FBD/LD |  | Structured text |
| :---: | :---: | :---: |
| [Without EN/ENO] | [With EN/ENO] | [Without EN/ENO] <br> d:=BOOL_TO_TIME(s); <br> [With EN/ENO] <br> d:=BOOL_TO_TIME_E(EN,ENO,s); |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | BOOL |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal or operation stop) | Output variable | BOOL |
| d | Output | Output variable | TIME |

## Processing details

## ■Operation processing

- These functions convert the value input to (s) from BOOL data type to TIME data type, and output the converted value from (d).
- When the input value is FALSE, 0 (TIME data type) is output.
- When the value is TRUE, 1 (TIME data type) is output.

- Input a BOOL data type value to (s).


## ■Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE*1 $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error.

### 20.6 Converting BOOL to STRING

## BOOL_TO_STRING(_E)

These functions convert a value from BOOL data type to STRING data type.

| Ladder, FBD/LD |  | Structured text |
| :---: | :---: | :---: |
| [Without EN/ENO] | [With EN/ENO] | $\begin{aligned} & \text { [Without EN/ENO] } \\ & \text { d:=BOOL_TO_STRING(s); } \\ & \text { [With EN/ENO] } \\ & \text { d:=BOOL_TO_STRING_E(EN,ENO,s); } \end{aligned}$ |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | STRING |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal or operation stop) | Output variable | BOOL |
| d | Output | Output variable | STRING |

## Processing details

■Operation processing

- These functions convert the value input to (s) from BOOL data type to STRING data type, and output the converted value from (d).
- When the input value is FALSE, 0 (STRING data type) is output.
- When the input value is TRUE, 1 (STRING data type) is output.

- Input a BOOL data type value to (s).


## ©Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE*1 $^{* 1}$ | Undefined value |

[^42]
## Operation error

There is no operation error.

### 20.7 Converting WORD to BOOL

## WORD_TO_BOOL(_E)

These functions convert a value from WORD data type to BOOL data type.

| Ladder, FBD/LD |  | Structured text |
| :---: | :---: | :---: |
| [Without EN/ENO] | [With EN/ENO] | ```[Without EN/ENO] d:=WORD_TO_BOOL(s); [With EN/ENO] d:=WORD_TO_BOOL_E(EN,ENO,s);``` |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | WORD |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | BOOL |

## Processing details

■Operation processing

- These functions convert the value input to (s) from WORD data type to BOOL data type, and output the converted value from (d).
- When the input value is 0 H, FALSE is output.
- When the input value is other than 0 H, TRUE is output.

- Input a WORD data type value to (s).


## ■Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE*1 $^{* 1}$ | Undefined value |

[^43]
## Operation error

There is no operation error.

### 20.8 Converting WORD to DWORD

## WORD_TO_DWORD(_E)

These functions convert a value from WORD data type to DWORD data type.

| Ladder, FBD/LD |  | Structured text |
| :---: | :---: | :---: |
| [Without EN/ENO] | [With EN/ENO] | $\begin{aligned} & \text { [Without EN/ENO] } \\ & \text { d:=WORD_TO_DWORD(s); } \\ & \text { [With EN/ENO] } \\ & \text { d:=WORD_TO_DWORD_E(EN,ENO,s); } \end{aligned}$ |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | WORD |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | DWORD |

## Processing details

■Operation processing

- These functions convert the value input to (s) from WORD data type to DWORD data type, and output the converted value from (d).
- After the data type is converted, the upper 16 bits are filled with 0s.

- Input a WORD data type value to (s).


## -Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE*1 | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error.

### 20.9 Converting WORD to INT

## WORD_TO_INT(_E)

These functions convert a value from WORD data type to INT data type.

| Ladder, FBD/LD |  | Structured text |
| :---: | :---: | :---: |
| [Without EN/ENO] | [With EN/ENO] | $\begin{aligned} & \text { [Without EN/ENO] } \\ & \text { d:=WORD_TO_INT(s); } \\ & \text { [With EN/ENO] } \\ & \text { d:=WORD_TO_INT_E(EN,ENO,s); } \end{aligned}$ |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | WORD |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | INT |

## Processing details

■Operation processing

- These functions convert the value input to (s) from WORD data type to INT data type, and output the converted value from (d).

- Input a WORD data type value to (s).


## Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE** $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error.

### 20.10 Converting WORD to DINT

## WORD_TO_DINT(_E)

These functions convert a value from WORD data type to DINT data type.

| Ladder, FBD/LD |
| :--- |
| [Without EN/ENO] |
| [With EN/ENO]  Structured text |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | WORD |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | DINT |

## Processing details

## ■Operation processing

- These functions convert the value input to (s) from WORD data type to DINT data type, and output the converted value from (d).
- After the data type is converted, the upper 16 bits are filled with 0s.

- Input a WORD data type value to (s).


## ©Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE** | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error.

### 20.11 Converting WORD to TIME

## WORD_TO_TIME(_E)

These functions convert a value from WORD data type to TIME data type.

| Ladder, FBD/LD |  | Structured text <br> [Without EN/ENO] d:=WORD_TO_TIME(s); <br> [With EN/ENO] d:=WORD_TO_TIME_E(EN,ENO,s); |
| :---: | :---: | :---: |
| [Without EN/ENO] | [With EN/ENO] |  |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN) | Input | Input variable | WORD |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | TIME |

## Processing details

■Operation processing

- These functions convert the value input to (s) from WORD data type to TIME data type, and output the converted value from (d).

- Input a WORD data type value to (s).


## Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE*1 $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error.

### 20.12 Converting WORD to STRING

## WORD_TO_STRING(_E)

These functions convert a value from WORD data type to STRING data type.


## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | WORD |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | STRING(4) |

## Processing details

■Operation processing

- These functions convert the value input to (s) from WORD data type to STRING data type, and output the converted value from (d).

- Input a WORD data type value to (s).
- When SM701 (Number of output characters selection) is off, 00 H is stored at the end of the string.


## ©Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE** | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error.

### 20.13 Converting DWORD to BOOL

## DWORD_TO_BOOL(_E)

These functions convert a value from DWORD data type to BOOL data type.


## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}(\mathrm{IN})$ | Input | Input variable | DWORD |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | BOOL |

## Processing details

## ■Operation processing

- These functions convert the value input to (s) from DWORD data type to BOOL data type, and output the converted value from (d).
- When the input value is 0 H, FALSE is output.
- When the input value is other than 0 H, TRUE is output.

- Input a DWORD data type value to (s).


## ©Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE*1 $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error.

### 20.14 Converting DWORD to WORD

DWORD_TO_WORD(_E)
These functions convert a value from DWORD data type to WORD data type.

| Ladder, FBD/LD |  | Structured text |
| :---: | :---: | :---: |
| [Without EN/ENO] | [With EN/ENO] | [Without EN/ENO] <br> d:=DWORD_TO_WORD(s); <br> [With EN/ENO] <br> d:=DWORD_TO_WORD_E(EN,ENO,s); |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | DWORD |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | WORD |

## Processing details

## ■Operation processing

- These functions convert the value input to (s) from DWORD data type to WORD data type, and output the converted value from (d).
- The upper 16-bit data of the input value (DWORD data type) are discarded.

- Input a DWORD data type value to (s).


## ■Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE** $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

```
Point?
When the DWORD_TO_WORD(_E) function is executed, the upper 16-bit data of the input value (DWORD
data type) are discarded.
```


## Operation error

There is no operation error.

### 20.15 Converting DWORD to INT

## DWORD_TO_INT(_E)

These functions convert a value from DWORD data type to INT data type.

| Ladder, FBD/LD |  | Structured text <br> [Without EN/ENO] d:=DWORD_TO_INT(s); <br> [With EN/ENO] <br> d:=DWORD_TO_INT_E(EN,ENO,s); |
| :---: | :---: | :---: |
| [Without EN/ENO] | [With EN/ENO] |  |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | DWORD |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | INT |

## Processing details

■Operation processing

- These functions convert the value input to (s) from DWORD data type to INT data type, and output the converted value from (d).
- The upper 16-bit data of the input value (DWORD data type) are discarded.

- Input a DWORD data type value to (s).


## ©Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE** $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

When the DWORD_TO_INT(_E) function is executed, the upper 16-bit data of the input value (DWORD data type) are discarded.

## Operation error

There is no operation error

### 20.16 Converting DWORD to DINT

## DWORD_TO_DINT(_E)

These functions convert a value from DWORD data type to DINT data type.

| Ladder, FBD/LD |  | Structured text |
| :---: | :---: | :---: |
| [Without EN/ENO] | [With EN/ENO] | $\begin{aligned} & \text { [Without EN/ENO] } \\ & \text { d:=DWORD_TO_DINT(s); } \\ & \text { [With EN/ENO] } \\ & \text { d:=DWORD_TO_DINT_E(EN,ENO,s); } \end{aligned}$ |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}(\mathrm{IN})$ | Input | Input variable | DWORD |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | DINT |

## Processing details

■Operation processing

- These functions convert the value input to (s) from DWORD data type to DINT data type, and output the converted value from (d).

- Input a DWORD data type value to (s).


## ©Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE** | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error.

### 20.17 Converting DWORD to TIME

## DWORD_TO_TIME(_E)

These functions convert a value from DWORD data type to TIME data type.


## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}(\mathrm{IN})$ | Input | Input variable | DWORD |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | TIME |

## Processing details

■Operation processing

- These functions convert the value input to (s) from DWORD data type to TIME data type, and output the converted value from (d).

- Input a DWORD data type value to (s).


## Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error.

### 20.18 Converting DWORD to STRING

## DWORD_TO_STRING(_E)

These functions convert a value from DWORD data type to STRING data type.

| Ladder, FBD/LD |  | Structured text <br> [Without EN/ENO] d:=DWORD_TO_STRING(s); <br> [With EN/ENO] <br> d:=DWORD_TO_STRING_E(EN,ENO,s); |
| :---: | :---: | :---: |
| [Without EN/ENO] | [With EN/ENO] |  |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}(\mathrm{IN})$ | Input | Input variable | DWORD |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | STRING(8) |

## Processing details

■Operation processing

- These functions convert the value input to (s) from DWORD data type to STRING data type, and output the converted value from (d).

- Input a DWORD data type value to (s).
- When SM701 (Number of output characters selection) is off, 00 H is stored at the end of the string.


## ©Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE** | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error.

### 20.19 Converting INT to BOOL

## INT_TO_BOOL(_E)

These functions convert a value from INT data type to BOOL data type.

| Ladder, FBD/LD |  | Structured text |
| :---: | :---: | :---: |
| [Without EN/ENO] | [With EN/ENO] | [Without EN/ENO] <br> d:=INT_TO_BOOL(s); <br> [With EN/ENO] d:=INT_TO_BOOL_E(EN,ENO,s); |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | INT |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | BOOL |

## Processing details

## ■Operation processing

- These functions convert the value input to (s) from INT data type to BOOL data type, and output the converted value from (d).
- When the value 0 is input, FALSE is output.
- When the value other than 0 is input, TRUE is output.

- Input an INT data type value to (s).


## OOperation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE** $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error.

### 20.20 Converting INT to WORD

INT_TO_WORD(_E)
These functions convert a value from INT data type to WORD data type.

| Ladder, FBD/LD |  | Structured text |
| :---: | :---: | :---: |
| [Without EN/ENO] | [With EN/ENO] | [Without EN/ENO] <br> d:=INT_TO_WORD(s); <br> [With EN/ENO] <br> d:=INT_TO_WORD_E(EN,ENO,s); |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | INT |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | WORD |

## Processing details

■Operation processing

- These functions convert the value input to (s) from INT data type to WORD data type, and output the converted value from (d).


- Input an INT data type value to (s).


## ©Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE** | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error.

### 20.21 Converting INT to DWORD

## INT_TO_DWORD(_E)

These functions convert a value from INT data type to DWORD data type.

| Ladder, FBD/LD |
| :--- |
| [Without EN/ENO] [With EN/ENO]  Structured text <br> [Without EN/ENO]    <br> d:=INT_TO_DWORD(s);    <br> [With EN/ENO]    <br> d:=INT_TO_DWORD_E(EN,ENO,s);    |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | INT |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | DWORD |

## Processing details

## ■Operation processing

- These functions convert the value input to (s) from INT data type to DWORD data type, and output the converted value from (d).
- After the data type is converted, the upper 16 bits are filled with 0s.

- Input an INT data type value to (s).


## Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error.

### 20.22 Converting INT to DINT

## INT_TO_DINT(_E)

These functions convert a value from INT data type to DINT data type.

| Ladder, FBD/LD |
| :--- |
| [Without EN/ENO] [With EN/ENO]  Structured text |
| [Without EN/ENO] <br> d:=INT_TO_DINT(s); <br> [With EN/ENO] <br> d:=INT_TO_DINT_E(EN,ENO,s); |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $s(I N)$ | Input | Input variable | INT |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | DINT |

## Processing details

■Operation processing

- These functions convert the value input to (s) from INT data type to DINT data type, and output the converted value from (d).

- Input an INT data type value to (s).


## OOperation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE** | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error.

### 20.23 Converting INT to BCD

INT_TO_BCD(_E)
These functions convert a value from INT data type to BCD data type.

| Ladder, FBD/LD |  | Structured text |
| :---: | :---: | :---: |
| [Without EN/ENO] | [With EN/ENO] | ```[Without EN/ENO] d:=INT_TO_BCD(s); [With EN/ENO] d:=INT_TO_BCD_E(EN,ENO,s);``` |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | INT |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | WORD |

## Processing details

■Operation processing

- These functions convert the value input to (s) from INT data type to BCD (WORD) data type, and output the converted value from (d).

- Input an INT data type value to (s) within the range of 0 to 9999.


## ©Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE*1 | Undefined value |

[^44]
### 20.24 Converting INT to REAL

## INT_TO_REAL(_E)

These functions convert a value from INT data type to REAL data type.


## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | INT |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | REAL |

## Processing details

■Operation processing

- These functions convert the value input to (s) from INT data type to REAL data type, and output the converted value from (d).

- Input an INT data type value to (s).


## OOperation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE** $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error

### 20.25 Converting INT to LREAL

## INT_TO_LREAL(_E)

These functions convert a value from INT data type to LREAL data type.

| Ladder, FBD/LD |  | Structured text |
| :---: | :---: | :---: |
| [Without EN/ENO] | [With EN/ENO] | [Without EN/ENO] <br> d:=INT_TO_LREAL(s); <br> [With EN/ENO] <br> d:=INT_TO_LREAL_E(EN,ENO,s); |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}(\mathrm{IN})$ | Input | Input variable | INT |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | LREAL |

## Processing details

■Operation processing

- These functions convert the value input to (s) from INT data type to LREAL data type, and output the converted value from (d).

- Input an INT data type value to (s).


## OOperation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE** | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error.

### 20.26 Converting INT to TIME

## INT_TO_TIME(_E)

These functions convert a value from INT data type to TIME data type.

| Ladder, FBD/LD |  | Structured text |
| :---: | :---: | :---: |
| [Without EN/ENO] | [With EN/ENO] | [Without EN/ENO] <br> d:=INT_TO_TIME(s); <br> [With EN/ENO] <br> d:=INT_TO_TIME_E(EN,ENO,s); |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | INT |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | TIME |

## Processing details

■Operation processing

- These functions convert the value input to (s) from INT data type to TIME data type, and output the converted value from (d).
(s)
(s)


INT type
(d)


- Input an INT data type value to (s).


## ■Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error.

### 20.27 Converting INT to STRING

## INT_TO_STRING(_E)

These functions convert a value from INT data type to STRING data type.

| Ladder, FBD/LD |  | Structured text |
| :---: | :---: | :---: |
| [Without EN/ENO] | [With EN/ENO] | [Without EN/ENO] <br> d:=INT_TO_STRING(s); <br> [With EN/ENO] <br> d:=INT_TO_STRING_E(EN,ENO,s); |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}(\mathrm{IN})$ | Input | Input variable | INT |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | STRING(6) |

## Processing details

## ■Operation processing

- These functions convert the value input to (s) from INT data type to STRING data type, and output the converted value from (d).
(d)

- Input an INT data type value to (s).
- As sign data, 20H (space) is stored if the input value is positive, and 2DH (-) is stored if the value is negative.
- If the number of digits in the input value is less than the number of significant digits, 20 H (space) is stored for the upper digit(s).


## Ex.

When the value -123 is input
(d)


- When SM701 (Number of output characters selection) is off, 00 H is stored at the end of the string (4th word).


## Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE*1 $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error.

### 20.28 Converting DINT to BOOL

## DINT_TO_BOOL(_E)

These functions convert a value from DINT data type to BOOL data type.

| Ladder, FBD/LD |  | Structured text |
| :---: | :---: | :---: |
| [Without EN/ENO] | [With EN/ENO] | [Without EN/ENO] <br> d:=DINT_TO_BOOL(s); <br> [With EN/ENO] <br> d:=DINT_TO_BOOL_E(EN,ENO,s); |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | DINT |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | BOOL |

## Processing details

■Operation processing

- These functions convert the value input to (s) from DINT data type to BOOL data type, and output the converted value from (d).
- When the value 0 is input, FALSE is output.
- When the value other than 0 is input, TRUE is output.

- Input a DINT data type value to (s).


## Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE** $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error

### 20.29 Converting DINT to WORD

## DINT_TO_WORD(_E)

These functions convert a value from DINT data type to WORD data type.

| Ladder, FBD/LD |  | Structured text |
| :---: | :---: | :---: |
| [Without EN/ENO] | [With EN/ENO] | [Without EN/ENO] <br> d:=DINT_TO_WORD(s); <br> [With EN/ENO] <br> d:=DINT_TO_WORD_E(EN,ENO,s); |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | DINT |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | WORD |

## Processing details

## OOperation processing

- These functions convert the value input to (s) from DINT data type to WORD data type, and output the converted value from (d).
- The upper 16-bit data of the input value (DINT data type) are discarded.

- Input a DINT data type value to (s).


## ■Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE** $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

When the DINT_TO_WORD(_E) function is executed, the upper 16-bit data of the input value (DINT data type) are discarded.

## Operation error

There is no operation error

### 20.30 Converting DINT to DWORD

## DINT_TO_DWORD(_E)

These functions convert a value from DINT data type to DWORD data type.

| Ladder, FBD/LD |
| :--- |
| [Without EN/ENO] [With EN/ENO]  Structured text <br> [Without EN/ENO]    <br> d:=DINT_TO_DWORD(s);    <br> [With EN/ENO]    <br> d:=DINT_TO_DWORD_E(EN,ENO,s);    |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | DINT |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | DWORD |

## Processing details

■Operation processing

- These functions convert the value input to (s) from DINT data type to DWORD data type, and output the converted value from (d).

- Input a DINT data type value to (s).


## OOperation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE** $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error

### 20.31 Converting DINT to INT

## DINT_TO_INT(_E)

These functions convert a value from DINT data type to INT data type.

| Ladder, FBD/LD |  | Structured text |
| :---: | :---: | :---: |
| [Without EN/ENO] | [With EN/ENO] | $\begin{aligned} & \text { [Without EN/ENO] } \\ & \text { d:=DINT_TO_INT(s); } \\ & \text { [With EN/ENO] } \\ & \text { d:=DINT_TO_INT_E(EN,ENO,s); } \end{aligned}$ |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | DINT |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | INT |

## Processing details

■Operation processing

- These functions convert the value input to (s) from DINT data type to INT data type, and output the converted value from (d).

- Input a DINT data type value to (s).


## ■Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE*1 | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3401 H | The 32-bit signed binary data input to $(\mathrm{s})$ is out of the range, -32768 to 32767. |

### 20.32 Converting DINT to BCD

## DINT_TO_BCD(_E)

These functions convert a value from DINT data type to BCD data type.

| Ladder, FBD/LD |
| :--- |
| [Without EN/ENO] [With EN/ENO]  Structured text <br> [Without EN/ENO]    <br> d:=DINT_TO_BCD(s);    <br> [With EN/ENO]    <br> d:=DINT_TO_BCD_E(EN,ENO,s);    |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}(\mathrm{IN})$ | Input | Input variable | DINT |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | ANY_BIT |

## Processing details

## ©Operation processing

- These functions convert the value input to (s) from DINT data type to BCD (DWORD) data type, and output the converted value from (d).


99999999


99999999H

- Input a DINT data type value to (s). When (d) is of WORD date type, the input value range is 0 to 9999 . When (d) is of DWORD date type, the input value range is 0 to 99999999 .
- WORD or DWORD data type can be specified for (d). BOOL data type cannot be specified.


## ■Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

- When (d) is of WORD data type

| Error code (SDO) | Description |
| :--- | :--- |
| 3401 H | The 32-bit signed binary data input to (s) is out of the range, -32768 to 32767. |
|  | Data input to $(\mathrm{s}$ ) is out of the range, 0 to 9999. |

- When (d) is of DWORD data type

| Error code (SDO) | Description |
| :--- | :--- |
| 3401 H | Data input to $(\mathrm{s})$ is out of the range, 0 to 99999999. |

### 20.33 Converting DINT to REAL

## DINT_TO_REAL(_E)

These functions convert a value from DINT data type to REAL data type.


## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | DINT |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | REAL |

## Processing details

## ■Operation processing

- These functions convert the value input to (s) from DINT data type to REAL data type, and output the converted value from (d).
(s)
(d)

- Input a DINT data type value to (s).
- The number of significant digits is about seven because a REAL data type value is processed in 32-bit single precision.
- If the integer value exceeds the range of -16777216 to 16777215 , a rounding error occurs in the converted value.


## ©Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error.

### 20.34 Converting DINT to LREAL

## DINT_TO_LREAL(_E)

These functions convert a value from DINT data type to LREAL data type.

| Ladder, FBD/LD |  | Structured text |
| :---: | :---: | :---: |
| [Without EN/ENO] | [With EN/ENO] | [Without EN/ENO] <br> d:=DINT_TO_LREAL(s); <br> [With EN/ENO] <br> d:=DINT_TO_LREAL_E(EN,ENO,s); |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | DINT |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | LREAL |

## Processing details

■Operation processing

- These functions convert the value input to (s) from DINT data type to LREAL data type, and output the converted value from (d).

- Input a DINT data type value to (s).


## Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE** $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error

### 20.35 Converting DINT to TIME

DINT_TO_TIME(_E)
These functions convert a value from DINT data type to TIME data type.


## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | DINT |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | TIME |

## Processing details

■Operation processing

- These functions convert the value input to (s) from DINT data type to TIME data type, and output the converted value from (d).
(s)


DINT type
(d)


TIME type

- Input a DINT data type value to (s).


## ©Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE** | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error.

### 20.36 Converting DINT to STRING

## DINT_TO_STRING(_E)

These functions convert a value from DINT data type to STRING data type.

| Ladder, FBD/LD |  |  | Structured text <br> [Without EN/ENO] <br> d:=DINT_TO_STRING(s); <br> [With EN/ENO] <br> d:=DINT_TO_STRING_E(EN,ENO,s); |
| :---: | :---: | :---: | :---: |
| [Without EN/ENO] | [With EN/ENO] | - |  |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | DINT |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | STRING(11) |

## Processing details

## ■Operation processing

- These functions convert the value input to (s) from DINT data type to STRING data type, and output the converted value from (d).
(d)

- Input a DINT data type value to (s).
- As sign data, 20 H (space) is stored if the input value is positive, and $2 \mathrm{DH}(-)$ is stored if the value is negative.
- If the number of digits in the input value is less than the number of significant digits, 20 H (space) is stored for the upper digit(s).


## Ex.

When the value - 123456 is input
(d)

| Upper byte | Lower byte |  |
| :---: | :---: | :---: |
| 20H (space) | $2 \mathrm{DH}(-)$ | 1st word |
| -123456 | 20 H (space) | 20 H (space) |
| 2nd word |  |  |
| $31 \mathrm{H}(1)$ | 20 H (space) | 3rd word |
| $33 \mathrm{H}(3)$ | $32 \mathrm{H}(2)$ | 4th word |
| $35 \mathrm{H}(5)$ | $34 \mathrm{H}(4)$ | 5th word |
| 00 H | $36 \mathrm{H}(6)$ | 6th word |

- When SM701 (Number of output characters selection) is off, 00 H is stored at the end of the string (upper bytes of the 6th word).


## Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error.

### 20.37 Converting BCD to INT

## BCD_TO_INT(_E)

These functions convert a value from BCD data type to INT data type.

| Ladder, FBD/LD |  | Structured text <br> [Without EN/ENO] $\mathrm{d}:=\mathrm{BCD}$ _TO_INT(s); <br> [With EN/ENO] d:=BCD_TO_INT_E(EN,ENO,s); |
| :---: | :---: | :---: |
| [Without EN/ENO] | [With EN/ENO] |  |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | WORD |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | INT |

## Processing details

■Operation processing

- These functions convert the value input to (s) from BCD (WORD) data type to INT data type, and output the converted value from (d).

- Input a WORD data type value to (s) within the range of 0 H to 9999 H (range of each digit: 0 to 9 ).


## ©Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE $^{* 1}$ | Undefined value |

[^45]
## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3401 H | A value other than 0 to 9 exists at any digit of the value input to (s). |

- Turning on SM754 can prevent the above error from being issued. If the specified value is out of the valid range, the BCD_TO_INT(_E) function is not executed regardless of the status (on/off) of SM754.


### 20.38 Converting BCD to DINT

## BCD_TO_DINT(_E)

These functions convert a value from BCD data type to DINT data type.


## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}(\mathrm{IN})$ | Input | Input variable | ANY_BIT |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | DINT |

## Processing details

## ©Operation processing

- These functions convert the value input to (s) from BCD (WORD or DWORD) data type to DINT data type, and output the converted value from (d).
- When (s) is of WORD data type

- When (s) is of DWORD data type


Filled with 0s.
- Input a WORD data type value within the range of 0 H to 9999 H (range of each digit: 0 to 9 ) or a DWORD date type value within the range of 0 H to 99999999 H (range of each digit: 0 to 9 ) to (s).
- WORD or DWORD data type can be specified for (s). BOOL data type cannot be specified.


## ■Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3401 H | A value other than 0 to 9 exists at any digit of the value input to (s). |

- Turning on SM754 can prevent the above error from being issued. If the specified value is out of the valid range, the BCD_TO_DINT(_E) function is not executed regardless of the status (on/off) of SM754.


### 20.39 Converting BCD to STRING

## BCD_TO_STRING(_E)

These functions convert a value from BCD data type to STRING data type.

| Ladder, FBD/LD |  | Structured text |
| :---: | :---: | :---: |
| [Without EN/ENO] | [With EN/ENO] | [Without EN/ENO] <br> d:=BCD_TO_STRING(s); <br> [With EN/ENO] <br> $\mathrm{d}:=\mathrm{BCD}$ _TO_STRING_E(EN,ENO,s); |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | ANY_BIT |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | STRING(8) |

## Processing details

©Operation processing

- These functions convert the value input to (s) from BCD (WORD or DWORD) data type to STRING data type, and output the converted value from (d).
- When (s) is of WORD data type

- When (s) is of DWORD data type
(s)


BCD type (DWORD type)
(d)


- WORD or DWORD data type can be specified for (s). BOOL data type cannot be specified.
- When SM701 (Number of output characters selection) is off, 00 H is stored at the end of the string.


## ©Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE $^{* 1}$ | Undefined value |

[^46]
## Operation error

- When (s) is of WORD data type

| Error code (SDO) | Description |
| :--- | :--- |
| 3401 H | Data input to (s) is out of the range, 0 to 9999. |

- When (s) is of DWORD data type
Error code (SDO) $\quad$ Description

3401H Data input to (s) is out of the range, 0 to 99999999.

### 20.40 Converting REAL to INT

## REAL_TO_INT(_E)

These functions convert a value from REAL data type to INT data type.

| Ladder, FBD/LD |  | Structured text <br> [Without EN/ENO] d:=REAL_TO_INT(s); <br> [With EN/ENO] <br> d:=REAL_TO_INT_E(EN,ENO,s); |
| :---: | :---: | :---: |
| [Without EN/ENO] | [With EN/ENO] |  |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | REAL |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | INT |

## Processing details

## ■Operation processing

- These functions convert the value input to (s) from REAL data type to INT data type, and output the converted value from (d).


- Input a REAL data type value to (s) within the range of -32768 to 32767 .
- After conversion, the first digit after the decimal point of the input value (REAL data type) is rounded off.


## ©Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE*1 $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

| Error code (SDO) | Description |
| :---: | :---: |
| 3401 H | The single-precision real number input to (s) is out of the range, -32768 to 32767 . |
| 3402 H | - An unusual number is input to (s). <br> - The single-precision real number input to (s) is not within the following range: $\begin{aligned} & -2^{128}<(\mathrm{s}) \leq-2^{-126}, 0,2^{-126} \leq(\mathrm{s})<2^{128} \\ & (\mathrm{E}-3.40282347+38 \sim \mathrm{E}-1.17549435-38,0, \mathrm{E} 1.17549435-38 \sim \mathrm{E} 3.40282347+38) \end{aligned}$ <br> - The value set to a device or label is -0 , a subnormal number, NaN (not a number), or $\pm \infty$. |

### 20.41 Converting REAL to DINT

## REAL_TO_DINT(_E)

These functions convert a value from REAL data type to DINT data type.

| Ladder, FBD/LD |  | Structured text |
| :---: | :---: | :---: |
| [Without EN/ENO] | [With EN/ENO] | $\begin{aligned} & \text { [Without EN/ENO] } \\ & \text { d:=REAL_TO_DINT(s); } \\ & \text { [With EN/ENO] } \\ & \text { d:=REAL_TO_DINT_E(EN,ENO,s); } \end{aligned}$ |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | REAL |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | DINT |

## Processing details

■Operation processing

- These functions convert the value input to (s) from REAL data type to DINT data type, and output the converted value from (d).

> (s)
(d)


- Input a REAL data type value to (s) within the range of -2147483648 to 2147483647.
- After conversion, the first digit after the decimal point of the input value (REAL data type) is rounded off.


## ■Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE** $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

| Error code (SD0) | Description |
| :---: | :---: |
| 3401H | The single-precision real number input to (s) is out of the range, -2147483648 to 2147483647. |
| 3402 H | An unusual number is input to (s). <br> - The single-precision real number input to (s) is not within the following range: $\begin{aligned} & -2^{128}<(\mathrm{s}) \leq-2^{-126}, 0,2^{-126} \leq(\mathrm{s})<2^{128} \\ & (\mathrm{E}-3.40282347+38 \sim \mathrm{E}-1.17549435-38,0, \mathrm{E} 1.17549435-38 \sim \mathrm{E} 3.40282347+38) \end{aligned}$ <br> - The value set to a device or label is -0 , a subnormal number, NaN (not a number), or $\pm \infty$. |

### 20.42 Converting REAL to LREAL

## REAL_TO_LREAL(_E)

These functions convert a value from REAL data type to LREAL data type.

| Ladder, FBD/LD |
| :--- |
| [Without EN/ENO] [With EN/ENO]  Structured text |
| [Without EN/ENO] <br> d:=REAL_TO_LREAL(s); <br> [With EN/ENO] <br> d:=REAL_TO_LREAL_E(EN,ENO,s); |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | REAL |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | LREAL |

## Processing details

## ■Operation processing

- These functions convert the value input to (s) from REAL data type to LREAL data type, and output the converted value from (d).

- Input a REAL data type value to (s).
- The number of significant digits is about seven because a REAL data type value is processed in 32-bit single precision.
- If the integer value exceeds the range of -16777216 to 16777215 , a rounding error occurs in the converted value.


## ©Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The input value is -0 or not in the following range: <br> $-2^{128}<(\mathrm{s}) \leq-2^{-126}, 0,2^{-126} \leq(\mathrm{s})<2^{128}$ |
|  | $(\mathrm{E}-3.40282347+38 \sim \mathrm{E}-1.17549435-38,0, \mathrm{E} 1.17549435-38 \sim \mathrm{E} 3.40282347+38)$ |
| 3403 H | The data output from (d) exceeds the following range. (An overflow has occurred.) <br> $\|(\mathrm{d})\|<2^{128}$ |

### 20.43 Converting REAL to STRING

REAL_TO_STRING(_E)
These functions converts a REAL data type value to STRING data type (exponential form).


## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | REAL |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | STRING(13) |

## Processing details

■Operation processing

- These functions convert the value input to (s) from REAL data type to STRING data type (exponential form), and output the converted value from (d).

- Input a REAL data type value to (s).
- The converted string data is output from (d) as follows.
- The number of digits for an integral part, decimal part, and exponent is fixed, integral part: one digit; decimal part: five digits; exponent: two digits
- As the second byte, 20 H (space) is stored; as the fourth byte, 2 EH (.) is stored; and as the 10 th byte $45 \mathrm{H}(\mathrm{E})$ is stored automatically.
(d)

- As sign data (for integral part), 20H (space) is stored if the input value is positive, and $2 \mathrm{DH}(-)$ is stored if the input value is negative
- The sixth and later digits of the decimal part are rounded off.
(d)

- If the number of digits in the input value is less than the number of significant digits, $30 \mathrm{H}(0)$ is stored in the decimal part.
(d)

- As sign data (for exponent), 2BH (+) is stored if the input value is positive, and $2 \mathrm{DH}(-)$ is stored if the input value is negative.
- When the exponent is one digit, $30 \mathrm{H}(0)$ is stored in the tens place of the exponent.
(d)

- The NULL code $(00 \mathrm{H})$ is automatically stored at the end (i.e. seventh word) of the converted string.


## Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE*1 $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

| Error code (SDO) | Description |
| :---: | :---: |
| 3402 H | - The value input to (s) is out of the following range: $\begin{aligned} & -2^{128}<(\mathrm{s}) \leq-2^{-126}, 0,2^{-126} \leq(\mathrm{s})<2^{128} \\ & (\mathrm{E}-3.40282347+38 \sim \mathrm{E}-1.17549435-38,0, \mathrm{E} 1.17549435-38 \sim \mathrm{E} 3.40282347+38) \end{aligned}$ <br> - The value input to (s) is -0 , a subnormal number, NaN (not a number), or $\pm \infty$. |
| 3406H | The entire string after conversion cannot be stored in the label or device area (between the specified device number and the last device number) specified by (d). (The number of required points is insufficient.) |

### 20.44 Converting LREAL to INT

## LREAL_TO_INT(_E)

These functions convert a value from LREAL data type to INT data type.

| Ladder, FBD/LD |
| :--- |
| [Without EN/ENO] [With EN/ENO]  Structured text <br> [Without EN/ENO]    <br> d:=LREAL_TO_INT(s);    <br> [With EN/ENO]    <br> d:=LREAL_TO_INT_E(EN,ENO,s);    |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | LREAL |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | INT |

## Processing details

■Operation processing

- These functions convert the value input to (s) from LREAL data type to INT data type, and output the converted value from (d).

- Input an LREAL data type value to (s).
- After conversion, the first digit after the decimal point of the input value (LREAL data type) is rounded off.


## ■Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The input value is -0 or not in the following range: |
|  | $-2^{1024<(\mathrm{s}),(\mathrm{d}) \leq-2^{-1022}, 0,2^{-1022} \leq(\mathrm{s}),(\mathrm{d})<2^{1024}}$ |
|  | $(\mathrm{E}-1.7976931348623157+308 \sim \mathrm{E}-2.2250738585072014-308,0, \mathrm{E} 2.2250738585072014-308 \sim \mathrm{E} 1.7976931348623157+308)$ |
|  | The input value is out of the range, -32768 to 32767. |

### 20.45 Converting LREAL to DINT

## LREAL_TO_DINT(_E)

These functions convert a value from LREAL data type to DINT data type.

| Ladder, FBD/LD |  | Structured text |
| :---: | :---: | :---: |
| [Without EN/ENO] | [With EN/ENO] | [Without EN/ENO] <br> d:=LREAL_TO_DINT(s); <br> [With EN/ENO] <br> d:=LREAL_TO_DINT_E(EN,ENO,s); |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}(\mathrm{IN})$ | Input | Input variable | LREAL |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | DINT |

## Processing details

■Operation processing

- These functions convert the value input to (s) from LREAL data type to DINT data type, and output the converted value from (d).

- Input an LREAL data type value to (s).
- After conversion, the first digit after the decimal point of the input value (LREAL data type) is rounded off.

■Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE*1 $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The input value is -0 or not in the following range: |
|  | $-2^{1024<(\mathrm{s}),(\mathrm{d}) \leq-2^{-1022}, 0,2^{-1022} \leq(\mathrm{s}),(\mathrm{d})<2^{1024}}$ |
|  | $(\mathrm{E}-1.7976931348623157+308 \sim \mathrm{E}-2.2250738585072014-308,0, \mathrm{E} 2.2250738585072014-308 \sim \mathrm{E} 1.7976931348623157+308)$ |
|  | The input value is out of the range, -2147483648 to 2147483647. |

### 20.46 Converting LREAL to REAL

## LREAL_TO_REAL(_E)

These functions convert a value from LREAL data type to REAL data type.


## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | LREAL |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | REAL |

## Processing details

■Operation processing

- These functions convert the value input to (s) from LREAL data type to REAL data type, and output the converted value from (d).

- Input an LREAL data type value to (s).


## ©Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The input value is -0 or not in the following range: <br> $--^{1024}<$ (s), (d) $\leq-2^{-1022}, 0,2^{-1022} \leq(\mathrm{s}),(\mathrm{d})<2^{1024}$ |
|  | $(\mathrm{E}-1.7976931348623157+308 \sim \mathrm{E}-2.2250738585072014-308,0$, E2.2250738585072014-308~E1.7976931348623157+308) |
| 3403 H | The data output from (d) exceeds the following range. (An overflow has occurred.) <br> $\|(\mathrm{d})\|<2^{128}$ |

### 20.47 Converting TIME to BOOL

## TIME_TO_BOOL(_E)

These functions convert a value from TIME data type to BOOL data type.

| Ladder, FBD/LD |
| :--- |
| [Without EN/ENO] [With EN/ENO]  Structured text <br> [Without EN/ENO]    <br> d:=TIME_TO_BOOL(s);    <br> [With EN/ENO]    <br> d:=TIME_TO_BOOL_E(EN,ENO,s);    |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | TIME |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | BOOL |

## Processing details

■Operation processing

- These functions convert the value input to (s) from TIME data type to BOOL data type, and output the converted value from (d).
(s) (d)



## ©Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE*1 | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error.

### 20.48 Converting TIME to WORD

## TIME_TO_WORD(_E)

These functions convert a value from TIME data type to WORD data type.

| Ladder, FBD/LD |  | Structured text <br> [Without EN/ENO] <br> d:=TIME_TO_WORD(s); <br> [With EN/ENO] <br> d:=TIME_TO_WORD_E(EN,ENO,s); |
| :---: | :---: | :---: |
| [Without EN/ENO] | [With EN/ENO] |  |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | TIME |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | WORD |

## Processing details

■Operation processing

- These functions convert the value input to (s) from TIME data type to WORD data type, and output the converted value from (d).

- Input a TIME data type value to (s).


## OOperation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE** $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error.

### 20.49 Converting TIME to DWORD

## TIME_TO_DWORD(_E)

These functions convert a value from TIME data type to DWORD data type.

| Ladder, FBD/LD |  | Structured text |
| :---: | :---: | :---: |
| [Without EN/ENO] | [With EN/ENO] | ```[Without EN/ENO] d:=TIME_TO_DWORD(s); [With EN/ENO] d:=TIME_TO_DWORD_E(EN,ENO,s);``` |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}(\mathrm{IN})$ | Input | Input variable | TIME |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | DWORD |

## Processing details

■Operation processing

- These functions convert the value input to (s) from TIME data type to DWORD data type, and output the converted value from (d).

- Input a TIME data type value to (s).


## OOperation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE** | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error.

### 20.50 Converting TIME to INT

## TIME_TO_INT(_E)

These functions convert a value from TIME data type to INT data type.

| Ladder, FBD/LD |  | Structured text |
| :---: | :---: | :---: |
| [Without EN/ENO] | [With EN/ENO] | ```[Without EN/ENO] d:=TIME_TO_INT(s); [With EN/ENO] d:=TIME_TO_INT_E(EN,ENO,s);``` |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | TIME |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal or operation stop) | Output variable | BOOL |
| d | Output | Output variable | INT |

## Processing details

■Operation processing

- These functions convert the value input to (s) from TIME data type to INT data type, and output the converted value from (d).

- Input a TIME data type value to (s).
- The upper 16-bit data of the input value (TIME data type) are discarded.


## ©Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error

### 20.51 Converting TIME to DINT

## TIME_TO_DINT(_E)

These functions convert a value from TIME data type to DINT data type.

| Ladder, FBD/LD |  | Structured text |
| :---: | :---: | :---: |
| [Without EN/ENO] | [With EN/ENO] | ```[Without EN/ENO] d:=TIME_TO_DINT(s); [With EN/ENO] d:=TIME_TO_DINT_E(EN,ENO,s);``` |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}(\mathrm{IN})$ | Input | Input variable | TIME |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal or operation stop) | Output variable | BOOL |
| d | Output | Output variable | DINT |

## Processing details

■Operation processing

- These functions convert the value input to (s) from TIME data type to DINT data type, and output the converted value from (d).


- Input a TIME data type value to (s).


## OOperation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE** | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error

### 20.52 Converting TIME to STRING

## TIME_TO_STRING(_E)

These functions convert a value from TIME data type to STRING data type.


## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | TIME |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | STRING <br> STRING(11) |

## Processing details

## Operation processing

- These functions convert the value input to (s) from TIME data type to STRING data type, and output the converted value from (d).
(s)
(d)

(s)

- Input a TIME data type value to (s).
- When SM701 (Number of output characters selection) is off, 00 H is stored at the end of the string.
- The operation result will be as follows.
- As the first character, 20 H (space) is stored if the output value is positive, and 2DH (-) is stored if the output value is negative.
- At the left of the number of significant digits, 20 H (space) is stored.


## ■Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result | (d) |
| :--- | :--- | :--- |
| EN | ENO | Operation result output value |
| TRUE (executed) | TRUE | Undefined value |
| FALSE (not executed) | FALSE $^{* 1}$ |  |

[^47]
## Operation error

There is no operation error

### 20.53 Converting STRING to BOOL

## STRING_TO_BOOL(_E)

These functions convert a value from STRING data type to BOOL data type.

| Ladder, FBD/LD |  | Structured text |
| :---: | :---: | :---: |
| [Without EN/ENO] | [With EN/ENO] | [Without EN/ENO] d:=STRING_TO_BOOL(s); <br> [With EN/ENO] <br> d:=STRING_TO_BOOL_E(EN,ENO,s); |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}(\mathrm{IN})$ | Input | Input variable | STRING(1) |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | BOOL |

## Processing details

■Operation processing

- These functions convert the value input to (s) from STRING data type (decimal form/exponential form) to BOOL data type, and output the converted value from (d).



## Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE*1 | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error.

### 20.54 Converting STRING to WORD

## STRING_TO_WORD(_E)

These functions convert a value from STRING data type to WORD data type.

| Ladder, FBD/LD |  | Structured text |
| :---: | :---: | :---: |
| [Without EN/ENO] | [With EN/ENO] | [Without EN/ENO] d:=STRING_TO_WORD(s); <br> [With EN/ENO] d:=STRING_TO_WORD_E(EN,ENO,s); |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | STRING(4) |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | WORD |

## Processing details

■Operation processing

- These functions convert the value input to (s) from STRING data type to WORD data type, and output the converted value from (d).

- Input a STRING data type value to (s).


## Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3401 H | An ASCII code other than 30 H to 39 H and 41 H to 46 H is input. |

### 20.55 Converting STRING to DWORD

## STRING_TO_DWORD(_E)

These functions convert a value from STRING data type to DWORD data type.

| Ladder, FBD/LD |
| :--- |
| [Without EN/ENO] [With EN/ENO]  Structured text <br> [Without EN/ENO]    <br> d:=STRING_TO_DWORD(s);    <br> [With EN/ENO]    <br> d:=STRING_TO_DWORD_E(EN,ENO,s);    |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN) | Input | Input variable | STRING(8) |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | DWORD |

## Processing details

■Operation processing

- These functions convert the value input to (s) from STRING data type to DWORD data type, and output the converted value from (d).

- Input a STRING data type value to (s).


## Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3401 H | An ASCII code other than 30 H to 39 H and 41 H to 46 H is input. |

### 20.56 Converting STRING to INT

## STRING_TO_INT(_E)

These functions convert a value from STRING data type to INT data type.

| Ladder, FBD/LD |  | Structured text |
| :---: | :---: | :---: |
| [Without EN/ENO] | [With EN/ENO] | ```[Without EN/ENO] d:=STRING_TO_INT(s); [With EN/ENO] d:=STRING_TO_INT_E(EN,ENO,s);``` |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | STRING(6) |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | INT |

## Processing details

■Operation processing

- These functions convert the value input to (s) from STRING data type to INT data type, and output the converted value from (d).
(s)

| 1st word | Upper byte | Lower byte | (d) |
| :---: | :---: | :---: | :---: |
|  | ASCII code (ten-thousands place) | ASCII code (sign data) |  |
| 2nd word | ASCII code (hundreds place) | ASCII code (thousands place) |  |
| 3rd word | ASCII code (ones place) | ASCII code (tens place) |  |
| 4th word | 00 H (Null character (end of string)) |  | INT type |

- Input a STRING data type value to (s) within the following range.
- ASCII code: 30 H to $39 \mathrm{H}, 20 \mathrm{H}, 2 \mathrm{DH}$, and 00 H
- STRING data type value: -32768 to 32767


## Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE** $^{*}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3401 H | An out-of-range value is input to $(\mathrm{s})$. <br>  <br>  <br>  <br>  <br> • ASCII code: other than 30 H to $39 \mathrm{H}, 20 \mathrm{H}$, and 00 H |

### 20.57 Converting STRING to DINT

## STRING_TO_DINT(_E)

These functions convert a value from STRING data type to DINT data type.


## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | STRING(11) |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | DINT |

## Processing details

■Operation processing

- These functions convert the value input to (s) from STRING data type to DINT data type, and output the converted value from (d).

- Input a STRING data type value to ( $s$ ) within the following range.
- ASCII code: 30 H to $39 \mathrm{H}, 20 \mathrm{H}, 2 \mathrm{DH}$, and 00 H
- STRING data type value: -2147483648 to 2147483647


## ©Operation result

## 1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3401 H | An out-of-range value is input to $(\mathrm{s})$. <br>  <br>  <br>  <br>  <br> • ASCII code: other than 30 H to $39 \mathrm{H}, 20 \mathrm{H}$, and 00 H |

### 20.58 Converting STRING to BCD

## STRING_TO_BCD(_E)

These functions convert a value from STRING data type to BCD data type.

| Ladder, FBD/LD |  | Structured text <br> [Without EN/ENO] d:=STRING_TO_BCD(s); <br> [With EN/ENO] d:=STRING_TO_BCD_E(EN,ENO,s); |
| :---: | :---: | :---: |
| [Without EN/ENO] | [With EN/ENO] |  |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}(\mathrm{IN})$ | Input | Input variable | STRING(8) |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | ANY_BIT |

## Processing details

■Operation processing

- These functions convert the value input to (s) from STRING data type to BCD (WORD) data type, and output the converted value from (d).
(s)
(d)

- The ASCII code 20 H (space) that exists in the the string is ignored.
- The ASCII codes 20 H (space) and $30 \mathrm{H}(0)$ that exist in the string are counted as one character as well.
- Input a STRING data type value to (s) within the following range.

ASCII code: 30 H to $39 \mathrm{H}, 20 \mathrm{H}, 00 \mathrm{H}$

- If the string input has less than four characters, the string will be supplemented with zero(s). (Zero(s) is/are added at the end of the string.) For this reason, if a string shorter than four characters is to be converted, input a character string padded with 0s (e.g. '0001' for '1').
- If the string length exceeds 4 characters, the four left characters are regarded as the targets to convert.

| Input string | Conversion target string | Output (BCD data type) |
| :---: | :---: | :---: |
| '1' | '1000' | 1000H (4096D) |
| '12' | '1200' | 1200H(4608D) |
| '123' | '1230' | 1230H(4656D) |
| '1234' | '1234' | 1234H(4660D) |
| '12345' | '1234' | 1234H(4660D) |

- WORD or DWORD data type can be specified for (d). BOOL data type cannot be specified.


## Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE*1 $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

| Error code (SD0) | Description |
| :--- | :--- |
| 3401 H | An ASCII code other than 30 H to $39 \mathrm{H}, 20 \mathrm{H}$, or 00 H is input. |

### 20.59 Converting STRING to REAL

## STRING_TO_REAL(_E)

These functions convert a value from STRING data type to REAL data type.

| Ladder, FBD/LD |
| :--- |
| [Without EN/ENO] [With EN/ENO]  Structured text <br> [Without EN/ENO]    <br> d:=STRING_TO_REAL(s);    <br> [With EN/ENO]    <br> d:=STRING_TO_REAL_E(EN,ENO,s);    |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}(\mathrm{IN})$ | Input | Input variable | STRING(24) |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | REAL |

## Processing details

■Operation processing

- These functions convert the value input to (s) from STRING data type (decimal form/exponential form) to REAL data type, and output the converted value from (d).

| 1st word | (s) |  |
| :---: | :---: | :---: |
|  | Upper byte Lower byte |  |
|  | ASCII code (1st character) ${ }_{\text {a }}$ ASCII code (sign data) |  |
| 2nd word | ASCII code (3rd character) ${ }^{\text {a }}$ ASCII code (2nd character) |  |
| 3rd word | ASCII code (5th character) ${ }_{\text {l }}$ ASCII code (4th character) | (d) |
| 4th word | ASCII code (7th character) ${ }_{\text {l }}$ ASCII code (6th character) | $\square$ |
| 5 th word | ASCII code (9th character) ${ }_{\text {l }}$ ASCII code (8th character) | REAL type |
| 6 th word | ASCII code (11th character) ${ }_{\text {a }}$ ASCII code (10th character) |  |
| 7 th word | 00 H (Null character (end of string)) |  |
|  | STRING type |  |

- The STRING data type value both in decimal form and exponential form can be converted.
- When (s) is in decimal form

- When (s) is in exponential form
(s)

| 1st word | Upper byte | Lower byte |  |
| :---: | :---: | :---: | :---: |
|  | $31 \mathrm{H}(1)$ | 2DH(-) |  |
| 2nd word | $33 \mathrm{H}(3)$ | 2EH(.) |  |
| 3rd word | $30 \mathrm{H}(0)$ | $35 \mathrm{H}(5)$ | (d) |
| 4th word | $34 \mathrm{H}(4)$ | $33 \mathrm{H}(3)$ | -1.35034E-10 |
| 5th word | 2DH(-) | $45 \mathrm{H}(\mathrm{E})$ | REAL type |
| 6th word | $30 \mathrm{H}(0)$ | $31 \mathrm{H}(1)$ | , |
| 7th word | 00H |  |  |

:-un

- The number of significant digits of the STRING data type value is six. (The sign, decimal point, and exponent are not included.) The seventh and later digits are rounded down when the data is converted.
- When (s) is in decimal form

- When (s) is in exponential form

- In decimal form, when 2BH (+) is specified as sign data or the sign data is omitted, the data is converted as a positive value. When 2DH (-) is specified, the data is converted as a negative value.
- In exponential form, when 2BH (+) is specified as sign data for the exponent or the sign data is omitted, the data is converted as a positive value. When 2DH (-) is specified, the data is converted as a negative value.
- The ASCII code 20 H (space) or $30 \mathrm{H}(0)$ that exists before the first numerical value 0 in the STRING data type value is ignored.
- When (s) is in decimal form

-When (s) is in exponential form

- The ASCII code $30 \mathrm{H}(0)$ that exists between E and a numerical value in the STRING data type value is ignored (in exponential form only).

- The ASCII code 20 H (space) that exists in the the string is ignored.
- Up to 24 characters can be input. The ASCII codes 20 H (space) and $30 \mathrm{H}(0)$ that exist in the string are counted as one character as well.
- Input a STRING data type value to (s) within the following range.
- ASCII code: 30 H to $39 \mathrm{H}, 45 \mathrm{H}, 2 \mathrm{BH}, 2 \mathrm{DH}, 2 \mathrm{EH}, 20 \mathrm{H}$, and 00 H


## -Operation result

## 1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).

## 2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2820 H | There is no NULL code $(00 \mathrm{H})$ in the device area (between the specified device number and the last device number) specified by (s). |
| 3401 H | An out-of-range value is input to (s). <br> - The integral part or decimal part contains a character other than $30 \mathrm{H}(0)$ to $39 \mathrm{H}(9)$. <br> - More than one $2 \mathrm{EH}($.$) exists in the specified string.$ <br> - The exponent of the specified character string contains a character other than $45 \mathrm{H}(\mathrm{E}), 65 \mathrm{H}(\mathrm{e}), 2 \mathrm{BH}(+)$, and $2 \mathrm{DH}(-)$. <br> - The specified character string contains more than one exponent $45 \mathrm{H}(\mathrm{E})$ or $65 \mathrm{H}(\mathrm{e})$. <br> - The exponent in the specified string contains a numerical value consisting of three digits or more. <br> - The exponent of the specified character string contains more than one sign $2 \mathrm{BH}(+)$ or $2 \mathrm{DH}(-)$. |
| - The specified string (in the integral part if the decimal format is used or in the mantissa if the exponent format is used) contains more |  |
| than one sign data of $2 \mathrm{BH}(+)$ or $2 \mathrm{DH}(-)$. |  |

### 20.60 Converting STRING to TIME

## STRING_TO_TIME(_E)

These functions convert a value from STRING data type to TIME data type.

| Ladder, FBD/LD |
| :--- |
| [Without EN/ENO] [With EN/ENO]  Structured text <br> [Without EN/ENO]    <br> d:=STRING_TO_TIME(s);    <br> [With EN/ENO]    <br> d:=STRING_TO_TIME_E(EN,ENO,s);    |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | STRING(11) |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | TIME |

## Processing details

■Operation processing

- These functions convert the value input to (s) from STRING data type to TIME data type, and output the converted value from (d).

- Input a STRING data type value to (s).


## Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3401 H | An ASCII code other than 30 H to $39 \mathrm{H}, 20 \mathrm{H}$, and 00 H is input. |
|  | The STRING data type value input is out of the following range: <br>  $\mathbf{- 2 1 4 7 4 8 3 6 4 8 \text { to } 4 1 4 7 4 8 3 6 4 7}$ |

### 20.61 Converting Bit Array to INT

## BITARR_TO_INT(_E)

These functions convert the specified number of bits in a bit array to an INT data type value.


## Setting data

-Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| s (BitArr) | Input (An element can be specified by a variable.) | Input variable | Boolean array element |
| n | Number of bits (4, 8, 12, or 16) | Input variable | INT |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal or operation stop) | Output variable | BOOL |
| d | Output | Output variable | ANY16 |

## Processing details

## OOperation processing

- These functions convert the number of bits specified by $(\mathrm{n})$ starting from the bit array element input to (s) to any 16-bit data type value, and output the converted value from (d).
- Zeros (0s) are set for all the bits exceeding the specified number of bits.


## OOperation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE** $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error.

### 20.62 Converting Bit Array to DINT

## BITARR_TO_DINT(_E)

These functions convert the specified number of bits in a bit array to a DINT data type value.


## Setting data

## Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| s (BitArr) | Input (An element can be specified by a variable.) | Input variable | Boolean array element |
| n | Number of bits (4, 8, 12, 16, 20, 24, 28, or 32) | Input variable | INT |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal or operation stop) | Output variable | BOOL |
| d | Output | Output variable | ANY32 |

## Processing details

## OOperation processing

- These functions convert the number of bits specified by ( n ) starting from the bit array element input to (s) to any 32-bit data type value, and output the converted value from (d).
- Zeros (0s) are set for all the bits exceeding the specified number of bits.


## OOperation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).

## 2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE** $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error.

### 20.63 Converting INT to Bit Array

## INT_TO_BITARR(_E)

These functions output the lower $n$ bits of the INT data type value to the bit array.


## Setting data

■Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| s | Input | Input variable | ANY16 |
| n | Number of bits (4, 8, 12, or 16) | Input variable | INT |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal or operation stop) | Output variable | BOOL |
| d | Output (An element can be specified by a variable.) | Output variable | Boolean array element |

## Processing details

## OOperation processing

- These functions output the lower (n) bits of ANY16 type data specified by (s) to (d).
- The output bits beyond the specified number of bits are not changed.


## ©Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error.

### 20.64 Converting DINT to Bit Array

## DINT_TO_BITARR(_E)

These functions output the lower $n$ bits of the DINT data type value to the bit array.


## Setting data

## Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| s | Input | Input variable | ANY32 |
| n | Number of bits (4, 8, 12, 16, 20, 24, 28, or 32) | Input variable | BOOL |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal or operation stop) | Output variable | BOOL |
| d | Output (An element can be specified by a variable.) | Output variable | Boolean array element |

## Processing details

## -Operation processing

- These functions output the lower (n) bits of any 32-bit data type value specified by (s) to (d).
- The output bits beyond the specified number of bits are not changed.


## ©Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error.

### 20.65 Copying the Bit Array

## CPY_BITARR(_E)

These functions copy the bit array by the specified number of bits.


## Setting data

■Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| s (BitArrIn) | Input | Input variable | Boolean array element |
| n | Number of bits (4, 8, 12, 16, 20, 24, 28, or 32) | Input variable | INT |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal or operation stop) | Output variable | BOOL |
| d | Output | Output variable | Boolean array element |

## Processing details

## -Operation processing

- These functions output the bit array (number of (n) bits) specified by (s) to (d).


## ■Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE*1 $^{*}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error.

### 20.66 Reading the Specified Bit of the Word Label

## GET_BIT_OF_INT(_E)

These functions read a value from the specified bit of a word label.

*1 FBD/LD is not supported. For FBD/LD, use the bit specification of labels.

## Setting data

■Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| s | Input | Input variable | INT |
| n | Number of bits (0 to 15) | Input variable | INT |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal or operation stop) | Output variable | BOOL |
| d | Output | Output variable | BOOL |

## Processing details

## Operation processing

- These functions output a value in the (n)th bit of (s).


## Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE*1 $^{*}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error.

By using the bit specification of labels, a concise program having the same operation as GET_BIT_OF_INT can be created.
The following example reads the value in bit 5 (b5) of g_int1 into g_bool1 the same as when GET_BIT_OF_INT is used.
Ladder


ST
g_bool1 := g_int1.5;
FBD/LD

### 20.67 Writing the Specified Bit of the Word Label

## SET_BIT_OF_INT(_E)

These functions write a value to the specified bit of a word label.

*1 FBD/LD is not supported. For FBD/LD, use the bit specification of labels.

## Setting data

—Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| s | Input | Input variable | BOOL |
| n | Number of bits (0 to 15) | Input variable | INT |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal or operation stop) | Output variable | BOOL |
| d | Input/output | Input/output variable | INT |

## Processing details

## Operation processing

- These functions write a BOOL data type value specified by (s) to the (n)th bit of (d).


## Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE*1 $^{*}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Precautions

When using SET_BIT_OF_INT(_E) in ST, create a program which assigns the return value to a variable.
//The return value of SET_BIT_OF_INT is assigned to a variable and used.
g_int1 := SET_BIT_OF_INT(TRUE, 0);
g_bool1 := GET_BIT_OF_INT(g_int1, 0);
The return value of SET_BIT_OF_INT(_E) also works as input/output. Do not directly assign it to the input argument of another instruction, function, or function block.
//In the following program, the value of the first argument of GET_BIT_OF_INT is undefined.
g_bool1 := GET_BIT_OF_INT( SET_BIT_OF_INT(TRUE, 0), 0);

## Operation error

There is no operation error.

By using the bit specification of labels, a concise program having the same operation as SET_BIT_OF_INT can be created.
The following example changes the value in bit 5 (b5) of g_int1 to the value of g_bool the same as when SET_BIT_OF_INT is used.
Ladder


ST
g_int1.5 := g_bool1;
FBD/LD


### 20.68 Copying the Specified Bit of the Word Label

CPY_BIT_OF_INT(_E)
These functions copy the specified bit of the word label to the specified bit of another word label.

*1 $\mathrm{FBD} / \mathrm{LD}$ is not supported. For FBD/LD, use the bit specification of labels.

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| s | Input | Input variable | INT |
| n 1 | Number of bits in input variable (0 to 15) | Input variable | INT |
| n 2 | Number of bits in output variable (0 to 15) | Input variable | INT |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal or operation stop) | Output variable | BOOL |
| d | Input/output | Input/output variable | INT |

## Processing details

## ■Operation processing

- These functions copy the value in the (n1)th bit of the word specified by (s) to the (n2)th bit of (d).


## ■Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE*1 | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Precautions

When using CPY_BIT_OF_INT(_E) in ST, create a program which assigns the return value to a variable.
//The return value of CPY_BIT_OF_INT is assigned to a variable and used.
g_int2 := CPY_BIT_OF_INT(g_int1,5,3);
g_bool1 := GET_BIT_OF_INT(g_int2,3);
The return value of CPY_BIT_OF_INT(_E) also works as input/output. Do not directly assign it to the input argument of another instruction, function, or function block.
//In the following program, the value of the first argument of GET_BIT_OF_INT is undefined.
g_bool1 := GET_BIT_OF_INT( CPY_BIT_OF_INT(g_int1,5,3), 3);

## Operation error

There is no operation error.

By using the bit specification of labels, a concise program having the same operation as CPY_BIT_OF_INT can be created.
The following example changes the value in bit $3(\mathrm{~b} 3)$ of $\mathrm{g}_{-}$int2 to the value of bit 5 (b5) of $\mathrm{g}_{-}$int 1 the same as when CPY_BIT_OF_INT is used.
Ladder


ST
g_int2.3 := g_int1.5;
FBD/LD


### 20.69 Getting the Start Data

## GET_BOOL_ADDR, GET_INT_ADDR, GET_WORD_ADDR

These functions output the start data of the specified data as BOOL, INT, or WORD type data.

| Ladder, FBD/LD |  | Structured text |
| :---: | :---: | :---: |
|  |  | $\begin{aligned} & \mathrm{d}:=\mathrm{GET} \text { _BOOL_ADDR(s) } \\ & \mathrm{d}:=\mathrm{GET} \text { _INT_ADDR(s); } \\ & \mathrm{d}:=\mathrm{GET} \text { _WORD_ADDR(s); } \end{aligned}$ |

Setting data
■Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| $s$ | Input | Input variable | ANY |
| $d$ | Output | Output variable | BOOL/INT/WORD |

## Processing details

## -Operation processing

- The GET_BOOL_ADDR function outputs the start data of the data specified by (s) as BOOL type data.
- The GET_INT_ADDR function outputs the start data of the data specified by (s) as INT type data.
- The GET_WORD_ADDR function outputs the start data of the data specified by (s) as WORD type data.

| Standard function | Input data type | Output data type |
| :--- | :--- | :--- |
| GET_BOOL_ADDR | BOOL |  |
|  | ARRAY OF BOOL | BOOL |
| GET_INT_ADDR | INT |  |
| GET_WORD_ADDR | DINT | WORD |
|  | REAL | WORD |
|  | TIME |  |
|  | STRING |  |
|  | ARRAY OF INT |  |
|  | ARRAY OF DINT |  |
|  | ARRAY OF WORD |  |
|  | ARRAY OF DWORD |  |
|  | ARRAY OF REAL |  |
|  | ARRAY OF TIME |  |

## Operation result

The operation processing is performed. The operation result is output from (d).

## Operation error

There is no operation error.

## 21 SINGLE VARIABLE FUNCTIONS

### 21.1 Calculating the Absolute Value

## ABS(_E)

These functions output the absolute value of an input value.


## Setting data

■Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}(\mathrm{IN})$ | Input | Input variable | ANY_NUM |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | ANY_NUM |

## Processing details

## Operation processing

- These functions output the absolute value of the INT, DINT, REAL, or LREAL data type value input to (s), in the same type of data as (s), from (d).
- When the input value is defined as $A$ and the output value is defined as $B$, the relationship of $A$ and $B$ will be as follows:
$B=|A|$
- Input an INT, DINT, REAL, or LREAL data type value to (s).
- If -32768 in INT data type is input to (s), (d) will output -32768.
- If -2147483648 in DINT data type is input to (s), (d) will output -2147483648. (No operation error occurs. When ABS_E is used, ENO outputs TRUE.)


## -Operation result

## 1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

- When (s) is of REAL data type

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The value output from $(\mathrm{d})$ is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |

- When (s) is of LREAL data type

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The value output from (d) is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |

### 21.2 Calculating the Square Root

## SQRT(_E)

These functions output the square root of an input value.


## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | ANY_REAL |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | ANY_REAL |

## Processing details

■Operation processing

- These functions output the square root of the REAL/LREAL data type value input to (s) from (d).
- When the input value is defined as $A$ and the output value is defined as $B$, the relationship of $A$ and $B$ will be as follows:
$B=\sqrt{A}$
- Input a positive REAL/LREAL data type value to (s).


## ■Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3405 H | The input value is negative. |

### 21.3 Calculating the Natural Logarithm

## LN(_E)

These functions output the natural logarithm (logarithm with base e) of an input value.

| Ladder, FBD/LD |
| :--- |
| [Without EN/ENO] [With EN/ENO]  Structured text <br> [Without EN/ENO]    <br> d:=LN(s);    <br> [With EN/ENO]    <br> d:=LN_E(EN,ENO,s);    |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | ANY_REAL |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | ANY_REAL |

## Processing details

■Operation processing

- These functions calculate the natural logarithm of the REAL/LREAL data type value input to (s), and output the operation result from (d).
- When the input value is defined as $A$ and the output value is defined as $B$, the relationship of $A$ and $B$ will be as follows:
$B=\log _{e} A$
- Natural logarithm operation is performed with the base (e) defined as 2.71828 .


## ■Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3405 H | The input value is negative. |

### 21.4 Calculating the Common Logarithm

## LOG(_E)

These functions output the common logarithm (logarithm with base 10) of an input value.

| Ladder, FBD/LD |
| :--- |
| [Without EN/ENO] [With EN/ENO]  Structured text <br> [Without EN/ENO]    <br> d:=LOG(s);    <br> [With EN/ENO]    <br> d:=LOG_E(EN,ENO,s);    |

Setting data
Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | ANY_REAL |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | ANY_REAL |

## Processing details

■Operation processing

- These functions calculate the common logarithm of the REAL or LREAL data type value input to (s), and output the operation result from (d).
- When the input value is defined as $A$ and the output value is defined as $B$, the relationship of $A$ and $B$ will be as follows:
$B=\log { }_{10} \mathrm{~A}$
- Input a REAL or LREAL data type value to (s).
- Input a positive value only. (Calculation cannot be performed with a negative value.)
- If the operation result is -0 or an underflow occurs, 0 will be output as the operation result.


## ©Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE $^{* 1}$ | Undefined value |

[^48]
## Operation error

- When (s) is of REAL data type

| Error code (SD0) | Description |
| :--- | :--- |
| 3402 H | The value input to (s) is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |
| 3405 H | Out-of-range data is set to (s). <br> • The specified value is a negative number. <br> • The specified value is 0. |

- When (s) is of LREAL data type

| Error code (SD0) | Description |
| :--- | :--- |
| 3402 H | The value input to (s) is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |
| 3405 H | Out-of-range data is set to (s). <br> - The specified value is a negative number. <br> • The specified value is 0. |

### 21.5 Calculating the Exponent

## EXP(_E)

These functions output the exponent of an input value.

| Ladder, FBD/LD |  | Structured text <br> [Without EN/ENO] d:=EXP(s); <br> [With EN/ENO] d:=EXP_E(EN,ENO,s); |
| :---: | :---: | :---: |
| [Without EN/ENO] | [With EN/ENO] |  |

Setting data
Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | ANY_REAL |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | ANY_REAL |

## Processing details

■Operation processing

- These functions calculate the exponent of the REAL/LREAL data type value input to (s), and output the operation result from (d).
- When the input value is defined as $A$ and the output value is defined as $B$, the relationship of $A$ and $B$ will be as follows:
$B=e^{A}$
- Exponent operation is performed with the base (e) defined as 2.71828 .
- Input a REAL or LREAL data type value to (s).


## ■Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE** $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The value input to (s) is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |
| 3403 H | The data output from (d) exceeds the following range. (An overflow has occurred.) <br> $\|(\mathrm{d})\|<2^{128}$ |

### 21.6 Calculating the Sine

## SIN(_E)

These functions output the sine of an input value.

| Ladder, FBD/LD |
| :--- |
| [Without EN/ENO] [With EN/ENO]  Structured text <br> [Without EN/ENO]    <br> d:=SIN(s);    <br> [With EN/ENO]    <br> d:=SIN_E(EN,ENO,s);    |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}(\mathrm{IN})$ | Input | Input variable | ANY_REAL |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | ANY_REAL |

## Processing details

■Operation processing

- These functions calculate the sine of the REAL data type value (angle) input to (s), and output the operation result from (d).
- When the input value is defined as $A$ and the output value is defined as $B$, the relationship of $A$ and $B$ will be as follows:

B=SIN A

- Input a REAL data type value to (s). Input a value (angle) in radians (angle $\times \pi / 180$ ).


## Operation result

## 1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE*1 $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The input value is -0. |

### 21.7 Calculating the Cosine

## cos(_E)

These functions output the cosine of an input value.


## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | ANY_REAL |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | ANY_REAL |

## Processing details

■Operation processing

- These functions calculate the cosine of the REAL data type value (angle) input to (s), and output the operation result from (d).
- When the input value is defined as $A$ and the output value is defined as $B$, the relationship of $A$ and $B$ will be as follows:
$\mathrm{B}=\operatorname{COS} \mathrm{A}$
- Input a REAL data type value to (s). Input a value (angle) in radians (angle $\times \pi / 180$ ).


## ©Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The input value is -0. |

### 21.8 Calculating the Tangent

## TAN(_E)

These functions output the tangent of an input value.


## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN) | Input | Input variable | ANY_REAL |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | ANY_REAL |

## Processing details

■Operation processing

- These functions calculate the tangent of the REAL data type value (angle) input to (s), and output the operation result from (d).
- When the input value is defined as $A$ and the output value is defined as $B$, the relationship of $A$ and $B$ will be as follows:

B=TAN A

- Note that even if the input value is $\pi / 2$ radian or $(3 / 2) \pi$ radian, no error will be issued because of the truncation error in the radian value.
- Input a REAL data type value to (s). Input a value (angle) in radians (angle $\times \pi / 180$ ).


## ■Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE*1 $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The input value is -0. |

### 21.9 Calculating the Arc Sine

## ASIN(_E)

These functions output the arc sine $\left(\mathrm{SIN}^{-1}\right)$ of an input value.

| Ladder, FBD/LD |
| :--- |
| [Without EN/ENO] [With EN/ENO]  Structured text <br> [Without EN/ENO]    <br> d:=ASIN(s);    <br> [With EN/ENO]    <br> d:=ASIN_E(EN,ENO,s);    |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | ANY_REAL |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | ANY_REAL |

## Processing details

■Operation processing

- These functions calculate the arc sine $\left(\mathrm{SIN}^{-1}\right)$ of the REAL data type value input to (s), and output the operation result from (d).
- When the input value is defined as $A$ and the output value is defined as $B$, the relationship of $A$ and $B$ will be as follows:
$\mathrm{B}=\mathrm{SIN}^{-1} \mathrm{~A}$
- Input a REAL data type value to (s) within the following range.

ASIN(_E): -1.0~1.0

- The value (angle) is output from (d) in radians (angle $\times \pi / 180$ ).


## ©Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE*1 $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The input value is -0. |
| 3405 H | The value input with ASIN(_E) is other than -1.0 to 1.0. |

### 21.10 Calculating the Arc Cosine

## ACOS(_E)

These functions output the arc cosine $\left(\mathrm{COS}^{-1}\right)$ of an input value.


## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | ANY_REAL |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | ANY_REAL |

## Processing details

■Operation processing

- These functions calculate the arc cosine $\left(\mathrm{COS}^{-1}\right)$ of the REAL data type value input to ( s ), and output the operation result from (d).
- When the input value is defined as $A$ and the output value is defined as $B$, the relationship of $A$ and $B$ will be as follows:
$B=\operatorname{COS}^{-1} \mathrm{~A}$
- Input a REAL data type value to (s) within the following range.

ACOS(_E): -1.0 to 1.0

- The value (angle) is output from (d) in radians (angle $\times \pi / 180$ ).


## ©Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE*1 $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The input value is -0. |
| 3405 H | The value input with ACOS(_E) is other than -1.0 to 1.0. |

### 21.11 Calculating the Arc Tangent

## ATAN(_E)

These functions output the arc tangent $\left(\mathrm{TAN}^{-1}\right)$ of an input value.

| Ladder, FBD/LD |  | Structured text <br> [Without EN/ENO] d:=ATAN(s); <br> [With EN/ENO] d:=ATAN_E(EN,ENO,s); |
| :---: | :---: | :---: |
| [Without EN/ENO] | [With EN/ENO] |  |

Setting data
Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | ANY_REAL |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | ANY_REAL |

## Processing details

■Operation processing

- These functions calculate the arc tangent ( $\mathrm{TAN}^{-1}$ ) of the REAL data type value input to ( s ), and output the operation result from (d).
- When the input value is defined as $A$ and the output value is defined as $B$, the relationship of $A$ and $B$ will be as follows:
$B=T A N^{-1} A$
- Input a REAL data type value to (s) within the following range.

ATAN(_E): $\pm 1.17549^{-38}$ to $\pm 3.40282^{+38}$

- The value (angle) is output from (d) in radians (angle $\times \pi / 180$ ).


## ©Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE*1 $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The input value is -0. |

## 22 ARITHMETIC OPERATION FUNCTIONS

### 22.1 Addition

## ADD(_E)

These functions output the sum of input values ((s1)+(s2)+...+(s28)).

*1 The input variable $s$ can be changed within the range from 2 to 28 .

## Setting data

■Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| s1 (IN1) to s28 (IN28) | Input | Input variable | ANY_NUM |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | ANY_NUM |

## Processing details

■Operation processing

- These functions add the INT, DINT, REAL, or LREAL data type values input to ( s 1 ) to ( s 28 ) ((s1)+(s2)+..+(s28)), and output the operation result, in the same data type as (s), from (d).


## Ex.

Data type: INT


- Input an INT, DINT, REAL, or LREAL data type value to (s1) to (s28).
- If an underflow or overflow occurs in the operation result, the output from (d) will be as follows.

| Data type: INT | Data type: DINT | Data type: REAL/LREAL |
| :---: | :---: | :---: |
| - Even if an underflow or overflow occurs, no operation error is issued. When ADD_E is used, ENO outputs TRUE. <br> [Example 1] $\begin{aligned} & 32767+2=-32767 \\ & (7 \mathrm{FFFH})+(0002 \mathrm{H})=(8001 \mathrm{H}) \end{aligned}$ <br> A negative value results because the most significant bit is 1 . <br> [Example 2] $\begin{aligned} & -32767+(-2)=32766 \\ & (8000 \mathrm{H})+(\text { FFFEH })=(7 \text { FFEH }) \end{aligned}$ <br> A positive value results because the most significant bit is 0 . | - Even if an underflow or overflow occurs, no operation error is issued. When ADD_E is used, ENO outputs TRUE. <br> [Example 1] $2147483647+2=-2147483647$ <br> (7FFFFFFFH) $+(00000002 \mathrm{H})=(80000001 \mathrm{H})$ <br> A negative value results because the most significant bit is 1 . <br> [Example 2] $\begin{aligned} & -2147483648+(-2)=2147483646 \\ & (80000000 \mathrm{H})+(\text { FFFEH })=(7 \text { FFFFFFFEH }) \end{aligned}$ <br> A positive value results because the most significant bit is 0 . | An operation error occurs and an undefined value is output. |

## Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE*1 $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

- When ( s 1 ) to ( s 28 ) are of REAL data type

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The value input to (s1) to (s28) is -0 , a subnormal number, NaN (not a number), or $\pm \infty$. |
|  | The value output from (d) is -0 , a subnormal number, NaN (not a number), or $\pm \infty$. |
| 3403 H | The data output from (d) exceeds the following range. (An overflow has occurred.) <br> $\|(d)\|<2^{128}$ |

- When (s1) to (s28) are of LREAL data type

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The value input to ( s 1 ) to ( s 28 ) is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |
|  | The value output from (d) is -0 , a subnormal number, NaN (not a number), or $\pm \infty$. |
| 3403 H | The data output from (d) exceeds the following range. (An overflow has occurred.) <br> $\|(\mathrm{d})\|<2^{1024}$ |

### 22.2 Multiplication

## MUL(_E)

These functions output the product of input values $((\mathrm{s} 1) \times(\mathrm{s} 2) \times \cdots \times(\mathrm{s} 28))$.

*1 The input variable s can be changed within the range from 2 to 28 .

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| s1 (IN1) to s28 (IN28) | Input | Input variable | ANY_NUM |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | ANY_NUM |

## Processing details

## OOperation processing

- These functions multiply the INT, DINT, REAL, or LREAL data type values input to (s1) to (s28) ((s1) $\times(\mathrm{s} 2) \times \cdots \times(\mathrm{s} 28)$ ), and output the operation result, in the same data type as (s), from (d).


## Ex.

Data type: INT


- Input an INT, DINT, REAL, or LREAL data type value to (s1) to (s28).
- If an underflow or overflow occurs in the operation result, the output from (d) will be as follows.

| Data type: INT | Data type: DINT | Data type: REAL/LREAL |
| :---: | :---: | :---: |
| - Even if an underflow or overflow occurs, no operation error is issued. When MUL_E is used, ENO outputs TRUE. <br> - Even if the operation result is outside the INT data type range, the INT data type value is output; (In this case, the output value is of INT data type with the upper 16 bits deleted although the operation result is a DINT data type value.) <br> - If the operation result is outside the INT data type range, convert the input value to the DINT data type by using the INT_TO_DINT function, and then perform operation. | - Even if an underflow or overflow occurs, no operation error is issued. When MUL_E is used, ENO outputs TRUE. <br> - Even if the operation result is outside the DINT data type range, the DINT data type value is output; (In this case, the output value is of DINT data type with the upper 32 bits deleted although the operation result is 64 -bit data.) <br> - If the operation result is outside the DINT data type range, convert the input value to the REAL data type by using the DINT_TO_REAL function, and then perform operation. | An operation error occurs and an undefined value is output. |

## Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE** $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from ( d ) will be undefined. Create a program so that the undefined value will not be used in operations.

## Point?

If the operation result is outside the data type range, convert the input value as appropriate before operation.

## Operation error

- When ( $s 1$ ) to ( $s 28$ ) are of REAL data type

| Error code (SD0) | Description |
| :--- | :--- |
| 3402 H | The value input to $(\mathrm{s} 1)$ to $(\mathrm{s} 28)$ is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |
| 3403 H | The data output from (d) exceeds the following range. (An overflow has occurred.) <br> $\|(\mathrm{d})\|<2^{128}$ |

- When (s1) to (s28) are of LREAL data type

| Error code (SD0) | Description |
| :--- | :--- |
| 3402 H | The value input to ( s 1 ) to $(\mathrm{s} 28)$ is -0 , a subnormal number, NaN (not a number), or $\pm \infty$. |
| 3403 H | The data output from (d) exceeds the following range. (An overflow has occurred.) <br> $\|(\mathrm{d})\|<2^{1024}$ |

### 22.3 Subtraction

## SUB(_E)

These functions output the difference between input values ((s1)-(s2)).


## Setting data

## Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| s1 (IN1), s2 (IN2) | Input | Input variable | ANY_NUM |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | ANY_NUM |

## Processing details

## Operation processing

- These functions perform subtraction between the INT, DINT, REAL, or LREAL data type values input to (s1) and (s2) ((s1)(s2)), and output the operation result, in the same data type as (s), from (d).


## Ex.

Data type: INT

INT type

INT type

INT type

- Input an INT, DINT, REAL, or LREAL data type value to (s1) and (s2).
- If an underflow or overflow occurs in the operation result, the output from (d) will be as follows.

| Data type: INT | Data type: DINT | Data type: REAL/LREAL |
| :---: | :---: | :---: |
| - Even if an underflow or overflow occurs, no operation error is issued. When SUB_E is used, ENO outputs TRUE. <br> [Example 1] $32767-(-2)=-32767$ (7FFFH)-(FFFEH)=(8001H) <br> A negative value results because the most significant bit is 1 . <br> [Example 2] $-32767-2=32766$ $(8000 \mathrm{H})-(0002 \mathrm{H})=(7 \mathrm{FFEH})$ <br> A positive value results because the most significant bit is 0 . | - Even if an underflow or overflow occurs, no operation error is issued. When SUB_E is used, ENO outputs TRUE. <br> [Example 1] $2147483647-(-2)=-2147483647$ <br> (7FFFFFFFH)-(0000FFFEH)=(80000001H) <br> A negative value results because the most significant bit is 1 . <br> [Example 2] $-2147483648-2=2147483646$ <br> ( 80000000 H )-(00000002H)=(7FFFFFFEH) <br> A positive value results because the most significant bit is 0 . | An operation error occurs and an undefined value is output. |

## Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE** $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from ( d ) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

- When (s1) and (s2) are of REAL data type

| Error code (SDO) | Description |
| :--- | :--- |
|  | The value input to (s1) is -0 , a subnormal number, NaN (not a number), or $\pm \infty$. |
|  | The value input to (s2) is -0 , a subnormal number, NaN (not a number), or $\pm \infty$. |
|  | The value output from (d) is -0 , a subnormal number, NaN (not a number), or $\pm \infty$. |
| 3403 H | The data output from (d) exceeds the following range. (An overflow has occurred.) <br> $\mid$ (d)\|<2 |

- When (s1) and (s2) are of LREAL data type

| Error code (SDO) | Description |
| :--- | :--- |
| 3402 H | The value input to (s1) is -0 , a subnormal number, NaN (not a number), or $\pm \infty$. |
|  | The value input to (s2) is -0 , a subnormal number, NaN (not a number), or $\pm \infty$. |
|  | The value output from (d) is -0 , a subnormal number, NaN (not a number), or $\pm \infty$. |
| 3403 H | The data output from (d) exceeds the following range. (An overflow has occurred.) <br> $\mid$ (d)\|<2 |

### 22.4 Division

## DIV(_E)

These functions output the quotient of input values ((s1) $\div(\mathrm{s} 2)$ ).


## Setting data

## ■Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| s1 (IN1) | Dividend | Input variable | ANY_NUM |
| s2 (IN2) | Divisor | Input variable | ANY_NUM |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | ANY_NUM |

## Processing details

## Operation processing

- These functions perform division between the INT, DINT, REAL, or LREAL data type values input to (s1) and (s2) ((s1) $\div(\mathrm{s} 2)$ ), and output the operation result, in the same data type as (s), from (d).


## Ex.

Data type: INT
(s1)


INT type
(s2)


INT type
(d)


- Input an INT, DINT, REAL, or LREAL data type value to ( $s$ 1) and ( s 2 ). provided that the value input to ( s 2 ) shall be other than 0.


## Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE*1 | Undefined value |

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## Operation error

- When (s1) and (s2) are of INT data type

| Error code (SDO) | Description |
| :--- | :--- |
| 3400 H | The value (divisor) input to (s2) is 0. |

- When ( $s 1$ ) and ( $s 2$ ) are of DINT data type

| Error code (SDO) | Description |
| :--- | :--- |
| 3400 H | The value (divisor) input to (s2) is 0. |

- When (s1) and (s2) are of REAL data type

| Error code (SDO) | Description |
| :--- | :--- |
| 3400 H | The value (divisor) input to (s2) is 0. |
| 3402 H | The value input to (s1) is -0, a subnormal number, NaN (not a number), or $\pm \infty 0$. |
|  | The value input to (s2) is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |
| 3403 H | The data output from (d) exceeds the following range. (An overflow has occurred.) <br> $\mid$ (d) $\mid<2^{128}$ |

- When (s1) and (s2) are of LREAL data type

| Error code (SDO) | Description |
| :--- | :--- |
| 3400 H | The value (divisor) input to (s2) is 0. |
| 3402 H | The value input to (s1) is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |
|  | The value input to (s2) is -0, a subnormal number, NaN (not a number), or $\pm \infty$. |
| 3403 H | The data output from (d) exceeds the following range. (An overflow has occurred.) <br> $\|(\mathrm{d})\|<2^{1024}$ |

### 22.5 Remainder

## MOD(_E)

These functions output the remainder of input values ((s1) $\div(\mathrm{s} 2)$ ).

| Ladder, FBD/LD |  |  |  | Structured text <br> [Without EN/ENO] <br> The function is described as an operator. ( $\square \square]$ MELSEC iQ-R Programming <br> Manual (Program Design)) <br> [With EN/ENO] d:=MOD_E(EN,ENO,s1,s2); |
| :---: | :---: | :---: | :---: | :---: |
| [Without EN/ENO] | - | [With EN/ENO] | - |  |

## Setting data

## ■Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| s1 (IN1) | Dividend | Input variable | ANY_INT |
| s2 (IN2) | Divisor | Input variable | ANY_INT |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | ANY_INT |

## Processing details

## Operation processing

- These functions perform division between the INT or DINT data type values input to (s1) and (s2) ((s1) $\div(\mathrm{s} 2)$ ), and output the remainder of the operation result, in the same data type as (s), from (d).


## Ex.

Data type: INT
(s1)


INT type
(s2)


INT type


The value is not output.
(d)


- Input an INT or DINT data type value to $(\mathrm{s} 1)$ and $(\mathrm{s} 2)$, provided that the value input to (s2) shall be other than 0 .


## ■Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE*1 $^{*}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

- When (s1) and (s2) are of INT data type

| Error code (SD0) | Description |
| :--- | :--- |
| 3400 H | The value (divisor) input to (s2) is 0. |

- When ( $s 1$ ) and ( $s 2$ ) are of DINT data type

| Error code (SDO) | Description |
| :--- | :--- |
| 3400 H | The value (divisor) input to (s2) is 0. |

### 22.6 Exponentiation

## EXPT(_E)

These functions output the exponentiation of an input value.


## Setting data

## Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| s1 (IN1) | Base | Input variable | ANY_REAL |
| s2 (IN2) | Exponent (power) | Input variable | ANY_NUM |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | ANY_REAL |

## Processing details

## Operation processing

- These functions exponentiate the REAL or LREAL data type value input to ( $s 1$ ) with the exponent (INT, DINT, REAL, or LREAL data type) input to (s2), and output the operation result from (d).



## Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

| Error code (SD0) | Description |
| :--- | :--- |
| 3402 H | The value input to ( $s 1$ ) or (s2) is -0 , a subnormal number, NaN (not a number), or $\pm \infty$. |

### 22.7 Assignment (Move Operation)

## MOVE(_E)

These functions output the assignment value of an input value.


## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | ANY |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | ANY |

## Processing details

■Operation processing

- These functions assign the value of the input variable specified by (s) to the output variable specified by (d).
- Input a BOOL, INT, DINT, WORD, DWORD, REAL, LREAL, STRING, TIME, structure, or array data type value to (s) and (d). The values input to (s) and (d) must be of the same data type.

(s)


DINT type


DWORD type

(s)
$1.79769313486231+308$
LREAL type


DWORD type

(d)


LREAL type

## Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE*1 $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2820 H | There is no NULL code (00H) in the setting area specified by (s) in the device/label memory. |
| 3405 H | The number of characters in the string input to (s) exceeds 16383. |
| 3406 H | The entire string cannot be stored in the setting area specified by (d) in the device/label memory. (The number of required points is <br> insufficient.) |

## 23 BIT SHIFT FUNCTIONS

### 23.1 Shifting Data to the Left by $\mathrm{n} \operatorname{Bit}(\mathrm{s})$

## SHL(_E)

These functions shift the input value to the left by ( n ) bit( s ), and output the operation result.

| Ladder, FBD/LD |  | Structured text <br> [Without EN/ENO] $\mathrm{d}:=\mathrm{SHL}(\mathrm{s}, \mathrm{n})$; <br> [With EN/ENO] d:=SHL_E(EN,ENO,s,n); |
| :---: | :---: | :---: |
| [Without EN/ENO] | [With EN/ENO] |  |
| s d |  |  |
|  |  |  |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}(\mathrm{IN})$ | Input | Input variable | ANY_BIT |
| $\mathrm{n}(\mathrm{N})$ | Number of bits to be shifted | Input variable | ANY_BIT |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | ANY_BIT |

## Processing details

## ■Operation processing

- These functions shift the WORD or DWORD data type value input to (s) to the left by ( n ) bit( s ), and output the operation result, in the same data type as (s), from (d).
- Specify the number of bits to be shifted in (n).


## Ex.

Data type of (s): WORD, Value input to (n): 8


- The ( n ) bit(s) from the least significant bit is/are filled with $0(\mathrm{~s})$.
- Input a WORD or DWORD data type value to (s).
- Input an INT data type value to (n) (Number of bits to be shifted) within the following range.

| Data type of $(\mathbf{s}):$ WORD | Data type of (s): DWORD |
| :--- | :--- |
| Range: 0 to 15 | Range: 0 to 31 |
| The lower 4-bit data is used. | The lower 5-bit data is used. |
| [Example] | [Example] |
| If the input value is $6: 6$ | If the input value is $6: 6$ |
| If the input value is $22: 6$ | If the input value is $22: 22$ |

## Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE*1 $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error.

### 23.2 Shifting Data to the Right by $\mathbf{n} \operatorname{Bit}(\mathbf{s})$

## SHR(_E)

These functions shift the input value to the right by ( n ) bit( s ), and output the operation result.


## Setting data

## ■Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}(\mathrm{IN})$ | Input | Input variable | ANY_BIT |
| $\mathrm{n}(\mathrm{N})$ | Number of bits to be shifted | Input variable | ANY_BIT |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | ANY_BIT |

## Processing details

## Operation processing

- These functions shift the WORD or DWORD data type value input to (s) to the right by ( n ) bit( s ), and output the operation result, in the same data type as (s), from (d).
- Specify the number of bits to be shifted in ( n ).

Ex.
Data type of (s): WORD, Value input to (n): 8


- The ( n ) bit(s) from the most significant bit is/are filled with 0 ( s ).
- Input a WORD or DWORD data type value to (s).
- Input an INT data type value to ( n ) (Number of bits to be shifted) within the following range.

| Data type of (s): WORD | Data type of (s): DWORD |
| :--- | :--- |
| Range: 0 to 15 | Range: 0 to 31 |
| The lower 4-bit data is used. | The lower 5-bit data is used. |
| [Example] | [Example] |
| If the input value is 6: 6 | If the input value is 6: 6 |
| If the input value is 22: 6 | If the input value is $22: 22$ |

## Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE*1 $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error.

### 23.3 Rotating Data to the Left by $\mathrm{n} \operatorname{Bit}(\mathbf{s})$

## ROL(_E)

These functions rotate the input value to the left by ( $n$ ) bit( $s$ ), and output the operation result.


## Setting data

## ■Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}(\mathrm{IN})$ | Input | Input variable | ANY_BIT |
| $\mathrm{n}(\mathrm{N})$ | Number of bits to be rotated | Input variable | ANY_BIT |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | ANY_BIT |

## Processing details

## Operation processing

- These functions rotate the WORD or DWORD data type value input to ( $s$ ) to the left by ( n ) bit( s ), and output the operation result, in the same data type as (s), from (d).
- Specify the number of bits to be rotated in (n).

WORD type, DWORD type


Ex.
Data type of (s): WORD, Value input to (n): 3 (The data rotates to the left by 3 bits.)


- Input a WORD or DWORD data type value to (s).
- Input an INT data type value to ( n ) (Number of bits to be shifted) within the following range.

| Data type of $(\mathbf{s})$ : WORD | Data type of (s): DWORD |
| :--- | :--- |
| Range: 0 to 15 | Range: 0 to 31 |
| The lower 4-bit data is used. | The lower 5-bit data is used. |
| [Example] | [Example] |
| If the input value is 6: 6 | If the input value is $6: 6$ |
| If the input value is 22: 6 | If the input value is $22: 22$ |

## Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE*1 $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error.

### 23.4 Rotating Data to the Right by $\mathrm{n} \operatorname{Bit}(\mathrm{s})$

## ROR(_E)

These functions rotate the input value to the right by ( n ) bit(s), and output the operation result.


## Setting data

## ■Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}(\mathrm{IN})$ | Input | Input variable | ANY_BIT |
| $\mathrm{n}(\mathrm{N})$ | Number of bits to be rotated | Input variable | ANY_BIT |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | ANY_BIT |

## Processing details

## Operation processing

- These functions rotate the WORD or DWORD data type value input to (s) to the right by ( n ) bit(s), and output the operation result, in the same data type as (s), from (d).
- Specify the number of bits to be rotated in (n).

WORD type, DWORD type


Ex.
Data type of (s): WORD, Value input to ( n ): 3 (The data rotates to the right by 3 bits.)


- Input a WORD or DWORD data type value to (s).
- Input an INT data type value to ( n ) (Number of bits to be shifted) within the following range.

| Data type of (s): WORD | Data type of (s): DWORD |
| :--- | :--- |
| Range: 0 to 15 | Range: 0 to 31 |
| The lower 4-bit data is used. | The lower 5-bit data is used. |
| [Example] | [Example] |
| If the input value is 6: 6 | If the input value is $6: 6$ |
| If the input value is $22: 6$ | If the input value is $22: 22$ |

## Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE*1 $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error.

## 24 boolean functions

### 24.1 AND Operation, OR Operation, and XOR Operation

## AND(_E), OR(_E), XOR(_E)

- AND(_E): These functions output the logical product of input values.
- OR(E): These functions output the logical sum of input values.
- OR(_E): These functions output the exclusive logical sum of input values.

| Ladder, FBD/LD*1 |  |  | Structured text ${ }^{* 1}$ <br> [Without EN/ENO] <br> The function is described as an operator. ([]] MELSEC iQ-R Programming Manual (Program Design)) <br> [With EN/ENO] $\begin{aligned} & \text { d:=AND_E(EN,ENO,s1,s2); } \\ & \text { d:=OR_E(EN,ENO,s1,s2); } \\ & \text { d:=XOR_E(EN,ENO,s1,s2); } \end{aligned}$ |
| :---: | :---: | :---: | :---: |
| [Without EN/ENO] | [With EN/ENO] | - |  |

*1 The input variable s can be changed within the range from 2 to 28.

## Setting data

## -Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| s1 (IN1) to s28 (IN28) | Input | Input variable | ANY_BIT |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | ANY_BIT |

## Processing details

## Operation processing

1. $A N D\left(\_E\right)$

- These functions perform an AND operation (bit-by-bit) on the BOOL, WORD, or DWORD data type values input to (s1) to (s28), and output the operation result, in the same data type as (s), from (d).


## Ex.

Data type: WORD
(s1)

(s2)

(d)

| 0 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |

2. $O R\left(\_E\right)$

- These functions perform an OR operation (bit-by-bit) on the BOOL, WORD, or DWORD data type values input to (s1) to (s28), and output the operation result, in the same data type as (s), from (d).


## Ex.

Data type: WORD
(s1)

(s2)

(d)
3. XOR(E)

- These functions perform an XOR operation (bit-by-bit) on the BOOL, WORD, or DWORD data type values input to (s1) to (s28), and output the operation result, in the same data type as (s), from (d).


## Ex.

Data type: WORD
(s1)

| 1 | 0 | 1 | 0 | 1 | 0 | 1 | 0 | 1 | 0 | 1 | 0 | 1 | 0 | 1 | 0 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |

(s2)

(d)

| 1 | 0 | 1 | 1 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 1 | 0 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |

- If three or more (s) settings exist, (s3) will be XORed with the result of XOR between (s1) and (s2). In addition, if ( s 4 ) exists, (s4) will be XORed with the result of subjecting (s) to XOR. After this, XOR will repeat for the number of (s) settings.


## Ex.

Data type: BOOL


## Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error.

### 24.2 NOT Operation

## NOT(_E)

These functions output the logical NOT of input values.

| Ladder, FBD/LD |  | Structured text <br> [Without EN/ENO] <br> The function is described as an operator. (LD] MELSEC iQ-R Programming Manual (Program Design)) <br> [With EN/ENO] <br> d:=NOT_E(EN,ENO,s); |
| :---: | :---: | :---: |
| [Without EN/ENO] | [With EN/ENO] |  |

## Setting data

■Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | ANY_BIT |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | ANY_BIT |

## Processing details

■Operation processing

- These functions perform a NOT operation (bit-by-bit) on the BOOL, WORD, or DWORD data type value input to (s), and output the operation result, in the same data type as (s), from (d).


## Ex.

Data type: WORD
(s1)

(d)

- Input a BOOL, WORD, or DWORD data type value to (s).


## ■Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE** | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error.

## 25 SELECTION FUNCTIONS

### 25.1 Selecting a Value

## SEL(E)

These functions output the selected input value.


## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| s1 (G) | Output condition (TRUE: s3 output, FALSE: s2 output) | Input variable | BOOL |
| s2 (INO) | Input | Input variable | ANY |
| s3 (IN1) |  |  |  |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | ANY |

## Processing details

## ■Operation processing

- These functions output either the (s2) or (s3) input value, in the same data type as (s2) or (s3), from (d) according to the value input to ( s 1 ).
- If the value input to ( $s 1$ ) is FALSE ( $=0$ ), the ( $s 2$ ) input value is output from (d).
- If the value input to ( $s 1$ ) is TRUE (=1), the ( $s 3$ ) input value is output from (d).


## Ex.

Data type of (s2) and (s3): INT (Argument names (s2) and (s3) correspond to the bit value (0 or 1) of (s1).)


- Input a BOOL data type value to ( s 1 ).
- Input a BOOL, INT, DINT, WORD, DWORD, REAL, LREAL, STRING, TIME, structure, or array data type value to (s2) and (s3).


## Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE** $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

- When (s2) and (s3) are of STRING data type

| Error code (SDO) | Description |
| :--- | :--- |
| 2820 H | There is no NULL code $(00 \mathrm{H})$ in the label or device area (between the specified device number and the last device number) specified by <br> (s2). |
|  | There is no NULL code $(00 \mathrm{H})$ in the label or device area (between the specified device number and the last device number) specified by <br> (s3). |
| 3406 H | The entire string cannot be stored in the label or device area (between the specified device number and the last device number) specified <br> by (d). (The number of required points is insufficient.) |

### 25.2 Selecting the Maximum/Minimum Value

## MAX(_E), MIN(_E)

- MAX(_E): These functions output the maximum input value.
- MIN(_E): These functions output the minimum input value.

| Ladder, FBD/LD*1 |  |  |  | Structured text <br> [Without EN/ENO] $\mathrm{d}:=\mathrm{MAX}(\mathrm{s} 1, \mathrm{~s} 2)$; <br> $\mathrm{d}:=\mathrm{MIN}(\mathrm{s} 1, \mathrm{~s} 2)$; <br> [With EN/ENO] <br> d:=MAX_E(EN,ENO,s1,s2); <br> d:=MIN_E(EN,ENO,s1,s2); |
| :---: | :---: | :---: | :---: | :---: |
| [Without EN/ENO] | - | [With EN/ENO] |  |  |

*1 The input variable $s$ can be changed within the range from 2 to 28 .

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| s1 (IN1) to s28 (IN28) | Input | Input variable | ANY_ELEMENTARY |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | ANY_ELEMENTARY |

## Processing details

## ■Operation processing

- MAX(_E)

These functions output the maximum value of the BOOL, INT, DINT, WORD, DWORD, REAL, LREAL, STRING, or TIME data type values input to (s1) to (s28), in the same data type as (s), from (d).

Ex.
Data type: INT


- MIN(_E)

These functions output the minimum value of the BOOL, INT, DINT, WORD, DWORD, REAL, LREAL, STRING, or TIME data type values input to (s1) to (s28), in the same data type as (s), from (d).

## Ex.

Data type: INT


- Input a BOOL, INT, DINT, WORD, DWORD, REAL, LREAL, STRING, or TIME data type value to ( s 1 ) to ( s 28 ).
- Conditions for comparing the STRING data type values are as follows:

Match:

- All characters in two strings matched

Bigger string:

Smaller string:

- The character string with a bigger character code (when different strings are compared)
- The one having a longer length (when strings are of different lengths)
- The character string with a smaller character code (when different strings are compared)
- The one having a shorter length (when strings are of different lengths)


## ©Operation result

## 1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2820 H | There is no NULL code (00H) in each setting area specified by (s1) to (s28) in the device/label memory. |
| 3405 H | The number of characters in the strings input to (s1) to (s28) exceeds 16383. |
| 3406 H | The entire string cannot be stored in the setting area specified by (d) in the device/label memory. (The number of required points is <br> insufficient.) |

### 25.3 Controlling the Upper/Lower Limit

## LIMIT(_E)

These functions output an input value that has been controlled in terms of the upper and lower limits.


## Setting data

## ■Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| s1 (MN) | Lower limit value (minimum output threshold value) | Input variable | ANY_ELEMENTARY |
| s2 (IN) | Input value to be controlled with the upper and lower limits | Input variable | ANY_ELEMENTARY |
| s3 (MX) | Upper limit value (maximum output threshold value) | Input variable | ANY_ELEMENTARY |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | ANY_ELEMENTARY |

## Processing details

## Operation processing

- These functions output the value, in the same data type as (s1), (s2), or (s3), from (d) according to the BOOL, INT, DINT, WORD, DWORD, REAL, LREAL, STRING, or TIME data type value input to (s1), (s2), and (s3).
- If the input values are ( s 1 )>( s 3 ), the value input to ( s 3 ) is output from (d).
- If the input values are ( s 2 ) <(s1), the value input to ( s 1 ) is output from (d).
- If the input values are (s1) $\leq(\mathrm{s} 2) \leq(\mathrm{s} 3)$, the value input to ( s 2 ) is output from (d).


## Ex.

Data type: INT


- Input a BOOL, INT, DINT, WORD, DWORD, REAL, LREAL, STRING, or TIME data type value to (s1), (s2), and (s3), provided that the input value is $(\mathrm{s} 1)<(\mathrm{s} 3)$.
- Conditions for comparing the STRING data type values are as follows:
Match:
- All characters in two strings matched
Bigger string:
- The character string with a bigger character code (when different strings are compared)
- The one having a longer length (when strings are of different lengths)
Smaller string:
- The character string with a smaller character code (when different strings are compared)
- The one having a shorter length (when strings are of different lengths)


## Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

- When (s1), (s2), and (s3) are of INT or WORD data type

| Error code (SDO) | Description |
| :--- | :--- |
| 3405 H | The lower limit value specified by (s1) is greater than the upper limit value specified by (s2). |

- When (s1), (s2), and (s3) are of DINT, DWORD, or TIME data type

| Error code (SD0) | Description |
| :--- | :--- |
| 3405 H | The lower limit value specified by (s1) is greater than the upper limit value specified by (s2). |

- When (s1), (s2), or (s3) are of BOOL data type

| Error code (SD0) | Description |
| :--- | :--- |
| 3405 H | The lower limit value specified by (s1) is greater than the upper limit value specified by (s3). |
| - When (s1), (s2), and (s3) are of REAL data type |  |


| Error code (SD0) | Description |
| :---: | :---: |
| 3402 H | The value input to ( s 1 ) is out of the following range: $\begin{aligned} & -2^{128}<(s 1) \leq-2^{-126}, 0,2^{-126} \leq(s 1)<2^{128} \\ & (E-3.40282347+38 \sim E-1.17549435-38,0, E 1.17549435-38 \sim E 3.40282347+38) \end{aligned}$ |
|  | The value input to ( s 1 ) is -0 , a subnormal number, NaN ( not a number), or $\pm \infty$. |
|  | The value input to ( s 2 ) is out of the following range: $\begin{aligned} & -2^{128}<(s 2) \leq-2^{-126}, 0,2^{-126} \leq(s 2)<2^{128} \\ & (E-3.40282347+38 \sim E-1.17549435-38,0, E 1.17549435-38 \sim E 3.40282347+38) \end{aligned}$ |
|  | The value input to (s2) is -0 , a subnormal number, NaN ( not a number), or $\pm \infty$. |
|  | The value input to ( s 3 ) is out of the following range: $\begin{aligned} & -2^{128}<(\mathrm{s} 3) \leq-2^{-126}, 0,2^{-126} \leq(\mathrm{s} 3)<2^{128} \\ & (\mathrm{E}-3.40282347+38 \sim \mathrm{E}-1.17549435-38,0, \mathrm{E} 1.17549435-38 \sim \mathrm{E} 3.40282347+38) \end{aligned}$ |
|  | The value input to (s3) is -0 , a subnormal number, NaN ( not a number), or $\pm \infty$. |
| 3405H | The lower limit value specified by (s1) is greater than the upper limit value specified by (s3). |

- When ( $s 1$ ), ( $s 2$ ), and ( $s 3$ ) are of LREAL data type

| Error code (SDO) | Description |
| :---: | :---: |
| 3402H | The value input to ( $s 1$ ) is out of the following range: $-2^{1024}<(\mathrm{s} 1) \leq-2^{-1022}, 0,2^{-1022} \leq(\mathrm{s} 1)<2^{1024}$ <br> (E-1.7976931348623157+308~E-2.2250738585072014-308, 0, E2.2250738585072014-308~E1.7976931348623157+308) |
|  | The value input to ( $\mathbf{s} 1$ ) is -0 , a subnormal number, NaN ( not a number), or $\pm \infty$. |
|  | The value input to (s2) is out of the following range: $-2^{1024}<(\mathrm{s} 2) \leq-2^{-1022}, 0,2^{-1022} \leq(\mathrm{s} 2)<2^{1024}$ <br> (E-1.7976931348623157+308~E-2.2250738585072014-308, 0, E2.2250738585072014-308~E1.7976931348623157+308) |
|  | The value input to (s2) is -0 , a subnormal number, NaN ( not a number), or $\pm \infty$. |
|  | The value input to ( s 3 ) is out of the following range: $\begin{aligned} & -2^{1024}<(\mathrm{s} 3) \leq-2^{-1022}, 0,2^{-1022} \leq(\mathrm{s} 3)<2^{1024} \\ & (\mathrm{E}-1.7976931348623157+308 \sim \mathrm{E}-2.2250738585072014-308,0, \mathrm{E} 2.2250738585072014-308 \sim \mathrm{E} 1.7976931348623157+308) \end{aligned}$ |
|  | The value input to ( s 3 ) is -0 , a subnormal number, NaN ( not a number), or $\pm \infty$. |
| 3405H | The lower limit value specified by (s1) is greater than the upper limit value specified by (s3). |
| - When (s1), (s2), and (s3) are of STRING data type |  |
| Error code (SDO) | Description |
| 2820 H | There is no NULL code ( 00 H ) in the label or device area (between the specified device number and the last device number) specified by ( s 1 ), ( s 2 ), or ( s 3 ). |
| 3405H | The lower limit value specified by (s1) is greater than the upper limit value specified by (s3). |
|  | The number of characters in the strings input to (s1), (s2), and (s3) exceeds 16383. |
| 3406 H | The entire string cannot be stored in the label or device area (between the specified device number and the last device number) specified by (d). (The number of required points is insufficient.) |

### 25.4 Multiplexer

## MUX(_E)

These functions output one of the input values.

*1 The input variable s can be changed within the range from 2 to 28.

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{n}(\mathrm{K})$ | Output value selection | Input variable | INT |
| s1 (INO) to s28 (IN27) | Input | Input variable | ANY |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | ANY |

## Processing details

## -Operation processing

- These functions output one of the values input to (s1) to (s28), in the same data type as (s), from (d) according to the value input to ( n ).
- If the ( $n$ ) input value is 0 , the value input to ( $s 1$ ) is output from (d).
- If the $(\mathrm{n})$ input value is $(\mathrm{n})-1$, the value input to $(\mathrm{sn})$ is output from (d).


## Ex.

Data type: INT
(s1)...(s28)


- If a value outside the range of the number of pins in ( $s$ ) is input to ( n ), an undefined value is output from (d). (No operation error occurs. When MUX_E is used, ENO outputs FALSE.)
- Input an INT data type value to (n) within the range of 0 to 27 , provided that it is within the range of the number of pins in (s).
- Input a BOOL, INT, DINT, WORD, DWORD, REAL, LREAL, STRING, TIME, structure, or array data type value to (s).


## Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE*1 $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2820 H | There is no NULL code (00H) in each setting area specified by (s1) to (s28) in the device/label memory. |
| 3405 H | The number of characters in the strings input to ( s 1 ) to ( s 28 ) exceeds 16383. |
| 3406 H | The entire string cannot be stored in the setting area specified by (d) in the device/label memory. (The number of required points is <br> insufficient.) |

## 26 comparison functions

### 26.1 Comparing Data

## GT(_E), GE(_E), EQ(_E), LE(_E), LT(_E)

These functions output the comparison result of input values.

| Ladder, FBD/LD*1 |  |  |  | ```Structured text \({ }^{* 1}\) [Without EN/ENO] The function is described as an operator. ( \(\square \square\) MELSEC iQ-R Programming Manual (Program Design)) [With EN/ENO] d:=GT_E(EN,ENO,s1,s2); \(\mathrm{d}:=\mathrm{GE}\) _E(EN,ENO,s1,s2); d:=EQ_E(EN,ENO,s1,s2); d:=LE_E(EN,ENO,s1,s2); d:=LT_E(EN,ENO,s1,s2);``` |
| :---: | :---: | :---: | :---: | :---: |
|  | - | [With EN/ENO] | - |  |

*1 The input variable $s$ can be changed within the range from 2 to 28 .

## Setting data

■Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| s1 (IN1) to s28 (IN28) | Input | Input variable | ANY_ELEMENTARY |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output (TRUE, FALSE) | Output variable | BOOL |

## Processing details

## ©Operation processing

- These functions perform comparison operation between the input values to (s), and output the operation result, in the BOOL data type, from (d).
$\cdot G T\left(\_\mathrm{E}\right)$ : Performs comparison of $[(\mathrm{s} 1)>(\mathrm{s} 2)] \&[(\mathrm{~s} 2)>(\mathrm{s} 3)] \& \cdots \&\left[(\mathrm{~s})_{(\mathrm{n}-1)}>(\mathrm{s})_{(n)}\right]$.
- If all values satisfy $(\mathrm{s})_{(\mathrm{n}-1)}>(\mathrm{s})_{(\mathrm{n})}$, TRUE is output.
- If one the values satisfies $(\mathrm{s})_{(n-1)} \leq(\mathrm{s})_{(n)}$, FALSE is output.
- $G E\left(\_E\right)$ : Performs comparison of $[(s 1) \geq(s 2)] \&[(s 2) \geq(s 3)] \& \cdots \&\left[(s)_{(n-1)} \geq(s)_{(n)}\right]$.
- If all values satisfy $(\mathrm{s})_{(n-1)} \geq(\mathrm{s})_{(n)}$, TRUE is output.
- If one the values satisfies $(\mathrm{s})_{(n-1)}<(\mathrm{s})_{(n)}$, FALSE is output.
- EQ(_E): Performs comparison of $[(s 1)=(s 2)] \&[(s 2)=(s 3)] \& \cdots \&\left[(s)_{(n-1)}=(s)_{(n)}\right)$.
- If all values satisfy $\left(s_{(n-1)}=(\mathrm{s})_{(n)}\right.$, TRUE is output.
- If one the values satisfies $(\mathrm{s})_{(n-1)} \neq(\mathrm{s})_{(n)}$, FALSE is output.
- LE(_E): Performs comparison of $[(s 1) \leq(s 2)] \&\left([(s 2) \leq(s 3)] \& \cdots \&(s)_{(n-1)} \leq(s)_{(n)}\right)$.
- If all values satisfy $(\mathrm{s})_{(n-1)} \leq(\mathrm{s})_{(n)}$, TRUE is output.
- If one the values satisfies $(\mathrm{s})_{(n-1)}>(\mathrm{s})_{(n)}$, FALSE is output.
- LT(_E): Performs comparison of $[(\mathrm{s} 1)<(\mathrm{s} 2)] \&[(\mathrm{~s} 2)<(\mathrm{s} 3)] \& \cdots \&\left[(\mathrm{~s})_{(n-1)}<(\mathrm{s})_{(n)}\right)$.
- If all values satisfy $(\mathrm{s})_{(n-1)}<(\mathrm{s})_{(n)}$, TRUE is output.
- If one the values satisfies $(\mathrm{s})_{(n-1)} \geq\left(s_{(n)}\right.$, FALSE is output.
- Input an INT, DINT, REAL, LREAL, BOOL, WORD, DWORD, TIME, or STRING type data value to (s). No WSTRING type Unicode string can be specified.
- Conditions for comparing the STRING data type values are as follows:

Match:
Bigger string:

Smaller string:

- All characters in two strings matched
- A character string with a bigger character code (when different strings are compared)
- The one having a longer length (when strings are of different lengths)
- A character string with a smaller character code (when different strings are compared)
- The one having a shorter length (when strings are of different lengths)


## Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE** $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2820 H | There is no NULL code $(00 \mathrm{H})$ in each setting area specified by (s1) to (s28) in the device/label memory. |
| 3405 H | The number of characters in the strings input to ( s 1 ) to (s28) exceeds 16383. |
| 3406 H | The entire string cannot be stored in the setting area specified by (d) in the device/label memory. (The number of required points is <br> insufficient.) |

### 26.2 Comparing Data

## NE(_E)

These functions output the comparison result of input values.


## Setting data

■Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| s1, s2 | Input | Input variable | ANY_ELEMENTARY |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output (TRUE, FALSE) | Output variable | BOOL |

## Processing details

## Operation processing

- These functions perform comparison operation between the input values to (s), and output the operation result, in the BOOL data type, from (d).
- NE(_E): Performs comparison of [(s1) $=(\mathrm{s} 2)]$.
- If ( s 1 ) $\ddagger(\mathrm{s} 2)$, TRUE is output.
- If ( $s 1$ ) $=(\mathrm{s} 2)$, FALSE is output.
- Input an INT, DINT, REAL, LREAL, BOOL, WORD, DWORD, TIME, or STRING type data value to (s). No WSTRING type Unicode string can be specified.
- Conditions for comparing the STRING data type values are as follows:

Match:

- All characters in two strings matched

Bigger string:

- A character string with a bigger character code (when different strings are compared)
- The one having a longer length (when strings are of different lengths)

Smaller string:

- The character string with a smaller character code (when different strings are compared)
- The one having a shorter length (when strings are of different lengths)


## Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2820 H | There is no NULL code (00H) in the setting area specified by (s) in the device/label memory. |
| 3405 H | The number of characters in the string input to (s) exceeds 16383. |
| 3406 H | The entire string cannot be stored in the setting area specified by (d) in the device/label memory. (The number of required points is <br> insufficient.) |

## 27 STRING FUNCTIONS

### 27.1 Detecting a String Length

## LEN(_E)

These functions detect and output the length of the string input.


## Setting data

■Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Input | Input variable | ANYSTRING_SINGLE |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | INT |

## Processing details

## Operation processing

- These functions detect the length of the string input to (s), and output the length from (d).

- Input a STRING data type value to (s) within the range of 0 to 255 bytes.


## ■Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error.

### 27.2 Extracting String Data From the Left/Right

## LEFT(_E), RIGHT(_E)

- LEFT(_E): These functions extract and output the specified number of characters, starting from the left end of the string input.
- RIGHT(_E): These functions extract and output the specified number of characters, starting from the right end of the string input.


Setting data
■Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}(\mathrm{IN})$ | Input | Input variable | ANYSTRING_SINGLE |
| $\mathrm{n}(\mathrm{L})$ | Number of characters to be extracted | Input variable | INT |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | ANYSTRING_SINGLE |

## Processing details

## Operation processing

- LEFT(_E)

These functions extract the specified number of characters, starting from the left end of the string input to (s), and output the operation result from (d).
Specify the number of characters to be extracted in (n).

## Ex.

When ( n )=7
(s) (d)
"ABCDEF12345"
Upper byte

| $42 \mathrm{H}(\mathrm{B})$ | $41 \mathrm{H}(\mathrm{A})$ |
| :---: | :---: |
| $44 \mathrm{H}(\mathrm{D})$ | $43 \mathrm{H}(\mathrm{C})$ |
| $46 \mathrm{H}(\mathrm{F})$ | $45 \mathrm{H}(\mathrm{E})$ |
| $32 \mathrm{H}(2)$ | $31 \mathrm{H}(1)$ |
| $34 \mathrm{H}(4)$ | $33 \mathrm{H}(3)$ |
| 00 H | $35 \mathrm{H}(5)$ |


"ABCDEF1"

| Upper byte | Lower byte | 1st word |
| :---: | :---: | :---: |
| 42H(B) | $41 \mathrm{H}(\mathrm{A})$ |  |
| 44 H (D) | 43H(C) | 2nd word |
| 46 H (F) | 45 H (E) | 3rd word |
| 00H | 33 H (1) | 4th word |

## - RIGHT(_E)

These functions extract the specified number of characters, starting from the right end of the string input to (s), and output the operation result from (d).

Specify the number of characters to be extracted in (n).

## Ex.

When ( n )=5

|  | "ABCD | $12345$ | $\square$ |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | Upper byte | Lower byte |  | Upper byte | Lower byte |  |
| 1st word | 42H(B) | $41 \mathrm{H}(\mathrm{A})$ |  | $32 \mathrm{H}(2)$ | 31H(1) | 1st word |
| 2nd word | 44H(D) | 43H(C) |  | $34 \mathrm{H}(4)$ | $33 \mathrm{H}(3)$ | 2nd word |
| 3rd word | 46 H (F) | 45 H (E) |  | 00H | 35H(5) | 3rd word |
| 4th word | $32 \mathrm{H}(2)$ | $31 \mathrm{H}(1)$ |  |  |  |  |
| 5th word | $34 \mathrm{H}(4)$ | $33 \mathrm{H}(3)$ | ber of ch |  |  |  |
| 6 th word | OOH | $35 \mathrm{H}(5)$ | to be extracted (n): 5 |  |  |  |

- Input a STRING data type value to (s) within the range of 0 to 255 bytes.
- Input an INT data type value to $(\mathrm{n})$ within the range of 0 to 255 , provided that it is within the number of characters in the string input to (s).


## Operation result

## 1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE*1 $^{*}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error.

### 27.3 Extracting String Data

## MID(_E)

These functions extract and output the specified number of characters, starting from the specified position of the string input.

| Ladder, FBD/LD |  |  | Structured text |
| :---: | :---: | :---: | :---: |
| [Without EN/ENO] | [With EN/ENO] |  |  |
| n1 <br> n2 | EN | ENO | $\mathrm{d}:=\mathrm{MID}$ _E(EN,ENO,s,n1,n2); |
|  | n1 |  |  |
|  |  |  |  |

## Setting data

—Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}(\mathrm{IN})$ | Input | Input variable | ANYSTRING_SINGLE |
| $\mathrm{n} 1(\mathrm{~L})$ | Number of characters to be extracted | Input variable | INT |
| $\mathrm{n} 2(P)$ | Extraction target character start position | Input variable | INT |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | ANYSTRING_SINGLE |

## Processing details

## -Operation processing

- These functions extract the specified number of characters, starting from the specified position of the string input to (s), and output the operation result from (d).
- Specify the number of characters to be extracted in (n1).
- Specify the start position of the string to be extracted in (n2).


## Ex.

When ( n 1 ) $=5,(\mathrm{n} 2)=5$

|  | $\begin{gathered} \text { (s) } \\ \text { "ABCDEF12345" } \end{gathered}$ |  | $\square$ | $\begin{gathered} \text { (d) } \\ \text { "EF123" } \end{gathered}$ |  | 1st word <br> 2nd word <br> 3rd word |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | Upper byte | Lower byte | Starting position specified by ( n 2 ): 5 th character | Upper byte | Lower byte |  |
| 1st word | 42H(B) | $41 \mathrm{H}(\mathrm{A})$ |  | 46 H (F) | 45H(E) |  |
| 2nd word | 44H(D) | 43H(C) |  | $32 \mathrm{H}(2)$ | 31H(1) |  |
| 3 rd word | $46 \mathrm{H}(\mathrm{F})$ | $45 \mathrm{H}(\mathrm{E}) 4$ |  | 00H | $33 \mathrm{H}(3)$ |  |
| 4th word | $32 \mathrm{H}(2)$ | $31 \mathrm{H}(1)$ |  |  |  |  |
| 5th word | $34 \mathrm{H}(4)$ | $33 \mathrm{H}(3)$ |  |  |  |  |
| 6th word | OOH | $35 \mathrm{H}(5)$ | Number of characters |  |  |  |

- Input a STRING data type value to (s) within the range of 0 to 255 bytes.
- Input an INT data type value to ( n 1 ) within the range of 0 to 255 , provided that it is within the number of characters in the string input to (s).
- Input an INT data type value to ( n 2 ) within the range of 1 to 255 , provided that it is within the number of characters in the string input to (s).


## Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE** $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

| Error code (SDO) | Description |
| :---: | :---: |
| 2820 H | There is no NULL code (00H) in the setting area specified by (s) in the device/label memory. |
| 3405H | The number of characters in the string input to (s) exceeds 16383. |
|  | Out-of-range data is set to ( n 1 ) or ( n 2 ). <br> - The value input to ( n 1 ) or ( n 2 ) is 0 or smaller. <br> - The value input to ( n 2 ) is other than the valid values $(-1,0,1$ or bigger). <br> - The value input to ( n 1 ) exceeds the number of characters in ( s ). <br> - The sum of ( n 1 ) and ( n 2 ) exceeds the number of characters in ( s ). |

### 27.4 Concatenating String Data

## CONCAT(_E)

These functions concatenate character strings, and output the operation result.

| Ladder, FBD/LD*1 |  |  |  | Structured text ${ }^{* 1}$ <br> [Without EN/ENO] <br> d:=CONCAT(s1,s2); <br> [With EN/ENO] <br> d:=CONCAT_E(EN,ENO,s1,s2); |
| :---: | :---: | :---: | :---: | :---: |
| [Without EN/ENO] |  | [With EN/ENO] | - |  |

*1 The input variable s can be changed within the range from 2 to 28 .

## Setting data

## ■Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| s1 (IN1) to s28 (IN28) | Input | Input variable | ANYSTRING_SINGLE |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | ANYSTRING_SINGLE |

## Processing details

## -Operation processing

- These functions concatenate the strings input to ( s 2 ) to ( s 28 ) to the end of the string input to ( s 1 ), and output the operation result from (d).
- The (s2) to (s28) strings are concatenated successively, ignoring 00 H , which indicates the end of the (s1) string.
- If the string after concatenation exceeds 255 bytes, the substring up to the 255 th byte will be output.

|  | $\begin{gathered} (\mathrm{s} 1) \\ \text { "ABCDE" } \end{gathered}$ |  | (s2) |  |  | $\square$ | (d) |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 1st word | Upper byte | wer byte | 1st word | Upper byte | wer byte | 1st word | Upper byte Lower byte |  |
|  | 42H(B) | $41 \mathrm{H}(\mathrm{A})$ |  | $32 \mathrm{H}(2)$ | $31 \mathrm{H}(1)$ |  | 42H(B) | $41 \mathrm{H}(\mathrm{A})$ |
| 2nd word | 44H(D) | 43H(C) |  | $34 \mathrm{H}(4)$ | $33 \mathrm{H}(3)$ |  | 44H(D) | 43H(C) |
| 3rd word | OOH | $45 \mathrm{H}(\mathrm{E})$ | 3rd word <br> 4th word | 36H(6) | $35 \mathrm{H}(5)$ | 3rd word | 31H(1) | $45 \mathrm{H}(\mathrm{E})$ |
|  |  |  |  | 00H |  | 4th word | 33H(3) | $32 \mathrm{H}(2)$ |
|  |  |  |  |  |  | 5th word | 35H(5) | $34 \mathrm{H}(4)$ |
|  |  |  |  |  |  | 6th word | 00H | $36 \mathrm{H}(6)$ |

- Input a STRING data type value to ( s 1 ) and ( s 2 ) to ( s 28 ) within the range of 0 to 255 bytes.


## ■Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE** $^{* 1}$ | Undefined value |

[^50]
## Operation error

| Error code (SD0) | Description |
| :---: | :---: |
| 2820H | There is no NULL code (00H) in each setting area specified by (s1) to (s28) in the device/label memory. |
|  | There is no NULL code (00H) in each setting area in the device/label memory in the device specified by (d) and later. |
| 2821H | The device numbers are overlapping between (s1) to (s28) and (d). |
| 3405H | The number of characters in the strings input to (s1) to (s28) exceeds 16383. |
|  | The number of characters in the strings input to ( s 1 ) to ( s 28$)$ is 0 . |
|  | The number of characters of the character string in the device specified by (d) exceeds 16383. |
| 3406H | The entire string after concatenate processing cannot be stored in the setting area specified by (d) in the device/label memory. (The number of required points is insufficient.) |

### 27.5 Inserting String Data

## INSERT(_E)

These functions insert a character string into another string, and output the operation result.


## Setting data

## Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| s 1 (IN1), s2 (IN2) | Input | Input variable | ANYSTRING_SINGLE |
| $\mathrm{n}(\mathrm{P})$ | Insertion target character start position | Input variable | INT |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | ANYSTRING_SINGLE |

## Processing details

## Operation processing

- These functions insert the string input to (s2) into the insertion start position, i.e. the 'n'th character position from the beginning of the string input to ( s 1 ), and output the operation result from (d).
- After the (s2) string is inserted into the (s1) string, 00 H , which indicates the end of the (s2) string, is ignored.
- If the string after insertion exceeds 255 bytes, the substring up to the 255 th byte will be output.


## Ex.

When ( n )=4
(s1)

(s2)
$" 123456$

|  | Upper byte | Lower byte |
| ---: | :---: | :---: |
| 1st word | $32 \mathrm{H}(2)$ | $31 \mathrm{H}(1)$ |
| 2nd word | $34 \mathrm{H}(4)$ | $33 \mathrm{H}(3)$ |
| 3rd word | $36 \mathrm{H}(6)$ | $35 \mathrm{H}(5)$ |
| 4th word | 00 H |  |
|  |  |  |

(d)
"ABC123456DE"

| Upper byte | Lower byte |  |
| :---: | :---: | :---: |
| 42H(B) | $41 \mathrm{H}(\mathrm{A})$ |  |
| $31 \mathrm{H}(1)$ | 43 H (C) | 2nd word |
| $33 \mathrm{H}(3)$ | $32 \mathrm{H}(2)$ | 3rd word |
| $35 \mathrm{H}(5)$ | $34 \mathrm{H}(4)$ | 4th word |
| 44H(D) | $36 \mathrm{H}(6)$ | 5th word |
| OOH | 45H(E) | 6th word |

- Input a STRING data type value to ( s 1 ) and ( s 2 ) within the range of 0 to 255 bytes.
- Input an INT data type value to ( n ) within the range of 1 to 255 , provided that it is within the number of characters in the string input to (s1).


## Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE*1 $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2820 H | There is no NULL code (00H) in each setting area specified by (s1) to (s28) in the device/label memory. |
|  | There is no NULL code (00H) in each setting area in the device/label memory in the device specified by (d) and later. |
| 2821 H | The device numbers are overlapping between ( s 1 ) to ( s 28 ) and (d). |
| 3405 H | The number of characters in the strings input to ( s 1 ) to ( s 28 ) exceeds 16383. |
|  | The number of characters in the strings input to ( s 1 ) to (s28) is 0. |
|  | The number of characters of the character string in the device specified by (d) exceeds 16383. |
| 3406 H | The entire string after concatenate processing cannot be stored in the setting area specified by (d) in the device/label memory. (The <br> number of required points is insufficient.) |

### 27.6 Deleting String Data

## DELETE(_E)

These functions delete the specified range in a character string, and output the operation result.


## Setting data

## Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}(\mathrm{IN})$ | Input | Input variable | ANYSTRING_SINGLE |
| $\mathrm{n} 1(\mathrm{~L})$ | Number of characters to be deleted | Input variable | INT |
| $\mathrm{n} 2(\mathrm{P})$ | Deletion target character start position | Input variable | INT |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | ANYSTRING_SINGLE |

## Processing details

## -Operation processing

- These functions delete the specified number of characters, starting from the desired position of the string input to (s), and output the remaining substring from (d).
- Specify the number of characters to be deleted in (n1).
- Specify the start position of the string to be deleted in ( n 2 ).


## Ex.

$$
\text { When }(n 1)=5,(n 2)=5
$$



- Input a STRING data type value to (s) within the range of 0 to 255 bytes.
- Input an INT data type value to ( n 1 ) within the range of 0 to 255 , provided that it is within the number of characters in the string input to (s).
- Input an INT data type value to ( n 2 ) within the range of 1 to 255 , provided that it is within the number of characters in the string input to (s).


## Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE** $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 2820 H | There is no NULL code $(00 \mathrm{H})$ in the label or device area (between the specified device number and the last device number) specified by <br> (s). |
| }{} | The number of characters in the string input to (s) exceeds 255. |
|  | The value input to ( n 1 ) is out of the range, 0 to 255. |
|  | The value input to ( n 2 ) is out of the range, 1 to 255. |
|  | The value input to ( n 1 ) exceeds the number of characters in (s). |
|  | The value input to ( n 2 ) exceeds the number of characters in (s). |
| The entire string after delete processing cannot be stored in the label or device area (between the specified device number and the last <br> device number) specified by (d). |  |

### 27.7 Replacing String Data

## REPLACE(_E)

These functions replace the specified range in a character string, and output the operation result.

| Ladder, FBD/LD |  | Structured text <br> [Without EN/ENO] $\mathrm{d}:=$ REPLACE(s1,s2,n1,n2); <br> [With EN/ENO] $\mathrm{d}:=$ REPLACE_E(EN,ENO,s1,s2,n1,n2); |
| :---: | :---: | :---: |
| [Without EN/ENO] | [With EN/ENO] |  |
| s1 d | EN ENO |  |
| - n 1 | s2 |  |
| n2 | n1 |  |
|  | n2 |  |

## Setting data

■Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| s1 (IN1), s2 (IN2) | Input | Input variable | ANYSTRING_SINGLE |
| n 1 (L) | Number of characters to be replaced | Input variable | INT |
| $\mathrm{n} 2(P)$ | Replacement target character start position | Input variable | INT |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | ANYSTRING_SINGLE |

## Processing details

## ■Operation processing

- These functions replace the specified number of characters starting from the desired position of the string input to (s1) with the string input to (s2), and output the operation result from (d).
- Specify the number of characters to be replaced in ( n 1 ).
- Specify the start position of the string to be replaced in (n2).


## Ex.

When ( n 1 ) $=5,(\mathrm{n} 2)=5$


- Input a STRING data type value to ( s 1 ) and ( s 2 ) within the range of 0 to 255 bytes.
- Input an INT data type value to ( n 1 ) within the range of 0 to 255 , provided that it is within the number of characters in the string input to (s1).
- Input an INT data type value to ( n 2 ) within the range of 1 to 255 , provided that it is within the number of characters in the string input to (s1).


## ©Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

| Error code (SD0) | Description |
| :---: | :---: |
| 2820H | There is no NULL code $(00 \mathrm{H})$ in the label or device area (between the specified device number and the last device number) specified by (s1). |
|  | There is no NULL code $(00 \mathrm{H})$ in the label or device area (between the specified device number and the last device number) specified by (s2). |
| 3405H | The number of characters in the string input to (s1) exceeds 255. |
|  | The number of characters in the string input to (s2) exceeds 255. |
|  | The value input to ( n 1 ) is out of the range, 0 to 255. |
|  | The value input to ( n 2 ) is out of the range, 1 to 255. |
|  | The value input to ( n 1 ) exceeds the number of characters in ( s 2 ). |
|  | The value input to ( n 2 ) exceeds the number of characters in ( s 1 ). |
| 3406 H | The entire string after delete processing cannot be stored in the label or device area (between the specified device number and the last device number) specified by (d). |

### 27.8 Searching String Data

## FIND(_E)

These functions search a character string, and output the operation result.

| Ladder, FBD/LD |  |  |  | Structured text <br> [Without EN/ENO] d:=FIND(s1,s2); <br> [With EN/ENO] d:=FIND_E(EN,ENO,s1,s2); |
| :---: | :---: | :---: | :---: | :---: |
| [Without EN/ENO] | - | [With EN/ENO] |  |  |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| s1 (IN1), s2 (IN2) | Input | Input variable | ANYSTRING_SINGLE |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | INT |

## Processing details

## -Operation processing

- These functions search the string input to (s2) from the beginning of the string input to ( $s$ 1) , and output the search result from (d).
- The start character position of the first string found is output as the search result.
- If the ( s 2 ) string is not found in the ( s 1 ) string, 0 will be output.

- Input a STRING data type value to (s1) and (s2) within the range of 0 to 255 bytes.


## Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE** $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error.

## 28 time data type functions

### 28.1 Addition

## ADD_TIME(_E)

These functions output the sum ((s1)+(s2)) of the TIME data type input values.


## Setting data

## Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| s1 (IN1), s2 (IN2) | Input | Input variable | TIME |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | TIME |

## Processing details

## Operation processing

- These functions perform addition of the TIME data type values input to (s1) and (s2) ((s1)+(s2)), and output the operation result, in the TIME data type, from (d).


## Ex.

When (s1)=T\#1d2h33m44s55ms (1 day, 2 hours, 33 minutes, 44 seconds, 55 milliseconds) and ( 22 )=T\#2ms ( 2 milliseconds)


- Input a TIME data type value to (s1) and (s2).
- Even if an underflow or overflow occurs in the operation result, no operation error is issued. The following is output to (d). When ADD_TIME_E is used, ENO outputs TRUE.


## Ex.

Overflow


A negative time value results because the most significant bit is 1 .

## Ex.

## Underflow

(s1)
T\#-24d20h31m23s648ms (80000000H) $+\underset{\text { (FFFFFFFFEH) }}{\text { T\#-2ms }}$
(d)


$$
\begin{array}{|c|}
\hline \text { T\#24d20h31m23s646ms } \\
\hline \text { (7FFFFFFEH) }
\end{array}
$$

A positive time value results because the most significant bit is 0 .

## Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE*1 $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error.

### 28.2 Subtraction

## SUB_TIME(_E)

These functions output the difference ((s1)-(s2)) between the TIME data type input values.


## Setting data

■Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| s1 (IN1), s2 (IN2) | Input | Input variable | TIME |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | TIME |

## Processing details

## -Operation processing

- These functions perform subtraction between the TIME data type values input to ( s 1 ) and ( s 2 ) ((s1)-(s2)), and output the operation result, in the TIME data type, from (d).


## Ex.

When (s1)=T\#1d2h33m44s55ms (1 day, 2 hours, 33 minutes, 44 seconds, 55 milliseconds) and ( s 2 ) $=\mathrm{T} \# 2 \mathrm{~ms}$ ( 2 milliseconds)


- Input a TIME data type value to ( $s 1$ ) and ( $s 2$ ).
- Even if an underflow or overflow occurs in the operation result, no operation error is issued. The following is output to (d). When SUB_TIME_E is used, ENO outputs TRUE.


## Ex.

Overflow
(s1)
T\#24d20h31m23s647ms (7FFFFFFFH)
(s2)

| T\#-2ms |
| :---: |
| (FFFFFFFEH) |

(d)
 T\#-24d20h31m23s647ms (80000001H)

A negative time value results because the most significant bit is 1 .

## Ex.

## Underflow



A positive time value results because the most significant bit is 0 .

## Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE*1 $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

There is no operation error.

### 28.3 Multiplication

## MUL_TIME(_E)

These functions output the product ((s1)×(s2)) of the TIME data type input values.


## Setting data

-Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| s1 (IN1) | Input | Input variable | TIME |
| s2 (IN2) | Input | Input variable | ANY_NUM |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | TIME |

## Processing details

## ■Operation processing

- These functions perform multiplication between the TIME data type values input to (s1) and (s2) ((s1)×(s2)), and output the operation result, in the TIME data type, from (d).


## Ex.

When (s1)=T\#1d2h33m44s55ms (1 day, 2 hours, 33 minutes, 44 seconds, 55 milliseconds) and ( s 2 )=2
(s1)

TIME type



- Input a TIME data type value to (s1).
- Input an INT, DINT, REAL, or LREAL data type value to (s2).
- Even if an underflow or overflow occurs in the operation result, no operation error is issued. The following is output to (d). When MUL_TIME_E is used, ENO outputs TRUE. (In this case, the output value is of TIME data type with the upper 32 bits deleted although the operation result is 64-bit data.)


## Ex.

## Overflow



A negative time value results because the most significant bit is 1 .

## Ex.

## Underflow

| (s1) |  | (s2) |  | (d) |
| :---: | :---: | :---: | :---: | :---: |
| T\#-24d20h31m23s648ms | $\times$ | 2 | $\longrightarrow$ | T\#0ms |
| (80000000H) |  | 000002H) |  | 0000000H) |

A positive time value results because the most significant bit is 0 .

## Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE** $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

- When (s2) is of LREAL data type

| Error code (SDO) | Description |
| :---: | :---: |
| 3402H | The value input to ( s 2 ) is out of the following range: $-2^{1024}<(\mathrm{s} 2) \leq-2^{-1022}, 0,2^{-1022} \leq(\mathrm{s} 2)<2^{1024}$ <br> (E-1.7976931348623157+308~E-2.2250738585072014-308, 0, E2.2250738585072014-308~E1.7976931348623157+308) |
|  | The value input to (s2) is -0 , a subnormal number, NaN ( not a number), or $\pm \infty$. |
| 3405H | The single-precision real number input to (s2) is out of the range, -2147483648 to 2147483647 . |

### 28.4 Division

## DIV_TIME(_E)

These functions output the quotient ((s1) $\div(\mathrm{s} 2)$ ) of the TIME data type input values.


## Setting data

-Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| s1 (IN1) | Input | Input variable | TIME |
| s2 (IN2) | Input | Input variable | ANY_NUM |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal) | Output variable | BOOL |
| d | Output | Output variable | TIME |

## Processing details

## ■Operation processing

- These functions perform division between the TIME data type values input to (s1) and (s2) ((s1) $\div(\mathrm{s} 2)$ ), and output the operation result, in the TIME data type, from (d). The remainder is rounded down.


## Ex.

When (s1)=T\#1d2h33m44s55ms (1 day, 2 hours, 33 minutes, 44 seconds, 55 milliseconds) and (s2)=2


- Input a TIME data type value to ( s 1 ).
- Input an INT, DINT, REAL, or LREAL data type value to ( $s 2$ ). (Note that the value input to ( $s 2$ ) shall be other than 0 .)


## ©Operation result

1. Function without EN/ENO

The operation processing is performed. The operation result is output from (d).
2. Function with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE $^{* 1}$ | Undefined value |

*1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

## Operation error

| Error code (SD0) |
| :--- |
| 3400 D |
| Description |
| When (s2) is of LREAL data type |
| Error code (SD0) |
| 3402 H |
| Description |
| 3405 H | | The value input to $(\mathrm{s} 2)$ is out of the following range: |
| :--- |
| $-2^{1024}<(\mathrm{s} 2) \leq-2^{-1022}, 0,2^{-1022} \leq(\mathrm{s} 2)<2^{1024}$ |
| $(\mathrm{E}-1.7976931348623157+308 \sim \mathrm{E}-2.2250738585072014-308,0, \mathrm{E} 2.2250738585072014-308 \sim \mathrm{E} 1.7976931348623157+308)$ |

## PART 6 STANDARD FUNCTION BLOCKS

Part 6 consists of the following chapters.

29 BISTABLE FUNCTION BLOCKS

30 EDGE DETECTION FUNCTION BLOCKS

31 COUNTER/TIMER FUNCTION BLOCKS

## 29 BISTABLE FUNCTION BLOCKS

### 29.1 Bistable Function Block (Set-Dominant)

## SR(_E)

These function blocks discriminate between two input values, and output 1 (TRUE) or 0 (FALSE).

| Ladder, FBD/LD |  |  |  | Structured text <br> [Without EN/ENO] <br> Instance name(S1:=s1,R:=s2,Q1:=d); <br> [With EN/ENO] <br> Instance name(EN:=en,ENO:=eno,S1:=s1,R:=s2,Q1:=d); |
| :---: | :---: | :---: | :---: | :---: |
| [Without EN/ENO] | - | [With EN/ENO] | - |  |

## Setting data

## ■Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| s1 (S1) | Set command | Input variable | BOOL |
| s2 (R) | Reset command | Input variable | BOOL |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal or operation stop) | Output variable | BOOL |
| d (Q1) | Output | Output variable | BOOL |

## Processing details

## ■Operation processing

- When ( s 1 ) turns on, (d) is set. Turning on (s2) while ( s 1 ) is off resets (d).
- Even when (s2) turns on while (s1) is on, (d) is not reset.


## ©Operation result

1. Function block without EN/ENO

The operation processing is performed. The operation result is output from (d).

- Timing chart
(s1)
(s2)


2. Function block with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE | Previous output value |

- Timing chart



## Operation error

There is no operation error.

### 29.2 Bistable Function Block (Reset-Dominant)

## RS(_E)

These function blocks discriminate between two input values, and output 1 (TRUE) or 0 (FALSE).


## Setting data <br> ■Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| s1 (S) | Set command | Input variable | BOOL |
| s2 (R1) | Reset command | Input variable | BOOL |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal or operation stop) | Output variable | BOOL |
| d (Q1) | Output | Output variable | BOOL |

## Processing details

## ■Operation processing

- When (s1) turns on, (d) is set. When (s2) turns on, (d) is reset.
- Even when ( s 1 ) turns on while (s2) is on, (d) is not reset.


## ©Operation result

1. Function block without EN/ENO

The operation processing is performed. The operation result is output from (d).

- Timing chart


2. Function block with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE | Previous output value |

- Timing chart



## Operation error

There is no operation error.

## 30 EDGE DETECTION FUNCTION BLOCKS

### 30.1 Detecting a Rising Edge

## R_TRIG(_E)

These function blocks detect a signal rising edge, and outputs the pulse signal.

| Ladder, FBD/LD |
| :--- |
| [Without EN/ENO] |
| [With EN/ENO] Structured text |

## Setting data

## ■Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $s(C L K)$ | Rising edge detection input | Input variable | BOOL |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal or operation stop) | Output variable | BOOL |
| $d(Q)$ | Output | Output variable | BOOL |

## Processing details

## ■Operation processing

When (s) turns on, (d) turns on only for one scan.

## ©Operation result

1. Function block without EN/ENO

The operation processing is performed. The operation result is output from (d).

- Timing chart
(s)


2. Function block with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE | Previous output value |

- Timing chart



## Operation error

There is no operation error.

### 30.2 Detecting a Falling Edge

## F_TRIG(_E)

These function blocks detect a signal falling edge, and outputs the pulse signal.

| Ladder, FBD/LD |  | Structured text <br> [Without EN/ENO] <br> Instance name(CLK:=s, Q:=d); <br> [With EN/ENO] <br> Instance name(EN:= en,ENO:=eno,CLK:=s,Q:=d); |
| :---: | :---: | :---: |
| [Without EN/ENO] | [With EN/ENO] |  |

## Setting data

Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| s (CLK) | Falling edge detection input | Input variable | BOOL |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal or operation stop) | Output variable | BOOL |
| d (Q) | Output | Output variable | BOOL |

## Processing details

## ■Operation processing

When (s) turns off, (d) turns on only for one scan.

## ©Operation result

1. Function block without EN/ENO

The operation processing is performed. The operation result is output from (d).

- Timing chart
(s)


2. Function block with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE | Previous output value |

- Timing chart



## Operation error

There is no operation error.

## 31 COUNTER/TIMER FUNCTION BLOCKS

### 31.1 Up Counter

## CTU(_E)

These function blocks count up the number of rising edges of a signal.

| Ladder, FBD/LD |  |  | Structured text <br> [Without EN/ENO] <br> Instance name(CU:=s1,R:=s2,PV:=n,Q:=d1,CV:=d2); <br> [With EN/ENO] <br> Instance name(EN:=en,ENO:=eno,CU:=s1,R:=s2,PV:=n,Q:=d1,CV:=d2); |
| :---: | :---: | :---: | :---: |
| [Without EN/ENO] | [With EN/ENO] | - |  |

## Setting data

■Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| s1 (CU) | Count signal input | Input variable | BOOL |
| s2 (R) | Count value reset | Input variable | BOOL |
| $\mathrm{n}($ PV $)$ | Maximum count value | Input variable | INT |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal or operation stop) | Output variable | BOOL |
| $\mathrm{d} 1(Q)$ | End of count | Output variable | BOOL |
| $\mathrm{d} 2(\mathrm{CV})$ | Count value | Output variable | INT |

## Processing details

## ■Operation processing

1. Counting up

- When ( s 1 ) changes from off to on, the value in (d2) is counted up by one.
- When (d2) reaches the ( n ) value, ( d 1 ) turns on and the counting stops.
- Set the maximum counter value to ( n ). When ( s 2 ) turns on, (d1) turns off and (d2) is set to 0 .

2. Maximum count value

The valid setting range of $(n)$ is 0 to 32767 .

## Operation result

1. Function block without EN/ENO

The operation processing is performed. The operation result is output from (d1) and (d2).

- Timing chart

When ( n ) $=3$

2. Function block with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d1), (d2) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE | Previous output value |

- Timing chart

When ( n ) $=3$


## Operation error

There is no operation error.

### 31.2 Down Counter

## CTD(_E)

These function blocks count down the number of rising edges of a signal.


## Setting data

■Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| s1(CD) | Count signal input | Input variable | BOOL |
| s2 (LD) | Count value set | Input variable | BOOL |
| $\mathrm{n}(\mathrm{PV})$ | Start count value | Input variable | INT |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal or operation stop) | Output variable | BOOL |
| d1 (Q) | End of count | Output variable | BOOL |
| d2 (CV) | Count value | Output variable | INT |

## Processing details

## ■Operation processing

## 1. Counting down

- When ( s 1 ) changes from off to on, the value in (d2) is counted down by one.
- When (d2) is 0 , (d1) turns on and the counting stops.
- Set the start count value to ( n ). When ( s 2 ) turns on, (d1) turns off and ( n ) is set to (d2).

2. Start count value

The valid setting range of $(n)$ is 0 to 32767 .

## ©Operation result

1. Function block without EN/ENO

The operation processing is performed. The operation result is output from (d1) and (d2).

- Timing chart

When ( n ) $=3$

2. Function block with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d1), (d2) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE | Previous output value |

- Timing chart

When ( n ) $=3$


When (s2) turns on while EN is on, When ( s 1 ) turns off while EN is on,
the value in (d2) is initialized. the value in (d2) is counted down.

## Operation error

There is no operation error.

### 31.3 Up/Down Counter

## CTUD(_E)

These function blocks count up or down the number of rising edges of a signal.


## Setting data

-Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| s1 (CU) | Count signal input (for counting-up) | Input variable | BOOL |
| s2 (CD) | Count signal input (for counting-down) | Input variable | BOOL |
| s3 (R) | Count value reset | Input variable | BOOL |
| s4 (LD) | Count value set | Input variable | BOOL |
| n (PV) | Maximum count value or start count value | Input variable | INT |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal or operation stop) | Output variable | BOOL |
| d1 (QU) | End of count (for counting-up) | Output variable | BOOL |
| d2 (QD) | End of count (for counting-down) | Output variable | BOOL |
| d3 (CV) | Current count value | Output variable | INT |

## Processing details

## ■Operation processing

1. Counting up

- When ( s 1 ) changes from off to on, the value in (d3) is counted up by one.
- When (d3) reaches the (n) value, (d1) turns on and the counting stops.
- Set the maximum counter value to ( n ). When ( s 3 ) turns on, ( d 1 ) turns off and ( d 3 ) is set to 0 .

2. Counting down

- When (s2) changes from off to on, the value in (d3) is counted down by one.
- When (d3) is 0 , (d2) turns on and the counting stops.
- Set the start count value to ( n ). When ( s 4 ) turns on, (d2) turns off and ( n ) is set to (d3).

3. Maximum count value or start count value

The valid setting range of $(\mathrm{n})$ is 0 to 32767 .
4. Others

- When ( s 1 ) and ( s 2 ) change from off to on simultaneously, the value in ( d 3 ) is counted up by one with priority given to (s1).
- When (s3) and (s4) turn on simultaneously, (d3) is set to 0 with priority given to ( s 3 ).


## ■Operation result

1. Function block without EN/ENO

The operation processing is performed. The operation result is output from (d1), (d2), and (d3).

- Timing chart

When ( n )=3

2. Function block with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d1), (d2), (d3) |
| TRUE (executed) | TRUE | Operation result output value |
| FALSE (not executed) | FALSE | Previous output value |

- Timing chart

When ( n )=3


## Operation error

There is no operation error.

### 31.4 Counter Function Block

## COUNTER_FB_M

This function block starts counting up when the execution condition is satisfied.

| Ladder, FBD/LD |  | Structured text |
| :---: | :---: | :---: |
|  | - | Instance name(Coil:=s1,Preset:=s2,Valueln:=s3,ValueOut:=d1,Status:=d2); |

## Setting data

## Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| s1 (Coil) | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| s2 (Preset) | Counter setting value | Input variable | INT |
| s3 (Valueln) | Initial counter value | Input variable | INT |
| d1 (ValueOut) | Current counter value | Output variable | ANY16 |
| d2 (Status) | Output | Output variable | BOOL |

## Processing details

## ■Operation processing

- The number of rising edges (status changes (off to on)) of ( s 1 ) is counted. Counting is not performed while ( s 1 ) remains on. The counting starts from the ( s 3 ) value. When it reached the ( s 2 ) value, ( d 2 ) turns on. The current value is stored in (d1).
- The valid setting range of ( s 2 ) is 0 to 32767 .
- The valid setting range of ( s 3 ) is -32768 to 32767 . Note that if a negative value is specified, 0 will be used as the initial value.
- To reset the current value (d1), reset (s1) of FB directly.


## Ex.

Label name: TIMER_CONT_FB_M_1
[Ladder program]

[ST program]
RST(M0,TIMER_CONT_FB_M_1.Coil)
[Ladder example]

[Timing chart]

MO

Value of D10


## Operation error

There is no operation error.

### 31.5 Pulse Timer

## TP(_E)

These function blocks keep the signal on for the specified period of time.


Setting data
■Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Start of output | Input variable | BOOL |
| $\mathrm{n}(\mathrm{PT})$ | Output time setting value | Input variable | TIME |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal or operation stop) | Output variable | BOOL |
| $\mathrm{d} 1(\mathrm{Q})$ | Output | Output variable | BOOL |
| $\mathrm{d} 2(\mathrm{ET})$ | Elapsed time | Output variable | TIME |

## Processing details

## ■Operation processing

1. Output

- When (s) turns on, (d1) turns on for the period of time set by ( n ). The time elapsed after (d1) turns on is set to (d2).
- Use the long timer to count the elapsed time.

2. End of output

- Once the elapsed time reaches the setting time, (d1) turns off.
- If ( $s$ ) is off after (d1) turns off, the elapsed time is reset.
- Even when (s) turns off while (d1) is on, (d1) does not turn off.

3. Output time setting

The valid setting range of $(\mathrm{n})$ is $\mathrm{T} \# 1 \mathrm{~ms}$ to T \# 2147483 ms . Note that the valid setting range will be as follows by changing the timer limit setting using the engineering tool.

| Minimum value | Maximum value |
| :---: | :---: |
| Identical to the long timer setting value [ms] in the timer limit setting. Note that if the long timer setting value is smaller than 1 ms , the minimum value will be 1 ms . | The time satisfying the following condition is used. <br> Note that the maximum value is a value that can be included within the range of time type because the output time setting value is of time type (32-bit value). <br> - Output time setting value $[\mathrm{ms}] \leq 2147483647[\mathrm{~ms}] \times$ Long timer setting value in the timer limit setting [ms] <br> [Example] <br> - If the long timer setting value is 0.001 ms : T\#1ms to $\mathrm{T} \# 2147483 \mathrm{~ms}$ <br> - If the long timer setting value is $1000 \mathrm{~ms}: \mathrm{T} \# 1000 \mathrm{~ms}$ to $\mathrm{T} \# 2147483000 \mathrm{~ms}$ |

The value at the rising edge (off to on) of (d1) is used for the setting value of ( $n$ ). When the ( $n$ ) value is changed when (d1) is on, the new value will be enabled at the next output start timing.

## Operation result

1. Function block without EN/ENO

The operation result will be as follows.

| Operation result | (d1), (d2) |
| :--- | :--- |
| No operation error | Operation result output value |
| Operation error | Undefined value |

- Timing chart

When $n=T \# 5 s(5 s)$


## 2. Function block with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d1), (d2) |
| TRUE (executed) | TRUE (no operation error) | Operation result output value |
|  | FALSE (operation error) | Undefined value |
| FALSE (not executed) | FALSE | Previous output value |

- Timing chart

When $n=T \# 5 s(5 s)$


## Operation error

| Error code (SD0) | Description |
| :--- | :--- |
| 3401 H | The output time setting value exceeds the valid range. |

### 31.6 On Delay Timer

## TON(_E)

These function blocks turn on a signal after the specified period of time.


## Setting data

■Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}(\mathrm{IN})$ | Time measurement | Input variable | BOOL |
| $\mathrm{n}(\mathrm{PT})$ | Delay time setting value | Input variable | TIME |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal or operation stop) | Output variable | BOOL |
| $\mathrm{d} 1(\mathrm{Q})$ | Output | Output variable | BOOL |
| $\mathrm{d} 2(\mathrm{ET})$ | Elapsed time | Output variable | TIME |

## Processing details

## -Operation processing

1. Output

- When (s) turns on, (d1) turns on after the time that was set by ( n ). The delay time elapsed after (d1) turns on is set to (d2).
- When (s) turns off, (d1) turns off and the delay elapsed time is also reset.
- Use the long timer to count the elapsed time.

2. Delay time setting

The valid setting range of $(\mathrm{n})$ is $\mathrm{T} \# 1 \mathrm{~ms}$ to T \# 2147483 ms . Note that the valid setting range will be as follows by changing the timer limit setting using the engineering tool.

| Minimum value | Maximum value |
| :---: | :---: |
| Identical to the long timer setting value [ms] in the timer limit setting. Note that if the long timer setting value is smaller than 1 ms , the minimum value will be 1 ms . | The time satisfying the following condition is used. <br> Note that the maximum value is a value that can be included within the range of time type because the delay time setting value is of time type (32-bit value). <br> - Delay time setting value $[\mathrm{ms}] \leq 2147483647$ [ms] $\times$ Long timer setting value of in the timer limit setting [ms] <br> [Example] <br> - If the long timer setting value is 0.001 ms : T\#1ms to $\mathrm{T} \# 2147483 \mathrm{~ms}$ <br> - If the long timer setting value is $1000 \mathrm{~ms}: \mathrm{T} \# 1000 \mathrm{~ms}$ to $\mathrm{T} \# 2147483000 \mathrm{~ms}$ |

The value at the rising edge (off to on) of (d) is used for the setting value of ( $n$ ). When the ( $n$ ) value is changed while ( $s$ ) is on, the new value will be enabled at the next rising edge of (s).

## Operation result

1. Function block without EN/ENO

The operation result will be as follows.

| Operation result | (d1), (d2) |
| :--- | :--- |
| No operation error | Operation result output value |
| Operation error | Undefined value |

- Timing chart

When $n=T \# 5 s(5 s)$

2. Function block with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d1), (d2) |
| TRUE (executed) | TRUE (no operation error) | Operation result output value |
|  | FALSE (operation error) | Previous output value |
| FALSE (not executed) | FALSE | Previous output value |

- Timing chart

When $n=T \# 5 s(5 s)$


## Operation error

| Error code (SDO) | Description |
| :--- | :--- |
| 3401 H | The output time setting value exceeds the valid range. |

### 31.7 Off Delay Timer

## TOF(_E)

These function blocks turn off a signal after the specified period of time.


## Setting data

-Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| EN | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| $\mathrm{s}($ IN $)$ | Time measurement | Input variable | BOOL |
| $\mathrm{n}(\mathrm{PT})$ | Delay time setting value | Input variable | TIME |
| ENO | Output status (TRUE: Normal, FALSE: Abnormal or operation stop) | Output variable | BOOL |
| $\mathrm{d} 1(\mathrm{Q})$ | Output | Output variable | BOOL |
| $\mathrm{d} 2(\mathrm{ET})$ | Elapsed time | Output variable | TIME |

## Processing details

## ■Operation processing

1. Output

- When (s) turns on, (d) turns on.
- When (s) changes from on to off, (d1) turns off after the time that was set by ( n ). The delay time elapsed after (d1) turns off is set to (d2).
- Use the long timer to count the elapsed time.

2. Delay time setting

The valid setting range of $(n)$ is $\mathrm{T} \# 1 \mathrm{~ms}$ to $\mathrm{T} \# 2147483 \mathrm{~ms}$. Note that the valid setting range will be as follows by changing the timer limit setting using the engineering tool.

| Minimum value | Maximum value |
| :---: | :---: |
| Identical to the long timer setting value [ ms ] in the timer limit setting. Note that if the long timer setting value is smaller than 1 ms , the minimum value will be 1 ms . | The time satisfying the following condition is used. <br> Note that the maximum value is a value that can be included within the range of time type because the delay time setting value is of time type (32-bit value). <br> - Delay time setting value $[\mathrm{ms}] \leq 2147483647[\mathrm{~ms}] \times$ Long timer setting value of in the timer limit setting [ms] <br> [Example] <br> - If the long timer setting value is 0.001 ms : T\#1ms to $\mathrm{T} \# 2147483 \mathrm{~ms}$ <br> - If the long timer setting value is 1000 ms : $\mathrm{T} \# 1000 \mathrm{~ms}$ to $\mathrm{T} \# 2147483000 \mathrm{~ms}$ |

The value at the falling edge (on to off) of (s) is used for the setting value of ( $n$ ). When the ( $n$ ) value is changed when ( $s$ ) is off, the new value will be enabled at the next falling edge of (s).

## Operation result

1. Function block without EN/ENO

The operation result will be as follows.

| Operation result | (d1), (d2) |
| :--- | :--- |
| No operation error | Operation result output value |
| Operation error | Undefined value |

- Timing chart

When $n=T \# 5 s(5 s)$

2. Function block with EN/ENO

The execution conditions and operation results will be as follows.

| Execution condition | Operation result |  |
| :--- | :--- | :--- |
| EN | ENO | (d) |
| TRUE (executed) | TRUE (no operation error) | Operation result output value |
|  | FALSE (operation error) | Previous output value |
| FALSE (not executed) | FALSE | Previous output value |

- Timing chart

When $n=T \# 5 s(5 s)$


## Operation error

| Error code (SD0) | Description |
| :--- | :--- |
| 3401 H | The output time setting value exceeds the valid range. |

### 31.8 Timer Function Block

## TIMER_ㅁM

These function blocks start counting a timer when the execution condition is satisfied, and continue counting until the timer reaches the set value.

| Ladder, FBD/LD | Structured text |
| :---: | :---: |
| ( $\square$ is to be replaced by any of the following: TIMER_10_FB_M, TIMER_100_FB_M, TIMER_HIGH_FB_M, TIMER_LOW_FB_M, TIMER_CONT_FB_M, or TIMER_CONTHFB_M.) | Instance name(Coil:=s1,Preset:=s2,Valueln:=s3,ValueOut:=d1,Status:=d2); |

## Setting data

## ©Description, type, data type

| Argument | Description | Type | Data type |
| :--- | :--- | :--- | :--- |
| s1 (Coil) | Execution condition (TRUE: Executed, FALSE: Not executed) | Input variable | BOOL |
| s2 (Preset) | Timer setting value | Input variable | INT |
| s3 (Valueln) | Initial timer value | Input variable | INT |
| d1 (ValueOut) | Current timer value | Output variable | ANY16 |
| d2 (Status) | Output | Output variable | BOOL |

## Processing details

## TIMER_10_FB_M

- When ( s 1 ) turns on, measurement of the current value starts. The measurement starts from ( s 3 ) $\times 10 \mathrm{~ms}$. When the value reaches (s2) $\times 10 \mathrm{~ms}$, (d2) turns on. The measured current value is output to (d1).
- When (s1) turns off, the current value returns to the initial value (s3), and (s2) also turns off.
- If the unit of measurement of the high-speed timer (in the timer limit setting) is changed from the default value using the engineering tool, a warning will be issued during compilation.
- The valid setting range of ( s 2 ) is 0 to 32767 .
- The valid setting range of ( s 3 ) is -32768 to 32767 . Note that if a negative value is specified, 0 will be used as the initial value.


## Ex.

[Ladder example]

[Timing chart]

-TIMER_100_FB_M

- When (s1) turns on, measurement of the current value starts. The measurement starts from ( s 3 ) $\times 100 \mathrm{~ms}$. When the value reaches ( s 2 ) $\times 100 \mathrm{~ms}$, (d2) turns on. The measured current value is output to (d1).
- When (s1) turns off, the current value returns to the initial value ( $s 3$ ), and ( $s 2$ ) also turns off.
- If the unit of measurement of the low-speed timer (in the timer limit setting) is changed from the default value using the engineering tool, a warning will be issued during compilation. a warning will be issued during compilation.
- The valid setting range of ( s 2 ) is 0 to 32767 .
- The valid setting range of ( s 3 ) is -32768 to 32767 . Note that if a negative value is specified, 0 will be used as the initial value.


## Ex.

[Ladder example]

[Timing chart]


## ITIMER_HIGH_FB_M

- This is a high-speed timer whose unit of measurement is 0.1 to 100 ms . When ( s 1 ) turns on, measurement of the current value starts. The measurement starts from (s3) $\times 0.1$ to 100 ms (variable; set in parameter). When the value reaches ( s 2 ) $\times 0.1$ to 100 ms , (d2) turns on. The measured current value is output to (d1).
- When (s1) turns off, the current value returns to the initial value (s3), and (s2) also turns off.
- The unit of measurement of the high-speed timer is 10 ms by default. The unit can be changed in the range from 0.01 to 100 ms .
- The valid setting range of ( s 2 ) is 0 to 32767 .
- The valid setting range of ( s 3 ) is -32768 to 32767 . Note that if a negative value is specified, 0 will be used as the initial value.


## Ex.

[Ladder example]


## [Timing chart]



## ITIMER_LOW_FB_M

- This is a low-speed timer whose unit of measurement is 1 to 1000 ms . When ( s 1 ) turns on, measurement of the current value starts. The measurement starts from ( s 3 ) $\times 1$ to 1000 ms (variable; set in parameter). When the value reaches ( s 2 ) $\times 1$ to 1000 ms , (d2) turns on. The measured current value is output to (d1).
- When (s1) turns off, the current value returns to the initial value (s3), and (s2) also turns off.
- The unit of measurement of the low-speed timer is 100 ms by default. The unit can be changed in the range from 1 to 1000 ms (in increments of 1 ms ).
- The valid setting range of ( s 2 ) is 0 to 32767 .
- The valid setting range of ( s 3 ) is -32768 to 32767 . Note that if a negative value is specified, 0 will be used as the initial value.


## Ex.

[Ladder example]

[Timing chart]


## ITIMER_CONT_FB_M/TIMER_CONTHFB_M

- This is a retentive timer that measures the on time of a variable. When ( $s 1$ ) turns on, measurement of the current value starts. There are two retentive timers: low-speed (TIMER_CONT_FB_M) and high-speed (TIMER_CONTHFB_M) retentive timers.
- The measurement starts from (s3) $\times 1$ to 1000 ms ( 0.1 to 100 ms for the high-speed retentive timer) (variable; set in parameter). When the value reaches (s2) $\times 1$ to 1000 ms ( 0.1 to 100 ms for the high-speed retentive timer), (d2) turns on. The measured current value is output to (d1).
- Even when ( s 1 ) is off, the on/off states of (d1) and (d2) are held. When ( s 1 ) turns on again, the measurement resumes with the measured value that has been held.
- The unit of measurement (time limit) for the retentive timers is common to both the low-speed timer (TIMER_LOW_FB_M) and high-speed timer (TIMER_HIGH_FB_M).
- Low-speed retentive timer: Low-speed timer
- High-speed retentive timer: High-speed timer
- The valid setting range of (s2) is 0 to 32767 .
- The valid setting range of ( s 3 ) is -32768 to 32767 . Note that if a negative value is specified, 0 will be used as the initial value.
- To reset (d1) of a retentive timer, reset (s1) of FB directly.


## Ex.

Label name: TIMER_CONT_FB_M_1
[Ladder program]


RST(M0,TIMER_CONT_FB_M_1.Coil)
[ST program]
[Ladder example]

[Timing chart]


## Operation error

There is no operation error.

## APPENDICES

## Appendix 1 Instruction Processing Time

The following table lists the processing time of each instruction.
The processing time varies slightly depending on the contents of the source and destination. Assume that the values in the table are reference processing time.

| Instruction name | Condition | Processing time ( $\mu \mathrm{s}$ ) |  |
| :---: | :---: | :---: | :---: |
|  |  | Minimum | Maximum |
| LD | Execution time | 0.00098 |  |
| LDI | Execution time | 0.00098 |  |
| AND | Execution time | 0.00098 |  |
| ANI | Execution time | 0.00098 |  |
| OR | Execution time | 0.00098 |  |
| ORI | Execution time | 0.00098 |  |
| LDP | Execution time | 0.00294 |  |
| LDF | Execution time | 0.00294 |  |
| ANDP | Execution time | 0.00294 |  |
| ANDF | Execution time | 0.00294 |  |
| ORP | Execution time | 0.00294 |  |
| ORF | Execution time | 0.00294 |  |
| LDPI | Execution time | 0.00294 |  |
| LDFI | Execution time | 0.00294 |  |
| ANDPI | Execution time | 0.00294 |  |
| ANDFI | Execution time | 0.00294 |  |
| ORPI | Execution time | 0.00294 |  |
| ORFI | Execution time | 0.00294 |  |
| ANB | - | 0.00098 |  |
| ORB | - | 0.00098 |  |
| MPS | - | 0.00098 |  |
| MRD | $-$ | 0.00098 |  |
| MPP | - | 0.00098 |  |
| INV | No execution time/execution time | 0.00098 |  |
| MEP | No execution time/execution time | 0.00098 |  |
| MEF | No execution time/execution time | 0.00098 |  |
| EGP | No execution time/execution time | 0.00196 |  |
| EGF | No execution time/execution time | 0.00196 |  |
| OUT | No execution time/execution time | 0.00196 |  |
| OUT (F) | No execution time | 0.00696 |  |
|  | Execution time | 50.900 | 81.600 |
| OUT (T/ST/C) | No execution time | 0.01196 |  |
|  | Execution time: Count time/after time over | 0.01196 |  |
| OUT (LT/LST) | No execution time | 0.00996 |  |
|  | Execution time: Count time/after time over | 0.00996 |  |
| OUT (LC) | No execution time | 0.01196 |  |
|  | Execution time: Count time/after time over | 0.01196 |  |
| SET | No execution time | 0.00196 |  |
|  | Execution time: Change time: no change time | 0.00196 |  |
| SET (F) | No execution time | 0.00696 |  |
|  | Execution time | 50.300 | 81.700 |
| RST | No execution time | 0.00196 |  |
|  | Execution time: Change time: no change time | 0.00196 |  |


| Instruction name | Condition | Processing time ( $\mu \mathrm{s}$ ) |  |
| :---: | :---: | :---: | :---: |
|  |  | Minimum | Maximum |
| RST (F) | No execution time | 0.00196 |  |
|  | Execution time | 15.900 | 29.300 |
| RST (T/ST/C) | No execution time | 0.00996 |  |
|  | Execution time | 0.00996 |  |
| RST (LT/LST) | No execution time | 0.00596 |  |
|  | Execution time | 0.00596 |  |
| RST (LC) | No execution time | 0.00996 |  |
|  | Execution time | 0.00996 |  |
| PLS | - | 0.00196 |  |
| PLF | - | 0.00196 |  |
| FF | No execution time/execution time | 0.00196 |  |
| DELTA | No execution time | 0.00392 |  |
|  | Execution time | 1.700 | 6.300 |
| SFT | No execution time | $0.00392$ |  |
|  | Execution time | 0.900 | 3.300 |
| MC | - | 0.00196 |  |
| MCR | - | 0.00196 |  |
| FEND | - | Refer to the following. <br> []] MELSEC iQ-R CPU Module User's <br> Manual (Application) |  |
| END | - |  |  |
| STOP | - | - |  |
| NOP | - | 0.00098 |  |
| LD= | Continuity/Non-continuity | $0.00588$ |  |
| LD<> | Continuity/Non-continuity | 0.00588 |  |
| LD> | Continuity/Non-continuity | 0.00588 |  |
| LD<= | Continuity/Non-continuity | 0.00588 |  |
| LD< | Continuity/Non-continuity | $0.00588$ |  |
| LD>= | Continuity/Non-continuity | 0.00588 |  |
| AND= | No execution time | 0.00392 |  |
|  | Execution time: Continuity/Non-continuity | $0.00588$ |  |
| AND<> | No execution time | 0.00392 |  |
|  | Execution time: Continuity/Non-continuity | 0.00588 |  |
| AND> | No execution time | 0.00392 |  |
|  | Execution time: Continuity/Non-continuity | $0.00588$ |  |
| AND<= | No execution time | $0.00392$ |  |
|  | Execution time: Continuity/Non-continuity | 0.00588 |  |
| AND< | No execution time | 0.00392 |  |
|  | Execution time: Continuity/Non-continuity | 0.00588 |  |
| AND>= | No execution time | $0.00392$ |  |
|  | Execution time: Continuity/Non-continuity | $0.00588$ |  |
| $\mathrm{OR}=$ | No execution time | $0.00392$ |  |
|  | Execution time: Continuity/Non-continuity | 0.00588 |  |
| OR<> | No execution time | $0.00392$ |  |
|  | Execution time: Continuity/Non-continuity | $0.00588$ |  |
| OR> | No execution time | $0.00392$ |  |
|  | Execution time: Continuity/Non-continuity | 0.00588 |  |
| OR<= | No execution time | 0.00392 |  |
|  | Execution time: Continuity/Non-continuity | $0.00588$ |  |
| OR< | No execution time | 0.00392 |  |
|  | Execution time: Continuity/Non-continuity | 0.00588 |  |
| OR>= | No execution time | 0.00392 |  |
|  | Execution time: Continuity/Non-continuity | 0.00588 |  |


| Instruction name | Condition | Processing time ( $\mu \mathrm{s}$ ) |  |
| :---: | :---: | :---: | :---: |
|  |  | Minimum | Maximum |
| LD=_U | Continuity/Non-continuity | 0.00588 |  |
| LD<>_U | Continuity/Non-continuity | 0.00588 |  |
| LD>_U | Continuity/Non-continuity | 0.00588 |  |
| LD<=_U | Continuity/Non-continuity | 0.00588 |  |
| LD<_U | Continuity/Non-continuity | 0.00588 |  |
| LD>=_U | Continuity/Non-continuity | 0.00588 |  |
| AND=_U | No execution time | 0.00392 |  |
|  | Execution time: Continuity/Non-continuity | 0.00588 |  |
| AND<>_U | No execution time | 0.00392 |  |
|  | Execution time: Continuity/Non-continuity | 0.00588 |  |
| AND>_U | No execution time | 0.00392 |  |
|  | Execution time: Continuity/Non-continuity | 0.00588 |  |
| AND<=_U | No execution time | 0.00392 |  |
|  | Execution time: Continuity/Non-continuity | 0.00588 |  |
| AND<_U | No execution time | 0.00392 |  |
|  | Execution time: Continuity/Non-continuity | 0.00588 |  |
| AND>=_U | No execution time | $0.00392$ |  |
|  | Execution time: Continuity/Non-continuity | 0.00588 |  |
| $\mathrm{OR}=$ _U | No execution time | 0.00392 |  |
|  | Execution time: Continuity/Non-continuity |  |  |
| OR<>_U | No execution time | $0.00392$ |  |
|  | Execution time: Continuity/Non-continuity | 0.00588 |  |
| OR>_U | No execution time | 0.00392 |  |
|  | Execution time: Continuity/Non-continuity | $0.00588$ |  |
| OR<=_U | No execution time | 0.00392 |  |
|  | Execution time: Continuity/Non-continuity | 0.00588 |  |
| OR<_U | No execution time | $0.00392$ |  |
|  | Execution time: Continuity/Non-continuity | $0.00588$ |  |
| OR>=_U | No execution time | 0.00392 |  |
|  | Execution time: Continuity/Non-continuity | 0.00588 |  |
| LDD= | Continuity/Non-continuity | $0.00588$ |  |
| LDD<> | Continuity/Non-continuity | 0.00588 |  |
| LDD> | Continuity/Non-continuity | 0.00588 |  |
| LDD<= | Continuity/Non-continuity | $0.00588$ |  |
| LDD< | Continuity/Non-continuity | $0.00588$ |  |
| LDD>= | Continuity/Non-continuity | 0.00588 |  |
| ANDD= | No execution time | 0.00392 |  |
|  | Execution time: Continuity/Non-continuity | $0.00588$ |  |
| ANDD<> | No execution time | 0.00392 |  |
|  | Execution time: Continuity/Non-continuity | 0.00588 |  |
| ANDD> | No execution time | 0.00392 |  |
|  | Execution time: Continuity/Non-continuity | 0.00588 |  |
| ANDD<= | No execution time | 0.00392 |  |
|  | Execution time: Continuity/Non-continuity | 0.00588 |  |
| ANDD< | No execution time | 0.00392 |  |
|  | Execution time: Continuity/Non-continuity | 0.00588 |  |
| ANDD>= | No execution time | $0.00392$ |  |
|  | Execution time: Continuity/Non-continuity | $0.00588$ |  |
| ORD= | No execution time | $0.00392$ |  |
|  | Execution time: Continuity/Non-continuity | $0.00588$ |  |
| ORD<> | No execution time | $0.00392$ |  |
|  | Execution time: Continuity/Non-continuity | $0.00588$ |  |


| Instruction name | Condition | Processing time ( $\mu \mathrm{s}$ ) |  |
| :---: | :---: | :---: | :---: |
|  |  | Minimum | Maximum |
| ORD> | No execution time | 0.00392 |  |
|  | Execution time: Continuity/Non-continuity | 0.00588 |  |
| ORD<= | No execution time | 0.00392 |  |
|  | Execution time: Continuity/Non-continuity | 0.00588 |  |
| ORD< | No execution time | 0.00392 |  |
|  | Execution time: Continuity/Non-continuity | 0.00588 |  |
| ORD>= | No execution time | 0.00392 |  |
|  | Execution time: Continuity/Non-continuity | 0.00588 |  |
| LDD=_U | Continuity/Non-continuity | 0.00588 |  |
| LDD<>_U | Continuity/Non-continuity | 0.00588 |  |
| LDD>_U | Continuity/Non-continuity | 0.00588 |  |
| LDD<=_U | Continuity/Non-continuity | 0.00588 |  |
| LDD<_U | Continuity/Non-continuity | 0.00588 |  |
| LDD>=_U | Continuity/Non-continuity | 0.00588 |  |
| ANDD=_U | No execution time | 0.00392 |  |
|  | Execution time: Continuity/Non-continuity | 0.00588 |  |
| ANDD<>_U | No execution time | 0.00392 |  |
|  | Execution time: Continuity/Non-continuity | 0.00588 |  |
| ANDD>_U | No execution time | 0.00392 |  |
|  | Execution time: Continuity/Non-continuity | $0.00588$ |  |
| ANDD<=_U | No execution time | 0.00392 |  |
|  | Execution time: Continuity/Non-continuity | 0.00588 |  |
| ANDD<_U | No execution time | 0.00392 |  |
|  | Execution time: Continuity/Non-continuity | $0.00588$ |  |
| ANDD>=_U | No execution time | 0.00392 |  |
|  | Execution time: Continuity/Non-continuity | 0.00588 |  |
| ORD=_U | No execution time | 0.00392 |  |
|  | Execution time: Continuity/Non-continuity | 0.00588 |  |
| ORD<>_U | No execution time | 0.00392 |  |
|  | Execution time: Continuity/Non-continuity | $0.00588$ |  |
| ORD>_U | No execution time | $0.00392$ |  |
|  | Execution time: Continuity/Non-continuity | 0.00588 |  |
| ORD<=_U | No execution time | $0.00392$ |  |
|  | Execution time: Continuity/Non-continuity | $0.00588$ |  |
| ORD<_U | No execution time | $0.00392$ |  |
|  | Execution time: Continuity/Non-continuity | $0.00588$ |  |
| ORD>=_U | No execution time | $0.00392$ |  |
|  | Execution time: Continuity/Non-continuity | 0.00588 |  |
| BKCMP= | ( n ) $=1$ | 3.600 | 10.500 |
|  | $(\mathrm{n})=96$ | 17.000 | 23.400 |
| BKCMP<> | $(\mathrm{n})=1$ | 3.700 | 9.900 |
|  | $(\mathrm{n})=96$ | 17.400 | 23.600 |
| BKCMP> | $(\mathrm{n})=1$ | 3.600 | 10.500 |
|  | $(\mathrm{n})=96$ | 16.800 | 24.600 |
| BKCMP<= | $(\mathrm{n})=1$ | 3.500 | 10.300 |
|  | $(\mathrm{n})=96$ | 17.500 | 23.300 |
| BKCMP< | $(\mathrm{n})=1$ | 3.600 | 10.300 |
|  | $(\mathrm{n})=96$ | 17.500 | 24.500 |
| BKCMP>= | $(\mathrm{n})=1$ | 3.600 | 10.000 |
|  | $(\mathrm{n})=96$ | 17.700 | 24.100 |
| BKCMP=_U | ( n ) $=1$ | 3.600 | 10.300 |
|  | $(\mathrm{n})=96$ | 16.900 | 23.900 |


| Instruction name | Condition | Processing time ( $\mu \mathrm{s}$ ) |  |
| :---: | :---: | :---: | :---: |
|  |  | Minimum | Maximum |
| BKCMP<>_U | $(\mathrm{n})=1$ | 3.700 | 9.800 |
|  | $(\mathrm{n})=96$ | 17.300 | 23.600 |
| BKCMP>_U | $(\mathrm{n})=1$ | 3.500 | 10.200 |
|  | $(\mathrm{n})=96$ | 16.900 | 23.600 |
| BKCMP<=_U | ( n ) $=1$ | 3.600 | 10.100 |
|  | $(\mathrm{n})=96$ | 16.700 | 23.300 |
| BKCMP<_U | $(\mathrm{n})=1$ | 3.700 | 10.100 |
|  | $(\mathrm{n})=96$ | 16.800 | 24.100 |
| BKCMP>=_U | $(\mathrm{n})=1$ | 3.600 | 9.900 |
|  | $(\mathrm{n})=96$ | 17.000 | 25.400 |
| DBKCMP= | $(\mathrm{n})=1$ | 3.700 | 10.400 |
|  | $(\mathrm{n})=96$ | 17.000 | 23.500 |
| DBKCMP<> | $(\mathrm{n})=1$ | 3.700 | 10.000 |
|  | $(\mathrm{n})=96$ | 17.400 | 23.700 |
| DBKCMP> | $(\mathrm{n})=1$ | 3.600 | 10.100 |
|  | ( n ) $=96$ | 17.400 | 24.100 |
| DBKCMP<= | $(\mathrm{n})=1$ | 3.800 | 10.100 |
|  | $(\mathrm{n})=96$ | 17.100 | 23.600 |
| DBKCMP< | $(\mathrm{n})=1$ | 3.700 | 9.900 |
|  | $(\mathrm{n})=96$ | 17.300 | 23.800 |
| DBKCMP>= | $(\mathrm{n})=1$ | 3.600 | 10.200 |
|  | $(\mathrm{n})=96$ | 17.100 | 22.900 |
| DBKCMP=_U | $(\mathrm{n})=1$ | 3.700 | 10.200 |
|  | $(\mathrm{n})=96$ | 17.000 | 23.600 |
| DBKCMP<>_U | $(\mathrm{n})=1$ | 3.700 | 10.000 |
|  | $(\mathrm{n})=96$ | 17.400 | 23.600 |
| DBKCMP>_U | $(\mathrm{n})=1$ | 3.700 | 10.200 |
|  | $(\mathrm{n})=96$ | 24.300 | 30.800 |
| DBKCMP<=_U | $(\mathrm{n})=1$ | 3.700 | 10.200 |
|  | $(\mathrm{n})=96$ | 23.900 | 30.300 |
| DBKCMP<_U | $(\mathrm{n})=1$ | 3.800 | 10.000 |
|  | $(\mathrm{n})=96$ | 24.300 | 31.000 |
| DBKCMP>=_U | $(\mathrm{n})=1$ | 3.800 | 10.500 |
|  | ( n ) $=96$ | 24.400 | 31.000 |
| + (s) (d) | Execution time | 0.00588 |  |
| + (s1) (s2) (d) | Execution time | 0.00588 |  |
| +_U (s) (d) | Execution time | 0.00588 |  |
| +_U (s1) (s2) (d) | Execution time | 0.00588 |  |
| - (s) (d) | Execution time | 0.00588 |  |
| - (s1) (s2) (d) | Execution time | 0.00588 |  |
| -_U (s) (d) | Execution time | $0.00588$ |  |
| -_U (s1) (s2) (d) | Execution time | 0.00588 |  |
| D+(s) (d) | Execution time | 0.00588 |  |
| D+(s1) (s2) (d) | Execution time | 0.00588 |  |
| D+_U (s) (d) | Execution time | $0.00588$ |  |
| D+_U (s1) (s2) (d) | Execution time | 0.00588 |  |
| D- (s) (d) | Execution time | 0.00588 |  |
| D- (s1) (s2) (d) | Execution time | 0.00588 |  |
| D-_U (s) (d) | Execution time | $0.00588$ |  |
| D-_U (s1) (s2) (d) | Execution time | 0.00588 |  |
| * | Execution time | $0.01176$ |  |
| *_U | Execution time | 0.01176 |  |


| Instruction name | Condition | Processing time ( $\mu \mathrm{s}$ ) |  |
| :---: | :---: | :---: | :---: |
|  |  | Minimum | Maximum |
| 1 | Execution time | 0.01960 |  |
| I_U | Execution time | 0.01960 |  |
| D* | Execution time | 0.01960 |  |
| D*_U | Execution time | 0.01960 |  |
| D/ | Execution time | 0.02940 |  |
| D/_U | Execution time | 0.02940 |  |
| B+ (s) (d) | Execution time | 1.300 | 3.700 |
| B+ (s1) (s2) (d) | Execution time | 1.800 | 5.100 |
| B- (s) (d) | Execution time | 1.300 | 3.800 |
| B- (s1) (s2) (d) | Execution time | 1.900 | 5.000 |
| $\mathrm{DB}+$ (s) (d) | Execution time | 2.000 | 6.200 |
| DB+ (s1) (s2) (d) | Execution time | 2.100 | 6.100 |
| DB- (s) (d) | Execution time | 2.000 | 6.100 |
| DB- (s1) (s2) (d) | Execution time | 2.100 | 5.900 |
| $B^{*}$ | Execution time | 1.400 | 4.900 |
| B/ | Execution time | 1.500 | 4.800 |
| DB* | Execution time | 2.400 | 8.000 |
| DB/ | Execution time | 2.200 | 7.900 |
| BK+ | ( n ) $=1$ | 3.500 | 8.100 |
|  | $(\mathrm{n})=96$ | 17.200 | 21.500 |
| BK+_U | $(\mathrm{n})=1$ | 3.200 | 8.300 |
|  | $(\mathrm{n})=96$ | 17.500 | 21.600 |
| BK- | $(\mathrm{n})=1$ | 3.100 | 8.200 |
|  | $(\mathrm{n})=96$ | 17.400 | 21.400 |
| BK-_U | $(\mathrm{n})=1$ | 3.200 | 8.300 |
|  | $(\mathrm{n})=96$ | 17.500 | 21.500 |
| DBK+ | $(\mathrm{n})=1$ | 3.300 | 6.900 |
|  | $(\mathrm{n})=96$ | 17.400 | 21.900 |
| DBK+_U | $(\mathrm{n})=1$ | 3.000 | 7.800 |
|  | $(\mathrm{n})=96$ | 17.400 | 21.900 |
| DBK- | $(\mathrm{n})=1$ | 3.000 | 7.900 |
|  | $(\mathrm{n})=96$ | 17.400 | 22.000 |
| DBK-_U | $(\mathrm{n})=1$ | 3.000 | 7.900 |
|  | ( n ) $=96$ | 17.400 | 22.000 |
| INC | Execution time | 0.00392 |  |
| INC_U | Execution time | $0.00392$ |  |
| DEC | Execution time | 0.00392 |  |
| DEC_U | Execution time | 0.00392 |  |
| DINC | Execution time | 0.00392 |  |
| DINC_U | Execution time | 0.00392 |  |
| DDEC | Execution time | 0.00392 |  |
| DDEC_U | Execution time | 0.00392 |  |
| WAND (s) (d) | Execution time | $0.00392$ |  |
| WAND (s1) (s2) (d) | Execution time | 0.00392 |  |
| DAND (s) (d) | Execution time | 0.00392 |  |
| DAND (s1) (s2) (d) | Execution time | $0.00392$ |  |
| BKAND | ( n ) $=1$ | 3.300 | 8.400 |
|  | ( n ) $=96$ | 17.500 | 22.400 |
| WOR (s) (d) | Execution time | 0.00392 |  |
| WOR (s1) (s2) (d) | Execution time | 0.00392 |  |
| DOR (s) (d) | Execution time | 0.00392 |  |
| DOR (s1) (s2) (d) | Execution time | 0.00392 |  |

APPENDICES

| Instruction name | Condition | Processing time ( $\mu \mathrm{s}$ ) |  |
| :---: | :---: | :---: | :---: |
|  |  | Minimum | Maximum |
| BKOR | $(\mathrm{n})=1$ | 3.300 | 8.300 |
|  | ( n ) $=96$ | 17.500 | 22.800 |
| WXOR (s) (d) | Execution time | 0.00392 |  |
| WXOR (s1) (s2) (d) | Execution time | 0.00392 |  |
| DXOR (s) (d) | Execution time | 0.00392 |  |
| DXOR (s1) (s2) (d) | Execution time | 0.00392 |  |
| BKXOR | $(\mathrm{n})=1$ | 3.300 | 8.300 |
|  | ( n ) $=96$ | 17.500 | 22.400 |
| WXNR (s) (d) | Execution time | 0.00392 |  |
| WXNR (s1) (s2) (d) | Execution time | 0.00392 |  |
| DXNR (s) (d) | Execution time | 0.00392 |  |
| DXNR (s1) (s2) (d) | Execution time | 0.00392 |  |
| BKXNR | $(\mathrm{n})=1$ | 3.300 | 8.200 |
|  | $(\mathrm{n})=96$ | 17.900 | 22.800 |
| BSET | $(\mathrm{n})=1$ | 0.00294 |  |
|  | ( n ) $=15$ | 0.00294 |  |
| BRST | $(\mathrm{n})=1$ | 0.00294 |  |
|  | ( n ) $=15$ | 0.00294 |  |
| TEST | Execution time | 0.0120 |  |
| DTEST | Execution time | 0.0190 |  |
| BKRST | ( n ) $=1$ | 0.900 | 2.000 |
|  | $(\mathrm{n})=96$ | 1.200 | 2.200 |
| SFR | $(\mathrm{n})=1$ | 0.00882 |  |
|  | $(\mathrm{n})=15$ | $0.00882$ |  |
| SFL | $(\mathrm{n})=1$ | 0.00882 |  |
|  | ( n ) $=15$ | 0.00882 |  |
| BSFR | $(\mathrm{n})=1$ | 0.800 | 1.800 |
|  | $(\mathrm{n})=96$ | 1.500 | 2.600 |
| BSFL | $(\mathrm{n})=1$ | 0.800 | 1.900 |
|  | ( n ) $=96$ | 1.500 | 2.600 |
| DSFR | $(\mathrm{n})=1$ | 1.100 | 3.300 |
|  | $(\mathrm{n})=96$ | 8.200 | 10.800 |
| DSFL | $(\mathrm{n})=1$ | 1.100 | 3.400 |
|  | $(\mathrm{n})=96$ | 8.200 | 10.700 |
| SFTBR | ( n 1 ) $=16,(\mathrm{n} 2)=1$ | 0.900 | 2.100 |
|  | ( n 1 ) $=16,(\mathrm{n} 2)=15$ | 0.900 | 2.100 |
| SFTBL | $(\mathrm{n} 1)=16,(\mathrm{n} 2)=1$ | 0.900 | 2.100 |
|  | ( n 1$)=16,(\mathrm{n} 2)=15$ | 0.900 | 2.100 |
| SFTWR | ( n 1 ) $=16,(\mathrm{n} 2)=1$ | 2.400 | 5.400 |
|  | ( n 1$)=16,(\mathrm{n} 2)=15$ | 2.400 | 5.700 |
| SFTWL | $(\mathrm{n} 1)=16,(\mathrm{n} 2)=1$ | 2.400 | 5.800 |
|  | ( n 1 )=16, (n2)=15 | 2.400 | 5.400 |
| BCD | Execution time | 0.01078 |  |
| DBCD | Execution time | 0.01862 |  |
| BIN | Execution time | 0.00686 |  |
| DBIN | Execution time | 0.00686 |  |
| FLT2INT | ( s ) $=0$ | 0.00686 |  |
|  | $(\mathrm{s})=32766.5$ | 0.00686 |  |
| FLT2UINT | ( s$)=0$ | 0.00686 |  |
|  | (s) $=65534.5$ | 0.00686 |  |
| FLT2DINT | ( s ) $=0$ | 0.00686 |  |
|  | $(s)=1234567890.3$ | 0.00686 |  |


| Instruction name | Condition | Processing time ( $\mu \mathrm{s}$ ) |  |
| :---: | :---: | :---: | :---: |
|  |  | Minimum | Maximum |
| FLT2UDINT | (s) $=0$ | 0.00686 |  |
|  | (s)=1234567890.3 | 0.00686 |  |
| DBL2INT | (s) $=0$ | 1.400 | 3.400 |
|  | (s) $=32766.5$ | 1.500 | 4.100 |
| DBL2UINT | (s) $=0$ | 1.300 | 3.600 |
|  | (s) $=65534.5$ | 1.500 | 4.000 |
| DBL2DINT | (s) $=0$ | 1.400 | 3.400 |
|  | (s) $=1234567890.3$ | 1.400 | 4.200 |
| DBL2UDINT | (s) $=0$ | 1.500 | 3.400 |
|  | (s)=1234567890.3 | 1.500 | 4.100 |
| INT2UINT | Execution time | 0.00294 |  |
| INT2DINT | Execution time | 0.00294 |  |
| INT2UDINT | Execution time | 0.00294 |  |
| UINT2INT | Execution time | 0.00294 |  |
| UINT2DINT | Execution time | 0.00294 |  |
| UINT2UDINT | Execution time | 0.00294 |  |
| DINT2INT | Execution time | 0.00294 |  |
| DINT2UINT | Execution time | 0.00294 |  |
| DINT2UDINT | Execution time | $0.00294$ |  |
| UDINT2INT | Execution time | 0.00294 |  |
| UDINT2UINT | Execution time | 0.00294 |  |
| UDINT2DINT | Execution time | 0.00294 |  |
| GRY | Execution time | $0.00490$ |  |
| GRY_U | Execution time | 0.00490 |  |
| DGRY | Execution time | 0.00490 |  |
| DGRY_U | Execution time | $0.00490$ |  |
| GBIN | Execution time | $0.00490$ |  |
| GBIN_U | Execution time | 0.00490 |  |
| DGBIN | Execution time | 0.00490 |  |
| DGBIN_U | Execution time | $0.00490$ |  |
| BKBCD | $(\mathrm{n})=1$ | 2.400 | 7.800 |
|  | ( n ) $=96$ | 20.700 | 25.700 |
| BKBIN | $(\mathrm{n})=1$ | 2.300 | 7.400 |
|  | ( n ) $=96$ | 17.100 | 21.900 |
| DABIN | ( s ) $=1$ | 2.300 | 8.000 |
|  | ( s ) $=-32768$ | 2.300 | 8.000 |
| DABIN_U | ( s ) $=1$ | 2.400 | 8.000 |
|  | (s) $=65535$ | 2.300 | 8.000 |
| DDABIN | ( s ) $=1$ | 2.700 | 8.700 |
|  | (s)=-2147483648 | 2.700 | 8.700 |
| DDABIN_U | ( s ) $=1$ | 2.700 | 8.600 |
|  | (s) $=4294967295$ | 2.700 | 8.700 |
| HABIN | ( s ) $=1$ | 2.100 | 7.200 |
|  | (s)=FFFFH | 2.200 | 7.200 |
| DHABIN | ( s ) $=1$ | 2.400 | 7.500 |
|  | (s)=FFFFFFFFFFH | 2.400 | 7.500 |
| DABCD | ( s ) $=1$ | 2.000 | 7.200 |
|  | (s)=9999 | 2.100 | 7.100 |
| DDABCD | ( s ) $=1$ | 2.400 | 7.500 |
|  | (s)=999999999 | 2.400 | 7.500 |
| VAL | - | 3.800 | 11.900 |
| VAL_U | $-$ | 4.100 | 11.900 |


| Instruction name | Condition | Processing time ( $\mu \mathrm{s}$ ) |  |
| :---: | :---: | :---: | :---: |
|  |  | Minimum | Maximum |
| DVAL | - | 4.900 | 13.100 |
| DVAL_U | - | 5.200 | 12.600 |
| ASC2INT | ( n ) $=1$ | 2.600 | 7.400 |
|  | $(\mathrm{n})=96$ | 8.300 | 13.700 |
| EMOD | - | 3.000 | 8.200 |
| NEG | Execution time | 0.00490 |  |
| DNEG | Execution time | 0.00490 |  |
| DECO | ( n ) $=2$ | 2.400 | 5.900 |
|  | $(\mathrm{n})=8$ | 2.400 | 6.300 |
| ENCO | $(\mathrm{n})=2, \mathrm{M} 1=\mathrm{ON}$ | 2.500 | 6.400 |
|  | ( n ) $=2, \mathrm{M} 4=\mathrm{ON}$ | 2.500 | 6.400 |
|  | ( n ) $=8, \mathrm{M} 1=O \mathrm{~N}$ | 3.500 | 7.800 |
|  | ( n ) $=8, \mathrm{M} 256=\mathrm{ON}$ | 2.400 | 6.200 |
| SEG | Execution time | 0.400 | 1.600 |
| DIS | $(\mathrm{n})=1$ | 2.200 | 4.200 |
|  | $(\mathrm{n})=4$ | 2.200 | 4.300 |
| UNI | $(\mathrm{n})=1$ | 2.400 | 5.300 |
|  | ( n ) $=4$ | 2.500 | 5.200 |
| NDIS | Execution time | 2.200 | 5.000 |
| NUNI | Execution time | 2.200 | 5.000 |
| WTOB | $(\mathrm{n})=1$ | 2.500 | 5.500 |
|  | $(\mathrm{n})=96$ | 13.000 | 16.000 |
| BTOW | $(\mathrm{n})=1$ | 2.600 | 5.500 |
|  | ( n ) $=96$ | 9.800 | 12.600 |
| MOV | - | 0.00196 |  |
| DMOV | - | 0.00196 |  |
| CML | - | 0.00196 |  |
| DCML | - | 0.00196 |  |
| CMLB | - | 0.00196 |  |
| BMOV | ( n ) $=1$ | 1.100 | 1.300 |
|  | $(\mathrm{n})=96$ | 2.000 | 2.200 |
| BMOVL | $(\mathrm{n})=1$ | 1.300 | 2.500 |
|  | $(\mathrm{n})=96$ | 2.200 | 3.600 |
| FMOV | $(\mathrm{n})=1$ | 0.700 | 1.000 |
|  | $(\mathrm{n})=96$ | 1.800 | 2.000 |
| FMOVL | $(\mathrm{n})=1$ | 1.000 | 2.000 |
|  | $(\mathrm{n})=96$ | 2.000 | 3.300 |
| DFMOV | $(\mathrm{n})=1$ | 1.000 | 2.000 |
|  | $(\mathrm{n})=96$ | 2.200 | 3.400 |
| DFMOVL | $(\mathrm{n})=1$ | 1.000 | 2.000 |
|  | ( n ) $=96$ | 2.200 | 3.500 |
| XCH | - | 0.900 | 1.500 |
| DXCH | - | 0.900 | 1.500 |
| BXCH | ( n ) $=1$ | 2.700 | 6.800 |
|  | $(\mathrm{n})=96$ | 16.900 | 20.700 |
| SWAP | - | 1.200 | 2.200 |
| MOVB | - | 0.00196 |  |
| BLKMOVB | ( n ) $=1$ | 3.100 | 7.600 |
|  | $(\mathrm{n})=96$ | 3.600 | 8.600 |
| ROR | $(\mathrm{n})=1$ | 0.00882 |  |
|  | $(\mathrm{n})=15$ | 0.00882 |  |


| Instruction name | Condition | Processing time ( $\mu \mathrm{s}$ ) |  |
| :---: | :---: | :---: | :---: |
|  |  | Minimum | Maximum |
| RCR | ( n ) $=1$ | 0.00882 |  |
|  | $(\mathrm{n})=15$ | 0.00882 |  |
| DROR | $(\mathrm{n})=1$ | 0.00882 |  |
|  | $(\mathrm{n})=31$ | 0.00882 |  |
| DRCR | $(\mathrm{n})=1$ | 0.00882 |  |
|  | $(\mathrm{n})=31$ | 0.00882 |  |
| ROL | $(\mathrm{n})=1$ | 0.00882 |  |
|  | $(\mathrm{n})=15$ | 0.00882 |  |
| RCL | $(\mathrm{n})=1$ | 0.00882 |  |
|  | $(\mathrm{n})=15$ | 0.00882 |  |
| DROL | $(\mathrm{n})=1$ | 0.00882 |  |
|  | $(\mathrm{n})=31$ | 0.00882 |  |
| DRCL | ( n ) $=1$ | 0.00882 |  |
|  | $(\mathrm{n})=31$ | 0.00882 |  |
| CJ | - | 1.000 | 2.000 |
| SCJ | - | 1.000 | 2.000 |
| JMP | - | 1.000 | 2.100 |
| GOEND | - | 0.9000 |  |
| DI | - | 2.800 | 4.200 |
| DI (s) | - | 3.000 | 4.200 |
| El | - | 4.100 | 9.200 |
| IMASK | - | 1.000 | 1.800 |
| SIMASK | - | 0.800 | 1.700 |
| IRET | - | 1.700 | 2.200 |
| WDT | - | 4.900 | 16.400 |
| FOR | - | 0.00196 |  |
| NEXT | - | 0.03920 |  |
| BREAK | - | 4.400 | 10.000 |
| CALL Pn | Local pointer | 0.800 | 1.500 |
|  | Global pointer | 3.900 | 13.500 |
| CALL Pn (s1) to (s5) | Local pointer | 17.400 | 40.600 |
| RET | Return to the own program | 0.2000 |  |
|  | Return to the another program | 2.0000 |  |
| FCALL Pn | Local pointer | 1.000 | 2.000 |
|  | Global pointer | 4.300 | 22.900 |
| FCALL Pn (s1) to (s5) | Local pointer | 16.400 | 37.800 |
| ECALL Pn | Local pointer, file name="P1" | 74.700 | 118.400 |
| ECALL Pn (s1) to (s5) | Local pointer, file name="P1" | 94.500 | 148.400 |
| EFCALL Pn | Local pointer, file name="P1" | 72.400 | 114.300 |
| EFCALL Pn (s1) to (s5) | Local pointer, file name="P1" | 91.100 | 132.200 |
| XCALL Pn | - | 3.800 | 16.700 |
| FIFR | Number of data blocks stored=1 | 2.100 | 4.800 |
|  | Number of data blocks stored=96 | 7.100 | 10.300 |
| FPOP | Number of data blocks stored=1 | 2.000 | 4.600 |
|  | Number of data blocks stored=96 | 2.100 | 4.600 |
| FIFW | Number of data blocks stored=0 | 2.100 | 4.800 |
|  | Number of data blocks stored=96 | 2.100 | 4.700 |
| FINS | Number of data blocks stored=0 | 2.500 | 5.800 |
|  | Number of data blocks stored=96 | 9.200 | 13.700 |
| FDEL | Number of data blocks stored=1 | 2.300 | 6.000 |
|  | Number of data blocks stored=96 | 7.400 | 11.500 |
| S.DEVLD | - | 4.300 | 7.300 |


| Instruction name | Condition | Processing time ( $\mu \mathrm{s}$ ) |  |
| :---: | :---: | :---: | :---: |
|  |  | Minimum | Maximum |
| SP.DEVST | - | 20.200 | 24.800 |
| SP.FREAD | - | 32.400 | 37.200 |
| SP.FWRITE | - | 33.500 | 37.300 |
| LEDR | Self-diagnostics error not caused | 2.100 | 5.200 |
|  | Self-diagnostics error caused (continuation error, one annunciator ON) | 15.000 | 25.900 |
| LD\$= | Continuity/Non-continuity | 1.600 | 4.500 |
| LD\$<> | Continuity/Non-continuity | 1.600 | 4.500 |
| LD\$> | Continuity/Non-continuity | 1.600 | 4.500 |
| LD\$<= | Continuity/Non-continuity | 1.600 | 4.500 |
| LD\$< | Continuity/Non-continuity | 1.600 | 4.500 |
| LD\$>= | Continuity/Non-continuity | 1.600 | 4.500 |
| AND\$= | No execution time | 0.00588 |  |
|  | Execution time: Continuity/Non-continuity | 1.600 | 4.500 |
| AND\$<> | No execution time | 0.00588 |  |
|  | Execution time: Continuity/Non-continuity | 1.600 | 4.500 |
| AND\$> | No execution time | 0.00588 |  |
|  | Execution time: Continuity/Non-continuity | 1.600 | 4.500 |
| AND\$<= | No execution time | 0.00588 |  |
|  | Execution time: Continuity/Non-continuity | 1.600 | 4.500 |
| AND\$< | No execution time | 0.00588 |  |
|  | Execution time: Continuity/Non-continuity | 1.600 | 4.500 |
| AND\$>= | No execution time | 0.00588 |  |
|  | Execution time: Continuity/Non-continuity | 1.600 | 4.500 |
| OR\$= | No execution time | 0.00588 |  |
|  | Execution time: Continuity/Non-continuity | 1.600 | 4.500 |
| OR\$<> | No execution time | 0.00588 |  |
|  | Execution time: Continuity/Non-continuity | 1.600 | 4.500 |
| OR\$> | No execution time | 0.00588 |  |
|  | Execution time: Continuity/Non-continuity | 1.600 | 4.500 |
| OR $\$<=$ | No execution time | 0.00588 |  |
|  | Execution time: Continuity/Non-continuity | 1.600 | 4.500 |
| OR\$< | No execution time | 0.00588 |  |
|  | Execution time: Continuity/Non-continuity | 1.600 | 4.500 |
| OR\$>= | No execution time | 0.00588 |  |
|  | Execution time: Continuity/Non-continuity | 1.600 | 4.500 |
| \$+ (s) (d) | Execution time | 2.400 | 6.700 |
| \$+ (s1) (s2) (d) | Execution time | 2.900 | 8.200 |
| \$MOV | 0 characters | 2.400 | 9.200 |
|  | 32 characters | 4.400 | 12.100 |
| \$MOV_WS | 0 characters | 2.400 | 10.100 |
|  | 32 characters | 6.100 | 14.200 |
| BINDA | ( s ) $=1$ | 2.000 | 5.100 |
|  | (s)=-32768 | 2.200 | 5.400 |
| BINDA_U | ( s ) $=1$ | 1.800 | 4.700 |
|  | (s) $=65535$ | 2.200 | 5.300 |
| DBINDA | ( s ) $=1$ | 1.800 | 4.800 |
|  | (s) $=-2147483648$ | 2.200 | 5.400 |
| DBINDA_U | ( s ) $=1$ | 1.900 | 5.100 |
|  | (s) $=4294967295$ | 2.400 | 5.200 |
| BINHA | ( s ) $=1$ | 1.900 | 5.000 |
|  | (s)=FFFFH | 1.900 | 5.000 |


| Instruction name | Condition | Processing time ( $\mu \mathrm{s}$ ) |  |
| :---: | :---: | :---: | :---: |
|  |  | Minimum | Maximum |
| DBINHA | (s) $=1$ | 2.000 | 4.700 |
|  | (s)=FFFFFFFFH | 2.000 | 4.800 |
| STR | - | 2.800 | 7.300 |
| STR_U | - | 2.900 | 7.300 |
| DSTR | - | 3.200 | 7.400 |
| DSTR_U | - | 3.300 | 7.900 |
| BCDDA | (s) $=1$ | 1.900 | 5.000 |
|  | (s)=9999 | 2.000 | 5.100 |
| DBCDDA | ( s ) $=1$ | 2.000 | 5.100 |
|  | (s)=999999999 | 2.200 | 5.200 |
| ESTR | - | 4.7000 | 17.1000 |
| INT2ASC | $(\mathrm{n})=1$ | 2.600 | 6.900 |
|  | ( n ) $=96$ | 6.100 | 11.000 |
| WS2SJIS | Number of characters=1 | 3.300 | 10.300 |
|  | Number of characters=96 | 52.500 | 59.200 |
| SJIS2WS | Number of characters=1 | 3.100 | 9.900 |
|  | Number of characters=96 | 48.300 | 55.200 |
| SJIS2WSB | Number of characters=1 | 3.200 | 9.800 |
|  | Number of characters=96 | 48.300 | 55.100 |
| LEN | 1 characters | 1.400 | 3.800 |
|  | 96 characters | 9.600 | 11.900 |
| RIGHT | Number of characters to be extracted=1 | 3.200 | 11.200 |
|  | Number of characters to be extracted=96 | 15.800 | 23.400 |
| LEFT | Number of characters to be extracted=1 | 3.200 | 11.100 |
|  | Number of characters to be extracted=96 | 15.700 | 23.200 |
| MIDR | - | 3.600 | 12.200 |
| MIDW | - | 4.200 | 12.300 |
| INSTR | No match | 7.500 | 15.000 |
|  | Match: Head | 5.300 | 13.000 |
|  | Match: Tail | 7.500 | 15.300 |
| STRINS | (s)=128, (d) =40, ( n$)=1$ | 17.900 | 27.200 |
|  | (s)=128, (d) $=40,(\mathrm{n})=48$ | 20.400 | 30.100 |
| STRDEL | (s)=128, (d) $=40,(\mathrm{n})=1$ | 15.800 | 23.000 |
|  | (s)=128, (d) $=40,(\mathrm{n})=48$ | 13.600 | 20.900 |
| LDE= | Continuity/Non-continuity | 0.00588 |  |
| LDE<> | Continuity/Non-continuity | 0.00588 |  |
| LDE> | Continuity/Non-continuity | 0.00588 |  |
| LDE<= | Continuity/Non-continuity | 0.00588 |  |
| LDE< | Continuity/Non-continuity | 0.00588 |  |
| LDE>= | Continuity/Non-continuity | 0.00588 |  |
| ANDE= | No execution time | 0.00588 |  |
|  | Execution time: Continuity/Non-continuity | 0.00588 |  |
| ANDE<> | No execution time | 0.00588 |  |
|  | Execution time: Continuity/Non-continuity | 0.00588 |  |
| ANDE> | No execution time | 0.00588 |  |
|  | Execution time: Continuity/Non-continuity | 0.00588 |  |
| ANDE<= | No execution time | 0.00588 |  |
|  | Execution time: Continuity/Non-continuity | 0.00588 |  |
| ANDE< | No execution time | 0.00588 |  |
|  | Execution time: Continuity/Non-continuity | 0.00588 |  |
| ANDE>= | No execution time | 0.00588 |  |
|  | Execution time: Continuity/Non-continuity | 0.00588 |  |


| Instruction name | Condition | Processing time ( $\mu \mathrm{s}$ ) |  |
| :---: | :---: | :---: | :---: |
|  |  | Minimum | Maximum |
| ORE= | No execution time | 0.00588 |  |
|  | Execution time: Continuity/Non-continuity | 0.00588 |  |
| ORE<> | No execution time | 0.00588 |  |
|  | Execution time: Continuity/Non-continuity | 0.00588 |  |
| ORE> | No execution time | 0.00588 |  |
|  | Execution time: Continuity/Non-continuity | 0.00588 |  |
| ORE<= | No execution time | 0.00588 |  |
|  | Execution time: Continuity/Non-continuity | 0.00588 |  |
| ORE< | No execution time | 0.00588 |  |
|  | Execution time: Continuity/Non-continuity | 0.00588 |  |
| ORE>= | No execution time | 0.00588 |  |
|  | Execution time: Continuity/Non-continuity | 0.00588 |  |
| LDED= | Continuity/Non-continuity | 1.900 | 4.500 |
| LDED<> | Continuity/Non-continuity | 1.900 | 4.500 |
| LDED> | Continuity/Non-continuity | 1.800 | 4.400 |
| LDED<= | Continuity/Non-continuity | 1.900 | 4.500 |
| LDED< | Continuity/Non-continuity | 1.900 | 4.500 |
| LDED>= | Continuity/Non-continuity | 1.900 | 4.500 |
| ANDED= | No execution time | 0.00588 |  |
|  | Execution time: Continuity/Non-continuity | 1.900 | 4.500 |
| ANDED<> | No execution time | $0.00588$ |  |
|  | Execution time: Continuity/Non-continuity | 1.900 | 4.500 |
| ANDED> | No execution time | 0.00588 |  |
|  | Execution time: Continuity/Non-continuity | 1.900 | 4.500 |
| ANDED<= | No execution time | 0.00588 |  |
|  | Execution time: Continuity/Non-continuity | 1.900 | 4.500 |
| ANDED< | No execution time | 0.00588 |  |
|  | Execution time: Continuity/Non-continuity | 1.900 | 4.500 |
| ANDED>= | No execution time | 0.00588 |  |
|  | Execution time: Continuity/Non-continuity | 1.900 | 4.500 |
| ORED= | No execution time | 0.00588 |  |
|  | Execution time: Continuity/Non-continuity | 1.900 | 4.500 |
| ORED<> | No execution time | 0.00588 |  |
|  | Execution time: Continuity/Non-continuity | 1.900 | 4.500 |
| ORED> | No execution time | 0.00588 |  |
|  | Execution time: Continuity/Non-continuity | 1.900 | 4.500 |
| ORED<= | No execution time | 0.00588 |  |
|  | Execution time: Continuity/Non-continuity | 1.900 | 4.500 |
| ORED< | No execution time | 0.00588 |  |
|  | Execution time: Continuity/Non-continuity | 1.900 | 4.500 |
| ORED>= | No execution time | 0.00588 |  |
|  | Execution time: Continuity/Non-continuity | 1.900 | 4.500 |
| $\mathrm{E}+(\mathrm{s})$ (d) | (s) $=0,(\mathrm{~d})=0$ | 0.0098 |  |
|  | (s) $=2^{127},(\mathrm{~d})=2^{127}$ | 0.0098 |  |
| $E+(s 1)(s 2)(d)$ | (s1) $=0,(\mathrm{~s} 2)=0$ | 0.0098 |  |
|  | (s1) $=2^{127}$, (s2) $=2^{127}$ | 0.0098 |  |
| E- (s) (d) | (s) $=0$, (d) $=0$ | 0.0098 |  |
|  | (s) $=2^{127}$, ( d$)=2^{127}$ | 0.0098 |  |
| $\mathrm{E}-(\mathrm{s} 1)(\mathrm{s} 2)(\mathrm{d})$ | (s1) $=0,(\mathrm{~s} 2)=0$ | 0.0098 |  |
|  | (s1) $=2^{127}$, (s2) $=2^{127}$ | 0.0098 |  |
| $E D+(s)(d)$ | (s) $=0,(\mathrm{~d})=0$ | 1.600 | 5.800 |
|  | (s) $=2^{1023},(\mathrm{~d})=2^{1023}$ | 2.000 | 7.000 |


| Instruction name | Condition | Processing time ( $\mu \mathrm{s}$ ) |  |
| :---: | :---: | :---: | :---: |
|  |  | Minimum | Maximum |
| $E D+(s 1)(s 2)(d)$ | (s1) $=0,(\mathrm{~s} 2)=0$ | 2.000 | 5.700 |
|  | (s1) $=2^{1023},(\mathrm{~s} 2)=2^{1023}$ | 2.200 | 7.000 |
| ED- (s) (d) | (s) $=0,(\mathrm{~d})=0$ | 1.800 | 5.500 |
|  | (s) $=2^{1023},(\mathrm{~d})=2^{1023}$ | 1.900 | 5.700 |
| ED- (s1) (s2) (d) | (s1) $=0,(\mathrm{~s} 2)=0$ | 2.000 | 5.500 |
|  | (s1) $=2^{1023},(\mathrm{~s} 2)=2^{1023}$ | 2.100 | 5.900 |
| E* | (s1) $=0,(\mathrm{~s} 2)=0$ | 0.0098 |  |
|  | (s1) $=2^{127}$, (s2) $=2^{127}$ | 0.0098 |  |
| E/ | (s1) $=2^{127}$, (s2) $=2^{127}$ | 0.5684 |  |
| ED* | (s1) $=0,(\mathrm{~s} 2)=0$ | 2.000 | 5.900 |
|  | (s1) $=2^{1023},(\mathrm{~s} 2)=2^{1023}$ | 2.300 | 7.300 |
| ED/ | (s1) $=2^{1023},(\mathrm{~s} 2)=2^{1023}$ | 2.300 | 7.300 |
| INT2FLT | ( s ) $=0$ | 0.00686 |  |
|  | (s) $=7 \mathrm{FFFH}$ | 0.00686 |  |
| UINT2FLT | (s) $=0$ | 0.00686 |  |
|  | (s)=FFFFH | 0.00686 |  |
| DINT2FLT | (s) $=0$ | 0.00686 |  |
|  | (s)=7FFFFFFFH | 0.00686 |  |
| UDINT2FLT | ( s ) $=0$ | 0.00686 |  |
|  | (s)=FFFFFFFFH | 0.00686 |  |
| DBL2FLT | - | 1.600 | 4.000 |
| INT2DBL | (s) $=0$ | 1.400 | 3.100 |
|  | ( s ) $=7 \mathrm{FFFFH}$ | 1.400 | 3.100 |
| UINT2DBL | (s)=0 | 1.400 | 3.300 |
|  | (s)=FFFFH | 1.400 | 3.200 |
| DINT2DBL | ( s ) $=0$ | 1.400 | 3.100 |
|  | (s)=7FFFFFFFH | 1.400 | 3.100 |
| UDINT2DBL | (s) $=0$ | 1.400 | 3.200 |
|  | (s)=FFFFFFFFH | 1.400 | 3.200 |
| FLT2DBL | - | 1.400 | 4.600 |
| EVAL | Decimal point format 2-digit full specification | 3.700 | 12.300 |
|  | Number of digits format 6-digit full specification | 4.100 | 12.000 |
| EREXP | - | 3.100 | 9.400 |
| ENEG | (d) $=0$ | 1.200 | 2.200 |
|  | (d) $=-1.0$ | 1.300 | 2.900 |
| EDNEG | (d) $=0$ | 1.300 | 4.300 |
|  | (d) $=-1.0$ | 1.100 | 4.400 |
| EMOV | - | 0.00196 |  |
| EDMOV | - | 0.00196 |  |
| SIN | - | 1.400 | 4.300 |
| COS | - | 1.400 | 4.200 |
| TAN | - | 1.400 | 4.200 |
| ASIN | - | 1.400 | 4.500 |
| ACOS | - | 1.400 | 4.500 |
| ATAN | - | 1.400 | 3.400 |
| SIND | - | 2.400 | 12.700 |
| COSD | - | 2.300 | 12.700 |
| TAND | - | 2.800 | 13.900 |
| ASIND | - | 2.400 | 10.500 |
| ACOSD | - | 2.200 | 9.700 |
| ATAND | - | 2.000 | 9.200 |
| BSIN | - | 2.600 | 9.300 |


| Instruction name | Condition | Processing time ( $\mu \mathrm{s}$ ) |  |
| :---: | :---: | :---: | :---: |
|  |  | Minimum | Maximum |
| BCOS | - | 2.600 | 8.900 |
| BTAN | - | 2.700 | 9.600 |
| BASIN | - | 2.400 | 7.300 |
| BACOS | - | 2.500 | 7.400 |
| BATAN | - | 2.400 | 7.400 |
| RAD | - | 1.400 | 2.700 |
| DEG | - | 1.400 | 2.900 |
| RADD | - | 1.600 | 7.300 |
| DEGD | - | 1.600 | 7.100 |
| ESQRT | - | 1.100 | 2.900 |
| EDSQRT | - | 1.700 | 7.200 |
| EXP | (s) $=-10$ | 1.400 | 4.500 |
|  | ( s ) $=1$ | 1.500 | 4.600 |
| EXPD | (s) $=-10$ | 2.200 | 10.500 |
|  | (s) $=1$ | 2.100 | 10.300 |
| LOG | (s)=1 | 1.300 | 4.100 |
|  | (s) $=10$ | 1.400 | 4.700 |
| LOGD | (s) $=1$ | 1.800 | 8.600 |
|  | (s) $=10$ | 2.300 | 10.900 |
| BSQRT | (s) $=0$ | 1.500 | 3.100 |
|  | (s)=9999 | 2.200 | 6.900 |
| BDSQRT | (s) $=0$ | 1.400 | 2.600 |
|  | (s)=99999999 | 2.100 | 6.000 |
| POW | (s1) $=1.23 \mathrm{E}+5,(\mathrm{~s} 2)=3.45 \mathrm{E}+0$ | 3.000 | 8.900 |
| POWD | (s1) $=1.23 \mathrm{E}+5,(\mathrm{~s} 2)=3.45 \mathrm{E}+0$ | 4.400 | 19.300 |
| LOG10 | (s) $=1.23 \mathrm{E}+20$ | 1.500 | 4.700 |
| LOG10D | (s) $=1.23 \mathrm{E}+20$ | 2.400 | 12.900 |
| EMAX | $(\mathrm{n})=1$ | 2.500 | 5.100 |
|  | ( n ) $=96$ | 14.700 | 17.900 |
| EDMAX | $(\mathrm{n})=1$ | 2.700 | 6.900 |
|  | ( n ) $=96$ | 27.700 | 32.500 |
| EMIN | $(\mathrm{n})=1$ | 2.500 | 5.100 |
|  | $(\mathrm{n})=96$ | 14.700 | 17.900 |
| EDMIN | $(\mathrm{n})=1$ | 2.700 | 6.800 |
|  | ( n ) $=96$ | 27.200 | 32.400 |
| RND | - | 0.800 | 1.700 |
| SRND | - | 1.100 | 1.800 |
| ZPUSH (d) | - | 0.900 | 1.700 |
| ZPUSH (s) (d) | Total range of $Z$ and $L Z$ | 1.600 | 3.200 |
|  | Z full range (default setting: 20 points) | 1.600 | 3.100 |
|  | LZ full range (default setting: 2 points) | 1.300 | 3.100 |
| ZPOP (d) | - | 0.900 | 1.800 |
| ZPOP (s) (d) | Total range of $Z$ and $L Z$ | 1.500 | 3.200 |
|  | Z full range (default setting: 20 points) | 1.500 | 3.100 |
|  | LZ full range (default setting: 2 points) | 1.300 | 3.100 |
| LIMIT | - | 1.000 | 1.800 |
| LIMIT_U | - | 1.000 | 1.800 |
| DLIMIT | - | 1.000 | 1.800 |
| DLIMIT_U | - | 1.000 | 1.800 |
| BAND | - | 1.800 | 3.100 |
| BAND_U | - | 1.800 | 3.100 |
| DBAND | - | 1.900 | 3.000 |


| Instruction name | Condition | Processing time ( $\mu \mathrm{s}$ ) |  |
| :---: | :---: | :---: | :---: |
|  |  | Minimum | Maximum |
| DBAND_U | - | 1.900 | 2.900 |
| ZONE | - | 1.800 | 3.000 |
| ZONE_U | - | 1.800 | 3.000 |
| DZONE | - | 1.900 | 2.900 |
| DZONE_U | - | 1.900 | 3.000 |
| SCL | SM755=ON, point No.1<(s1)<point No. 2 | 3.300 | 8.900 |
|  | SM755=ON, point No.9<(s1)<point No. 10 | 3.300 | 8.900 |
|  | SM755=OFF, point No.1<(s1)<point No. 2 | 3.100 | 8.700 |
|  | SM755=OFF, point No.9<(s1)<point No. 10 | 3.700 | 8.900 |
| SCL_U | SM755=ON, point No.1<(s1)<point No. 2 | 3.200 | 8.700 |
|  | SM755=ON, point No.9<(s1)<point No. 10 | 3.200 | 8.800 |
|  | SM755=OFF, point No.1<(s1)<point No. 2 | 3.000 | 8.500 |
|  | SM755=OFF, point No.9<(s1)<point No. 10 | 3.400 | 8.900 |
| DSCL | SM755=ON, point No.1<(s1)<point No. 2 | 3.300 | 9.900 |
|  | SM755=ON, point No.9<(s1)<point No. 10 | 3.300 | 9.800 |
|  | SM755=OFF, point No.1<(s1)<point No. 2 | 3.000 | 9.300 |
|  | SM755=OFF, point No.9<(s1)<point No. 10 | 3.600 | 9.700 |
| DSCL_U | SM755=ON, point No.1<(s1)<point No. 2 | 3.200 | 10.200 |
|  | SM755=ON, point No.9<(s1)<point No. 10 | 3.200 | 10.200 |
|  | SM755=OFF, point No.1<(s1)<point No. 2 | 2.900 | 9.500 |
|  | SM755=OFF, point No.9<(s1)<point No. 10 | 3.500 | 9.900 |
| SCL2 | SM755=ON, point No.1<(s1)<point No. 2 | 3.300 | 9.200 |
|  | SM755=ON, point No.9<(s1)<point No. 10 | 3.300 | 9.100 |
|  | SM755=OFF, point No.1<(s1)<point No. 2 | 3.300 | 8.700 |
|  | SM755=OFF, point No.9<(s1)<point No. 10 | 3.500 | 8.800 |
| SCL2_U | SM755=ON, point No.1<(s1)<point No. 2 | 3.200 | 8.800 |
|  | SM755=ON, point No.9<(s1)<point No. 10 | 3.300 | 8.900 |
|  | SM755=OFF, point No.1<(s1)<point No. 2 | 2.900 | 8.500 |
|  | SM755=OFF, point No.9<(s1)<point No. 10 | 3.500 | 9.000 |
| DSCL2 | SM755=ON, point No.1<(s1)<point No. 2 | 3.300 | 10.000 |
|  | SM755=ON, point No.9<(s1)<point No. 10 | 3.300 | 10.000 |
|  | SM755=OFF, point No.1<(s1)<point No. 2 | 3.000 | 9.500 |
|  | SM755=OFF, point No.9<(s1)<point No. 10 | 3.300 | 10.100 |
| DSCL2_U | SM755=ON, point No.1<(s1)<point No. 2 | 3.200 | 10.100 |
|  | SM755=ON, point No.9<(s1)<point No. 10 | 3.300 | 10.100 |
|  | SM755=OFF, point No.1<(s1)<point No. 2 | 2.900 | 9.600 |
|  | SM755=OFF, point No.9<(s1)<point No. 10 | 3.500 | 10.000 |
| UDCNT1 | - | 0.800 | 1.600 |
| UDCNT2 | - | 0.800 | 1.600 |
| TTMR | - | 2.000 | 5.900 |
| STMR | - | 2.700 | 8.100 |
| ROTC | - | 4.800 | 8.600 |
| RAMPQ | - | 3.400 | 8.200 |
| SPD | - | 0.700 | 1.600 |
| PLSY | - | 0.800 | 1.400 |
| PWM | - | 0.800 | 1.400 |
| MTR | - | 3.400 | 11.600 |
| SERDATA | ( n )=1: All match | 2.700 | 6.700 |
|  | $(\mathrm{n})=1$ : All mismatch | 2.700 | 6.600 |
|  | ( n )=96: All match | 9.700 | 15.000 |
|  | (n)=96: All mismatch | 9.700 | 15.100 |


| Instruction name | Condition | Processing time ( $\mu \mathrm{s}$ ) |  |
| :---: | :---: | :---: | :---: |
|  |  | Minimum | Maximum |
| DSERDATA | $(\mathrm{n})=1$ : All match | 3.000 | 7.300 |
|  | $(\mathrm{n})=1$ : All mismatch | 3.000 | 7.300 |
|  | ( n )=96: All match | 14.500 | 18.900 |
|  | $(\mathrm{n})=96$ : All mismatch | 14.500 | 18.900 |
| SUM | (s) $=0$ | 1.300 | 1.900 |
|  | (s)=FFFFH | 1.300 | 1.900 |
| DSUM | ( s ) $=0$ | 1.700 | 2.400 |
|  | (s)=FFFFFFFFH | 1.700 | 2.400 |
| MAX | ( n ) $=1$ | 2.400 | 5.000 |
|  | ( n ) $=96$ | 9.500 | 12.300 |
| MAX_U | ( n ) $=1$ | 2.500 | 6.100 |
|  | ( n ) $=96$ | 10.700 | 14.700 |
| DMAX | $(\mathrm{n})=1$ | 2.500 | 5.300 |
|  | $(\mathrm{n})=96$ | 17.400 | 20.400 |
| DMAX_U | $(\mathrm{n})=1$ | 2.500 | 5.800 |
|  | $(\mathrm{n})=96$ | 10.300 | 14.100 |
| MIN | $(\mathrm{n})=1$ | 2.400 | 5.000 |
|  | $(\mathrm{n})=96$ | 9.500 | 12.400 |
| MIN_U | $(\mathrm{n})=1$ | 2.500 | 5.900 |
|  | $(\mathrm{n})=96$ | 10.700 | 14.400 |
| DMIN | $(\mathrm{n})=1$ | 2.500 | 5.400 |
|  | $(\mathrm{n})=96$ | 17.500 | 20.500 |
| DMIN_U | $(\mathrm{n})=1$ | 2.400 | 5.900 |
|  | $(\mathrm{n})=96$ | 10.300 | 14.000 |
| SORTD | $(\mathrm{n})=1,(\mathrm{~s} 1)=1$ | 3.100 | 5.600 |
|  | $(\mathrm{n})=96,(\mathrm{~s} 1)=16$ | 8.400 | 11.900 |
| SORTD_U | $(\mathrm{n})=1,(\mathrm{~s} 1)=1$ | 3.100 | 5.600 |
|  | $(\mathrm{n})=96,(\mathrm{~s} 1)=16$ | 8.800 | 12.900 |
| DSORTD | $(\mathrm{n})=1,(\mathrm{~s} 1)=1$ | 3.000 | 5.500 |
|  | $(\mathrm{n})=96,(\mathrm{~s} 1)=16$ | 9.900 | 13.500 |
| DSORTD_U | $(\mathrm{n})=1,(\mathrm{~s} 1)=1$ | 3.100 | 5.800 |
|  | $(\mathrm{n})=96,(\mathrm{~s} 1)=16$ | 11.100 | 15.800 |
| WSUM | $(\mathrm{n})=1$ | 0.900 | 1.800 |
|  | $(\mathrm{n})=96$ | 4.700 | 5.600 |
| WSUM_U | $(\mathrm{n})=1$ | 0.900 | 1.800 |
|  | $(\mathrm{n})=96$ | 4.700 | 5.600 |
| DWSUM | $(\mathrm{n})=1$ | 2.300 | 7.000 |
|  | $(\mathrm{n})=96$ | 9.400 | 14.100 |
| DWSUM_U | $(\mathrm{n})=1$ | 2.400 | 7.800 |
|  | $(\mathrm{n})=96$ | 9.500 | 14.900 |
| MEAN | $(\mathrm{n})=1$ | 1.900 | 4.900 |
|  | $(\mathrm{n})=96$ | 5.100 | 8.800 |
| MEAN_U | $(\mathrm{n})=1$ | 1.900 | 4.800 |
|  | $(\mathrm{n})=96$ | 4.700 | 8.700 |
| DMEAN | $(\mathrm{n})=1$ | 2.600 | 7.800 |
|  | $(\mathrm{n})=96$ | 8.900 | 14.200 |
| DMEAN_U | $(\mathrm{n})=1$ | 2.200 | 7.500 |
|  | $(\mathrm{n})=96$ | 8.300 | 13.400 |
| DBOPEN | Execution time | 18.300 | 20.900 |
| DBCLOSE | Execution time | 11.500 | 13.100 |
| DBINSERT | (s3)=1 | 25.400 | 28.900 |
|  | $(\mathrm{s} 3)=16$ | 46.400 | 50.100 |


| Instruction name | Condition | Processing time ( $\mu \mathrm{s}$ ) |  |
| :---: | :---: | :---: | :---: |
|  |  | Minimum | Maximum |
| DBUPDATE | (s3)=1 | 31.700 | 34.700 |
|  | (s3) $=16$ | 49.300 | 52.900 |
| DBSELECT | (s3)=1 | 32.100 | 35.300 |
|  | (s3) $=16$ | 51.600 | 55.000 |
| DBDELETE | (s3)=1 | 26.000 | 29.900 |
|  | (s3) $=2$ | 27.400 | 31.200 |
| DBIMPORT | Execution time | 16.200 | 19.200 |
| DBEXPORT | Execution time | 16.000 | 19.000 |
| DBTRANS | Execution time | 11.400 | 13.000 |
| DBCOMMIT | Execution time | 11.600 | 13.000 |
| DBROLBAK | Execution time | 11.500 | 12.900 |
| RSET | - | 1.800 | 6.300 |
| QDRSET | - | 65.100 | 92.100 |
| ZRRDB | - | 1.600 | 2.900 |
| ZRWRB | - | 1.700 | 3.200 |
| ADRSET | - | 1.100 | 2.000 |
| DATERD | - | 3.400 | 10.700 |
| DATEWR | - | 12.200 | 37.400 |
| DATE+ | No carry | 3.300 | 6.800 |
|  | Carry | 3.400 | 6.500 |
| DATE- | No carry | 3.400 | 6.500 |
|  | Carry | 3.400 | 6.600 |
| TIME2SEC | - | 1.900 | 3.700 |
| SEC2TIME | - | 1.800 | 3.900 |
| LDDT= | Compare with specified date: Continuity | 2.900 | 9.500 |
|  | Compare with specified date: Non-continuity | 2.900 | 9.400 |
|  | Compare with specified date: Continuity | 4.700 | 15.000 |
|  | Compare with specified date: Non-continuity | 4.800 | 15.100 |
| LDDT<> | Compare with specified date: Continuity | 2.600 | 9.300 |
|  | Compare with specified date: Non-continuity | 2.600 | 9.400 |
|  | Compare with specified date: Continuity | 4.600 | 15.600 |
|  | Compare with specified date: Non-continuity | 4.800 | 15.500 |
| LDDT> | Compare with specified date: Continuity | 2.700 | 9.500 |
|  | Compare with specified date: Non-continuity | 2.700 | 9.700 |
|  | Compare with specified date: Continuity | 4.600 | 15.500 |
|  | Compare with specified date: Non-continuity | 4.600 | 15.300 |
| LDDT<= | Compare with specified date: Continuity | 2.700 | 9.900 |
|  | Compare with specified date: Non-continuity | 2.800 | 9.800 |
|  | Compare with specified date: Continuity | 4.800 | 14.600 |
|  | Compare with specified date: Non-continuity | 4.900 | 14.400 |
| LDDT< | Compare with specified date: Continuity | 2.800 | 9.900 |
|  | Compare with specified date: Non-continuity | 2.700 | 9.900 |
|  | Compare with specified date: Continuity | 4.800 | 15.000 |
|  | Compare with specified date: Non-continuity | 4.800 | 14.900 |
| LDDT>= | Compare with specified date: Continuity | 2.700 | 9.800 |
|  | Compare with specified date: Non-continuity | 2.800 | 9.800 |
|  | Compare with specified date: Continuity | 5.000 | 14.500 |
|  | Compare with specified date: Non-continuity | 4.900 | 14.700 |


| Instruction name | Condition | Processing time ( $\mu \mathrm{s}$ ) |  |
| :---: | :---: | :---: | :---: |
|  |  | Minimum | Maximum |
| ANDDT= | No execution time | 0.00588 |  |
|  | Compare with specified date: Continuity | 2.400 | 10.000 |
|  | Compare with specified date: Non-continuity | 2.500 | 10.100 |
|  | Compare with specified date: Continuity | 4.600 | 15.900 |
|  | Compare with specified date: Non-continuity | 4.500 | 15.900 |
| ANDDT<> | No execution time | 0.00588 |  |
|  | Compare with specified date: Continuity | 2.900 | 9.400 |
|  | Compare with specified date: Non-continuity | 2.900 | 9.400 |
|  | Compare with specified date: Continuity | 4.800 | 15.800 |
|  | Compare with specified date: Non-continuity | 4.900 | 15.800 |
| ANDDT> | No execution time | 0.00588 |  |
|  | Compare with specified date: Continuity | 2.700 | 9.700 |
|  | Compare with specified date: Non-continuity | 2.700 | 10.000 |
|  | Compare with specified date: Continuity | 4.700 | 15.600 |
|  | Compare with specified date: Non-continuity | 4.700 | 15.600 |
| ANDDT<= | No execution time | 0.00588 |  |
|  | Compare with specified date: Continuity | 2.700 | 9.800 |
|  | Compare with specified date: Non-continuity | 2.800 | 9.900 |
|  | Compare with specified date: Continuity | 4.900 | 14.600 |
|  | Compare with specified date: Non-continuity | 4.900 | 14.500 |
| ANDDT< | No execution time | 0.00588 |  |
|  | Compare with specified date: Continuity | 2.600 | 9.400 |
|  | Compare with specified date: Non-continuity | 2.600 | 9.600 |
|  | Compare with specified date: Continuity | 4.800 | 16.200 |
|  | Compare with specified date: Non-continuity | 4.900 | 15.200 |
| ANDDT>= | No execution time | 0.00588 |  |
|  | Compare with specified date: Continuity | 2.700 | 9.900 |
|  | Compare with specified date: Non-continuity | 2.700 | 9.700 |
|  | Compare with specified date: Continuity | 4.900 | 14.600 |
|  | Compare with specified date: Non-continuity | 4.900 | 14.900 |
| ORDT= | No execution time | 0.00588 |  |
|  | Compare with specified date: Continuity | 2.700 | 9.800 |
|  | Compare with specified date: Non-continuity | 2.600 | 9.800 |
|  | Compare with specified date: Continuity | 4.800 | 15.600 |
|  | Compare with specified date: Non-continuity | 4.800 | 15.400 |
| ORDT<> | No execution time | 0.00588 |  |
|  | Compare with specified date: Continuity | 2.800 | 10.000 |
|  | Compare with specified date: Non-continuity | 2.700 | 10.100 |
|  | Compare with specified date: Continuity | 4.700 | 15.900 |
|  | Compare with specified date: Non-continuity | 4.800 | 15.600 |
| ORDT> | No execution time | 0.00588 |  |
|  | Compare with specified date: Continuity | 2.800 | 10.000 |
|  | Compare with specified date: Non-continuity | 2.700 | 10.000 |
|  | Compare with specified date: Continuity | 4.800 | 15.600 |
|  | Compare with specified date: Non-continuity | 4.800 | 15.700 |
| ORDT<= | No execution time | 0.00588 |  |
|  | Compare with specified date: Continuity | 2.700 | 9.800 |
|  | Compare with specified date: Non-continuity | 2.600 | 10.000 |
|  | Compare with specified date: Continuity | 4.900 | 15.000 |
|  | Compare with specified date: Non-continuity | 4.900 | 15.000 |


| Instruction name | Condition | Processing time ( $\mu \mathrm{s}$ ) |  |
| :---: | :---: | :---: | :---: |
|  |  | Minimum | Maximum |
| ORDT< | No execution time | 0.00588 |  |
|  | Compare with specified date: Continuity | 2.800 | 10.200 |
|  | Compare with specified date: Non-continuity | 2.700 | 10.100 |
|  | Compare with specified date: Continuity | 4.800 | 15.500 |
|  | Compare with specified date: Non-continuity | 4.900 | 15.400 |
| ORDT>= | No execution time | 0.00588 |  |
|  | Compare with specified date: Continuity | 2.800 | 9.500 |
|  | Compare with specified date: Non-continuity | 2.800 | 9.600 |
|  | Compare with specified date: Continuity | 4.700 | 14.500 |
|  | Compare with specified date: Non-continuity | 4.700 | 14.900 |
| LDTM= | Compare with specified time: Continuity | 2.900 | 9.500 |
|  | Compare with specified time: Non-continuity | 2.900 | 9.500 |
|  | Compare with current time: Continuity | 4.700 | 15.600 |
|  | Compare with current time: Non-continuity | 4.700 | 14.900 |
| LDTM<> | Compare with specified time: Continuity | 2.600 | 9.200 |
|  | Compare with specified time: Non-continuity | 2.600 | 9.300 |
|  | Compare with current time: Continuity | 4.900 | 14.500 |
|  | Compare with current time: Non-continuity | 4.800 | 14.500 |
| LDTM> | Compare with specified time: Continuity | 2.600 | 9.200 |
|  | Compare with specified time: Non-continuity | 2.600 | 9.300 |
|  | Compare with current time: Continuity | 4.800 | 14.600 |
|  | Compare with current time: Non-continuity | 4.900 | 14.700 |
| LDTM<= | Compare with specified time: Continuity | 2.600 | 9.200 |
|  | Compare with specified time: Non-continuity | 2.700 | 9.200 |
|  | Compare with current time: Continuity | 4.900 | 14.500 |
|  | Compare with current time: Non-continuity | 4.900 | 14.600 |
| LDTM< | Compare with specified time: Continuity | 2.600 | 9.300 |
|  | Compare with specified time: Non-continuity | 2.600 | 9.300 |
|  | Compare with current time: Continuity | 4.700 | 14.800 |
|  | Compare with current time: Non-continuity | 4.900 | 14.700 |
| LDTM>= | Compare with specified time: Continuity | 2.500 | 9.100 |
|  | Compare with specified time: Non-continuity | 2.600 | 9.200 |
|  | Compare with current time: Continuity | 4.900 | 14.500 |
|  | Compare with current time: Non-continuity | 4.800 | 14.600 |
| ANDTM $=$ | No execution time | $0.00588$ |  |
|  | Compare with specified time: Continuity | 2.400 | 9.100 |
|  | Compare with specified time: Non-continuity | 2.500 | 9.200 |
|  | Compare with current time: Continuity | 4.900 | 14.600 |
|  | Compare with current time: Non-continuity | 4.900 | 14.700 |
| ANDTM<> | No execution time | 0.00588 |  |
|  | Compare with specified time: Continuity | 2.400 | 9.100 |
|  | Compare with specified time: Non-continuity | 2.600 | 9.400 |
|  | Compare with current time: Continuity | 4.900 | 14.800 |
|  | Compare with current time: Non-continuity | 4.900 | 14.800 |
| ANDTM> | No execution time | 0.00588 |  |
|  | Compare with specified time: Continuity | 2.600 | 9.400 |
|  | Compare with specified time: Non-continuity | 2.600 | 9.500 |
|  | Compare with current time: Continuity | 4.800 | 14.700 |
|  | Compare with current time: Non-continuity | 4.900 | 14.800 |


| Instruction name | Condition | Processing time ( $\mu \mathrm{s}$ ) |  |
| :---: | :---: | :---: | :---: |
|  |  | Minimum | Maximum |
| ANDTM<= | No execution time | 0.00588 |  |
|  | Compare with specified time: Continuity | 2.600 | 9.400 |
|  | Compare with specified time: Non-continuity | 2.600 | 9.400 |
|  | Compare with current time: Continuity | 4.900 | 15.000 |
|  | Compare with current time: Non-continuity | 4.900 | 14.900 |
| ANDTM< | No execution time | 0.00588 |  |
|  | Compare with specified time: Continuity | 2.600 | 9.500 |
|  | Compare with specified time: Non-continuity | 2.600 | 9.600 |
|  | Compare with current time: Continuity | 4.900 | 14.700 |
|  | Compare with current time: Non-continuity | 4.900 | 14.800 |
| ANDTM $>=$ | No execution time | 0.00588 |  |
|  | Compare with specified time: Continuity | 2.600 | 9.400 |
|  | Compare with specified time: Non-continuity | 2.600 | 9.400 |
|  | Compare with current time: Continuity | 4.900 | 14.900 |
|  | Compare with current time: Non-continuity | 4.800 | 14.900 |
| ORTM $=$ | No execution time | 0.00588 |  |
|  | Compare with specified time: Continuity | 2.700 | 9.200 |
|  | Compare with specified time: Non-continuity | 2.700 | 9.100 |
|  | Compare with current time: Continuity | 5.000 | 14.900 |
|  | Compare with current time: Non-continuity | 5.000 | 14.800 |
| ORTM<> | No execution time | 0.00588 |  |
|  | Compare with specified time: Continuity | 2.700 | 9.200 |
|  | Compare with specified time: Non-continuity | 2.700 | 9.200 |
|  | Compare with current time: Continuity | 5.000 | 15.000 |
|  | Compare with current time: Non-continuity | 5.000 | 15.000 |
| ORTM> | No execution time | 0.00588 |  |
|  | Compare with specified time: Continuity | 2.700 | 9.300 |
|  | Compare with specified time: Non-continuity | 2.800 | 9.300 |
|  | Compare with current time: Continuity | 5.100 | 15.100 |
|  | Compare with current time: Non-continuity | 5.100 | 15.100 |
| ORTM<= | No execution time | 0.00588 |  |
|  | Compare with specified time: Continuity | 2.700 | 9.200 |
|  | Compare with specified time: Non-continuity | 2.600 | 9.300 |
|  | Compare with current time: Continuity | 5.000 | 14.900 |
|  | Compare with current time: Non-continuity | 4.900 | 15.000 |
| ORTM< | No execution time | 0.00588 |  |
|  | Compare with specified time: Continuity | 2.700 | 9.400 |
|  | Compare with specified time: Non-continuity | 2.700 | 9.400 |
|  | Compare with current time: Continuity | 5.000 | 14.800 |
|  | Compare with current time: Non-continuity | 4.900 | 14.900 |
| ORTM>= | No execution time | 0.00588 |  |
|  | Compare with specified time: Continuity | 2.700 | 9.200 |
|  | Compare with specified time: Non-continuity | 2.500 | 9.200 |
|  | Compare with current time: Continuity | 4.900 | 14.900 |
|  | Compare with current time: Non-continuity | 4.900 | 14.900 |
| S.DATERD | - | 3.500 | 11.600 |
| S.DATE+ | No carry | 3.500 | 7.600 |
|  | Carry | 3.600 | 7.600 |
| S.DATE- | No borrow | 3.600 | 8.000 |
|  | Borrow | 3.500 | 7.800 |
| DUTY | - | 2.400 | 6.600 |
| TIMCHK | - | 2.200 | 4.800 |


| Instruction name | Condition | Processing time ( $\mu \mathrm{s}$ ) |  |
| :---: | :---: | :---: | :---: |
|  |  | Minimum | Maximum |
| RFS (X) | $(\mathrm{n})=1$ | 5.500 | 15.500 |
|  | $(\mathrm{n})=64$ | 13.000 | 33.800 |
| RFS (Y) | ( n ) $=1$ | 4.700 | 14.900 |
|  | $(\mathrm{n})=64$ | 9.000 | 29.300 |
| COM | Only I/O refresh is selected. | 6.500 | 19.700 |
|  | Only CC-Link IE Controller Network refresh is selected. (Control station side) | 17.500 | 34.700 |
|  | Only CC-Link IE Field Network refresh is selected. (Master station side) | 17.700 | 37.400 |
|  | Only CC-Link IE Controller Network refresh is selected. (Normal station side) | 18.800 | 31.800 |
|  | Only CC-Link IE Field Network refresh is selected. (Local station side) | 18.100 | 36.000 |
|  | Only an intelligent function module is selected. | 6.800 | 11.500 |
|  | Refresh of multiple CPU system using the CPU buffer memory (in END processing) | 3.800 | 12.100 |
|  | Import of input/output outside the group of multiple CPU system Input: 64 points + output: 64 points | 3.800 | 12.700 |
|  | Device/label access service processing <br> (Communication with the engineering tool, GOT, or other external devices) | 7.100 | 23.600 |
| S.ZCOM | Only CC-Link IE Controller Network refresh is selected. (Control station side) | 18.700 | 51.600 |
|  | Only CC-Link IE Field Network refresh is selected. (Master station side) | 22.300 | 51.500 |
|  | Only CC-Link IE Controller Network refresh is selected. (Normal station side) | 17.000 | 38.000 |
|  | Only CC-Link IE Field Network refresh is selected. (Local station side) | 21.000 | 41.100 |
| FROM | Reading buffer memory, ( n )=1 | 3.700 | 4.300 |
|  | Reading buffer memory, ( n )=1000 | 38.000 | 55.600 |
|  | Reading host CPU buffer memory, ( n )=1 | 1.100 | 1.500 |
|  | Reading host CPU buffer memory, ( n )=320 | 14.100 | 27.000 |
|  | Reading another CPU buffer memory, ( n )=1 | 3.700 | 4.300 |
|  | Reading another CPU buffer memory, ( n )=320 | 21.900 | 39.500 |
| DFROM | Reading buffer memory, ( n )=1 | 4.000 | 5.500 |
|  | Reading buffer memory, ( n )=500 | 39.400 | 69.400 |
|  | Reading host CPU buffer memory, ( n )=1 | 1.200 | 2.800 |
|  | Reading host CPU buffer memory, ( n )=320 | 21.900 | 42.900 |
|  | Reading another CPU buffer memory, ( n )=1 | 3.800 | 5.500 |
|  | Reading another CPU buffer memory, ( n )=320 | 32.300 | 66.300 |
| TO | Writing to buffer memory, ( n )=1 | 2.100 | 2.700 |
|  | Writing to buffer memory, ( n )=1000 | 45.000 | 65.500 |
|  | Writing to host CPU buffer memory, ( n )=1 | 0.900 | 1.200 |
|  | Writing to host CPU buffer memory, ( n ) $=320$ | 11.200 | 26.600 |
| DTO | Writing to buffer memory, (n)=1 | 2.400 | 4.000 |
|  | Writing to buffer memory, ( n )=500 | 44.400 | 71.000 |
|  | Writing to host CPU buffer memory, ( n )=1 | 1.000 | 2.600 |
|  | Writing to host CPU buffer memory, ( n ) $=320$ | 15.500 | 37.900 |
| FROMD | Reading buffer memory, ( n )=1 | 4.000 | 5.700 |
|  | Reading buffer memory, ( n )=1000 | 41.000 | 63.600 |
|  | Reading host CPU buffer memory, ( n )=1 | 1.400 | 2.900 |
|  | Reading host CPU buffer memory, ( n )=320 | 15.500 | 34.700 |
|  | Reading another CPU buffer memory, ( n )=1 | 4.000 | 5.700 |
|  | Reading another CPU buffer memory, ( n )=320 | 24.500 | 47.600 |
| DFROMD | Reading buffer memory, ( n )=1 | 4.000 | 5.700 |
|  | Reading buffer memory, ( n )=500 | 41.400 | 70.400 |
|  | Reading host CPU buffer memory, ( n )=1 | 1.200 | 2.800 |
|  | Reading host CPU buffer memory, ( n )=320 | 22.400 | 45.300 |
|  | Reading another CPU buffer memory, ( n )=1 | 3.900 | 5.600 |
|  | Reading another CPU buffer memory, ( n )=320 | 34.000 | 62.800 |


| Instruction name | Condition | Processing time ( $\mu \mathrm{s}$ ) |  |
| :---: | :---: | :---: | :---: |
|  |  | Minimum | Maximum |
| TOD | Writing to buffer memory, (n)=1 | 2.300 | 4.100 |
|  | Writing to buffer memory, ( n )=1000 | 47.100 | 76.000 |
|  | Writing to host CPU buffer memory, (n)=1 | 1.200 | 2.700 |
|  | Writing to host CPU buffer memory, (n)=320 | 13.700 | 37.600 |
| DTOD | Writing to buffer memory, ( n )=1 | 2.300 | 4.100 |
|  | Writing to buffer memory, ( n ) $=500$ | 46.300 | 81.400 |
|  | Writing to host CPU buffer memory, ( n )=1 | 1.000 | 2.800 |
|  | Writing to host CPU buffer memory, (n)=320 | 17.200 | 46.500 |
| TYPERD | - | 10.500 | 25.400 |
| UNIINFRD | $(\mathrm{n})=1$ | 13.400 | $20.000$ <br> (When process CPU is used: 22.900) |
|  | $(\mathrm{n})=16$ | 26.100 | $33.100$ <br> (When process CPU is used: 36.800 ) |
| S.RTREAD | - | 2.400 | 5.900 |
| S.RTWRITE | - | 13.500 | 43.800 |
| LOGTRG | - | 55.700 | 74.500 |
| LOGTRGR | - | 11.000 | 18.600 |
| SP.SOCOPEN | TCP Active | 15.600 | 33.800 |
|  | TCP Unpassive | 15.600 | 33.800 |
|  | TCP Fullpassive | 15.600 | 33.800 |
|  | UDP | 15.600 | 33.800 |
| SP.SOCCLOSE | TCP: Execution from own device | 15.300 | 34.000 |
|  | TCP: Execution from another device | 15.300 | 34.000 |
|  | UDP | 15.300 | 34.000 |
| SP.SOCRCV | TCP: Minimum amount of data (1 byte) | 15.000 | 36.700 |
|  | TCP: Maximum amount of data (10238 bytes) | 15.000 | 36.700 |
|  | UDP: Minimum amount of data (1 byte) | 15.200 | 36.800 |
|  | UDP: Maximum amount of data (10238 bytes) | 15.200 | 36.800 |
| S.SOCRCVS | TCP: Minimum amount of data (1 byte) | 15.800 | 32.000 |
|  | TCP: Maximum amount of data (10238 bytes) | 141.000 | 180.000 |
|  | UDP: Minimum amount of data (1 byte) | 15.800 | 32.000 |
|  | UDP: Maximum amount of data (10238 bytes) | 145.000 | 182.000 |
| SP.SOCSND | TCP: Minimum amount of data (1 byte) | 18.000 | 35.400 |
|  | TCP: Maximum amount of data (10238 bytes) | 155.000 | 180.000 |
|  | UDP: Minimum amount of data (1 byte) | 18.000 | 35.400 |
|  | UDP: Maximum amount of data (10238 bytes) | 155.000 | 180.000 |
| SP.SOCCINF | - | 3.100 | 9.200 |
| SP.SOCCSET | - | 3.300 | 14.500 |
| SP.SOCRMODE | Switching from normal mode to fixed-length mode | 4.800 | 14.200 |
|  | Switching from fixed-length mode to normal mode | 4.800 | 13.900 |
| S.SOCRDATA | Minimum amount of data (1 word) | 4.200 | 10.100 |
|  | Maximum amount of data ( 5120 words) | 114.400 | 162.800 |
| SP.ECPRTCL | - | 33.200 | 39.800 |
| PSTOP | File name: "P1" | 43.000 | 78.000 |
| POFF | File name: "P1" | 42.300 | 77.000 |
| PSCAN | File name: "P1" | 43.700 | 78.200 |
| S.PIDINIT | 1 loop | 3.500 | 7.000 |
|  | $32 \text { loop }$ | 53.300 | 56.900 |


| Instruction name | Condition | Processing time ( $\mu \mathrm{s}$ ) |  |
| :---: | :---: | :---: | :---: |
|  |  | Minimum | Maximum |
| S.PIDCONT | 1 loops (first time) | 16.600 | 18.100 |
|  | 1 loops (second and later) | 13.900 | 19.500 |
|  | 32 loops (first time) | 205.000 | 208.700 |
|  | 32 loops (second and later) | 193.300 | 214.100 |
| S.PIDSTOP | 1 loop | 1.100 | 2.600 |
| S.PIDRUN | 1 loop | 1.500 | 2.700 |
| S.PIDPRMW | 1 loop | 3.700 | 6.500 |
| PIDINIT | 1 loop | 3.000 | 6.700 |
|  | 32 loop | 37.000 | 40.500 |
| PIDCONT | 1 loops (first time) | 16.600 | 17.900 |
|  | 1 loops (second and later) | 13.500 | 15.100 |
|  | 32 loops (first time) | 199.600 | 201.600 |
|  | 32 loops (second and later) | 190.800 | 201.700 |
| PIDSTOP | 1 loop | 1.200 | 2.500 |
| PIDRUN | 1 loop | 1.200 | 2.500 |
| PIDPRMW | 1 loop | 3.000 | 6.500 |
| D.DDRD | Number of read data points $=1$ | 75.300 | 121.500 |
|  | Number of read data points $=16$ | 75.500 | 121.500 |
|  | Number of read data points $=96$ | 78.000 | 124.000 |
|  | Number of read data points $=8192$ | 153.100 | 196.800 |
| D.DDWR | Number of read data points $=1$ | 73.900 | 121.100 |
|  | Number of read data points $=16$ | 74.300 | 121.200 |
|  | Number of read data points $=96$ | 74.400 | 121.400 |
|  | Number of read data points $=8192$ | 74.500 | 121.400 |
| M.DDRD | Number of write data points $=1$ | 64.700 | 112.400 |
|  | Number of write data points $=16$ | 65.700 | 112.700 |
|  | Number of write data points $=96$ | 65.400 | 113.100 |
|  | Number of write data points $=8192$ | 145.200 | 188.200 |
| M.DDWR | Number of write data points $=1$ | 63.600 | 111.400 |
|  | Number of write data points $=16$ | 63.600 | 112.400 |
|  | Number of write data points $=96$ | 63.900 | 113.500 |
|  | Number of write data points $=8192$ | 64.700 | 113.600 |
| S.IN | The loop is running and no ALM bit is on. | 32.400 | 33.800 |
| S.OUT1 | The loop is running in AUT mode and no ALM bit is on. | 27.500 | 27.900 |
| S.OUT2 | The loop is running in AUT mode and no ALM bit is on. | 25.700 | 26.600 |
| S.MOUT | The loop is running in MAN mode. | 19.900 | 20.400 |
| S.DUTY | Execution cycle $=1$, Control output cycle $=10$ <br> The loop is running in AUT mode and no ALM bit is on. | 29.200 | 29.900 |
| S.BC | The loop is running in AUT mode and no ALM bit is on. | 21.200 | 21.500 |
| S.PSUM | Integration start signal = On, Integration hold signal = Off | 14.700 | 15.700 |
| S.PID | Set value pattern $=3$ (without a cascade) <br> Tracking bit $=0$ <br> Execution cycle $=$ Control cycle $=1$ <br> Integral constant $\neq 0$ <br> Derivative constant $\neq 0$ <br> The loop is running in AUT mode and no ALM bit is on. | 48.700 | 49.700 |
| S.2PID | Set value pattern $=3$ (without a cascade) <br> Tracking bit $=0$ <br> Execution cycle $=$ Control cycle $=1$ <br> Integral constant $\neq 0$ <br> Derivative constant $\neq 0$ <br> The loop is running in AUT mode and no ALM bit is on. | 57.300 | 59.100 |


| Instruction name | Condition | Processing time ( $\mu \mathrm{s}$ ) |  |
| :---: | :---: | :---: | :---: |
|  |  | Minimum | Maximum |
| S.PIDP | Set value pattern = 3 (without a cascade) <br> Tracking bit $=0$ <br> Execution cycle $=$ Control cycle $=1$ <br> Integral constant $\neq 0$ <br> Derivative constant $\neq 0$ <br> The loop is running in AUT mode and no ALM bit is on. | 53.200 | 54.600 |
| S.SPI | Set value pattern $=3$ (without a cascade) <br> Tracking bit $=0$ <br> Operating time $=$ Sample cycle $(\mathrm{ST}=\mathrm{STHT})$ <br> Integral constant $\neq 0$ <br> The loop is running in AUT mode and no ALM bit is on. | 35.900 | 36.900 |
| S.IPD | Set value pattern $=3$ (without a cascade) <br> Tracking bit =0 <br> Execution cycle $=$ Control cycle $=1$ <br> Integral constant $\neq 0$ <br> Derivative constant $\neq 0$ <br> The loop is running in AUT mode and no ALM bit is on. | 45.600 | 47.800 |
| S.BPI | Set value pattern $=3$ (without a cascade) <br> Tracking bit $=0$ <br> Execution cycle $=$ Control cycle $=1$ <br> Integral constant $\neq 0$ <br> The loop is running in AUT mode and no ALM bit is on. | 34.700 | 35.400 |
| S.R | Set value pattern $=3$ (without a cascade) <br> Tracking bit $=0$ <br> Execution cycle $=$ Control cycle $=1$ <br> The loop is running in AUT mode. | 31.300 | 32.600 |
| S.PHPL | The loop is running in AUT mode and no ALM bit is on. | 38.000 | 39.500 |
| S.LLAG | ```Input data = 50 with lead-lag compensation Lead time = 1, Lag time = 1``` | 21.100 | 21.400 |
| S.I | $\begin{aligned} & \text { Input data }=50, \text { Integral time }=1 \\ & \text { Output initial value }=0 \end{aligned}$ | 17.100 | 17.700 |
| S.D | Input data $=50$, Derivative time $=1$ Output initial value $=0$ | 18.500 | 18.900 |
| S.DED | Input data $=50$ <br> Operation control signal $0 \rightarrow 1$ <br> Data sampling interval $=1$ <br> Sampling count = 10 <br> Output initial value $=0$ <br> Initial output switching $=0$ | 10.200 | 10.900 |
| S.HS | $\begin{aligned} & \text { Number of inputs }=5 \\ & \text { Input data }=50,100,150,200,250 \end{aligned}$ | 13.300 | 13.700 |
| S.LS | $\begin{aligned} & \text { Number of inputs }=5 \\ & \text { Input data }=50,100,150,200,250 \end{aligned}$ | 13.000 | 13.200 |
| S.MID | $\begin{aligned} & \text { Number of inputs }=5 \\ & \text { Input data }=50,100,150,200,250 \end{aligned}$ | 17.400 | 17.900 |
| S.AVE | Number of inputs $=2$, Input data $=50,100$ | 16.000 | 16.300 |
| S.LIMIT | Input data = 50 <br> Upper limit value $=100$ <br> Lower limit value $=0$ <br> Upper limit hysteresis $=0$ <br> Lower limit hysteresis = 0 | 18.200 | 18.700 |
| S.VLMT1 | Input data $=50$ <br> Positive direction limit value $=100$ <br> Negative direction limit value $=100$ <br> Positive direction hysteresis $=0$ <br> Negative direction hysteresis $=0$ | 17.500 | 17.600 |
| S.VLMT2 | Input data $=50$ <br> Positive direction limit value $=100$ <br> Negative direction limit value $=100$ <br> Positive direction hysteresis $=0$ <br> Negative direction hysteresis $=0$ | 17.300 | 17.800 |


| Instruction name | Condition | Processing time ( $\mu \mathrm{s}$ ) |  |
| :---: | :---: | :---: | :---: |
|  |  | Minimum | Maximum |
| S.ONF2 | Input data = 10 <br> Set value pattern $=3$ (without a cascade) <br> Tracking bit $=0$ <br> Execution cycle $=$ Control cycle $=1$ <br> The loop is running in MAN mode. | 32.600 | 34.100 |
| S.ONF3 | Input data = 10 <br> Set value pattern $=3$ (without a cascade) <br> Tracking bit $=0$ <br> Execution cycle $=$ Control cycle $=1$ <br> The loop is running in MAN mode. | 34.500 | 35.800 |
| S.DBND | ```Input data = 50 Dead band upper limit = 100, Dead band lower limit = 0 Input range = 1``` | 17.000 | 17.600 |
| S.PGS | Number of operation constant break points <br> Operation type $=0$ (Hold type) <br> Execution cycle $=1$ <br> Set value $=10$ <br> The loop is running in AUT mode and no ALM bit is on. | 27.500 | 28.000 |
| S.SEL | Set value pattern $=18 \mathrm{H}$ (E1 and E2 used, without a cascade) Tracking bit =0 <br> The loop is running in AUT mode and no ALM bit is on. | 31.300 | 32.400 |
| S.BUMP | ```Output set value = 0, Output control value =50 Mode switching signal = 1 Delay time = 1, Delay time zone = 1``` | 12.000 | 12.400 |
| S.AMR | Output addition value $=50$, Output subtraction value $=50$ <br> Output set value $=0$, Operation mode signal $=1$ <br> Output addition signal $=1$, Output subtraction signal $=0$ <br> Output upper limit value $=50$, Output lower limit value $=0$ | 15.100 | 15.600 |
| S.FG | Input data $=50$, Number of break points $=2$ <br> Break point coordinates $(30,40),(60,70)$ | 21.600 | 21.900 |
| S.IFG | Input data $=50$, Number of break points $=2$ <br> Break point coordinates $(30,40),(60,70)$ | 20.600 | 21.300 |
| S.FLT | ```Input data = 50, Data sampling interval = 1 Sampling count = 10``` | 20.800 | 21.500 |
| S.SUM | Input data $=50$ <br> Input low-cut value $=0$, Initial value $=0$ Input range = 1 | 17.500 | 18.000 |
| S.TPC | Both temperature and pressure are corrected. <br> Differential pressure $=100$, Measured temperature $=300$ <br> Measured pressure $=10000$, Design temperature $=0$ <br> Bias $($ temperature $)=273.15$ <br> Design pressure $=0$ <br> Bias (pressure) $=10332.0$ | 19.300 | 19.800 |
| S.ENG | $\begin{aligned} & \text { Input data }=50 \text {, Engineering value upper limit }=100 \\ & \text { Engineering value lower limit }=0 \end{aligned}$ | 18.800 | 19.000 |
| S.IENG | Input data $=50$, Engineering value upper limit $=100$ Engineering value lower limit = 0 | 18.600 | 18.900 |
| S.ADD | Number of inputs $=2$, Input data $=50,100$ <br> Number of coefficients $=2$, Coefficient $=1,1$, Bias $=0$ | 17.400 | 17.800 |
| S.SUB | Number of inputs $=2$, Input data $=50,100$ <br> Number of coefficients $=2$, Coefficient $=1,1$, Bias $=0$ | 18.800 | 19.200 |
| S.MUL | Number of inputs $=2$, Input data $=50,100$ <br> Number of coefficients $=2$, Coefficient $=1,1$, Bias $=0$ | 17.500 | 17.900 |
| S.DIV | $\begin{aligned} & \text { Input data }=50,100 \\ & \text { Coefficient }=1,1,1, \text { Bias }=0,0,0 \end{aligned}$ | 18.600 | 19.000 |
| S.SQR | $\begin{aligned} & \text { Input data }=50 \\ & \text { Output low-cut value }=0, \text { Coefficient }=10 \end{aligned}$ | 15.800 | 16.500 |
| S.ABS | Input data $=50$ | 11.400 | 11.800 |
| S.> | Input data $=50,100$ <br> Set value $=0$, Hysteresis $=0$ | 15.700 | 16.100 |
| S.< | Input data $=50,100$ <br> Set value $=0$, Hysteresis $=0$ | 13.900 | 14.200 |


| Instruction name | Condition | Processing time ( $\mu \mathrm{s}$ ) |  |
| :---: | :---: | :---: | :---: |
|  |  | Minimum | Maximum |
| S.= | $\begin{aligned} & \text { Input data }=50,100 \\ & \text { Set value }=0 \end{aligned}$ | 14.000 | 14.600 |
| S.>= | Input data $=50,100$ <br> Set value $=0$, Hysteresis $=0$ | 15.600 | 16.200 |
| S.<= | Input data $=50,100$ <br> Set value $=0$, Hysteresis $=0$ | 13.800 | 14.100 |
| S.AT1 | Set value pattern $=3$ (without a cascade) <br> Tracking bit $=0$ <br> Execution cycle $=1$ <br> The loop is running in MAN mode. | 24.600 | 25.700 |

## Appendix 2 Number of Basic Steps and Availability of Subset Processing

The number of basic steps and the availability of subset processing are shown below.

| Instruction name | Number of basic steps | Subset availability |
| :---: | :---: | :---: |
| LD | 1 | $\bigcirc$ |
| LDI | 1 | $\bigcirc$ |
| AND | 1 | $\bigcirc$ |
| ANI | 1 | $\bigcirc$ |
| OR | 1 | $\bigcirc$ |
| ORI | 1 | $\bigcirc$ |
| LDP | 2 | $\bigcirc$ |
| LDF | 2 | $\bigcirc$ |
| ANDP | 2 | $\bigcirc$ |
| ANDF | 2 | $\bigcirc$ |
| ORP | 2 | $\bigcirc$ |
| ORF | 2 | $\bigcirc$ |
| LDPI | 2 | $\bigcirc$ |
| LDFI | 2 | $\bigcirc$ |
| ANDPI | 2 | $\bigcirc$ |
| ANDFI | 2 | $\bigcirc$ |
| ORPI | 2 | $\bigcirc$ |
| ORFI | 2 | $\bigcirc$ |
| ANB | 1 | - |
| ORB | 1 | - |
| MPS | 1 | - |
| MRD | 1 | - |
| MPP | 1 | - |
| INV | 1 | - |
| MEP | 1 | - |
| MEF | 1 | - |
| EGP | 1 | - |
| EGF | 1 | - |
| OUT | 1 | $\bigcirc$ |
| OUT T/ST | 4 | - |
| OUT LT/LST | 2 | - |
| OUT C | 4 | - |
| OUT LC | 4 | - |
| OUT F | 2 | - |
| OUTH T/ST | 4 | - |
| SET | 1 | $\bigcirc$ |
| RST | 1 | $\bigcirc$ |
| SET F | 3 | - |
| RST F | 3 | - |
| PLS | 2 | $\bigcirc$ |
| PLF | 2 | $\bigcirc$ |
| FF | 2 | $\bigcirc$ |
| DELTA | 2 | - |
| DELTAP | 3 | - |
| SFT | 2 | - |
| SFTP | 3 | - |
| MC | 2 | $\bigcirc$ |
| MCR | 1 | - |


| Instruction name | Number of basic steps | Subset availability |
| :---: | :---: | :---: |
| FEND | 2 | - |
| END | 2 | - |
| STOP | 1 | - |
| NOP | 1 | - |
| LD= | 3 | $\bigcirc$ |
| LD<> | 3 | $\bigcirc$ |
| LD> | 3 | $\bigcirc$ |
| LD<= | 3 | $\bigcirc$ |
| LD< | 3 | $\bigcirc$ |
| LD>= | 3 | $\bigcirc$ |
| AND= | 3 | $\bigcirc$ |
| AND<> | 3 | $\bigcirc$ |
| AND> | 3 | $\bigcirc$ |
| AND<= | 3 | $\bigcirc$ |
| AND< | 3 | $\bigcirc$ |
| AND>= | 3 | $\bigcirc$ |
| OR= | 3 | $\bigcirc$ |
| OR<> | 3 | $\bigcirc$ |
| OR> | 3 | $\bigcirc$ |
| OR<= | 3 | $\bigcirc$ |
| OR< | 3 | $\bigcirc$ |
| OR>= | 3 | $\bigcirc$ |
| LD=_U | 3 | $\bigcirc$ |
| LD<>_U | 3 | $\bigcirc$ |
| LD>_U | 3 | $\bigcirc$ |
| LD<=_U | 3 | $\bigcirc$ |
| LD<_U | 3 | $\bigcirc$ |
| LD>=_U | 3 | $\bigcirc$ |
| AND=_U | 3 | $\bigcirc$ |
| AND<>_U | 3 | $\bigcirc$ |
| AND>_U | 3 | $\bigcirc$ |
| AND<=_U | 3 | $\bigcirc$ |
| AND<_U | 3 | $\bigcirc$ |
| AND>=_U | 3 | $\bigcirc$ |
| OR=_U | 3 | $\bigcirc$ |
| OR<>_U | 3 | $\bigcirc$ |
| OR>_U | 3 | $\bigcirc$ |
| OR<=_U | 3 | $\bigcirc$ |
| OR<_U | 3 | $\bigcirc$ |
| OR>=_U | 3 | $\bigcirc$ |
| LDD= | 3 | $\bigcirc$ |
| LDD<> | 3 | $\bigcirc$ |
| LDD> | 3 | $\bigcirc$ |
| LDD<= | 3 | $\bigcirc$ |
| LDD< | 3 | $\bigcirc$ |
| LDD>= | 3 | $\bigcirc$ |
| ANDD= | 3 | $\bigcirc$ |
| ANDD<> | 3 | $\bigcirc$ |
| ANDD> | 3 | $\bigcirc$ |
| ANDD<= | 3 | $\bigcirc$ |
| ANDD< | 3 | $\bigcirc$ |
| ANDD>= | 3 | $\bigcirc$ |
| ORD= | 3 | $\bigcirc$ |


| Instruction name | Number of basic steps | Subset availability |
| :---: | :---: | :---: |
| ORD<> | 3 | $\bigcirc$ |
| ORD> | 3 | $\bigcirc$ |
| ORD<= | 3 | $\bigcirc$ |
| ORD< | 3 | $\bigcirc$ |
| ORD>= | 3 | $\bigcirc$ |
| LDD=_U | 3 | $\bigcirc$ |
| LDD<>_U | 3 | $\bigcirc$ |
| LDD>_U | 3 | $\bigcirc$ |
| LDD<=_U | 3 | $\bigcirc$ |
| LDD<_U | 3 | $\bigcirc$ |
| LDD>=_U | 3 | $\bigcirc$ |
| ANDD=_U | 3 | $\bigcirc$ |
| ANDD<>_U | 3 | $\bigcirc$ |
| ANDD>_U | 3 | $\bigcirc$ |
| ANDD<=_U | 3 | $\bigcirc$ |
| ANDD<_U | 3 | $\bigcirc$ |
| ANDD>=_U | 3 | $\bigcirc$ |
| ORD=_U | 3 | $\bigcirc$ |
| ORD<>_U | 3 | $\bigcirc$ |
| ORD>_U | 3 | $\bigcirc$ |
| ORD<=_U | 3 | $\bigcirc$ |
| ORD<_U | 3 | $\bigcirc$ |
| ORD>=_U | 3 | $\bigcirc$ |
| BKCMP= | 5 | - |
| BKCMP<> | 5 | - |
| BKCMP> | 5 | - |
| BKCMP<= | 5 | - |
| BKCMP< | 5 | - |
| BKCMP>= | 5 | - |
| BKCMP=P | 6 | - |
| BKCMP<>P | 6 | - |
| BKCMP>P | 6 | - |
| BKCMP<=P | 6 | - |
| BKCMP<P | 6 | - |
| BKCMP>=P | 6 | - |
| BKCMP=_U | 5 | - |
| BKCMP<>_U | 5 | - |
| BKCMP>_U | 5 | - |
| BKCMP<=_U | 5 | - |
| BKCMP<_U | 5 | - |
| BKCMP>=_U | 5 | - |
| BKCMP=P_U | 6 | - |
| BKCMP<>P_U | 6 | - |
| BKCMP>P_U | 6 | - |
| BKCMP<=P_U | 6 | - |
| BKCMP<P_U | 6 | - |
| BKCMP>=P_U | 6 | - |
| DBKCMP= | 5 | - |
| DBKCMP<> | 5 | - |
| DBKCMP> | 5 | - |
| DBKCMP<= | 5 | - |
| DBKCMP< | 5 | - |
| DBKCMP>= | 5 | - |


| Instruction name | Number of basic steps | Subset availability |
| :---: | :---: | :---: |
| DBKCMP=P | 6 | - |
| DBKCMP<>P | 6 | - |
| DBKCMP>P | 6 | - |
| DBKCMP<=P | 6 | - |
| DBKCMP<P | 6 | - |
| DBKCMP>=P | 6 | - |
| DBKCMP=_U | 5 | - |
| DBKCMP<>_U | 5 | - |
| DBKCMP>_U | 5 | - |
| DBKCMP<=_U | 5 | - |
| DBKCMP<_U | 5 | - |
| DBKCMP>=_U | 5 | - |
| DBKCMP=P_U | 6 | - |
| DBKCMP<>P_U | 6 | - |
| DBKCMP>P_U | 6 | - |
| DBKCMP<=P_U | 6 | - |
| DBKCMP<P_U | 6 | - |
| DBKCMP>=P_U | 6 | - |
| + (s) (d) | 3 | $\bigcirc$ |
| +P (s) (d) | 4 | $\bigcirc$ |
| + (s1) (s2) (d) | 3 | $\bigcirc$ |
| +P (s1) (s2) (d) | 4 | $\bigcirc$ |
| +_U (s) (d) | 3 | $\bigcirc$ |
| +P_U (s) (d) | 4 | $\bigcirc$ |
| +_U (s1) (s2) (d) | 3 | $\bigcirc$ |
| +P_U (s1) (s2) (d) | 4 | $\bigcirc$ |
| - (s) (d) | 3 | $\bigcirc$ |
| -P (s) (d) | 4 | $\bigcirc$ |
| - (s1) (s2) (d) | 3 | $\bigcirc$ |
| -P (s1) (s2) (d) | 4 | $\bigcirc$ |
| -_U (s) (d) | 3 | $\bigcirc$ |
| -P_U (s) (d) | 4 | $\bigcirc$ |
| -_U (s1) (s2) (d) | 3 | $\bigcirc$ |
| -P_U (s1) (s2) (d) | 4 | $\bigcirc$ |
| D+ (s) (d) | 3 | $\bigcirc$ |
| D+P (s) (d) | 4 | $\bigcirc$ |
| D+ (s1) (s2) (d) | 3 | $\bigcirc$ |
| D+P (s1) (s2) (d) | 4 | $\bigcirc$ |
| D+_U (s) (d) | 3 | $\bigcirc$ |
| D+P_U (s) (d) | 4 | $\bigcirc$ |
| D+_U (s1) (s2) (d) | 3 | $\bigcirc$ |
| D+P_U (s1) (s2) (d) | 4 | $\bigcirc$ |
| D- (s) (d) | 3 | $\bigcirc$ |
| D-P (s) (d) | 4 | $\bigcirc$ |
| D- (s1) (s2) (d) | 3 | $\bigcirc$ |
| D-P (s1) (s2) (d) | 4 | $\bigcirc$ |
| D-_U (s) (d) | 3 | $\bigcirc$ |
| D-P_U (s) (d) | 4 | $\bigcirc$ |
| D-_U (s1) (s2) (d) | 3 | $\bigcirc$ |
| D-P_U (s1) (s2) (d) | 4 | $\bigcirc$ |
| * | 3 | $\bigcirc$ |
| *P | 4 | $\bigcirc$ |
| *_U | 3 | $\bigcirc$ |


| Instruction name | Number of basic steps | Subset availability |
| :---: | :---: | :---: |
| *P_U | 4 | $\bigcirc$ |
| 1 | 3 | $\bigcirc$ |
| /P | 4 | $\bigcirc$ |
| I_U | 3 | $\bigcirc$ |
| /P_U | 4 | $\bigcirc$ |
| D* | 3 | $\bigcirc$ |
| D*P | 4 | $\bigcirc$ |
| D*U | 3 | $\bigcirc$ |
| D*P_U | 4 | $\bigcirc$ |
| D/ | 3 | $\bigcirc$ |
| D/P | 4 | $\bigcirc$ |
| D/_U | 3 | $\bigcirc$ |
| D/P_U | 4 | $\bigcirc$ |
| $B+$ (s) (d) | 3 | $\bigcirc$ |
| $B+P(s)(d)$ | 4 | $\bigcirc$ |
| $B+(s 1)(\mathrm{s} 2)(\mathrm{d})$ | 4 | - |
| B+P (s1) (s2) (d) | 5 | - |
| B- (s) (d) | 3 | $\bigcirc$ |
| B-P (s) (d) | 4 | $\bigcirc$ |
| B- (s1) (s2) (d) | 4 | - |
| B-P (s1) (s2) (d) | 5 | - |
| DB+ (s) (d) | 3 | - |
| DB+P (s) (d) | 4 | - |
| DB+ (s1) (s2) (d) | 4 | - |
| DB+P (s1) (s2) (d) | 5 | - |
| DB- (s) (d) | 3 | - |
| DB-P (s) (d) | 4 | - |
| DB- (s1) (s2) (d) | 4 | - |
| DB-P (s1) (s2) (d) | 5 | - |
| $B^{*}$ | 4 | $\bigcirc$ |
| B*P | 5 | $\bigcirc$ |
| B/ | 4 | $\bigcirc$ |
| B/P | 5 | $\bigcirc$ |
| DB* | 4 | - |
| DB*P | 5 | - |
| DB/ | 4 | - |
| DB/P | 5 | - |
| BK+ | 5 | - |
| BK+P | 6 | - |
| BK+_U | 5 | - |
| BK+P_U | 6 | - |
| BK- | 5 | - |
| BK-P | 6 | - |
| BK-_U | 5 | - |
| BK-P_U | 6 | - |
| DBK+ | 5 | - |
| DBK+P | 6 | - |
| DBK+_U | 5 | - |
| DBK+P_U | 6 | - |
| DBK- | 5 | - |
| DBK-P | 6 | - |
| DBK-_U | 5 | - |
| DBK-P_U | 6 | - |


| Instruction name | Number of basic steps | Subset availability |
| :---: | :---: | :---: |
| INC | 2 | $\bigcirc$ |
| INCP | 3 | $\bigcirc$ |
| INC_U | 2 | $\bigcirc$ |
| INCP_U | 3 | $\bigcirc$ |
| DEC | 2 | $\bigcirc$ |
| DECP | 3 | $\bigcirc$ |
| DEC_U | 2 | $\bigcirc$ |
| DECP_U | 3 | $\bigcirc$ |
| DINC | 2 | $\bigcirc$ |
| DINCP | 3 | $\bigcirc$ |
| DINC_U | 2 | $\bigcirc$ |
| DINCP_U | 3 | $\bigcirc$ |
| DDEC | 2 | $\bigcirc$ |
| DDECP | 3 | $\bigcirc$ |
| DDEC_U | 2 | $\bigcirc$ |
| DDECP_U | 3 | $\bigcirc$ |
| WAND (s) (d) | 3 | $\bigcirc$ |
| WANDP (s) (d) | 4 | $\bigcirc$ |
| WAND (s1) (s2) (d) | 3 | $\bigcirc$ |
| WANDP (s1) (s2) (d) | 4 | $\bigcirc$ |
| DAND (s) (d) | 3 | $\bigcirc$ |
| DANDP (s) (d) | 4 | $\bigcirc$ |
| DAND (s1) (s2) (d) | 3 | $\bigcirc$ |
| DANDP (s1) (s2) (d) | 4 | $\bigcirc$ |
| BKAND | 5 | - |
| BKANDP | 6 | - |
| WOR (s) (d) | 3 | $\bigcirc$ |
| WORP (s) (d) | 4 | $\bigcirc$ |
| WOR (s1) (s2) (d) | 3 | $\bigcirc$ |
| WORP (s1) (s2) (d) | 4 | $\bigcirc$ |
| DOR (s) (d) | 3 | $\bigcirc$ |
| DORP (s) (d) | 4 | $\bigcirc$ |
| DOR (s1) (s2) (d) | 3 | $\bigcirc$ |
| DORP (s1) (s2) (d) | 4 | $\bigcirc$ |
| BKOR | 5 | - |
| BKORP | 6 | - |
| WXOR (s) (d) | 3 | $\bigcirc$ |
| WXORP (s) (d) | 4 | $\bigcirc$ |
| WXOR (s1) (s2) (d) | 3 | $\bigcirc$ |
| WXORP (s1) (s2) (d) | 4 | $\bigcirc$ |
| DXOR (s) (d) | 3 | $\bigcirc$ |
| DXORP (s) (d) | 4 | $\bigcirc$ |
| DXOR (s1) (s2) (d) | 3 | $\bigcirc$ |
| DXORP (s1) (s2) (d) | 4 | $\bigcirc$ |
| BKXOR | 5 | - |
| BKXORP | 6 | - |
| WXNR (s) (d) | 3 | $\bigcirc$ |
| WXNRP (s) (d) | 4 | $\bigcirc$ |
| WXNR (s1) (s2) (d) | 3 | $\bigcirc$ |
| WXNRP (s1) (s2) (d) | 4 | $\bigcirc$ |
| DXNR (s) (d) | 3 | $\bigcirc$ |
| DXNRP (s) (d) | 4 | $\bigcirc$ |
| DXNR (s1) (s2) (d) | 3 | $\bigcirc$ |


| Instruction name | Number of basic steps | Subset availability |
| :---: | :---: | :---: |
| DXNRP (s1) (s2) (d) | 4 | $\bigcirc$ |
| BKXNR | 5 | - |
| BKXNRP | 6 | - |
| BSET | 3 | $\bigcirc$ |
| BSETP | 4 | $\bigcirc$ |
| BRST | 3 | $\bigcirc$ |
| BRSTP | 4 | $\bigcirc$ |
| TEST | 4 | $\bigcirc$ |
| TESTP | 5 | $\bigcirc$ |
| DTEST | 5 | $\bigcirc$ |
| DTESTP | 6 | $\bigcirc$ |
| BKRST | 3 | $\bigcirc$ |
| BKRSTP | 4 | $\bigcirc$ |
| SFR | 4 | $\bigcirc$ |
| SFRP | 5 | $\bigcirc$ |
| SFL | 4 | $\bigcirc$ |
| SFLP | 5 | $\bigcirc$ |
| BSFR | 3 | $\bigcirc$ |
| BSFRP | 4 | $\bigcirc$ |
| BSFL | 3 | $\bigcirc$ |
| BSFLP | 4 | $\bigcirc$ |
| DSFR | 3 | $\bigcirc$ |
| DSFRP | 4 | $\bigcirc$ |
| DSFL | 3 | $\bigcirc$ |
| DSFLP | 4 | $\bigcirc$ |
| SFTBR | 4 | $\bigcirc$ |
| SFTBRP | 5 | $\bigcirc$ |
| SFTBL | 4 | $\bigcirc$ |
| SFTBLP | 5 | $\bigcirc$ |
| SFTWR | 4 | $\bigcirc$ |
| SFTWRP | 5 | $\bigcirc$ |
| SFTWL | 4 | $\bigcirc$ |
| SFTWLP | 5 | $\bigcirc$ |
| BCD | 2 | $\bigcirc$ |
| BCDP | 3 | $\bigcirc$ |
| DBCD | 2 | $\bigcirc$ |
| DBCDP | 3 | $\bigcirc$ |
| BIN | 2 | $\bigcirc$ |
| BINP | 3 | $\bigcirc$ |
| DBIN | 2 | $\bigcirc$ |
| DBINP | 3 | $\bigcirc$ |
| FLT2INT | 2 | $\bigcirc$ |
| FLT2INTP | 3 | $\bigcirc$ |
| FLT2UINT | 2 | $\bigcirc$ |
| FLT2UINTP | 3 | $\bigcirc$ |
| FLT2DINT | 2 | $\bigcirc$ |
| FLT2DINTP | 3 | $\bigcirc$ |
| FLT2UDINT | 2 | $\bigcirc$ |
| FLT2UDINTP | 3 | $\bigcirc$ |
| DBL2INT | 3 | - |
| DBL2INTP | 4 | - |
| DBL2UINT | 3 | - |
| DBL2UINTP | 4 | - |


| Instruction name | Number of basic steps | Subset availability |
| :---: | :---: | :---: |
| DBL2DINT | 3 | - |
| DBL2DINTP | 4 | - |
| DBL2UDINT | 3 | - |
| DBL2UDINTP | 4 | - |
| INT2UINT | 3 | $\bigcirc$ |
| INT2UINTP | 4 | $\bigcirc$ |
| INT2DINT | 3 | $\bigcirc$ |
| INT2DINTP | 4 | $\bigcirc$ |
| INT2UDINT | 3 | $\bigcirc$ |
| INT2UDINTP | 4 | $\bigcirc$ |
| UINT2INT | 3 | $\bigcirc$ |
| UINT2INTP | 4 | $\bigcirc$ |
| UINT2DINT | 3 | $\bigcirc$ |
| UINT2DINTP | 4 | $\bigcirc$ |
| UINT2UDINT | 3 | $\bigcirc$ |
| UINT2UDINTP | 4 | $\bigcirc$ |
| DINT2INT | 2 | $\bigcirc$ |
| DINT2INTP | 3 | $\bigcirc$ |
| DINT2UINT | 3 | $\bigcirc$ |
| DINT2UINTP | 4 | $\bigcirc$ |
| DINT2UDINT | 3 | $\bigcirc$ |
| DINT2UDINTP | 4 | $\bigcirc$ |
| UDINT2INT | 3 | $\bigcirc$ |
| UDINT2INTP | 4 | $\bigcirc$ |
| UDINT2UINT | 3 | $\bigcirc$ |
| UDINT2UINTP | 4 | $\bigcirc$ |
| UDINT2DINT | 3 | $\bigcirc$ |
| UDINT2DINTP | 4 | $\bigcirc$ |
| GRY | 2 | $\bigcirc$ |
| GRYP | 3 | $\bigcirc$ |
| GRY_U | 2 | $\bigcirc$ |
| GRYP_U | 3 | $\bigcirc$ |
| DGRY | 2 | $\bigcirc$ |
| DGRYP | 3 | $\bigcirc$ |
| DGRY_U | 2 | $\bigcirc$ |
| DGRYP_U | 3 | $\bigcirc$ |
| GBIN | 2 | $\bigcirc$ |
| GBINP | 3 | $\bigcirc$ |
| GBIN_U | 2 | $\bigcirc$ |
| GBINP_U | 3 | $\bigcirc$ |
| DGBIN | 2 | $\bigcirc$ |
| DGBINP | 3 | $\bigcirc$ |
| DGBIN_U | 2 | $\bigcirc$ |
| DGBINP_U | 3 | $\bigcirc$ |
| BKBCD | 4 | - |
| BKBCDP | 5 | - |
| BKBIN | 4 | - |
| BKBINP | 5 | - |
| DABIN | 3 | - |
| DABINP | 4 | - |
| DABIN_U | 3 | - |
| DABINP_U | 4 | - |
| DDABIN | 3 | - |


| Instruction name | Number of basic steps | Subset availability |
| :---: | :---: | :---: |
| DDABINP | 4 | - |
| DDABIN_U | 3 | - |
| DDABINP_U | 4 | - |
| HABIN | 3 | - |
| HABINP | 4 | - |
| DHABIN | 3 | - |
| DHABINP | 4 | - |
| DABCD | 3 | - |
| DABCDP | 4 | - |
| DDABCD | 3 | - |
| DDABCDP | 4 | - |
| VAL | 4 | - |
| VALP | 5 | - |
| VAL_U | 4 | - |
| VALP_U | 5 | - |
| DVAL | 4 | - |
| DVALP | 5 | - |
| DVAL_U | 4 | - |
| DVALP_U | 5 | - |
| ASC2INT | 4 | - |
| ASC2INTP | 5 | - |
| EMOD | 4 | - |
| EMODP | 5 | - |
| NEG | 2 | $\bigcirc$ |
| NEGP | 3 | $\bigcirc$ |
| DNEG | 2 | $\bigcirc$ |
| DNEGP | 3 | $\bigcirc$ |
| DECO | 4 | - |
| DECOP | 5 | - |
| ENCO | 4 | - |
| ENCOP | 5 | - |
| SEG | 3 | $\bigcirc$ |
| SEGP | 4 | $\bigcirc$ |
| DIS | 4 | - |
| DISP | 5 | - |
| UNI | 4 | - |
| UNIP | 5 | - |
| NDIS | 4 | - |
| NDISP | 5 | - |
| NUNI | 4 | - |
| NUNIP | 5 | - |
| WTOB | 4 | - |
| WTOBP | 5 | - |
| BTOW | 4 | - |
| BTOWP | 5 | - |
| MOV | 2 | $\bigcirc$ |
| MOVP | 3 | $\bigcirc$ |
| DMOV | 2 | $\bigcirc$ |
| DMOVP | 3 | $\bigcirc$ |
| CML | 2 | $\bigcirc$ |
| CMLP | 3 | $\bigcirc$ |
| DCML | 2 | $\bigcirc$ |
| DCMLP | 3 | $\bigcirc$ |


| Instruction name | Number of basic steps | Subset availability |
| :---: | :---: | :---: |
| CMLB | 3 | $\bigcirc$ |
| CMLBP | 4 | $\bigcirc$ |
| BMOV | 4 | $\bigcirc$ |
| BMOVP | 5 | $\bigcirc$ |
| BMOVL | 4 | $\bigcirc$ |
| BMOVLP | 5 | $\bigcirc$ |
| FMOV | 4 | $\bigcirc$ |
| FMOVP | 5 | $\bigcirc$ |
| FMOVL | 4 | $\bigcirc$ |
| FMOVLP | 5 | $\bigcirc$ |
| DFMOV | 4 | $\bigcirc$ |
| DFMOVP | 5 | $\bigcirc$ |
| DFMOVL | 4 | $\bigcirc$ |
| DFMOVLP | 5 | $\bigcirc$ |
| XCH | 3 | $\bigcirc$ |
| XCHP | 4 | $\bigcirc$ |
| DXCH | 3 | $\bigcirc$ |
| DXCHP | 4 | $\bigcirc$ |
| BXCH | 4 | - |
| BXCHP | 5 | - |
| SWAP | 2 | - |
| SWAPP | 3 | - |
| MOVB | 2 | $\bigcirc$ |
| MOVBP | 3 | $\bigcirc$ |
| BLKMOVB | 4 | - |
| BLKMOVBP | 5 | - |
| ROR | 4 | $\bigcirc$ |
| RORP | 5 | $\bigcirc$ |
| RCR | 4 | $\bigcirc$ |
| RCRP | 5 | $\bigcirc$ |
| DROR | 4 | $\bigcirc$ |
| DRORP | 5 | $\bigcirc$ |
| DRCR | 4 | $\bigcirc$ |
| DRCRP | 5 | $\bigcirc$ |
| ROL | 4 | $\bigcirc$ |
| ROLP | 5 | $\bigcirc$ |
| RCL | 4 | $\bigcirc$ |
| RCLP | 5 | $\bigcirc$ |
| DROL | 4 | $\bigcirc$ |
| DROLP | 5 | $\bigcirc$ |
| DRCL | 4 | $\bigcirc$ |
| DRCLP | 5 | $\bigcirc$ |
| CJ | 4 | - |
| SCJ | 5 | - |
| JMP | 4 | - |
| GOEND | 1 | - |
| DI | 1 | - |
| DI (s) | 2 | $\bigcirc$ |
| El | 1 | $\bigcirc$ |
| IMASK | 2 | $\bigcirc$ |
| SIMASK | 3 | $\bigcirc$ |
| IRET | 1 | - |
| WDT | 1 | - |


| Instruction name | Number of basic steps | Subset availability |
| :---: | :---: | :---: |
| WDTP | 2 | - |
| FOR | 2 | $\bigcirc$ |
| NEXT | 1 | - |
| BREAK | 4 | - |
| BREAKP | 5 | - |
| CALL (P) | 3 | - |
| CALL (P) (s1) | 4 | - |
| CALL (P) (s1) (s2) | 5 | - |
| CALL (P) (s1) (s2) (s3) | 6 | - |
| CALL (P) (s1) (s2) (s3) (s4) | 7 | - |
| CALL (P) (s1) (s2) (s3) (s4) (s5) | 8 | - |
| CALLP (P) | 4 | - |
| CALLP (P) (s1) | 5 | - |
| CALLP (P) (s1) (s2) | 6 | - |
| CALLP (P) (s1) (s2) (s3) | 7 | - |
| CALLP (P) (s1) (s2) (s3) (s4) | 8 | - |
| CALLP (P) (s1) (s2) (s3) (s4) (s5) | 9 | - |
| RET | 1 | - |
| FCALL (P) | 3 | - |
| FCALL (P) (s1) | 4 | - |
| FCALL (P) (s1) (s2) | 5 | - |
| FCALL (P) (s1) (s2) (s3) | 6 | - |
| FCALL (P) (s1) (s2) (s3) (s4) | 7 | - |
| FCALL (P) (s1) (s2) (s3) (s4) (s5) | 8 | - |
| FCALLP (P) | 4 | - |
| FCALLP (P) (s1) | 5 | - |
| FCALLP (P) (s1) (s2) | 6 | - |
| FCALLP (P) (s1) (s2) (s3) | 7 | - |
| FCALLP (P) (s1) (s2) (s3) (s4) | 8 | - |
| FCALLP (P) (s1) (s2) (s3) (s4) (s5) | 9 | - |
| ECALL (file name) (P) | $4+$ Number of characters in the file name | - |
| ECALL (file name) (P) (s1) | $5+$ Number of characters in the file name | - |
| ECALL (file name) (P) (s1) (s2) | $6+$ Number of characters in the file name | - |
| ECALL (file name) (P) (s1) (s2) (s3) | 7 + Number of characters in the file name | - |
| ECALL (file name) (P) (s1) (s2) (s3) (s4) | $8+$ Number of characters in the file name | - |
| ECALL (file name) (P) (s1) (s2) (s3) (s4) (s5) | $9+$ Number of characters in the file name | - |
| ECALLP (file name) (P) | $5+$ Number of characters in the file name | - |
| ECALLP (file name) (P) (s1) | $6+$ Number of characters in the file name | - |
| ECALLP (file name) (P) (s1) (s2) | $7+$ Number of characters in the file name | - |
| ECALLP (file name) (P) (s1) (s2) (s3) | $8+$ Number of characters in the file name | - |
| ECALLP (file name) (P) (s1) (s2) (s3) (s4) | $9+$ Number of characters in the file name | - |
| ECALLP (file name) (P) (s1) (s2) (s3) (s4) (s5) | $10+$ Number of characters in the file name | - |
| EFCALL (file name) (P) | $4+$ Number of characters in the file name | - |


| Instruction name | Number of basic steps | Subset availability |
| :---: | :---: | :---: |
| EFCALL (file name) (P) (s1) | $5+$ Number of characters in the file name | - |
| EFCALL (file name) (P) (s1) (s2) | $6+$ Number of characters in the file name | - |
| EFCALL (file name) (P) (s1) (s2) (s3) | 7 + Number of characters in the file name | - |
| EFCALL (file name) (P) (s1) (s2) (s3) (s4) | $8+$ Number of characters in the file name | - |
| EFCALL (file name) (P) (s1) (s2) (s3) (s4) (s5) | $9+$ Number of characters in the file name | - |
| EFCALLP (file name) (P) | $5+$ Number of characters in the file name | - |
| EFCALLP (file name) (P) (s1) | $6+$ Number of characters in the file name | - |
| EFCALLP (file name) (P) (s1) (s2) | $7+$ Number of characters in the file name | - |
| EFCALLP (file name) (P) (s1) (s2) (s3) | $8+$ Number of characters in the file name | - |
| EFCALLP (file name) (P) (s1) (s2) (s3) (s4) | $9+$ Number of characters in the file name | - |
| EFCALLP (file name) (P) (s1) (s2) (s3) (s4) (s5) | $10+$ Number of characters in the file name | - |
| XCALL (P) | 4 | - |
| XCALL (P) (s1) | 5 | - |
| XCALL (P) (s1) (s2) | 6 | - |
| XCALL (P) (s1) (s2) (s3) | 7 | - |
| XCALL (P) (s1) (s2) (s3) (s4) | 8 | - |
| XCALL (P) (s1) (s2) (s3) (s4) (s5) | 9 | - |
| FIFR | 3 | - |
| FIFRP | 4 | - |
| FPOP | 3 | - |
| FPOPP | 4 | - |
| FIFW | 3 | - |
| FIFWP | 4 | - |
| FINS | 4 | - |
| FINSP | 5 | - |
| FDEL | 4 | - |
| FDELP | 5 | - |
| S.DEVLD | 4 | - |
| SP.DEVLD | 5 | - |
| SP.DEVST | 6 | - |
| SP.FREAD | 8 | - |
| SP.FWRITE | 8 | - |
| LEDR | 2 | - |
| LD\$= | 3 | $\bigcirc$ |
| LD\$<> | 3 | $\bigcirc$ |
| LD\$> | 3 | $\bigcirc$ |
| LD\$<= | 3 | $\bigcirc$ |
| LD\$< | 3 | $\bigcirc$ |
| LD\$>= | 3 | $\bigcirc$ |
| AND\$= | 3 | $\bigcirc$ |
| AND\$<> | 3 | $\bigcirc$ |
| AND\$> | 3 | $\bigcirc$ |
| AND\$<= | 3 | $\bigcirc$ |
| AND\$< | 3 | $\bigcirc$ |
| AND\$>= | 3 | $\bigcirc$ |


| Instruction name | Number of basic steps | Subset availability |
| :---: | :---: | :---: |
| OR\$= | 3 | $\bigcirc$ |
| OR\$<> | 3 | $\bigcirc$ |
| OR\$> | 3 | $\bigcirc$ |
| OR\$<= | 3 | $\bigcirc$ |
| OR\$< | 3 | $\bigcirc$ |
| OR\$>= | 3 | $\bigcirc$ |
| \$+(s) (d) | 3 | $\bigcirc$ |
| \$+P (s) (d) | 4 | $\bigcirc$ |
| \$+ (s1) (s2) (d) | 4 | $\bigcirc$ |
| \$+P (s1) (s2) (d) | 5 | $\bigcirc$ |
| \$MOV | 3 | - |
| \$MOVP | 4 | - |
| \$MOV_WS | 3 | - |
| \$MOVP_WS | 4 | - |
| BINDA | 3 | - |
| BINDAP | 4 | - |
| BINDA_U | 3 | - |
| BINDAP_U | 4 | - |
| DBINDA | 3 | - |
| DBINDAP | 4 | - |
| DBINDA_U | 3 | - |
| DBINDAP_U | 4 | - |
| BINHA | 3 | - |
| BINHAP | 4 | - |
| DBINHA | 3 | - |
| DBINHAP | 4 | - |
| STR | 4 | - |
| STRP | 5 | - |
| STR_U | 4 | - |
| STRP_U | 5 | - |
| DSTR | 4 | - |
| DSTRP | 5 | - |
| DSTR_U | 4 | - |
| DSTRP_U | 5 | - |
| BCDDA | 3 | - |
| BCDDAP | 4 | - |
| DBCDDA | 3 | - |
| DBCDDAP | 4 | - |
| ESTR | 4 | - |
| ESTRP | 5 | - |
| INT2ASC | 4 | - |
| INT2ASCP | 5 | - |
| WS2SJIS | 3 | - |
| WS2SJISP | 4 | - |
| SJIS2WS | 3 | - |
| SJIS2WSP | 4 | - |
| SJIS2WSB | 3 | - |
| SJIS2WSBP | 4 | - |
| LEN | 3 | $\bigcirc$ |
| LENP | 4 | $\bigcirc$ |
| RIGHT | 4 | - |
| RIGHTP | 5 | - |
| LEFT | 4 | - |


| Instruction name | Number of basic steps | Subset availability |
| :---: | :---: | :---: |
| LEFTP | 5 | - |
| MIDR | 4 | - |
| MIDRP | 5 | - |
| MIDW | 4 | - |
| MIDWP | 5 | - |
| INSTR | 5 | - |
| INSTRP | 6 | - |
| STRINS | 4 | - |
| STRINSP | 5 | - |
| STRDEL | 4 | - |
| STRDELP | 5 | - |
| LDE= | 3 | $\bigcirc$ |
| LDE<> | 3 | $\bigcirc$ |
| LDE> | 3 | $\bigcirc$ |
| LDE<= | 3 | $\bigcirc$ |
| LDE< | 3 | $\bigcirc$ |
| LDE>= | 3 | $\bigcirc$ |
| ANDE= | 3 | $\bigcirc$ |
| ANDE<> | 3 | $\bigcirc$ |
| ANDE> | 3 | $\bigcirc$ |
| ANDE<= | 3 | $\bigcirc$ |
| ANDE< | 3 | $\bigcirc$ |
| ANDE>= | 3 | $\bigcirc$ |
| ORE= | 3 | $\bigcirc$ |
| ORE<> | 3 | $\bigcirc$ |
| ORE> | 3 | $\bigcirc$ |
| ORE<= | 3 | $\bigcirc$ |
| ORE< | 3 | $\bigcirc$ |
| ORE>= | 3 | $\bigcirc$ |
| LDED= | 3 | - |
| LDED<> | 3 | - |
| LDED> | 3 | - |
| LDED<= | 3 | - |
| LDED< | 3 | - |
| LDED>= | 3 | - |
| ANDED= | 3 | - |
| ANDED<> | 3 | - |
| ANDED> | 3 | - |
| ANDED<= | 3 | - |
| ANDED< | 3 | - |
| ANDED>= | 3 | - |
| ORED= | 3 | - |
| ORED<> | 3 | - |
| ORED> | 3 | - |
| ORED<= | 3 | - |
| ORED< | 3 | - |
| ORED>= | 3 | - |
| $\mathrm{E}+(\mathrm{s})$ (d) | 3 | $\bigcirc$ |
| E+P (s) (d) | 4 | $\bigcirc$ |
| $\mathrm{E}+(\mathrm{s} 1)$ (s2) (d) | 3 | $\bigcirc$ |
| E+P (s1) (s2) (d) | 4 | $\bigcirc$ |
| E- (s) (d) | 3 | $\bigcirc$ |
| E-P (s) (d) | 4 | $\bigcirc$ |


| Instruction name | Number of basic steps | Subset availability |
| :---: | :---: | :---: |
| E- (s1) (s2) (d) | 3 | $\bigcirc$ |
| E-P (s1) (s2) (d) | 4 | $\bigcirc$ |
| ED+ (s) (d) | 3 | - |
| ED+P (s) (d) | 4 | - |
| ED+ (s1) (s2) (d) | 4 | - |
| ED+P (s1) (s2) (d) | 5 | - |
| ED- (s) (d) | 3 | - |
| ED-P (s) (d) | 4 | - |
| ED- (s1) (s2) (d) | 4 | - |
| ED-P (s1) (s2) (d) | 5 | - |
| E* | 3 | $\bigcirc$ |
| E*P | 4 | $\bigcirc$ |
| E/ | 3 | $\bigcirc$ |
| E/P | 4 | $\bigcirc$ |
| ED* | 4 | - |
| ED*P | 5 | - |
| ED/ | 4 | - |
| ED/P | 5 | - |
| INT2FLT | 2 | $\bigcirc$ |
| INT2FLTP | 3 | $\bigcirc$ |
| UINT2FLT | 2 | $\bigcirc$ |
| UINT2FLTP | 3 | $\bigcirc$ |
| DINT2FLT | 2 | $\bigcirc$ |
| DINT2FLTP | 3 | $\bigcirc$ |
| UDINT2FLT | 2 | $\bigcirc$ |
| UDINT2FLTP | 3 | $\bigcirc$ |
| DBL2FLT | 3 | - |
| DBL2FLTP | 4 | - |
| INT2DBL | 3 | - |
| INT2DBLP | 4 | - |
| UINT2DBL | 3 | - |
| UINT2DBLP | 4 | - |
| DINT2DBL | 3 | - |
| DINT2DBLP | 4 | - |
| UDINT2DBL | 3 | - |
| UDINT2DBLP | 4 | - |
| FLT2DBL | 3 | - |
| FLT2DBLP | 4 | - |
| EVAL | 3 | - |
| EVALP | 4 | - |
| EREXP | 4 | - |
| EREXPP | 5 | - |
| ENEG | 2 | - |
| ENEGP | 3 | - |
| EDNEG | 2 | - |
| EDNEGP | 3 | - |
| EMOV | 2 | $\bigcirc$ |
| EMOVP | 3 | $\bigcirc$ |
| EDMOV | 2 | $\bigcirc$ |
| EDMOVP | 3 | $\bigcirc$ |
| SIN | 3 | - |
| SINP | 4 | - |
| cos | 3 | - |


| Instruction name | Number of basic steps | Subset availability |
| :---: | :---: | :---: |
| COSP | 4 | - |
| TAN | 3 | - |
| TANP | 4 | - |
| ASIN | 3 | - |
| ASINP | 4 | - |
| ACOS | 3 | - |
| ACOSP | 4 | - |
| ATAN | 3 | - |
| ATANP | 4 | - |
| SIND | 3 | - |
| SINDP | 4 | - |
| COSD | 3 | - |
| COSDP | 4 | - |
| TAND | 3 | - |
| TANDP | 4 | - |
| ASIND | 3 | - |
| ASINDP | 4 | - |
| ACOSD | 3 | - |
| ACOSDP | 4 | - |
| ATAND | 3 | - |
| ATANDP | 4 | - |
| BSIN | 3 | - |
| BSINP | 4 | - |
| BCOS | 3 | - |
| BCOSP | 4 | - |
| BTAN | 3 | - |
| BTANP | 4 | - |
| BASIN | 3 | - |
| BASINP | 4 | - |
| BACOS | 3 | - |
| BACOSP | 4 | - |
| BATAN | 3 | - |
| BATANP | 4 | - |
| RAD | 3 | - |
| RADP | 4 | - |
| DEG | 3 | - |
| DEGP | 4 | - |
| RADD | 3 | - |
| RADDP | 4 | - |
| DEGD | 3 | - |
| DEGDP | 4 | - |
| ESQRT | 3 | - |
| ESQRTP | 4 | - |
| EDSQRT | 3 | - |
| EDSQRTP | 4 | - |
| EXP | 3 | - |
| EXPP | 4 | - |
| EXPD | 3 | - |
| EXPDP | 4 | - |
| LOG | 3 | - |
| LOGP | 4 | - |
| LOGD | 3 | - |
| LOGDP | 4 | - |


| Instruction name | Number of basic steps | Subset availability |
| :---: | :---: | :---: |
| BSQRT | 3 | - |
| BSQRTP | 4 | - |
| BDSQRT | 3 | - |
| BDSQRTP | 4 | - |
| POW | 4 | - |
| POWP | 5 | - |
| POWD | 4 | - |
| POWDP | 5 | - |
| LOG10 | 3 | - |
| LOG10P | 4 | - |
| LOG10D | 3 | - |
| LOG10DP | 4 | - |
| EMAX | 4 | - |
| EMAXP | 5 | - |
| EDMAX | 4 | - |
| EDMAXP | 5 | - |
| EMIN | 4 | - |
| EMINP | 5 | - |
| EDMIN | 4 | - |
| EDMINP | 5 | - |
| RND | 2 | - |
| RNDP | 3 | - |
| SRND | 2 | - |
| SRNDP | 3 | - |
| ZPUSH (d) | 2 | $\bigcirc$ |
| ZPUSHP (d) | 3 | $\bigcirc$ |
| ZPUSH (s) (d) | 3 | $\bigcirc$ |
| ZPUSHP (s) (d) | 4 | $\bigcirc$ |
| ZPOP (d) | 2 | $\bigcirc$ |
| ZPOPP (d) | 3 | $\bigcirc$ |
| ZPOP (s) (d) | 3 | $\bigcirc$ |
| ZPOPP (s) (d) | 4 | $\bigcirc$ |
| LIMIT | 5 | $\bigcirc$ |
| LIMITP | 6 | $\bigcirc$ |
| LIMIT_U | 5 | $\bigcirc$ |
| LIMITP_U | 6 | $\bigcirc$ |
| DLIMIT | 5 | $\bigcirc$ |
| DLIMITP | 6 | $\bigcirc$ |
| DLIMIT_U | 5 | $\bigcirc$ |
| DLIMITP_U | 6 | $\bigcirc$ |
| BAND | 5 | - |
| BANDP | 6 | - |
| BAND_U | 5 | - |
| BANDP_U | 6 | - |
| DBAND | 5 | - |
| DBANDP | 6 | - |
| DBAND_U | 5 | - |
| DBANDP_U | 6 | - |
| ZONE | 5 | - |
| ZONEP | 6 | - |
| ZONE_U | 5 | - |
| ZONEP_U | 6 | - |
| DZONE | 5 | - |

APPENDICES
Appendix 2 Number of Basic Steps and Availability of Subset Processing

| Instruction name | Number of basic steps | Subset availability |
| :---: | :---: | :---: |
| DZONEP | 6 | - |
| DZONE_U | 5 | - |
| DZONEP_U | 6 | - |
| SCL | 4 | - |
| SCLP | 5 | - |
| SCL_U | 4 | - |
| SCLP_U | 5 | - |
| DSCL | 4 | - |
| DSCLP | 5 | - |
| DSCL_U | 4 | - |
| DSCLP_U | 5 | - |
| SCL2 | 4 | - |
| SCL2P | 5 | - |
| SCL2_U | 4 | - |
| SCL2P_U | 5 | - |
| DSCL2 | 4 | - |
| DSCL2P | 5 | - |
| DSCL2_U | 4 | - |
| DSCL2P_U | 5 | - |
| UDCNT1 | 5 | - |
| UDCNT2 | 5 | - |
| TTMR | 4 | - |
| STMR | 4 | - |
| ROTC | 5 | - |
| RAMPQ | 7 | - |
| SPD | 5 | - |
| PLSY | 5 | - |
| PWM | 5 | - |
| MTR | 5 | - |
| SERDATA | 5 | - |
| SERDATAP | 6 | - |
| DSERDATA | 5 | - |
| DSERDATAP | 6 | - |
| SUM | 3 | $\bigcirc$ |
| SUMP | 4 | $\bigcirc$ |
| DSUM | 3 | $\bigcirc$ |
| DSUMP | 4 | $\bigcirc$ |
| MAX | 4 | - |
| MAXP | 5 | - |
| MAX_U | 4 | - |
| MAXP_U | 5 | - |
| DMAX | 4 | - |
| DMAXP | 5 | - |
| DMAX_U | 4 | - |
| DMAXP_U | 5 | - |
| MIN | 4 | - |
| MINP | 5 | - |
| MIN_U | 4 | - |
| MINP_U | 5 | - |
| DMIN | 4 | - |
| DMINP | 5 | - |
| DMIN_U | 4 | - |
| DMINP_U | 5 | - |


| Instruction name | Number of basic steps | Subset availability |
| :---: | :---: | :---: |
| SORTD | 7 | - |
| SORTD_U | 7 | - |
| DSORTD | 7 | - |
| DSORTD_U | 7 | - |
| WSUM | 4 | $\bigcirc$ |
| WSUMP | 5 | $\bigcirc$ |
| WSUM_U | 4 | $\bigcirc$ |
| WSUMP_U | 5 | $\bigcirc$ |
| DWSUM | 4 | - |
| DWSUMP | 5 | - |
| DWSUM_U | 4 | - |
| DWSUMP_U | 5 | - |
| MEAN | 4 | - |
| MEANP | 5 | - |
| MEAN_U | 4 | - |
| MEANP_U | 5 | - |
| DMEAN | 4 | - |
| DMEANP | 5 | - |
| DMEAN_U | 4 | - |
| DMEANP_U | 5 | - |
| DBOPEN | 5 | - |
| DBOPENP | 6 | - |
| DBCLOSE | 4 | - |
| DBCLOSEP | 5 | - |
| DBINSERT | 7 | - |
| DBINSERTP | 8 | - |
| DBUPDATE | 8 | - |
| DBUPDATEP | 9 | - |
| DBSELECT | 8 | - |
| DBSELECTP | 9 | - |
| DBDELETE | 6 | - |
| DBDELETEP | 7 | - |
| DBIMPORT | 4 | - |
| DBIMPORTP | 5 | - |
| DBEXPORT | 4 | - |
| DBEXPORTP | 5 | - |
| DBTRANS | 4 | - |
| DBTRANSP | 5 | - |
| DBCOMMIT | 4 | - |
| DBCOMMITP | 5 | - |
| DBROLBAK | 4 | - |
| DBROLBAKP | 5 | - |
| RSET | 2 | - |
| RSETP | 3 | - |
| QDRSET | $2+$ Number of characters in the file name | - |
| QDRSETP | $3+$ Number of characters in the file name | - |
| ZRRDB | 3 | - |
| ZRRDBP | 4 | - |
| ZRWRB | 3 | - |
| ZRWRBP | 4 | - |
| ADRSET | 3 | - |


| Instruction name | Number of basic steps | Subset availability |
| :---: | :---: | :---: |
| ADRSETP | 4 | - |
| DATERD | 2 | - |
| DATERDP | 3 | - |
| DATEWR | 2 | - |
| DATEWRP | 3 | - |
| DATE+ | 4 | - |
| DATE+P | 5 | - |
| DATE- | 4 | - |
| DATE-P | 5 | - |
| TIME2SEC | 3 | - |
| TIME2SECP | 4 | - |
| SEC2TIME | 3 | - |
| SEC2TIMEP | 4 | - |
| LDDT= | 4 | - |
| LDDT<> | 4 | - |
| LDDT> | 4 | - |
| LDDT<= | 4 | - |
| LDDT< | 4 | - |
| LDDT>= | 4 | - |
| ANDDT= | 4 | - |
| ANDDT<> | 4 | - |
| ANDDT> | 4 | - |
| ANDDT<= | 4 | - |
| ANDDT< | 4 | - |
| ANDDT>= | 4 | - |
| ORDT= | 4 | - |
| ORDT<> | 4 | - |
| ORDT> | 4 | - |
| ORDT<= | 4 | - |
| ORDT< | 4 | - |
| ORDT>= | 4 | - |
| LDTM $=$ | 4 | - |
| LDTM<> | 4 | - |
| LDTM> | 4 | - |
| LDTM<= | 4 | - |
| LDTM< | 4 | - |
| LDTM>= | 4 | - |
| ANDTM $=$ | 4 | - |
| ANDTM<> | 4 | - |
| ANDTM> | 4 | - |
| ANDTM<= | 4 | - |
| ANDTM< | 4 | - |
| ANDTM $>=$ | 4 | - |
| ORTM= | 4 | - |
| ORTM<> | 4 | - |
| ORTM> | 4 | - |
| ORTM<= | 4 | - |
| ORTM< | 4 | - |
| ORTM>= | 4 | - |
| S.DATERD | 2 | - |
| SP.DATERD | 3 | - |
| S.DATE+ | 4 | - |
| SP.DATE+ | 5 | - |


| Instruction name | Number of basic steps | Subset availability |
| :---: | :---: | :---: |
| S.DATE- | 4 | - |
| SP.DATE- | 5 | - |
| DUTY | 5 | - |
| TIMCHK | 5 | - |
| RFS | 3 | - |
| RFSP | 4 | - |
| COM | 1 | - |
| COMP | 2 | - |
| S.ZCOM | 2 | - |
| SP.ZCOM | 3 | - |
| FROM | 5 | $\bigcirc$ |
| FROMP | 6 | $\bigcirc$ |
| DFROM | 5 | $\bigcirc$ |
| DFROMP | 6 | $\bigcirc$ |
| TO | 5 | $\bigcirc$ |
| TOP | 6 | $\bigcirc$ |
| DTO | 5 | $\bigcirc$ |
| DTOP | 6 | $\bigcirc$ |
| FROMD | 5 | $\bigcirc$ |
| FROMDP | 6 | $\bigcirc$ |
| DFROMD | 5 | $\bigcirc$ |
| DFROMDP | 6 | $\bigcirc$ |
| TOD | 5 | $\bigcirc$ |
| TODP | 6 | $\bigcirc$ |
| DTOD | 5 | $\bigcirc$ |
| DTODP | 6 | $\bigcirc$ |
| TYPERD | 3 | - |
| TYPERDP | 4 | - |
| UNIINFRD | 4 | - |
| UNIINFRDP | 5 | - |
| S.RTREAD | 3 | - |
| SP.RTREAD | 4 | - |
| S.RTWRITE | 3 | - |
| SP.RTWRITE | 4 | - |
| LOGTRG | 3 | - |
| LOGTRGR | 3 | - |
| SP.SOCOPEN | 5 | - |
| SP.SOCCLOSE | 6 | - |
| SP.SOCRCV | 7 | - |
| S.SOCRCVS | 4 | - |
| SP.SOCSND | 7 | - |
| SP.SOCCINF | 6 | - |
| SP.SOCCSET | 5 | - |
| SP.SOCRMODE | 5 | - |
| S.SOCRDATA | 6 | - |
| SP.SOCRDATA | 7 | - |
| SP.ECPRTCL | 7 | - |
| PSTOP | $2+$ Number of characters in the file name | - |
| PSTOPP | 3 + Number of characters in the file name | - |
| POFF | $2+$ Number of characters in the file name | - |


| Instruction name | Number of basic steps | Subset availability |
| :---: | :---: | :---: |
| POFFP | $3+$ Number of characters in the file name | - |
| PSCAN | $2+$ Number of characters in the file name | - |
| PSCANP | 3 + Number of characters in the file name | - |
| S.PIDINIT | 2 | - |
| SP.PIDINIT | 3 | - |
| S.PIDCONT | 2 | - |
| SP.PIDCONT | 3 | - |
| S.PIDSTOP | 2 | - |
| SP.PIDSTOP | 3 | - |
| S.PIDRUN | 2 | - |
| SP.PIDRUN | 3 | - |
| S.PIDPRMW | 3 | - |
| SP.PIDPRMW | 4 | - |
| PIDINIT | 2 | - |
| PIDINITP | 3 | - |
| PIDCONT | 2 | - |
| PIDCONTP | 3 | - |
| PIDSTOP | 2 | - |
| PIDSTOPP | 3 | - |
| PIDRUN | 2 | - |
| PIDRUNP | 3 | - |
| PIDPRMW | 3 | - |
| PIDPRMWP | 4 | - |
| D.DDRD | 9 | - |
| DP.DDRD | 10 | - |
| D.DDWR | 9 | - |
| DP.DDWR | 10 | - |
| M.DDRD | 9 | - |
| MP.DDRD | 10 | - |
| M.DDWR | 9 | - |
| MP.DDWR | 10 | - |
| S.IN | 5 | - |
| S.OUT1 | 5 | - |
| S.OUT2 | 5 | - |
| S.MOUT | 5 | - |
| S.DUTY | 5 | - |
| S.BC | 5 | - |
| S.PSUM | 5 | - |
| S.PID | 6 | - |
| S.2PID | 6 | - |
| S.PIDP | 6 | - |
| S.SPI | 6 | - |
| S.IPD | 6 | - |
| S.BPI | 6 | - |
| S.R | 6 | - |
| S.PHPL | 5 | - |
| S.LLAG | 5 | - |
| S.I | 5 | - |
| S.D | 5 | - |
| S.DED | 5 | - |
| S.HS | 5 | - |


| Instruction name | Number of basic steps | Subset availability |
| :--- | :--- | :--- |
| S.LS | 5 | - |
| S.MID | 5 | - |
| S.AVE | 5 | - |
| S.LIMT | 5 | - |
| S.VLMT1 | 5 | - |
| S.VLMT2 | 5 | - |
| S.ONF2 | 6 | - |
| S.ONF3 | 6 | - |
| S.DBND | 5 | - |
| S.PGS | 5 | - |
| S.SEL | 6 | - |
| S.BUMP | 5 | - |
| S.AMR | 5 | - |
| S.FG | 5 | - |
| S.IFG | 5 | - |
| S.FLT | 5 | - |
| S.SUM | 5 | - |
| S.TPC | 5 | - |
| S.ENG | 5 | - |
| S.IENG | 5 | - |
| S.ADD | 5 | - |
| S.SUB | 5 | - |
| S.MUL | 5 | - |
| S.DIV | 5 | - |
| S.SQR | 5 | - |
| S.ABS | 5 | - |
| S.> | 5 | - |
| S.< | 5 | - |

## Appendix 3 Process Control Program Examples

This section provides examples of process control programs using process control instructions.
With the following program, the operation mode enters the manual mode when the power is turned on.
Turning on X10 enters the auto mode (AUTO) and performs PID control.

(0) Set the operation mode to AUTO.

Set the alarm detection (ALM) to 0 .
(8) Jump to the P0 label when SM402 is on.
(12) Set the last value hold processing.
(14) Reset the last value hold processing.
(16) Set the output value hold processing.
(18) Reset the output value hold processing.
(20) Adjust to the time set for the execution cycle.
(28) Jump to the P1 label when M0 is on.

Turning M0 on clears T0 to 0 .
(36) The end of the sequence program in which SM402 is off is indicated.

(38) Change the D0 value to a single-precision real number and stores it in R0. Set each start device of the S.IN instruction.

Transfer the R100 value of the S.IN instruction to R20 of S.PHPL.
Set each start device of the S.PHPL instruction.
Transfer the R120 value of the S.PHPL instruction to R40 of S.PID.
Set the start device of the S.PID instruction.
Transfer the R140 value of the S.PID instruction to R60 of S.OUT1.
Set each start device of the S.OUT1 instruction
Convert the single-precision real number in R160 and R161 to binary
(71) The subroutine program ends.
(72) The main routine program ends.

(74) Adjust the execution cycle to 0.1 s .

Clear the output values in S.IN, S.PHPL, S.PID, and S.OUT1 to 0 .
Clear the loop tag to 0 .
Set the default value of the loop tag.

(130) Set the default value of the loop tag.

(179) Set the operation constant of the S.IN instruction.
(204) Set the operation constant of the S.PID instruction.
(217) Set the operation constant of the S.OUT1 instruction.
(224) The subroutine program ends.

## Appendix 4 List of Loop Tag Memory Areas Used by Process Control Instructions

## PID control (SPID), two-degree-of-freedom PID control (S2PID), sample PI control (SSPI)

| Offset | Instruction used | Item | Name | Recommended range | Data type | Set by |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  |  | SPID, S2PID | SSPI |
| +1 | Shared among the instructions | MODE | Operation mode | 0 to FFFFFH | 16-bit unsigned binary | User/system | User/system |
| +3 |  | ALM | Alarm detection | 0 to FFFFFH | 16-bit unsigned binary | User/system | User/system |
| +4 |  | INH | Alarm inhibition | 0 to FFFFFH | 16-bit unsigned binary | User/system | User/system |
| $\begin{aligned} & +10 \\ & +11 \end{aligned}$ | S.PHPL | PV | Process value | RL to RH | Single-precision real number | System | System |
| $\begin{aligned} & +12 \\ & +13 \end{aligned}$ | S.OUT1/ S.DUTY | MV | Manipulated value | -10 to 110 [\%] | Single-precision real number | User/system | User/system |
| $\begin{aligned} & +14 \\ & +15 \end{aligned}$ | S.PID/ <br> S.2PID/S.SPI | SV | Set value | RL to RH | Single-precision real number | User | User |
| $\begin{aligned} & +16 \\ & +17 \end{aligned}$ | $\begin{aligned} & \text { S.PID/ } \\ & \text { S.2PID/S.SPI } \end{aligned}$ | DV | Deviation | -110 to 110 [\%] | Single-precision real number | System | System |
| $\begin{aligned} & +18 \\ & +19 \end{aligned}$ | S.OUT1/ S.DUTY | MH | Output upper limit value | -10 to 110 [\%] | Single-precision real number | User | User |
| $\begin{aligned} & +20 \\ & +21 \end{aligned}$ | S.OUT1/ S.DUTY | ML | Output lower limit value | -10 to 110 [\%] | Single-precision real number | User | User |
| $\begin{aligned} & +22 \\ & +23 \end{aligned}$ | S.PHPL/ <br> S.PID/ <br> S.2PID/S.SPI | RH | Engineering value upper limit | -999999 to 999999 | Single-precision real number | User | User |
| $\begin{aligned} & +24 \\ & +25 \end{aligned}$ | S.PHPL/ <br> S.PID/ <br> S.2PID/S.SPI | RL | Engineering value lower limit | -999999 to 999999 | Single-precision real number | User | User |
| $\begin{aligned} & +26 \\ & +27 \end{aligned}$ | S.PHPL | PH | Upper limit alarm value | RL to RH PL < PH | Single-precision real number | User | User |
| $\begin{aligned} & +28 \\ & +29 \end{aligned}$ | S.PHPL | PL | Lower limit alarm value | $\begin{aligned} & \text { RL to } \mathrm{RH} \\ & \mathrm{PL} \text { < } \mathrm{PH} \end{aligned}$ | Single-precision real number | User | User |
| $\begin{aligned} & +30 \\ & +31 \end{aligned}$ | S.PHPL | HH | Upper upper limit alarm value | RL to RH <br> $\mathrm{PH} \leq \mathrm{HH}$ | Single-precision real number | User | User |
| $\begin{aligned} & +32 \\ & +33 \end{aligned}$ | S.PHPL | LL | Lower lower limit alarm value | $\text { RL to } \mathrm{RH}$ $\mathrm{LL} \leq \mathrm{PL}$ | Single-precision real number | User | User |
| $\begin{aligned} & +38 \\ & +39 \end{aligned}$ | S.IN | $\alpha$ | Filter coefficient | 0 to 1 | Single-precision real number | User | User |
| $\begin{aligned} & +40 \\ & +41 \end{aligned}$ | S.PHPL | HS | Upper/lower limit alarm hysteresis | 0 to 999999 [\%] | Single-precision real number | User | User |
| $\begin{aligned} & +42 \\ & +43 \end{aligned}$ | S.PHPL | CTIM | Variation rate alarm check time | 0 to 999999 [s] | Single-precision real number | User | User |
| $\begin{aligned} & +44 \\ & +45 \end{aligned}$ | S.PHPL | DPL | Variation rate alarm value | 0 to 100 [\%] | Single-precision real number | User | User |
| $\begin{aligned} & +46 \\ & +47 \end{aligned}$ | S.PID/ <br> S.2PID/S.SPI | CT/ST | Control cycle/Operating time | 0 to 999999 [s] | Single-precision real number | User (Set CT.) | $\begin{aligned} & \text { User (Set } \\ & \text { ST.) } \end{aligned}$ |
| $\begin{aligned} & +48 \\ & +49 \end{aligned}$ | S.OUT1/ S.DUTY | DML | Output variation rate limit value | 0 to 100 [\%] | Single-precision real number | User | User |
| $\begin{aligned} & +50 \\ & +51 \end{aligned}$ | S.PID/ <br> S.2PID/S.SPI | DVL | Deviation limit value | 0 to 100 [\%] | Single-precision real number | User | User |
| $\begin{aligned} & +52 \\ & +53 \end{aligned}$ | S.PID/ <br> S.2PID/S.SPI | P | Gain | 0 to 999999 | Single-precision real number | User | User |


| Offset | Instruction used | Item | Name | Recommended range | Data type | Set by |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  |  | SPID, S2PID | SSPI |
| $\begin{aligned} & +54 \\ & +55 \end{aligned}$ | S.PID/ <br> S.2PID/ <br> S.SPI/ <br> S.OUT1/ <br> S.DUTY | ${ }^{* 1}$ | Integral constant | 0 to 999999 [s] | Single-precision real number | User | User |
| $\begin{aligned} & +56 \\ & +57 \end{aligned}$ | S.PID/ <br> S.2PID/S.SPI | D/STHT | Derivative constant/ Sampling cycle | 0 to 999999 [s] | Single-precision real number | User (Set D.) | User (Set STHT.) |
| $\begin{aligned} & +58 \\ & +59 \end{aligned}$ | S.PID/ <br> S.2PID/S.SPI | GW | Gap width | 0 to 100 [\%] | Single-precision real number | User | User |
| $\begin{aligned} & +60 \\ & +61 \end{aligned}$ | S.PID/ <br> S.2PID/S.SPI | GG | Gap gain | 0 to 999999 | Single-precision real number | User | User |
| $\begin{aligned} & +62 \\ & +63 \end{aligned}$ | S.PID/ <br> S.2PID/ <br> S.SPI/ <br> S.OUT1/ <br> S.DUTY | MVP | MV internal operation value | -999999 to 999999 [\%] | Single-precision real number | System | System |
| $\begin{aligned} & +64 \\ & +65 \end{aligned}$ | S.2PID | $\alpha$ | Two-degree-of-freedom parameter $\alpha$ | 0 to 1 | Single-precision real number | User | - |
| $\begin{aligned} & +66 \\ & +67 \end{aligned}$ | S.2PID | $\beta$ | Two-degree-of-freedom parameter $\beta$ | 0 to 1 | Single-precision real number | User | - |
| $\begin{aligned} & +68 \\ & +69 \end{aligned}$ | S.DUTY | CTDUTY | Control output cycle | 0 to 999999 [s] | Single-precision real number | User | - |

*1 The following instruction pairs use the same value in I.
S.PID instruction and S.OUT1 instruction
S.PID instruction and S.DUTY instruction
S.2PID instruction and S.OUT1 instruction
S.2PID instruction and S.DUTY instruction
S.SPI instruction and S.OUT1 instruction

## I-PD control (SIPD), blend PI control (SBPI)

| Offset | Instruction used | Item | Name | Recommended range | Data type | Set by |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  |  | SIPD | SBPI |
| +1 | Shared among the instructions | MODE | Operation mode | 0 to FFFFH | 16-bit unsigned binary | User/system | User/system |
| +3 |  | ALM | Alarm detection | 0 to FFFFH | 16-bit unsigned binary | User/system | User/system |
| +4 |  | INH | Alarm inhibition | 0 to FFFFH | 16-bit unsigned binary | User/system | User/system |
| $\begin{aligned} & +10 \\ & +11 \end{aligned}$ | S.PHPL | PV | Process value | RH to RL | Single-precision real number | System | System |
| $\begin{aligned} & +12 \\ & +13 \end{aligned}$ | S.OUT1 | MV | Manipulated value | -10 to 110 [\%] | Single-precision real number | User/system | User/system |
| $\begin{aligned} & +14 \\ & +15 \end{aligned}$ | S.IPD/S.BPI | SV | Set value | RL to RH | Single-precision real number | User | User |
| $\begin{aligned} & +16 \\ & +17 \end{aligned}$ | S.IPD/S.BPI | DV | Deviation | -110 to 110 [\%] | Single-precision real number | System | System |
| $\begin{aligned} & +18 \\ & +19 \end{aligned}$ | S.OUT1 | MH | Output upper limit value | -10 to 110 [\%] | Single-precision real number | User | User |
| $\begin{aligned} & +20 \\ & +21 \end{aligned}$ | S.OUT1 | ML | Output lower limit value | -10 to 110 [\%] | Single-precision real number | User | User |
| $\begin{aligned} & +22 \\ & +23 \end{aligned}$ | S.PHPL/ <br> S.IPD/S.BPI | RH | Engineering value upper limit | -999999 to 999999 | Single-precision real number | User | User |
| $\begin{aligned} & +24 \\ & +25 \end{aligned}$ | S.PHPL/ <br> S.IPD/S.BPI | RL | Engineering value lower limit | -999999 to 999999 | Single-precision real number | User | User |
| $\begin{aligned} & +26 \\ & +27 \end{aligned}$ | S.PHPL | PH | Upper limit alarm value | $\begin{aligned} & \text { RL to } \mathrm{RH} \\ & \mathrm{PL} \text { < } \mathrm{PH} \end{aligned}$ | Single-precision real number | User | User |
| $\begin{aligned} & +28 \\ & +29 \end{aligned}$ | S.PHPL | PL | Lower limit alarm value | RL to RH $\mathrm{PL}<\mathrm{PH}$ | Single-precision real number | User | User |
| $\begin{aligned} & +30 \\ & +31 \end{aligned}$ | S.PHPL | HH | Upper upper limit alarm value | RL to RH $\mathrm{PH} \leq \mathrm{HH}$ | Single-precision real number | User | User |
| $\begin{aligned} & +32 \\ & +33 \end{aligned}$ | S.PHPL | LL | Lower lower limit alarm value | RL to RH <br> $\mathrm{LL} \leq \mathrm{PL}$ | Single-precision real number | User | User |
| $\begin{aligned} & +38 \\ & +39 \end{aligned}$ | S.IN | $\alpha$ | Filter coefficient | 0 to 1 | Single-precision real number | User | User |
| $\begin{aligned} & +40 \\ & +41 \end{aligned}$ | S.PHPL | HS | Upper/lower limit alarm hysteresis | 0 to 999999 [\%] | Single-precision real number | User | User |
| $\begin{aligned} & +42 \\ & +43 \end{aligned}$ | S.PHPL | CTIM | Variation rate alarm check time | 0 to 999999 [s] | Single-precision real number | User | User |
| $\begin{aligned} & +44 \\ & +45 \end{aligned}$ | S.PHPL | DPL | Variation rate alarm value | 0 to 100 [\%] | Single-precision real number | User | User |
| $\begin{aligned} & +46 \\ & +47 \end{aligned}$ | S.IPD/S.BPI | CT | Control cycle | 0 to 999999 [s] | Single-precision real number | User | User |
| $\begin{aligned} & +48 \\ & +49 \end{aligned}$ | S.OUT1 | DML | Output variation rate limit value | 0 to 100 [\%] | Single-precision real number | User | User |
| $\begin{aligned} & +50 \\ & +51 \end{aligned}$ | S.IPD/S.BPI | DVL | Deviation limit value | 0 to 100 [\%] | Single-precision real number | User | User |
| $\begin{aligned} & +52 \\ & +53 \end{aligned}$ | S.IPD/S.BPI | P | Gain | 0 to 999999 | Single-precision real number | User | User |
| $\begin{aligned} & +54 \\ & +55 \end{aligned}$ | $\begin{aligned} & \text { S.IPD/S.BPI/ } \\ & \text { S.OUT1 } \end{aligned}$ | $1^{* 1}$ | Integral constant | 0 to 999999 [s] | Single-precision real number | User | User |
| $\begin{aligned} & +56 \\ & +57 \end{aligned}$ | S.IPD/S.BPI | D/SDV | Derivative constant/DV cumulative total | D: 0 to 999999 [s] | Single-precision real number | User | - |
|  |  |  |  | SDV: -999999 to 999999 [\%] | Single-precision real number | - | System |
| $\begin{aligned} & +58 \\ & +59 \end{aligned}$ | S.IPD/S.BPI | GW | Gap width | 0 to 100 [\%] | Single-precision real number | User | User |
| $\begin{aligned} & +60 \\ & +61 \end{aligned}$ | S.IPD/S.BPI | GG | Gap gain | 0 to 999999 | Single-precision real number | User | User |


| Offset | Instruction <br> used | Item | Name | Recommended range | Data type | Set by |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
|  |  |  | SIPD |  |  |  |  |
| +62 | S.IPD/ <br> S.OUT1 | MVP | MV internal operation <br> value | -999999 to $999999[\%]$ | Single-precision <br> real number | System | - |

*1 The instruction pairs, S.IPD instruction and S.OUT1 instruction, S.BPI instruction and S.OUT1 instruction, use the same value in I.

Manual output (SMOUT), monitor (SMON)

| Offset | Instruction used | Item | Name | Recommended range | Data type | Set by |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  |  | SMOUT | SMON |
| +1 | Shared among the instructions | MODE | Operation mode | 0 to FFFFFH | 16-bit unsigned binary | User/system | User/system |
| +3 |  | ALM | Alarm detection | 0 to FFFFFH | 16-bit unsigned binary | User/system | User/system |
| +4 |  | INH | Alarm inhibition | 0 to FFFFFH | 16-bit unsigned binary | - | User/system |
| $\begin{aligned} & +10 \\ & +11 \end{aligned}$ | S.PHPL | PV | Process value | RL to RH | Single-precision real number | - | System |
| $\begin{aligned} & +12 \\ & +13 \end{aligned}$ | S.MOUT | MV | Manipulated value | -10 to 110 [\%] | Single-precision real number | User | - |
| $\begin{aligned} & +22 \\ & +23 \end{aligned}$ | S.PHPL | RH | Engineering value upper limit | -999999 to 999999 | Single-precision real number | - | User |
| $\begin{aligned} & +24 \\ & +25 \end{aligned}$ | S.PHPL | RL | Engineering value lower limit | -999999 to 999999 | Single-precision real number | - | User |
| $\begin{aligned} & +26 \\ & +27 \end{aligned}$ | S.PHPL | PH | Upper limit alarm value | $\begin{aligned} & \text { RL to } \mathrm{RH} \\ & \mathrm{PL}<\mathrm{PH} \end{aligned}$ | Single-precision real number | - | User |
| $\begin{aligned} & +28 \\ & +29 \end{aligned}$ | S.PHPL | PL | Lower limit alarm value | $\begin{aligned} & \text { RL to } \mathrm{RH} \\ & \mathrm{PL}<\mathrm{PH} \end{aligned}$ | Single-precision real number | - | User |
| $\begin{aligned} & +30 \\ & +31 \end{aligned}$ | S.PHPL | HH | Upper upper limit alarm value | RL to RH $\mathrm{PH} \leq \mathrm{HH}$ | Single-precision real number | - | User |
| $\begin{aligned} & +32 \\ & +33 \end{aligned}$ | S.PHPL | LL | Lower lower limit alarm value | $\begin{aligned} & \mathrm{RL} \text { to } \mathrm{RH} \\ & \mathrm{LL} \leq \mathrm{PL} \end{aligned}$ | Single-precision real number | - | User |
| $\begin{aligned} & +38 \\ & +39 \end{aligned}$ | S.IN | $\alpha$ | Filter coefficient | 0 to 1 | Single-precision real number | - | User |
| $\begin{aligned} & +40 \\ & +41 \end{aligned}$ | S.PHPL | HS | Upper/lower limit alarm hysteresis | 0 to 999999 [\%] | Single-precision real number | - | User |
| $\begin{aligned} & +42 \\ & +43 \end{aligned}$ | S.PHPL | CTIM | Variation rate alarm check time | 0 to 999999 [s] | Single-precision real number | - | User |
| $\begin{aligned} & +44 \\ & +45 \end{aligned}$ | S.PHPL | DPL | Variation rate alarm value | 0 to 100 [\%] | Single-precision real number | - | User |

Manual output with monitor (SMWM), PIDP control (SPIDP)

| Offset | Instruction used | Item | Name | Recommended range | Data type | Set by |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  |  | SMWM | SPIDP |
| +1 | Shared among the instructions | MODE | Operation mode | 0 to FFFFH | 16-bit unsigned binary | User/system | User/system |
| +3 |  | ALM | Alarm detection | 0 to FFFFFH | 16-bit unsigned binary | User/system | User/system |
| +4 |  | INH | Alarm inhibition | 0 to FFFFFH | 16-bit unsigned binary | User/system | User/system |
| $\begin{aligned} & +10 \\ & +11 \end{aligned}$ | S.PHPL | PV | Process value | RL to RH | Single-precision real number | System | System |
| $\begin{aligned} & +12 \\ & +13 \end{aligned}$ | S.MOUT/ <br> S.PIDP | MV | Manipulated value | -10 to 110 [\%] | Single-precision real number | User | User/system |
| $\begin{aligned} & +14 \\ & +15 \end{aligned}$ | S.PIDP | SV | Set value | RL to RH | Single-precision real number | - | User |
| $\begin{aligned} & +16 \\ & +17 \end{aligned}$ | S.PIDP | DV | Deviation | -110 to 110 [\%] | Single-precision real number | - | System |
| $\begin{aligned} & +18 \\ & +19 \end{aligned}$ | S.PIDP | MH | Output upper limit value | -10 to 110 [\%] | Single-precision real number | - | User |
| $\begin{aligned} & +20 \\ & +21 \end{aligned}$ | S.PIDP | ML | Output lower limit value | -10 to 110 [\%] | Single-precision real number | - | User |
| $\begin{aligned} & +22 \\ & +23 \end{aligned}$ | S.PHPL/ S.PIDP | RH | Engineering value upper limit | -999999 to 999999 | Single-precision real number | User | User |
| $\begin{aligned} & +24 \\ & +25 \end{aligned}$ | S.PHPL/ <br> S.PIDP | RL | Engineering value lower limit | -999999 to 999999 | Single-precision real number | User | User |
| $\begin{aligned} & +26 \\ & +27 \end{aligned}$ | S.PHPL | PH | Upper limit alarm value | $\text { RL to } \mathrm{RH}$ $\mathrm{PL}<\mathrm{PH}$ | Single-precision real number | User | User |
| $\begin{aligned} & +28 \\ & +29 \end{aligned}$ | S.PHPL | PL | Lower limit alarm value | $\begin{aligned} & \mathrm{RL} \text { to } \mathrm{RH} \\ & \mathrm{PL} \text { < } \mathrm{PH} \end{aligned}$ | Single-precision real number | User | User |
| $\begin{aligned} & +30 \\ & +31 \end{aligned}$ | S.PHPL | HH | Upper upper limit alarm value | RL to RH $\mathrm{PH} \leq \mathrm{HH}$ | Single-precision real number | User | User |
| $\begin{aligned} & +32 \\ & +33 \end{aligned}$ | S.PHPL | LL | Lower lower limit alarm value | RL to RH <br> $\mathrm{LL} \leq \mathrm{PL}$ | Single-precision real number | User | User |
| $\begin{aligned} & +38 \\ & +39 \end{aligned}$ | S.IN | $\alpha$ | Filter coefficient | 0 to 1 | Single-precision real number | User | User |
| $\begin{aligned} & +40 \\ & +41 \end{aligned}$ | S.PHPL | HS | Upper/lower limit alarm hysteresis | 0 to 999999 [\%] | Single-precision real number | User | User |
| $\begin{aligned} & +42 \\ & +43 \end{aligned}$ | S.PHPL | CTIM | Variation rate alarm check time | 0 to 999999 [s] | Single-precision real number | User | User |
| $\begin{aligned} & +44 \\ & +45 \end{aligned}$ | S.PHPL | DPL | Variation rate alarm value | 0 to 100 [\%] | Single-precision real number | User | User |
| $\begin{aligned} & +46 \\ & +47 \end{aligned}$ | S.PIDP | CT | Control cycle | 0 to 999999 [s] | Single-precision real number | - | User |
| $\begin{aligned} & +48 \\ & +49 \end{aligned}$ | S.PIDP | DML | Output variation rate limit value | 0 to 100 [\%] | Single-precision real number | - | User |
| $\begin{aligned} & +50 \\ & +51 \end{aligned}$ | S.PIDP | DVL | Deviation limit value | 0 to 100 [\%] | Single-precision real number | - | User |
| $\begin{aligned} & +52 \\ & +53 \end{aligned}$ | S.PIDP | P | Gain | 0 to 999999 | Single-precision real number | - | User |
| $\begin{aligned} & +54 \\ & +55 \end{aligned}$ | S.PIDP | 1 | Integral constant | 0 to 999999 [s] | Single-precision real number | - | User |
| $\begin{aligned} & +56 \\ & +57 \end{aligned}$ | S.PIDP | D | Derivative constant | 0 to 999999 [s] | Single-precision real number | - | User |
| $\begin{aligned} & +58 \\ & +59 \end{aligned}$ | S.PIDP | GW | Gap width | 0 to 100 [\%] | Single-precision real number | - | User |
| $\begin{aligned} & +60 \\ & +61 \end{aligned}$ | S.PIDP | GG | Gap gain | 0 to 999999 | Single-precision real number | - | User |

Two-position (on/off) control (SONF2), three-position (on/off) control (SONF3)

| Offset | Instruction used | Item | Name | Recommended range | Data type | Set by |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  |  | SONF2 | SONF3 |
| +1 | Shared among the instructions | MODE | Operation mode | 0 to FFFFH | 16-bit unsigned binary | User/system | User/system |
| +3 |  | ALM | Alarm detection | 0 to FFFFFH | 16-bit unsigned binary | User/system | User/system |
| +4 |  | INH | Alarm inhibition | 0 to FFFFFH | 16-bit unsigned binary | User/system | User/system |
| $\begin{aligned} & +10 \\ & +11 \end{aligned}$ | S.PHPL | PV | Process value | RL to RH | Single-precision real number | System | System |
| $\begin{aligned} & +12 \\ & +13 \end{aligned}$ | S.ONF2/ <br> S.ONF3 | MV | Manipulated value | -10 to 110 [\%] | Single-precision real number | User/system | User/system |
| $\begin{aligned} & +14 \\ & +15 \end{aligned}$ | S.ONF2/ <br> S.ONF3 | SV | Set value | RL to RH | Single-precision real number | User | User |
| $\begin{aligned} & +16 \\ & +17 \end{aligned}$ | S.ONF2/ <br> S.ONF3 | DV | Deviation | -110 to 110 [\%] | Single-precision real number | System | System |
| $\begin{aligned} & +18 \\ & +19 \end{aligned}$ | S.ONF2/ S.ONF3 | HSO | Hysteresis | 0 to 999999 | Single-precision real number | User | User |
| $\begin{aligned} & +20 \\ & +21 \end{aligned}$ | S.ONF3 | HS1 | Hysteresis | 0 to 999999 | Single-precision real number | - | User |
| $\begin{aligned} & +22 \\ & +23 \\ & +23 \end{aligned}$ | S.PHPL | RH | Engineering value upper limit | -999999 to 999999 | Single-precision real number | User | User |
| $\begin{aligned} & +24 \\ & +25 \end{aligned}$ | S.PHPL | RL | Engineering value lower limit | -999999 to 999999 | Single-precision real number | User | User |
| $\begin{aligned} & +26 \\ & +27 \end{aligned}$ | S.PHPL | PH | Upper limit alarm value | $\begin{aligned} & R L \text { to } R H \\ & P L<P H \end{aligned}$ | Single-precision real number | User | User |
| $\begin{aligned} & +28 \\ & +29 \end{aligned}$ | S.PHPL | PL | Lower limit alarm value | $\begin{aligned} & \text { RL to } \mathrm{RH} \\ & \text { PL < PH } \end{aligned}$ | Single-precision real number | User | User |
| $\begin{aligned} & +30 \\ & +31 \end{aligned}$ | S.PHPL | HH | Upper upper limit alarm value | RL to RH $\mathrm{PH} \leq \mathrm{HH}$ | Single-precision real number | User | User |
| $\begin{aligned} & +32 \\ & +33 \end{aligned}$ | S.PHPL | LL | Lower lower limit alarm value | RL to RH $\mathrm{LL} \leq \mathrm{PL}$ | Single-precision real number | User | User |
| $\begin{aligned} & +38 \\ & +39 \end{aligned}$ | S.IN | $\alpha$ | Filter coefficient | 0 to 1 | Single-precision real number | User | User |
| $\begin{aligned} & +40 \\ & +41 \end{aligned}$ | S.PHPL | HS | Upper/lower limit alarm hysteresis | 0 to 999999 [\%] | Single-precision real number | User | User |
| $\begin{aligned} & +42 \\ & +43 \end{aligned}$ | S.PHPL | CTIM | Variation rate alarm check time | 0 to 999999 [s] | Single-precision real number | User | User |
| $\begin{aligned} & +44 \\ & +45 \end{aligned}$ | S.PHPL | DPL | Variation rate alarm value | 0 to 100 [\%] | Single-precision real number | User | User |
| $\begin{aligned} & +46 \\ & +47 \end{aligned}$ | S.ONF2/ S.ONF3 | CT | Control cycle | 0 to 999999 [s] | Single-precision real number | User | User |

## Batch counter (SBC)

| Offset | Instruction used | Item | Name | Recommended range | Data type | Set by |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  |  | SBC |
| +1 | Shared among the | MODE | Operation mode | 0 to FFFFFH | 16-bit unsigned binary | User/system |
| +3 | instructions | ALM | Alarm detection | 0 to FFFFFH | 16-bit unsigned binary | User/system |
| +4 |  | INH | Alarm inhibition | 0 to FFFFFH | 16-bit unsigned binary | User/system |
| $\begin{aligned} & +10 \\ & +11 \end{aligned}$ | S.PSUM | SUM1 | Integrated value (integral part) | 0 to 2147483647 | 32-bit unsigned binary | System |
| $\begin{aligned} & +12 \\ & +13 \end{aligned}$ | S.PSUM | SUM2 | Integrated value (decimal part) | 0 to 2147483647 | 32-bit unsigned binary | System |
| $\begin{aligned} & +14 \\ & +15 \end{aligned}$ | S.BC | SV1 | Set value 1 | 0 to 2147483647 | 32-bit unsigned binary | User |
| $\begin{aligned} & +16 \\ & +17 \end{aligned}$ | S.BC | SV2 | Set value 2 | 0 to 2147483647 | 32-bit unsigned binary | User |
| $\begin{aligned} & +26 \\ & +27 \end{aligned}$ | S.BC | PH | Upper limit alarm value | 0 to 2147483647 | 32-bit unsigned binary | User |
| $\begin{aligned} & +42 \\ & +43 \end{aligned}$ | S.BC | CTIM | Variation rate alarm check time | 0 to 999999 [s] | Single-precision real number | User |
| $\begin{aligned} & +44 \\ & +45 \end{aligned}$ | S.BC | DPL | Variation rate alarm value | 0 to 2147483647 | 32-bit unsigned binary | User |

Ratio control (SR)

| Offset | Instruction <br> used | Item | Name | Recommended range | Data type | Set by |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $+\boldsymbol{l}$ |  | Shared <br> among the <br> instructions | MODE | Operation mode | 0 to FFFFH | SR |

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## REVISIONS

*The manual number is given on the bottom left of the back cover.

| Revision date | *Manual number | Description |
| :--- | :--- | :--- |
| June 2014 | SH(NA)-081266ENG-A | First edition |
| July 2014 | SH(NA)-081266ENG-B | Error correction |
| October 2014 | SH(NA)-081266ENG-C | ■Added or modified parts <br> Section 1.3, Chapter 11, 13, "Execution condition" (for each instruction) |
| January 2015 | SH(NA)-081266ENG-D | ■Added or modified parts <br> Chapter 10, "FBD/LD" (for each instruction) |

Japanese manual number: SH-081226-D
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## WARRANTY

Please confirm the following product warranty details before using this product.

## 1. Gratis Warranty Term and Gratis Warranty Range

If any faults or defects (hereinafter "Failure") found to be the responsibility of Mitsubishi occurs during use of the product within the gratis warranty term, the product shall be repaired at no cost via the sales representative or Mitsubishi Service Company.
However, if repairs are required onsite at domestic or overseas location, expenses to send an engineer will be solely at the customer's discretion. Mitsubishi shall not be held responsible for any re-commissioning, maintenance, or testing on-site that involves replacement of the failed module.
[Gratis Warranty Term]
The gratis warranty term of the product shall be for one year after the date of purchase or delivery to a designated place.
Note that after manufacture and shipment from Mitsubishi, the maximum distribution period shall be six (6) months, and the longest gratis warranty term after manufacturing shall be eighteen (18) months. The gratis warranty term of repair parts shall not exceed the gratis warranty term before repairs.
[Gratis Warranty Range]
(1) The range shall be limited to normal use within the usage state, usage methods and usage environment, etc., which follow the conditions and precautions, etc., given in the instruction manual, user's manual and caution labels on the product.
(2) Even within the gratis warranty term, repairs shall be charged for in the following cases.

1. Failure occurring from inappropriate storage or handling, carelessness or negligence by the user. Failure caused by the user's hardware or software design.
2. Failure caused by unapproved modifications, etc., to the product by the user.
3. When the Mitsubishi product is assembled into a user's device, Failure that could have been avoided if functions or structures, judged as necessary in the legal safety measures the user's device is subject to or as necessary by industry standards, had been provided.
4. Failure that could have been avoided if consumable parts (battery, backlight, fuse, etc.) designated in the instruction manual had been correctly serviced or replaced.
5. Failure caused by external irresistible forces such as fires or abnormal voltages, and Failure caused by force majeure such as earthquakes, lightning, wind and water damage.
6. Failure caused by reasons unpredictable by scientific technology standards at time of shipment from Mitsubishi.
7. Any other failure found not to be the responsibility of Mitsubishi or that admitted not to be so by the user.
8. Onerous repair term after discontinuation of production
(1) Mitsubishi shall accept onerous product repairs for seven (7) years after production of the product is discontinued.

Discontinuation of production shall be notified with Mitsubishi Technical Bulletins, etc.
(2) Product supply (including repair parts) is not available after production is discontinued.
3. Overseas service

Overseas, repairs shall be accepted by Mitsubishi's local overseas FA Center. Note that the repair conditions at each FA Center may differ.
4. Exclusion of loss in opportunity and secondary loss from warranty liability

Regardless of the gratis warranty term, Mitsubishi shall not be liable for compensation of damages caused by any cause found not to be the responsibility of Mitsubishi, loss in opportunity, lost profits incurred to the user by Failures of Mitsubishi products, special damages and secondary damages whether foreseeable or not, compensation for accidents, and compensation for damages to products other than Mitsubishi products, replacement by the user, maintenance of on-site equipment, start-up test run and other tasks.

## 5. Changes in product specifications

The specifications given in the catalogs, manuals or technical documents are subject to change without prior notice.

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When exported from Japan, this manual does not require application to the Ministry of Economy, Trade and Industry for service transaction permission.


[^0]:    ＊1 Cannot be specified in ladder programs．

[^1]:    ＊1 Only bit type labels assigned to the device（V）can be used．

[^2]:    ＊1 Only T and ST can be used．
    ＊2 T，ST，and C cannot be used．
    ＊3 Only K（decimal constant）can be used．

[^3]:    *1 Only F can be used.

[^4]:    There is no operation error.

[^5]:    ＊1 The same device number can be specified for（s1）and（d）or（s2）and（d）．

[^6]:    ＊1 The same device number can be specified for（s1）and（d）or（s2）and（d）．

[^7]:    *1 The same device number can be specified for ( s 1 ) and (d) or ( s 2 ) and (d).

[^8]:    ＊1 T，C，and ST cannot be used．

[^9]:    (1) Specified in units of words
    (2) Same as the number of write data if case of normal completion

[^10]:    *1 A two-byte character such as a kanji character represented in shift JIS code should be counted 2.

[^11]:    *1 A two-byte character such as a kanji character represented in shift JIS code should be counted 2.

[^12]:    *1 A two-byte character such as a kanji character represented in shift JIS code should be counted 2.

[^13]:    ＊1 Only $X$ can be used．Note，however，that it can be used only within the range of the number of I／O points（the number of points that can access I／O modules）
    ＊2 Only C can be used．

[^14]:    ＊1 Only T can be used．

[^15]:    *1 Only X can be used.

[^16]:    ＊1 Only Y can be used．

[^17]:    *1 Only labels assigned to device $(\mathrm{X})$ can be used

[^18]:    ＊1 T，C，and ST cannot be used．

[^19]:    *1 T, C, and ST cannot be used.

[^20]:    *1 For details, refer to the following.
    $\longmapsto$ Page 47 Checking the ranges of instruction runtime devices and labels
    For the error code stored in the completion status of the operand, refer to the following.
    $\longmapsto$ Page 844 Error codes related to database access instructions

[^21]:    *1 Only SM420 to SM424 can be used.

[^22]:    ＊1 Only $X$ and $Y$ can be used．

[^23]:    ＊1 Only the DFROM $(P)$ instruction can be used．

[^24]:    ＊1 Only the $\mathrm{DTO}(\mathrm{P})$ instruction can be used．

[^25]:    ＊1 Only the DTOD（P）instruction can be used．

[^26]:    *1 A local device and a file register which is set for each program cannot be used.

[^27]:    *1 If the DPPI or ERRI of the disable alarm detection (INH) is set to 1 , the variation rate alarm (BB3) and the DPPA of the alarm detection (ALM) are set to 0 .

[^28]:    *1 If the DMLI or ERRI of the disable alarm detection (INH) is set to 1, the DVLA of the alarm detection (ALM) and the large deviation alarm (BB1) are set to 0.

[^29]:    *1 For the specifications of the string data to be specified, refer to the following.
    $\longmapsto$ Page 1193 Specifications of character string data specified by link dedicated instructions

[^30]:    - Write processing is performed only once on the rising edge when the write command turns on.

[^31]:    *1 For the specifications of the string data to be specified, refer to the following.

[^32]:    - Completion device (d2)

    The completion device turns on in END processing of the scan performed upon completion of the J(P).ZNWR instruction and turns off in the next END processing.

    - Completion status indication device (d2)+1

    The completion device turns on of depending on the completion status of the $J(P)$.ZNWR instruction.
    When completed normally: Unchanged from off.
    When completed with an error: Turns on in END processing of the scan performed upon completion of the $J(P)$.ZNWR instruction and turns off in the next END processing.

[^33]:    *1 Value that was set in the CC-Link configuration window of the engineering tool.

[^34]:    *1 If this operand is omitted, specify the dummy device or label.

[^35]:    *1 For two or more stations occupied, specify only the slave station start number.

[^36]:    $(1$ The data specified by (s2) is stored in the send buffer of the master station
    (2) A request message is sent to the target station specified by ( s 1 ) +1 .
    (3) The target station analyzes the request message received and sends back a response message.
    (4) The response message received is stored in the receive buffer of the master station.
    (5 The received data is stored in the device specified by (d1) and later, and the device specified by (d2) is turned on

[^37]:    ＊1 T，ST，and C cannot be used．

[^38]:    ＊1 T，ST，and C cannot be used

[^39]:    *1 T, ST, and C cannot be used.

[^40]:    *1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

[^41]:    *1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

[^42]:    *1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

[^43]:    *1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

[^44]:    *1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

[^45]:    *1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

[^46]:    *1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

[^47]:    *1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

[^48]:    *1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

[^49]:    *1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

[^50]:    *1 If the value FALSE is output from ENO, the output data from (d) will be undefined. Create a program so that the undefined value will not be used in operations.

